

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT4853572

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
DAVID PRITCHARD	06/22/2016
LIXIA LEI	07/15/2016
DENIZ E. CIVAY	06/28/2016
SCOTT D. LUNING	08/30/2017
NEHA NAYYAR	06/27/2016
RECEIVING PARTY DATA	
Name:	GLOBALFOUNDRIES INC.
Street Address:	P.O. BOX 309
Internal Address:	UGLAND HOUSE
City:	GRAND CAYMAN
State/Country:	CAYMAN ISLANDS
Postal Code:	KY1-1104
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	15912141
CORRESPONDENCE DATA	
Fax Number:	(703)519-9958
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	7035199951
Email:	docket@dcpatent.com
Correspondent Name:	DITTHAVONG & STEINER P.C.
Address Line 1:	44 CANAL CENTER PLAZA
Address Line 2:	SUITE 322
Address Line 4:	ALEXANDRIA, VIRGINIA 22314
ATTORNEY DOCKET NUMBER:	P8474US01
NAME OF SUBMITTER:	BRIAN K. SEIDLECK
SIGNATURE:	/Brian K. Seidleck/
DATE SIGNED:	03/06/2018

Total Attachments: 5

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COMBINED DECLARATION and ASSIGNMENT (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

Title of Invention	FULLY DEPLETED SILICON-ON-INSULATOR (FDSOI) TRANSISTOR DEVICE AND SELF-ALIGNED ACTIVE AREA IN FDSOI BULK EXPOSED REGIONS
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DECLARATION

As the below named inventor, I hereby declare that:

This declaration is directed to: ☒ The attached application, or
☐ United States application or PCT international application number _____ filed on _____

The above-identified application was made or authorized to be made by me.

I believe that I am the original inventor or an original joint inventor of a claimed invention in the application.

I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) years, or both.

I have reviewed and understand the contents of the application and am aware of the duty to disclose to the US Patent and Trademark Office all information known to me to be material to patentability as defined in 37 CFR § 1.56.

ASSIGNMENT

For good and valuable consideration, the receipt, sufficiency and adequacy of which are hereby acknowledged, I hereby sell, assign, transfer, to GLOBALFOUNDRIES Inc. having a business address of PO Box 309, Ugland House, Grand Cayman KY1-1104 its successors and assigns, absolutely and forever, my entire right, title and interest in and to the invention listed above, together with the Application, any and all Patents that may issue in the United States and/or any foreign countries, and any reissues, re-examinations, renewals, continuations, continuation-in-parts, divisionals or extensions thereof that may be issued or granted, and all right, title and interest to the inventions contained in said Patents and Applications, for the United States and all foreign countries, and all the rights and privileges relating thereto including but not limited to the priority rights arising from said Applications, the right to recover and take all such proceedings as may be necessary for the recovery of damages or otherwise in respect of past, present and future infringement, and the right to apply for, take and maintain patents on said inventions.

If any portion of this Combined Declaration or Assignment is held unenforceable, then remaining portions hereof shall nevertheless remain in full force and effect.

LEGAL NAME OF INVENTOR

Inventor: David PRITCHARD

Date: 22 June 2010

Signature: 

Note: An application data sheet (PTO/SB/14 or equivalent), including naming the entire inventive entity, must accompany this form. Use an additional form for each additional inventor.

COMBINED DECLARATION and ASSIGNMENT (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

**Title of
Invention**

**FULLY DEPLETED SILICON-ON-INSULATOR (FDSOI) TRANSISTOR DEVICE
AND SELF-ALIGNED ACTIVE AREA IN FDSOI BULK EXPOSED REGIONS**

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LEGAL NAME OF INVENTOR

Inventor: Lixia LEI

Date: 07/15/2016

Signature: Lixia LEI

PATENT

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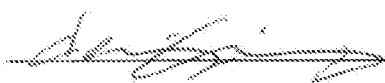
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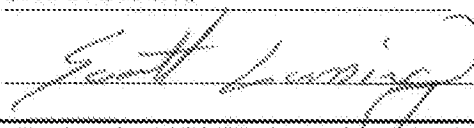
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LEGAL NAME OF INVENTOR	
Inventor: Deniz E. CIVAY	Date: 6/28/2016
Signature: 	

Note: An application data sheet (PTO/SB/14 or equivalent), including naming the entire inventive entity, must accompany this form. Use an additional form for each additional inventor.

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LEGAL NAME OF INVENTOR	
Inventor:	Scott D. LUNING
Date:	2/30/17
Signature:	
<p><small>Note: An application data sheet (PTO/SB/14 or equivalent), including naming the entire inventive entity, must accompany this form. Use an additional form for each additional inventor.</small></p>	

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InventionFULLY DEPLETED SILICON-ON-INSULATOR (FDSOI) TRANSISTOR DEVICE
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LEGAL NAME OF INVENTOR

Inventor: Neha NAYYARDate: 06/27/2016Signature: Neha Nayyar

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