504815799 03/12/2018

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
BENNY POLLAK	03/12/2018
DANA MICHELLE VANTREASE	03/07/2018
ADI HABUSHA	03/07/2018

RECEIVING PARTY DATA

Name:	AMAZON TECHNOLOGIES, INC.	
Street Address:	PO BOX 81226	
City:	SEATTLE	
State/Country:	WASHINGTON	
Postal Code:	98108-1226	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	15918930

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	102028-1067294 (101500US)	
NAME OF SUBMITTER:	KIMBERLY PAPP	
SIGNATURE:	/Kimberly Papp/	
DATE SIGNED:	03/12/2018	

Total Attachments: 6

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source=2018-03-12_P43644-US_102028-1067294-101500US_XAsgn#page5.tif source=2018-03-12_P43644-US_102028-1067294-101500US_XAsgn#page6.tif

U.S. Patent Appln. No. <u>15/918,930</u> Attorney Docket No. <u>102028-1067294 (101500US)</u>
Filing Date <u>03/12/2018</u> Client Docket No. <u>P43644-US</u>

PARTIES TO THE ASSIGNMENT

Assignor(s):

Benny Pollak 410 Terry Avenue North Seattle, WA 98109-5210

Dana Michelle Vantrease 410 Terry Avenue North Seattle, WA 98109-5210

Adi Habusha 410 Terry Avenue North Seattle, WA 98109-5210

Assignee: Amazon Technologies, Inc. PO Box 81226 Seattle, WA 98108-1226

AGREEMENT

WHEREAS, ASSIGNOR(S) (listed above) are inventor(s) of an invention entitled

"INTEGRATED CIRCUIT WITH RATE LIMITING" (Invention) for which a non-provisional application

for United States Letters Patent (Application):

was filed on _______ and accorded U.S. Application No. _______; or

will be filed concurrently with the submission of this executed PATENT ASSIGNMENT for recordation.

ASSIGNOR(S) hereby authorizes and requests ASSIGNEE'S legal representatives, of <u>Kilpatrick Townsend & Stockton LLP</u>, associated with Customer No. <u>107508</u>, to insert in the header above and here in parentheses (U.S. Patent Application No. <u>15/918,930</u>, filed <u>03/12/2018</u>) this application's U.S. application number and filing date, when known.

U.S. Patent Appln. No. 15/918,930

Attorney Docket No. 102028-1067294 (101500US)

Filing Date 03/12/2018

Client Docket No. P43644-US

Title of Invention: INTEGRATED CIRCUIT WITH RATE LIMITING

Inventors: Benny Pollak, Dana Michelle Vantrease, Adi Habusha

WHEREAS, ASSIGNEE, a corporation of the State of Nevada, is desirous of acquiring the

entire right, title and interest in and to the Invention and in and to any letters patent that may be

granted therefor in the United States and in any and all foreign countries.

NOW, THEREFORE, in exchange for good and valuable consideration, the receipt of which

is hereby acknowledged, ASSIGNOR(S) hereby sell, assign and transfer unto ASSIGNEE its

successors and assigns, the entire right, title and interest in and to said Invention and any

improvements thereto, said Application and any and all letters patent which may be granted for

said Invention in the United States of America and its territorial possessions and in any and all

foreign countries, and in any and all provisional, divisions, reissues, re-examinations and

continuations thereof, including the right to file foreign applications directly in the name of

ASSIGNEE and to claim priority rights deriving from said Application to which said foreign

applications are entitled by virtue of international convention, treaty or otherwise, said Invention,

Application and all letters patent on said Invention to be held and enjoyed by ASSIGNEE and its

successors and assigns for their use and benefit and of their successors and assigns as fully and

entirely as the same would have been held and enjoyed by ASSIGNOR(S) had this assignment,

transfer and sale not been made. ASSIGNOR(S) hereby authorize and request the Commissioner

of Patents and Trademarks, as well as all other applicable patent offices throughout the world, to

issue all letters patent on said Invention to ASSIGNEE. ASSIGNOR(S) agree to execute all

instruments and documents required for the making and prosecution of applications for United

States and foreign letters patent on said Invention, for litigation regarding said letters patent, or

for the purpose of protecting title to said Invention or letters patent therefor.

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U.S. Patent Appln. No. 15/918,930

Attorney Docket No. 102028-1067294 (101500US)

Filing Date 03/12/2018

Client Docket No. P43644-US

Title of Invention: INTEGRATED CIRCUIT WITH RATE LIMITING

Inventors: Benny Pollak, Dana Michelle Vantrease, Adi Habusha

AND ASSIGNOR(S) DOES HEREBY sell, assign, transfer, and convey to ASSIGNEE, its

successors, legal representatives, and assigns all claims for damages and all remedies arising out

of any violation of the rights assigned hereby that may have accrued prior to the date of

assignment to ASSIGNEE, or may accrue hereafter, including, but not limited to, the right to sue

for, collect, and retain damages for past infringements of said letters patent before or after

issuance.

AND ASSIGNOR(S) DOES HEREBY covenant and agree that ASSIGNOR(S) will communicate

to ASSIGNEE, its successors, legal representatives and assigns, any facts known to ASSIGNOR

respecting the Invention or said Application, and testify in any legal proceeding, assist in the

preparation of any other patent property relating to the Application and the Invention or any

improvements made thereto, sign/execute all lawful papers, provide all requested documents,

execute and make all rightful oaths and/or declarations in connection with the Application and

the Invention including any improvements made thereto, any patent applications filed therefrom,

and any continuing application filed from any of the aforementioned applications, and generally

do everything possible to aid ASSIGNEE, its successors, legal representatives and assigns, to obtain

and enforce proper patent protection for the Invention in all countries. These provisions are

binding upon ASSIGNOR's heirs, legal representatives, administrators, and assigns.

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U.S. Patent Appln. No. <u>15/918,930</u>	Attorney Docket No. <u>102028-1067294 (101500US)</u>
Filing Date <u>03/12/2018</u>	Client Docket No. <u>P43644-US</u>
Title of Invention: INTEGRATED CIRCUIT WITH	I RATE LIMITING
Inventors: Benny Pollak, Dana Michelle Van	trease, Adi Habusha
<u>DECLARATION</u> As a below named inventor, I hereby declare	that:
This declaration is directed to:	
igstyle igstyle the attached application; or	
United States Patent Application	number; or
PCT International Application nu	mber filed on
The above-identified application was made o	or authorized to be made by me.
I believe that I am the original inventor or an	original joint inventor of a claimed Invention in the
application.	
I have reviewed and understand the content	s of the above-identified application, and
acknowledge the duty to disclose to the US F	atent & Trademark Office all information known to
me to be material to the patentability as def	ined in 37 CFR 1.56.
I hereby acknowledge that any willful false sunder 18 USC 1001 by fine or imprisonment	tatement made in this declaration is punishable of not more than five (5) years, or both.
Co w	
Benny Pollak	
12-Mar-2018	
Date	

U.S. Patent Appln. No. <u>15/918,930</u>	Attorney Docket No. <u>102028-106/294 (10150005</u>
Filing Date <u>03/12/2018</u>	Client Docket No. <u>P43644-US</u>
Title of Invention: INTEGRATED CIRCUIT WITH	RATE LIMITING
Inventors: Benny Pollak, Dana Michelle Vanti	rease, Adi Habusha
<u>DECLARATION</u>	
As a below named inventor, I hereby declare t	that:
This declaration is directed to:	
the attached application; or	
United States Patent Application r	number; or
PCT International Application num	ber filed on
The above-identified application was made or	authorized to be made by me.
I believe that I am the original inventor or an α	original joint inventor of a claimed Invention in the
application.	
I have reviewed and understand the contents	of the above-identified application, and
acknowledge the duty to disclose to the US Pa	tent & Trademark Office all information known to
me to be material to the patentability as defir	ned in 37 CFR 1.56.
I hereby acknowledge that any willful false sta	itement made in this declaration is punishable
under 18 USC 1001 by fine or imprisonment o	f not more than five (5) years, or both.
Docusigned by: Dana Michelle Vantrease/	
Dana Michelle Vantrease	
March 7, 2018	
Date	

U.S. Patent Appln. No. <u>15/918,930</u>	Attorney Docket No. <u>102028-1067294 (101500US)</u>
Filing Date <u>03/12/2018</u>	Client Docket No. P43644-US
Title of Invention: INTEGRATED CIRCUIT WIT	H RATE LIMITING
Inventors: Benny Pollak, Dana Michelle Var	ntrease, Adi Habusha
DECLARATION	
As a below named inventor, I hereby declare	e that:
This declaration is directed to:	
igotimes the attached application; or	
United States Patent Application	n number; or
PCT International Application nu	ımber filed on
The above-identified application was made	or authorized to be made by me.
I believe that I am the original inventor or a	n original joint inventor of a claimed Invention in the
application.	
I have reviewed and understand the conten	• •
·	Patent & Trademark Office all information known to
me to be material to the patentability as de	fined in 37 CFR 1.56.
I hereby acknowledge that any willful false s	statement made in this declaration is punishable
under 18 USC 1001 by fine or imprisonment	of not more than five (5) years, or both.
Ja S	
Adi Habusha	
7 Mar 2018 Date	
for Sit Situal	

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RECORDED: 03/12/2018