

PATENT ASSIGNMENT COVER SHEET

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Application Number:	09098118
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Invention Title: DC BUS VOLTAGE BALANCING AND CONTROL IN MULTI-LEVEL INVERTERS

Inventor(s): Thomas Lipo, Gautam Sinha

U.S. Filing/Issue Date: June 16, 1998

Patent or Application Serial No.: 09/098118

Grant/Contract Number(s): National Science Foundation Grant No: 9510115

Foreign Applications filed/intended in (countries):

The invention identified above is a Subject Invention under **35 U.S.C. 200, et seq.**, and the Standard Patent Rights clause at **37 CFR 401.14, FAR 52.227-11** or **FAR 52.227-12** (if applicable) which are included among the terms of the above identified grant or contract award from the United State Government. This document is confirmatory of:

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(Name of Grantee/Contractor Official) (Signature)



Title General Counsel _____

For Wisconsin Alumni Research Foundation
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