

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT4970193

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	SECURITY INTEREST
CONVEYING PARTY DATA	
Name	Execution Date
INTEGRATED DEVICE TECHNOLOGY, INC.	05/21/2018
RECEIVING PARTY DATA	
Name:	JPMORGAN CHASE BANK, N.A.
Street Address:	270 PARK AVENUE
City:	NEW YORK
State/Country:	NEW YORK
Postal Code:	10017
PROPERTY NUMBERS Total: 9	
Property Type	Number
Application Number:	15923631
Application Number:	15889737
Application Number:	15904320
Application Number:	15867553
Application Number:	15924668
Application Number:	15887013
Application Number:	15903832
Application Number:	15886955
Application Number:	15880957
CORRESPONDENCE DATA	
Fax Number:	(408)284-8454
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	4083601517
Email:	richard.swope@idt.com
Correspondent Name:	RICHARD SWOPE
Address Line 1:	6024 SILVER CREEK VALLEY ROAD
Address Line 4:	SAN JOSE, CALIFORNIA 95138
NAME OF SUBMITTER:	RICHARD SWOPE
SIGNATURE:	/Richard Swope/

DATE SIGNED:	05/21/2018
Total Attachments: 4 source=Patent Security Agreement -- Additional Patents 5-21-2018_Executed#page1.tif source=Patent Security Agreement -- Additional Patents 5-21-2018_Executed#page2.tif source=Patent Security Agreement -- Additional Patents 5-21-2018_Executed#page3.tif source=Patent Security Agreement -- Additional Patents 5-21-2018_Executed#page4.tif	

Patent Security Agreement

This Patent Security Agreement dated as of May 21, 2018 made by Integrated Device Technology, Inc. as pledgor and debtor (in such capacities and together with any successors in such capacities, individually, a "Pledgor"), in favor of JPMORGAN CHASE BANK, N.A., in its capacity as Collateral Agent pursuant to the Credit Agreement (as defined in the Security Agreement), as pledgee, assignee and secured party (in such capacities and together with any successors in such capacities, the "Collateral Agent").

WITNESSETH:

WHEREAS, the Pledgor is party to a Security Agreement of even date herewith (as amended, amended and restated, supplemented or otherwise modified from time to time in accordance with the provisions thereof, the "Security Agreement") made in favor of the Collateral Agent pursuant to which the Pledgor is required to execute and deliver this Patent Security Agreement;

NOW, THEREFORE, in consideration of the foregoing premises and other good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, and to induce the Collateral Agent, for the benefit of the Secured Parties, to enter into the Credit Agreement, the Pledgor and the Collateral Agent hereby agree as follows:

SECTION 1. Defined Terms. Unless otherwise defined herein, terms defined in the Security Agreement and used herein have the meaning given to them in the Security Agreement.

SECTION 2. Grant of Security Interest in Patent Collateral. As collateral security for the payment and performance in full of all Secured Obligations, each Pledgor hereby pledges and grants to the Collateral Agent for the benefit of the Secured Parties, a lien on and security interest in all of the right, title and interest of such Pledgor in, to and under the following property, wherever located, and whether now existing or hereafter arising or acquired from time to time (collectively, the "Patent Collateral"):

(a) all patents and all patent applications (whether issued, allowed or filed in the United States or any other country or any trans-national patent registry) of such Pledgor now or hereafter, owned, filed or acquired by, or assigned to, such Pledgor, including the patents and patent applications listed on Schedule I attached hereto, together with any and all (i) rights and privileges arising under applicable law with respect to the foregoing and all rights corresponding thereto throughout the world, (ii) inventions, discoveries, designs and improvements described or claimed therein, (iii) reissues, divisions, continuations, reexaminations, extensions and continuations-in-part thereof and amendments thereto and (iv) rights to sue for past, present and future infringements thereof; and

(b) all Proceeds of any and all of the foregoing.

Notwithstanding anything to the contrary contained in clauses (a) and (b) above or otherwise set forth in this Patent Security Agreement, the security interest created by this Patent Security Agreement shall not extend to, and the term "Patent Collateral" shall not include, any Excluded Property.

SECTION 3. Security Agreement. The lien and security interest granted pursuant to

this Patent Security Agreement is granted in conjunction with the lien and security interest granted to the Collateral Agent pursuant to the Security Agreement and the Pledgor hereby acknowledge and affirm that the rights and remedies of the Collateral Agent with respect to the security interest in the Patent Collateral made and granted hereby are more fully set forth in the Security Agreement, the terms and provisions of which are incorporated by reference herein as if fully set forth herein. In the event that any provision of this Patent Security Agreement is deemed to conflict with the Security Agreement, the provisions of the Security Agreement shall control unless the Collateral Agent shall otherwise determine.

SECTION 4. Termination. Upon the payment in full of the Secured Obligations (other than contingent obligations for which no claim has been asserted) and termination of the Security Agreement and at the other times required by Section 9.15 of the Credit Agreement, the Collateral Agent shall promptly, upon reasonable request of the Pledgor, execute, acknowledge, and deliver to the Pledgor an instrument in writing in recordable form releasing the collateral pledge, grant, assignment, lien and security interest in the Patent Collateral under this Patent Security Agreement, all at the Pledgor's sole cost and expense.

SECTION 5. Counterparts. This Patent Security Agreement and any amendments, waivers, consents or supplements hereto may be executed in any number of counterparts and by different parties hereto in separate counterparts, each of which when so executed and delivered shall be deemed to be an original, but all such counterparts together shall constitute one and the same instrument. Delivery of any executed counterpart of a signature page of this Patent Security Agreement by facsimile or other electronic transmission shall be effective as delivery of a manually executed counterpart of this Patent Security Agreement.

SECTION 6. Governing Law; Consent to Jurisdiction and Service of Process; Waiver of Jury Trial. Sections 9.09 and 9.10 of the Credit Agreement are incorporated herein, *mutatis mutandis*, as if a part hereof.

[signature page follows]

In Witness Whereof, the Pledgor has caused this Patent Security Agreement to be duly executed and delivered by its duly authorized officers as of the date first above written.

INTEGRATED DEVICE TECHNOLOGY, INC.,
As Pledgor

By: 

Name: Matthew Brangalise

Title: Vice President, Chief Legal Officer and
Corporate Secretary

SCHEDULE I
to
PATENT SECURITY AGREEMENT
UNITED STATES PATENT REGISTRATIONS AND UNITED STATES PATENT
APPLICATIONS

Owner	App. No.	Title
Integrated Device Technology, Inc.	15/923,631	CIRCUITS AND SYSTEMS FOR WIRELESS POWER TRANSMISSION
Integrated Device Technology, Inc.	15/889,737	APPARATUS AND METHODS FOR AUTOMATIC TIME MEASUREMENTS
Integrated Device Technology, Inc.	15/904,320	Auto-Phase-Shifting and Dynamic on Time Control Current Balancing Multi-Phase Constant On Time Buck Converter
Integrated Device Technology, Inc.	15/867,553	LASER DISTANCE SENSING USING PRIOR MEASUREMENT INFORMATION
Integrated Device Technology, Inc.	15/924,668	Standing and resonant wave clocking in DDR RCD and Data buffer
Integrated Device Technology, Inc.	15/887,013	RF Amplifier Linearity Enhancement with Dynamically Adjusted Variable Load
Integrated Device Technology, Inc.	15/903,832	Surface Mapping of Inductive Physical Profile of Wireless Power Products
Integrated Device Technology, Inc.	15/886,955	high signal voltage tolerance in single-ended memory interface
Integrated Device Technology, Inc.	15/880,957	90 DEGREE DIFFERENTIAL SIGNAL LAYOUT TRANSITION