

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

EPAS ID: PAT4909012

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT

**CONVEYING PARTY DATA**

Name	Execution Date
THE SILANNA GROUP PTY LTD	04/09/2018

**RECEIVING PARTY DATA**

<b>Name:</b>	SILANNA UV TECHNOLOGIES PTE LTD
<b>Street Address:</b>	10 COLLYER QUAY
<b>Internal Address:</b>	#10-01 OCEAN FINANCIAL CENTRE
<b>City:</b>	SINGAPORE
<b>State/Country:</b>	SINGAPORE
<b>Postal Code:</b>	049315

**PROPERTY NUMBERS Total: 11**

Property Type	Number
Patent Number:	9412911
Patent Number:	9614122
Application Number:	61844228
Application Number:	14976814
Application Number:	15594015
Application Number:	15853379
Application Number:	15407640
Patent Number:	9685587
Patent Number:	9691938
Patent Number:	9871165
Patent Number:	9590157

**CORRESPONDENCE DATA**

Fax Number: (858)847-0017

*Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.*

Phone: 8584811883

Email: jon@themuellerlawoffice.com

Correspondent Name: L. JON LINDSAY

Address Line 1: THE MUELLER LAW OFFICE, P.C.

Address Line 2: 12707 HIGH BLUFF DRIVE, SUITE 200

**PATENT**

**Address Line 4:** SAN DIEGO, CALIFORNIA 92130

**ATTORNEY DOCKET NUMBER:** SILUG001

**NAME OF SUBMITTER:** L. JON LINDSAY

**SIGNATURE:** /L. Jon Lindsay/

**DATE SIGNED:** 04/10/2018

**Total Attachments: 5**

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CONFIRMING PATENT ASSIGNMENT

This Confirming Patent Assignment is Effective November 4, 2016, and it confirms the original Assignment of Existing UV Intellectual Property attached as Exhibit A.

For good and valuable consideration, the receipt of which is hereby acknowledged, **The Silanna Group Pty Ltd** ("Assignor"), having a registered office at 37 Brandl Street, Eight Mile Plains, QLD, 4113 Australia, hereby confirms the assignment to **Silanna UV Technologies Pte Ltd** ("Assignee"), having a registered office at 10 Collyer Quay, #10-01 Ocean Financial Centre, Singapore, 049315, all of its right, title and interest in and to the patents and applications listed in Exhibit B ("Patents") and the right to file non-provisionals, divisionals, continuations, continuations-in-part, substitute, continued prosecution, request for continued examination, renewals, reexaminations and reissues, and foreign filing of the Patents, including but not limited to the right to file, prosecute and issue patent applications and patents claiming priority to the Patents for the invention set forth in the Patents and, including without limitation, any and all claims and potential claims of past, present and future infringement of such Patents and the right to sue and collect for past, present and future damages and any other causes of action under such Patents ("Assigned Patent Rights").

Assignor also hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, letters patents, utility models or certificates of invention which may be granted upon the Assigned Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

IN WITNESS WHEREOF Assignor has executed this Confirming Patent Assignment this 9<sup>th</sup> day of April, 2018.

**The Silanna Group Pty Ltd**

*Authorized signature*

Jonathan Murray

*Name (Printed)*

Company Secretary

*Title*

**Silanna UV Technologies Pte Ltd**

*Authorized signature*

Jonathan Murray

*Name (Printed)*

Director

*Title*

EXHIBIT A

THE SILANNA GROUP  
PTY. LTD.

ACHNAREN No. 117 828 105  
(Incorporated in Australia)  
(Company)

DIRECTOR'S RESOLUTION IN WRITING

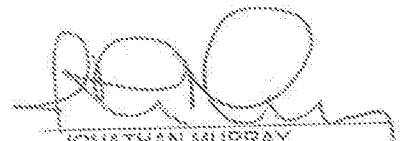
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ASSIGNMENT OF EXISTING UV INTELLECTUAL PROPERTY

RESOLVED that effective the 4<sup>th</sup> November 2016 (the incorporation date of Silanna UV Technologies Pte Ltd, 201630336G ) all existing UV intellectual property owned by The Silanna Group Pty Ltd should be assigned to Silanna UV Technologies Pte Ltd , 201630336G of Singapore. This minute records the intention of the company as at the above date.

Dated this 28th day of March 2018

  
LOBBAN ROBERT (Director)

  
JONATHAN MURRAY  
(Secretary)

**EXHIBIT B**

SILA Ref	SILU Ref	Patent No.	Issue Date	Serial No.	Filing Date	Title	Inventors	Country
SILAP001	SILUP001	9412911	8/9/2016	14/325,131	7/7/2014	OPTICAL TUNING OF LIGHT EMITTING SEMICONDUCTOR JUNCTIONS	Petar Atanackovic	US
SILAP001D1	SILUP001D1	9614122	4/4/2017	15/192,731	6/24/2016	OPTICAL TUNING OF LIGHT EMITTING SEMICONDUCTOR JUNCTIONS	Petar Atanackovic	US
SILAP001P1	SILUP001P1			61/844,228	7/9/2013	OPTICAL TUNING FOR LIGHT EMITTING SEMICONDUCTOR JUNCTIONS	Petar Atanackovic	US
SILAP002CN	SILUP002CN			201580027680.9	11/25/2016	An Optoelectronic Device	Petar Atanackovic	CN
SILAP002JP	SILUP002JP			2016-568438	11/17/2016	An Optoelectronic Device	Petar Atanackovic	JP
SILAP002KR	SILUP002KR			10-2016-7033467	11/29/2016	An Optoelectronic Device	Petar Atanackovic	KR
SILAP002PAU	SILUP002PAU			2014902007	5/27/2014	An Optoelectronic Device	Petar Atanackovic	AU
SILAP002TW	SILUP002TW			164113397	4/27/2015	An Optoelectronic Device	Petar Atanackovic	TW
SILAP002US	SILUP002US			14/975,814	12/21/2015	Optoelectronic Device	Petar Atanackovic	US
SILAP002WO	SILUP002WO			PCT/IB15/52480	4/6/2015	An Optoelectronic Device	Petar Atanackovic	WO
SILAP003aPAU	SILUP003aPAU			2014902010	5/27/2014	Electronic Devices Comprising N-Type and P-Type Superlattices	Petar Atanackovic	AU
SILAP003bPAU	SILUP003bPAU			2014902009	5/27/2014	N-Type and P-Type Superlattices and Fabrication Thereof	Petar Atanackovic	AU
SILAP003CN	SILUP003CN			201580027679.6	11/25/2016	Electronic Devices Comprising N-Type and P-Type Superlattices	Petar Atanackovic	CN
SILAP003JP	SILUP003JP			2016-569627	11/25/2016	Electronic Devices Comprising N-Type and P-Type Superlattices	Petar Atanackovic	JP
SILAP003KR	SILUP003KR			10-2016-7033462	11/29/2016	Electronic Devices Comprising N-Type and P-Type Superlattices	Petar Atanackovic	KR
SILAP003TW	SILUP003TW			104116463	5/27/2015	Electronic Devices Comprising N-Type and P-Type Superlattices	Petar Atanackovic	TW
SILAP003US	SILUP003US	9683587	6/26/2017	14/976,208	12/21/2015	Electronic Devices Comprising N-Type and P-Type Superlattices	Petar Atanackovic	US
SILAP003USD1	SILUP003USD1			15/594,015	5/12/2017	Electronic Devices Comprising N-Type and P-Type Superlattices	Petar Atanackovic	US
SILAP003WO	SILUP003WO			PCT/IB15/53179	4/30/2015	Electronic Devices Comprising N-Type and P-Type Superlattices	Petar Atanackovic	WO

SILAP017EN	SILUP004CN			201580027846.7	11/25/2016	Advanced Electronic Device Structures Using Semiconductor Structures and Superlattices	Petar Atanackovic; Matthew Godfrey	CN
SILAP017JP	SILUP004JP			2016-568023	11/16/2016	Advanced Electronic Device Structures Using Semiconductor Structures and Superlattices	Petar Atanackovic; Matthew Godfrey	JP
SILAP017KR	SILUP004KR			10-2016-7033172	11/25/2016	Advanced Electronic Device Structures Using Semiconductor Structures and Superlattices	Petar Atanackovic; Matthew Godfrey	KR
SILAP017PAU	SILUP004PAU			2014802008	5/27/2014	Advanced Electronic Device Structures Using Semiconductor Structures and Superlattices	Petar Atanackovic; Matthew Godfrey	AU
SILAP017TW	SILUP004TW			104116670	5/25/2015	Advanced Electronic Device Structures Using Semiconductor Structures and Superlattices	Petar Atanackovic; Matthew Godfrey	TW
SILAP017US	SILUP004US	9691938	5/27/2017	14/976,337	12/21/2015	Advanced Electronic Device Structures Using Semiconductor Structures and Superlattices	Petar Atanackovic; Matthew Godfrey	US
SILAP017USD1	SILUP004USD1	9871165	1/15/2018	15/601,890	5/22/2017	Advanced Electronic Device Structures Using Semiconductor Structures and Superlattices	Petar Atanackovic; Matthew Godfrey	US
SILAP017USD2	SILUP004USD2			15/853,379	12/22/2017	ADVANCED ELECTRONIC DEVICE STRUCTURES USING SEMICONDUCTOR STRUCTURES AND SUPERLATTICES	Petar Atanackovic; Matthew Godfrey	US
SILAP017WO	SILUP004WO			PCT/IB15/53203	5/1/2015	Advanced Electronic Device Structures Using Semiconductor Structures and Superlattices	Petar Atanackovic; Matthew Godfrey	WO
SILAP013	SILUP005	9590157	3/7/2017	14/790,500	6/4/2015	EFFICIENT DUAL METAL CONTACT FORMATION FOR A SEMICONDUCTOR DEVICE	Johnny Cai Tang; Christopher Flynn	US
SILAP013D1	SILUP005D1			15/487,640	1/17/2017	EFFICIENT DUAL METAL CONTACT FORMATION FOR A SEMICONDUCTOR DEVICE	Johnny Cai Tang; Christopher Flynn	US
SILAP013TW	SILUP005TW			105118068	5/16/2016	EFFICIENT DUAL METAL CONTACT FORMATION FOR A SEMICONDUCTOR DEVICE	Johnny Cai Tang; Christopher Flynn	TW

SHAP013WO	SILUPDD5WO			PC7/816/53196	5/31/2016	EFFICIENT DUAL METAL CONTACT FORMATION FOR A SEMICONDUCTOR DEVICE	Johnny Cai Tang; Christopher Flynn	WO
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