504969252 06/20/2018

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
LESTER LAMPERT	06/10/2018
RAVI PILLARISETTY	06/18/2018
NICOLE K. THOMAS	06/06/2018
HUBERT C. GEORGE	06/06/2018
JEANETTE M. ROBERTS	06/06/2018
DAVID J. MICHALAK	06/06/2018
ROMAN CAUDILLO	06/10/2018
ZACHARY R. YOSCOVITS	06/08/2018
JAMES S. CLARKE	06/06/2018

RECEIVING PARTY DATA

Name:	INTEL CORPORATION
Street Address:	2200 MISSION COLLEGE BOULEVARD
City:	SANTA CLARA
State/Country:	CALIFORNIA
Postal Code:	95054

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	16013384

CORRESPONDENCE DATA

Fax Number: (972)722-9040

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 972-722-9040

Email: docketing@patcapgroup.com, donnaroark@patcapgroup.com

Correspondent Name: PATENT CAPITAL GROUP Address Line 1: 2816 LAGO VISTA LANE Address Line 4: ROCKWALL, TEXAS 75032

ATTORNEY DOCKET NUMBER:	AA9805-US
NAME OF SUBMITTER:	THOMAS J. FRAME

PATENT REEL: 046147 FRAME: 0159 504969252

SIGNATURE:	/Thomas J. Frame/	
DATE SIGNED:	06/20/2018	
Total Attachments: 10		
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PATENT REEL: 046147 FRAME: 0160

Attorney Docket No.: AA9805-US

ASSIGNMENT

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we, the undersigned inventor(s):

Lester Lampert
Ravi Pillarisetty
Nicole K. Thomas
Hubert C. George
Jeanette M. Roberts
David J. Michalak
Roman Caudillo
Zachary R. Yoscovits
James S. Clarke

hereby sell, assign, and transfer to:

Intel Corporation

a corporation of Delaware, having a principal place of business at 2200 Mission College Boulevard, Santa Clara, California, 95054 USA ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all other countries, in and to any and all inventions and improvements that are disclosed in the application for the patent entitled:

QUANTUM CIRCUIT ASSEMBLIES WITH TRIAXIAL CABLES

(I hereby authorize and request any attorney having appropriate authority from the assignee to insert on the designated lines below, the filing date and application number of said application when known.)

which was filed on	June 20, 2018	as	
US	Application Number	16/013,384	and
COUNTRY or Internation			

which has been executed by the undersigned prior hereto or concurrently herewith on the date(s) indicated below,

and in and to said application and all Additional Applications, and all other patent applications that have been or shall be filed in the United States and all other countries and international filing offices on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all other countries and international filing offices on said inventions and improvements; and in and to all rights of priority resulting from the filing of said applications; as used herein "Additional Applications" includes but is not limited to provisional, design, utility, utility model, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and national phase applications on said inventions and improvements;

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List Lines	6/10/2018
Lester Lampert	(Today's Date)
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David J. Michalak	(Today's Date)
	6/10/2018
Roman Caudillo	(Today's Date)

Attorney Docket No.: AA9805-US

Zachary R. Yoscovits

Zachary R. Yoscovits

(Today's Date)

James S. Clarke

(Today's Date)

Attorney Docket No.: AA9805-US

Zachary R. Yoscovits

(Today's Date)

James S. Clarke

(Today's Date)