

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
BROADCOM CORPORATION	01/20/2017
RECEIVING PARTY DATA	
Name:	AVAGO TECHNOLOGIES GENERAL IP (SINGAPORE) PTE. LTD.
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PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	16017068
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DATE SIGNED:	06/25/2018
Total Attachments: 2	
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PATENT ASSIGNMENT

THIS PATENT ASSIGNMENT ("Patent Assignment") is made and entered into effective as of November 28, 2016 (the "Effective Date"), by and between **Broadcom Corporation**, a company organized and existing under the laws of the State of California, with its principal place of business located at 5300 California Avenue, Irvine, California, 92617, U.S.A., ("Assignor") and **Avago Technologies General IP (Singapore) Pte. Ltd.**, a Singapore company with UEN 2005-12430-D, having a principal place of business at 1 Yishun Avenue 7, Singapore 768923 ("Assignee").

WHEREAS, Assignor and Assignee are parties to a certain Intellectual Property Purchase Agreement dated November 28, 2016 whereupon Assignor has agreed to assign the Patents (as defined below) to Assignee.

NOW, THEREFORE, in consideration of the sum of One U.S. Dollar (US\$1.00) or equivalent and other good and valuable consideration, the receipt for and sufficiency of which is hereby acknowledged, Assignor hereby assigns, transfers, sells and conveys to Assignee all of its rights, title and interest in and to any patent and/or patent application in which Assignor has any right, title or interest in any country, including each of the patents and patent applications that are specifically listed in Exhibit A attached hereto and made a part hereof, and any continuations, divisionals, continuations-in-part, provisionals and/or other applications that claim priority from any of such patents and patent applications and any patents issuing on any of the foregoing, and any reissues, reexaminations, substitutions, renewals, extensions and derivatives of any of the foregoing (collectively "the Patents"), and all rights, claims and privileges pertaining to the Patents, including, without limitation, rights to the underlying inventions, the right to prosecute and maintain the Patents, and the right to sue and recover damages for past, present and future infringement of any of the Patents and obtain injunctive relief.

IN WITNESS WHEREOF, Assignor and Assignee have caused this Patent Assignment to be signed and executed by the undersigned officers thereunto duly authorized as of the Effective Date.

BROADCOM CORPORATION

By: _____

Name: Jeyhan Karaoguz

Title: Vice President & General Manager, IPL

Date: 1-20-2017

**AVAGO TECHNOLOGIES GENERAL IP
(SINGAPORE) PTE. LTD.**

By: _____

Name: Jeyhan Karaoguz

Title: Vice President & General Manager, IPL

Date: 1-20-2017

Exhibit A to November 28, 2016 Patent Assignment from Broadcom Corp

Patent No.	Grant Date	App No.	Filed Date	Country	App Title
		14/805,945	2015-07-22	United States of America	THERMAL ENHANCEMENT FOR QUAD FLAT NO LEAD (QFN) PACKAGES
7913121	2011-03-22	11/948,763	2007-11-30	United States of America	WIRELESS REMOTE FIRMWARE DEBUGGING FOR EMBEDDED WIRELESS DEVICE
8,749,011	2014-06-10	11/114,418	2005-04-26	United States of America	System and Method for Reducing Voltage Drops in Integrated Circuits
		14/815,270	2015-07-31	United States of America	METHOD, SYSTEM, AND APPARATUS FOR MEMORY WITH FEEDTHROUGH AND RETIMING PATHS TO SUPPORT MEMORY TO MEMORY REQUESTS
		14/790,831	2015-07-02	United States of America	WIRELESS POWER TRANSMITTER AND METHODS FOR USE THEREWITH
		10/889,047	2004-07-13	United States of America	MOBILE COMMUNICATION DEVICE WITH ADAPTIVE AUDIBLE USER NOTIFICATION (fka CELL PHONE WITH SMART RING)
		14/819,769	2015-08-06	United States of America	Efficient Hash Table Key Storage
		15/146,553	2016-05-04	United States of America	APPARATUS AND METHOD FOR OVERLAPPED MOTION COMPENSATION FOR VIDEO CODING
		11/049,905	2005-02-03	United States of America	DATA STORAGE SYSTEM AND METHOD THAT SUPPORTS PERSONAL VIDEO RECORDER FUNCTIONALITY
		14/755,625	2015-06-30	United States of America	Three-Dimensional Monolithic LDMOS Transistor
		15/205,991	2016-07-08	United States of America	Thin Recon Interposer Package without TSV for Fine Input/Output Pitch Fan-Out
		15/172,023	2016-06-02	United States of America	INLINE CODEC SWITCHING
8,813,136	2014-08-19	11/049,771	2005-02-03	United States of America	Data On Demand Using A Centralized Data Storage Device
		14/954,095	2015-11-30	United States of America	WIRELESS COMMUNICATION DEVICE AND POWER RECEIVING UNIT WITH SWITCHING PREDICTION AND METHODS FOR USE THEREWITH
		14/980,296	2015-12-28	United States of America	POWER TRANSMITTING UNIT AND POWER RECEIVING UNIT WITH CONTROL DATA COMMUNICATION AND METHODS FOR USE THEREWITH
7,395,402	2008-07-01	11/087,136	2005-03-22	United States of America	DATA STORAGE CAPACITY ALLOCATION AND MANAGEMENT USING ONE OR MORE DATA STORAGE DRIVES
		14/965,400	2015-12-10	United States of America	MULTI-MODE POWER RECEIVING UNIT AND METHODS FOR USE THEREWITH
		11/107,017	2005-04-15	United States of America	AUTOMATIC DISCOVERY OF A NETWORKED DEVICE (fka AUTOMATIC DISCOVERY OF CONFIGURATION IN A NAS SYSTEM)
		14/974,390	2015-12-18	United States of America	POWER TRANSMITTING UNIT WITH ADJUSTABLE FIELD AND METHODS FOR USE THEREWITH
		11/107,224	2005-04-15	United States of America	AUTOMATIC SETUP OF PARAMETERS IN A NETWORKED DEVICES (fka AUTOMATIC SETUP OF PARAMETERS OF DEVICES IN NETWORKS (fka AUTOMATIC SETTING OF PARAMETERS IN A NAS SYSTEM))
		14/928,765	2015-10-30	United States of America	Heat Spreader Having Thermal Interface Material Retainment
		15/148,396	2016-05-06	United States of America	Motion Vector Prediction
		11/102,441	2005-04-08	United States of America	AUTHENTICATION MECHANISM PERMITTING ACCESS TO DATA STORED IN A DATA PROCESSING DEVICE(fka SYSTEM AND METHOD EMPLOYING PROXIMITY ACTUATION FOR STORAGE ACCESS AUTHENTICATION)
		14/884,446	2015-10-15	United States of America	PROGRAMMING MEMORY ELEMENTS USING TWO PHASE BOOST
		15/360,107	2016-11-23	United States of America	Data Path Architecture for Supporting Scalable Number of Processors
		14/831,106	2015-08-20	United States of America	WIMPY FINFET DEVICES AND METHODS FOR FABRICATING THE SAME