PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT5024281

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST

CONVEYING PARTY DATA

Name	Execution Date
SILICON VALLEY BANK	02/07/2011

RECEIVING PARTY DATA

Name:	EQUATOR TECHNOLOGIES, INC
Street Address:	1300 WHITE OAKS ROAD
City:	CAMPBELL
State/Country:	CALIFORNIA
Postal Code:	95008

PROPERTY NUMBERS Total: 2

Property Type	Number
Patent Number:	6498816
Patent Number:	6507293

CORRESPONDENCE DATA

Fax Number: (425)679-0580

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Email: ivrecording@intven.com
Correspondent Name: INTELLECTUAL VENTURES MANAGEMENT- IP LEGAL
Address Line 1: 3150 139TH AVENUE SE
Address Line 2: BUILDING 4, FLOOR 3
Address Line 4: BELLEVUE, WASHINGTON 98005

NAME OF SUBMITTER:	JANICE L. GOEBEL
SIGNATURE:	/Janice L. Goebel/
DATE SIGNED:	06/26/2018

Total Attachments: 15
source=release of security 3-16-11#page1.tif
source=release of security 3-16-11#page2.tif
source=release of security 3-16-11#page3.tif
source=release of security 3-16-11#page4.tif
source=release of security 3-16-11#page5.tif
source=release of security 3-16-11#page6.tif

source=release of security 3-16-11#page7.tif
source=release of security 3-16-11#page8.tif
source=release of security 3-16-11#page9.tif
source=release of security 3-16-11#page10.tif
source=release of security 3-16-11#page11.tif
source=release of security 3-16-11#page12.tif
source=release of security 3-16-11#page13.tif
source=release of security 3-16-11#page14.tif
source=release of security 3-16-11#page15.tif

RELEASE OF SECURITY INTEREST

This release of security interest is made and executed by Silicon Valley Bank, a California corporation ("***SVB***") in favor of Equator Technologies, Inc., a Delaware corporation ("***Company***").

A. SVB loaned money to Company, and Company granted SVB a security interest in all of Company's intellectual property assets to secure the indebtedness and obligations of Company to SVB (the "***Obligations***").

B. SVB recorded its security interest with the United States Patent & Trademark Office as follows:

Debtor	**Secured Party**	**Execution Date**	**Reel/Frame**	**Recordation Date**
Equator Technologies, Inc.	Silicon Valley Bank	07-05-2001	011982/0986	07-20-2001
Equator Technologies, Inc.	Silicon Valley Bank	02-13-2003	013791/0274	03-03-2003

NOW, THEREFORE, for valuable consideration received, by its execution of this Release of Security Interest, SVB hereby irrevocably and unconditionally releases all right, title and interest in all of the following (the "***Released Collateral***"):

(a) the patents and patent applications listed in Exhibit A (the "***Patents***");

(b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, (ii) for which any of the Patents directly or indirectly forms a basis for priority, and/or (iii) that directly or indirectly incorporate by reference, or are directly or indirectly incorporated by reference into, the Patents;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);

(d) all non-United States patents, patent applications, and counterparts relating to the Patents or any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, other governmental grants or issuances, and any rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or governmental grants or issuances of any type related to any of the Patents and the inventions, invention disclosures, and discoveries therein;

(e) inventions, invention disclosures, and discoveries described in any of the Patents of any item in the foregoing categories (a) though (d);

(f) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (e), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(g) causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or the rights described in the above subparagraphs (a) through (f), including, without limitation, all causes of action and other enforcement rights for

(i) damages,
(ii) injunctive relief, and
(iii) any other remedies of any kind

for past, current and future infringement; and

(h) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (a) through (g).

Notwithstanding anything to the contrary in the foregoing, all proceeds, including all payments, whether made in cash or otherwise, (a) to Company by the purchasers of the Patents, in consideration for the sale thereof, or (b) otherwise paid to Company on account of such Patents, shall be part of the collateral for the Obligations.

If necessary or desired, SVB hereby authorizes Company's authorized representative to file UCC Financing Statement Amendment(s) with the applicable filing office(s) in order to memorialize the release of the security interest of SVB in the Released Collateral.

This Release is governed by the law of the State of Delaware, excluding its choice of law principles to the contrary. This Release shall be binding upon Company and its successors and assigns and inures, to the benefit of, with respect to the Patents (including any purchaser).

IN WITNESS WHEREOF, the undersigned has executed this Release of Security Interest on this 7th day of February 2011.

Silicon Valley Bank

By: _____

Name: Justin Mauch

Its: Relationship Manager

Exhibit A

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
6414512	US	10/10/2000	On-chip termination circuit Todd K Moyer
6690836	US	12/18/2000	Circuit and method for decoding an encoded version of an image having a first resolution directly into a decoded version of the image having a second resolution George T Campbell
6990241	US	12/22/2003	Circuit and method for decoding an encoded version of an image having a first resolution directly into a decoded version of the image having a second resolution George T Campbell
7630583	US	07/06/2005	Circuit and method for decoding an encoded version of an image having a first resolution directly into a decoded version of the image having a second resolution George T Campbell
12/605298	US	10/23/2009	Circuit and method for decoding an encoded version of an image having a first resolution directly into a decoded version of the image having a second resolution George T Campbell
7095448	US	12/21/2000	Image processing circuit and method for modifying a pixel value

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			Qinggang Zhou
6507293	US	12/21/2000	Processing circuit and method for variable-length coding and decoding Richard M Deeley
6587058	US	10/25/2002	Processing circuit and method for variable-length coding and decoding Richard M Deeley
7262720	US	06/30/2003	Processing circuit and method for variable-length coding and decoding Richard M Deeley
SG077999	SG	06/24/1999	Processing circuity and method for variable-length coding and decoding Richard Deeley
6909752	US	02/01/2001	Circuit and method for generating filler pixels from the original pixels in a video stream Zhou Qinggang
7076105	US	02/12/2001	Circuit and method for performing a two-dimensional transform during the processing of an image Woobin Lee
7095785	US	06/28/2002	Determination of prediction direction in mpeg-4 Chris Basoglu

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
7068850	US	06/28/2002	Decoding of predicted dc coefficient without division Jeongnam Youn
7103102	US	06/28/2002	Bit stream code lookup table for an mpeg-4 code word Chris Basoglu
7113646	US	06/28/2002	Decoding of predicted ac coefficient without division Jeongnam Youn
7391914	US	05/05/2006	Decoding of predicted dc coefficient without division Jeongnam Youn
7028245	US	08/21/2001	Even-load software reed-solomon decoder Jian Zhang
7502989	US	12/02/2005	Even-load software reed-solomon decoder Jian Zhang
7093256	US	12/13/2002	Method and apparatus for scheduling real-time and non-real-time access to a shared resource Rudolf Henricus Johannes Bloks
11/457762	US	07/14/2006	Method and apparatus for scheduling real-time and non-real-time access to a shared resource Rudolf Henricus Johannes Bloks

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
7194032	US	09/03/1999	Circuit and method for modifying a region of an encoded image John S O'Donnell
11/674121	US	02/12/2007	Circuit and method for modifying a region of an encoded image John S O'Donnell
6498816	US	09/03/1999	Circuit and method for formatting each of a series of encoded video images into respective regions John S O'Donnell
6385638	US	09/04/1997	Processor resource distributor and method Miche Baker-Harvey
JP4174178	JP	09/03/1998	Processor resource distributor and method Atsuo Kawaguchi
KR10-0625779	KR	09/03/1998	Processor resource distributor and method Atsuo Kawaguchi
TWI136368	TW	09/04/1998	Processor resource distributor and method Baker-Harvey Miche
6721948	US	06/30/2000	Method for managing shared tasks in a multi-tasking data processing system William E Morgan

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
7137121	US	04/19/2002	Data-processing circuit and method for switching between application programs without an operating system Evan Cheng
60/194674	US	04/04/2000	On-chip termination scheme Todd K Moyer
60/089832	US	06/19/1998	Method and apparatus for decoding digital video at reduced resolutions George Campbell
CN99807561.2	CN	06/18/1999	Decoding encoded image having first resolution directly into decoded image having second resolution George T Campbell
EP99930485.0	EP	06/18/1999	Decoding an encoded image having a first resolution directly into a decoded image having a second resolution George T Campbell
JP2000-555203	JP	06/18/1999	Circuit and method for decoding an encoded version of an image having a first resolution directly into a decoded version of the image having a second resolution George T Campbell
KR10-2000-7014418	KR	06/18/1999	Circuit and method for decoding an encoded version of an image having a first resolution directly into a decoded version of the image

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			having a second resolution George T Campbell
PCT/US1999/013952	WO	06/18/1999	Decoding an encoded image having a first resolution directly into a decoded image having a second resolution George T Campbell
SG200072090-0	SG	06/18/1999	Decoding an encoded image having a first resolution directly into a decoded image having a second resolution George T Campbell
TWI162985	TW	12/17/1999	Circuit and method for decoding an encoded version of an image having a first resolution directly into a decoded version of the image having a second resolution George T Campbell
60/091407	US	07/01/1998	Method and apparatus for removing contouring artifacts Qinggang Zhou
CN99808014.4	CN	06/30/1999	Image processing circuit and method for modifying a pixel valve Qinggang Zhou
EP99935368.3	EP	06/30/1999	Image processing circuit and method for modifying a pixel value Qinggang Zhou
JP2000-558663	JP	06/30/1999	Image processing circuit and method for modifying a pixel

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			value Qinggang Zhou
KR10-2000-7015014	KR	12/29/2000	Image processing circuit and method for modifying a pixel value Qinggang Zhou
PCT/US1999/014701	WO	06/30/1999	Image processing circuit and method for modifying a pixel value Qinggang Zhou
SG200007210-8	SG	12/29/2000	Image processing circuit and method for modifying a pixel value Qinggang Zhou
TWI144786	TW	12/23/1999	Image processing circuit and method for modifying a pixel value Qinggang Zhou
60/090648	US	06/25/1998	Method and apparatus for variable length video compression Richard M Deeley
CN99807791.7	CN	06/24/1999	Processing circuit and processing method of variable length coding and decoding Richard Deeley
EP99930778.8	EP	06/24/1999	Processing circuit and method for variable-length coding and decoding Richard Deeley

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
JP2000-556447	JP	06/24/1999	Processing circuity and method for variable-length coding and decoding Richard Deeley
KR10-2000-7014670	KR	12/22/2000	Processing circuity and method for variable-length coding and decoding Richard Deeley
PCT/US1999/014560	WO	06/24/1999	Processing circuit and method for variable-length coding and decoding Richard Deeley
TWI139564	TW	12/20/1999	Processing circuit and method for variable-length coding and decoding Richard Deeley
60/095201	US	08/03/1998	Video de-interlacing method Qinggang Zhou
CN99809301.7	CN	08/03/1999	Circuit and method for generating filler pixels from the original pixels in a video stream Qinggang Zhou
EP99942001.1	EP	08/03/1999	Circuit and method for generating filler pixels from the original pixels in a video stream Qinggang Zhou
JP2000-564387	JP	08/03/1999	Circuit and method for generating filler pixels from the original pixels in a video

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			stream Qinggang Zhou
KR10-2001-7001405	KR	08/03/1999	Circuit and method for generating filler pixels from the original pixels in a video stream Qinggang Zhou
PCT/US1999/017606	WO	08/03/1999	Circuit and method for generating filler pixels from the original pixels in a video stream Qinggang Zhou
SG200100958-8	SG	02/02/2001	Circuit and method for generating filler pixels from the original pixels in a video stream Qinggang Zhou
TWI142899	TW	08/03/1999	Circuit and method for generating filler pixels from the original pixels in a video stream Qinggang Zhou
60/096534	US	08/13/1998	Method of computing cosine transform for video compression Woobin Lee
CN99809627.X	CN	08/13/1999	Circuit and method for performing bidimentional transform during processing of an image Woobin Lee
EP99942195.1	EP	08/13/1999	Circuit and method for

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			performing a two-dimensional transform during the processing of an image Woobin Lee
JP2000-565664	JP	08/13/1999	Circuit and method for performing a two-dimensional transform during the processing of an image Woobin Lee
KR10-2001-7001811	KR	02/12/2001	Circuit and method for performing a two-dimensional transform during the processing of an image Woobin Lee
PCT/US1999/018533	WO	08/13/1999	Circuit and method for performing a two-dimensional transform during the processing of an image Woobin Lee
SG200100442-3	SG	02/12/2001	Performing transform during processing of an image Woobin Lee
TWI148583	TW	08/13/1999	Circuit and method for implicitly transposing a matrix of values during the processing of an image Woobin Lee
60/301866	US	06/29/2001	Dc and ac prediction in mpeg-4 without divide, transposition of decoded block data in mpeg-4, and a new bit stream code lookup table for mpeg-4 having a last bit with each code word

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			Chris Basoglu
CN00812348.9	CN	08/31/2000	Circuit and method for modifying a region of an encoded image John S O'Donnell
EP00959897.0	EP	08/31/2000	Circuit and method for modifying a region of an encoded image John S O'Donnell
JP2001-522777	JP	08/31/2000	Circuit and method for modifying a region of an encoded image John S O'Donnell
KR10-2002-7002863	KR	08/31/2000	Circuit and method for modifying a region of an encoded image John S O'Donnell
PCT/US2000/024334	WO	08/31/2000	Circuit and method for modifying a region of an encoded image John S O'Donnell
SG200200930-6	SG	08/31/2000	Circuit and method for modifying a region of an encoded image John S O'Donnell
TWI161234	TW	09/02/2000	Circuit and method for modifying a region of an encoded image John S O'Donnell
CN00812350.0	CN	08/31/2000	Circuit and method for

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			formatting each of a series of encoded video images into respective regions John S O'Donnell
EP00959896.2	EP	08/31/2000	Circuit and method for formatting each of a series of encoded video images into respective regions John S O'Donnell
JP2001-522776	JP	08/31/2000	Circuit and method for formatting each of a series of encoded video images into respective regions John S O'Donnell
KR10-2002-7002862	KR	08/31/2000	Circuit and method for formatting each of a series of encoded video images into respective regions John S O'Donnell
PCT/US2000/024333	WO	08/31/2000	Circuit and method for formatting each of a series of encoded video images into respective regions John S O'Donnell
SG200200931-4	SG	08/31/2000	Circuity and method for formatting each of a series of encoded video images into respective regions John S O'Donnell
TWI172627	TW	09/02/2000	Circuit and method for formatting each of a series of encoded video images into respective regions

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			John S O'Donnell
EP98944765.1	EP	09/03/1998	Processor resource distributor and method Atsuo Kawaguchi
KR10-0625779	KR	09/03/1998	Processor resource distributor and method Atsuo Kawaguchi
PCT/US1998/018530	WO	09/03/1998	Processor resource distributor and method Atsuo Kawaguchi