

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT5038660

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
BEAMREACH-SOLEXEL ASSETS, LLC	06/20/2018
RECEIVING PARTY DATA	
Name:	TRUTAG TECHNOLOGIES, INC.
Street Address:	2045 LAUWILIWILI STREET
Internal Address:	UNIT 301
City:	KAPOLEI
State/Country:	HAWAII
Postal Code:	96707
PROPERTY NUMBERS Total: 11	
Property Type	Number
Application Number:	12688495
Application Number:	14589667
Application Number:	14589847
Application Number:	61145018
Application Number:	12774667
Application Number:	14679731
Application Number:	61175535
Application Number:	13026239
Application Number:	13554103
Application Number:	15219213
Application Number:	61304340
CORRESPONDENCE DATA	
Fax Number:	(408)973-2595
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	408-207-4763
Email:	mhuang@ip-patent.com
Correspondent Name:	VAN PELT, YI & JAMES LLP
Address Line 1:	10050 N. FOOTHILL BLVD., SUITE 200
Address Line 4:	CUPERTINO, CALIFORNIA 95014

PATENT

ATTORNEY DOCKET NUMBER:	CBIO
NAME OF SUBMITTER:	TERRENCE L. WONG
SIGNATURE:	/Terrence L. Wong/
DATE SIGNED:	07/06/2018
Total Attachments: 4 source=CBIO Executed Assignment Beamreach-Solexel#page1.tif source=CBIO Executed Assignment Beamreach-Solexel#page2.tif source=CBIO Executed Assignment Beamreach-Solexel#page3.tif source=CBIO Executed Assignment Beamreach-Solexel#page4.tif	

EXHIBIT A
PATENT ASSIGNMENT

WHEREAS, Beamreach-Solexel Assets, LLC, a Pennsylvania Limited Liability Company with offices at 198 Cinnabar Lane, Yardley, PA 19067, (the “Seller”) is the sole and exclusive owner of those certain patents and patent applications set forth on **Attachment A** hereto, together with any and all related patents or patent applications that directly claim priority to the patents and patent applications set forth on **Attachment A**, all foreign corresponding patents and applications therefor (in all countries) and all patents (including utility models, and certificates of inventorship) resulting from reissues, continuations, continuations-in-part, divisions, renewals, reexaminations, substitutions and extensions of such patents or patent applications, all inventions disclosed in those patents or patent applications, and all intellectual property rights for any of the assets listed on **Attachment A**, all of which are collectively referred to as the “Purchased Assets”;

WHEREAS, TruTag Technologies, Inc., a Delaware Corporation with offices at 2045 Lauwiliwili St., Unit 301, Kapolei, HI 96707 (“Buyer”) desires to acquire all right, title and interest in, to and under the said Purchased Assets;

1. Assignment.

NOW, THEREFORE, for good and valuable consideration the sufficiency of which is acknowledged by the parties:

Seller does hereby irrevocably and perpetually assign, convey, and transfer to Buyer all of Seller’s right, title and interest throughout the world, in and to the Purchased Assets, all of which are to be held and enjoyed by Buyer for its own use, exploitation and enjoyment, and for the use exploitation and enjoyment of its successors, assigns or other legal representatives, to the end of the term or terms for which said Purchased Assets are or may be granted, reissued or extended; together with all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Purchased Assets, including, without limitation, all causes of action and other enforcement rights for (i) damages, (ii) injunctive relief, and (iii) any other remedies of any kind for past, current and future infringement, and all rights to collect royalties or other payments under or on account of any of the Purchased Assets, all for Buyer’s own use and behalf, and for the use and behalf of its successors, assigns or other legal representatives.

Seller hereby authorizes and requests the Commissioner of Patents and Trademarks, or an equivalent officer in any jurisdiction in which a Patent may have issued, to issue any and all Letters Patent on said inventions to Buyer as assignee of the entire interest, and hereby covenants that Seller has full right to convey the entire interest herein assigned, and that, except as otherwise explicitly agreed and acknowledged in writing between the parties, Seller has not executed, and will not execute, any agreements in conflict therewith.

2. **Cooperation.** Buyer and Seller agree to cooperate with each other to execute and deliver such other documents and instruments and to do such further acts and things as may be reasonably requested by the other to evidence, document or carry out the assignment of the Purchased Assets now or anytime in the future.

3. **Effect of Agreement.** Nothing in this Assignment shall, or shall be deemed to, modify any provisions of the Patent Purchase Agreement or affect the rights of the parties under the Patent Purchase Agreement. In the event of any conflict between the provisions hereof and the provisions of the Patent Purchase Agreement, the provisions of the Patent Purchase Agreement shall govern and control.

IN WITNESS WHEREOF, Seller and Buyer have caused this Patent Assignment to be executed on the date first written below.

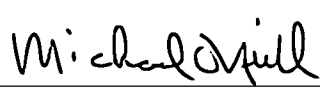
SELLER:
Beamreach-Solexel Assets, LLC



Daryl Martin
On behalf of Member IPmetrics LLC


Date: **20 June 2018**

BUYER:
TruTag Technologies, Inc.

By: 

Name: Michael O'Neill
Title: CSO

Date: **20 JUNE 2018**



David Shoneman
On behalf of Member Darwin IP Advisors LLC

Date: **20 June 2018**

Schedule A to Intellectual Property Agreement // Attachment A to Patent Assignment

DOCKET REFERENCE NUMBER	CNTRY CODE	APPLICATION OR SERIAL NUMBER	PATENT NUMBER	TITLE	STATUS May 2018
020DE	DE	10732153.1	EP2387458	Porous Silicon Electro-Etching System And Method	ISSUED
020DK	DK	10732153.1	EP2387458	Porous Silicon Electro-Etching System And Method	ABANDONED
020EP	EP	10732153.1	EP2387458	Porous Silicon Electro-Etching System And Method	ISSUED
020FR	FR	10732153.1	EP2387458	Porous Silicon Electro-Etching System And Method	ISSUED
020GB	GB	10732153.1	EP2387458	Porous Silicon Electro-Etching System And Method	ISSUED
020JP	JP	2011-546393		Porous Silicon Electro-Etching System And Method	ABANDONED
020MY	MY	PI2011003342		Porous Silicon Electro-Etching System And Method	PENDING
020NL	NL	10732153.1	EP2387458	Porous Silicon Electro-Etching System And Method (Validated in DE DK FR GB & NL)	ISSUED
020US0	US	12/688,495	8,926,803	Porous Silicon Electro-Etching System And Method	ISSUED
020US1	US	14/589,667		Porous Silicon Electro-Etching System And Method	ABANDONED
020US2	US	14/589,847	TBD	Porous Silicon Electro-Etching System And Method	ABANDONED
020USP	US	61/145,018		Porous Silicon Electro-Etching System And Method	EXPIRED
020WO	WO	PCT/US2010/021209		Porous Silicon Electro-Etching System And Method	EXPIRED
029CN	CN	201080030023.7	ZL201080030023.7	High-Productivity Porous Silicon Manufacturing Equipment	ISSUED
029EP	EP	10772799.2		High-Productivity Porous Silicon Manufacturing Equipment	PUBLISHED
029JP	JP	2012-509957	5872456	High-Productivity Porous Silicon Manufacturing Equipment	ISSUED
029MY	MY	PI2011005354		High-Productivity Porous Silicon Manufacturing Equipment	ALLOWED
029US0	US	12/774,667	8,999,058	High-Productivity Porous Silicon Manufacturing Equipment	ISSUED
029US0C1	US	14/679,731	9,869,031	High-Productivity Porous Silicon Manufacturing Equipment	ISSUED

Schedule A to Intellectual Property Agreement // Attachment A to Patent Assignment

DOCKET REFERENCE NUMBER	CNTRY CODE	APPLICATION OR SERIAL NUMBER	PATENT NUMBER	TITLE	STATUS May 2018
029USP	US	61/175,535		High-Productivity Porous Silicon Manufacturing Equipment	EXPIRED
029WO	WO	PCT/US2010/033792		High-Productivity Porous Silicon Manufacturing Equipment	EXPIRED
041CN	CN	201180018589.2	ZL201180018589.2	Double-Sided Reusable Template For Fabrication Of Semiconductor Substrates For Photovoltaic Cell And Microelectronics Device Manufacturing	ISSUED
041EP	EP	11742933.2		Double-Sided Reusable Template For Fabrication Of Semiconductor Substrates For Photovoltaic Cell And Microelectronics Device Manufacturing	ABANDONED
041US0	US	13/026,239	8,241,940	Double-Sided Reusable Template For Fabrication Of Semiconductor Substrates For Photovoltaic Cell And Microelectronics Device Manufacturing	ISSUED
041US1	US	13/554,103	9,401,276	Apparatus For Forming Porous Silicon Layers On At Least Two Surfaces Of A Plurality Of Silicon Templates	ISSUED
041US2	US	15/219,213		Double-Sided Reusable Template For Fabrication Of Semiconductor Substrates For Photovoltaic Cell And Microelectronics Device Manufacturing	ABANDONED
041USP	US	61/304,340		Double-Sided Reusable Template For Fabrication Of Semiconductor Substrates For Photovoltaic Cell And Microelectronics Device Manufacturing	EXPIRED
041WO	WO	PCT/US2011/024670		Double-Sided Reusable Template For Fabrication Of Semiconductor Substrates For Photovoltaic Cell And Microelectronics Device Manufacturing	EXPIRED