

<b>PATENT ASSIGNMENT COVER SHEET</b>
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<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	EXECUTIVE ORDER 9424, CONFIRMATORY LICENSE
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
GEORGE MASON UNIVERSITY	06/29/2018
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<b>Property Type</b>	<b>Number</b>
<b>Patent Number:</b>	7180135
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(Sign and submit this executed document to the United States Patent and Trademark Office -- Electronic Patent Assignment System (EPAS) <http://epas.uspto.gov>)

**Executive Order 9424: Government Support License (Government Support Clause)**

Invention title: DOUBLE GATE (DG) SOI RATIOED LOGIC WITH INTRINSICALLY ON SYMMETRIC DG-MOSFET LOAD

Inventor(s): Dimitri Ioannou, Akram Salman, Mitra Souvick

U.S. Filing/Issue Date: 02/20/2007

Patent or Application Serial No: 7,180,135

Grant/Contract Number(s): ECS0221126, ECS9900464

Foreign Applications filed/intended in (countries): \_\_\_\_\_

The invention identified above is a subject invention under 35 U.S.C. 200, et seq., and the Standard Patent Rights Clause at 37 CFR 401.14, FAR 52.227-11 or FAR 52.227-12 (if applicable) which are included among the terms of the above identified grant or contract award from the United States Government. This document is confirmatory of:

1. As required by 37 CFR 401.14 (f) (2) and Executive Order 9424:  
This invention was made with government support under Grant/Contract Number(s) ECS0221126 and ECS9900464 awarded by the National Science Foundation. The government has certain rights in the invention.
2. All other rights acquired by the Government by reason of the above identified grant/contract award and the laws and regulations that are applicable to the award.

The Government is hereby granted an irrevocable power to inspect and make copies of the above identified patent application.

Signed this 29<sup>th</sup> day of June 2018

By: Hina Mehta  
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Hina Mehta  
Signature

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**PATENT**