

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
 Stylesheet Version v1.2

EPAS ID: PAT5063487

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT	
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT	
<b>SEQUENCE:</b>	2	
<b>CONVEYING PARTY DATA</b>		
	<b>Name</b>	<b>Execution Date</b>
	SUNEDISON SEMICONDUCTOR LIMITED	06/06/2018
<b>RECEIVING PARTY DATA</b>		
<b>Name:</b>	GLOBAL WAFERS CO., LTD.	
<b>Street Address:</b>	NO. 8 INDUSTRIAL EAST ROAD	
<b>Internal Address:</b>	SCIENCE-BASED INDUSTRIAL PARK	
<b>City:</b>	HSINCHU	
<b>State/Country:</b>	TAIWAN	
<b>PROPERTY NUMBERS Total: 1</b>		
	<b>Property Type</b>	<b>Number</b>
	Application Number:	16072203
<b>CORRESPONDENCE DATA</b>		
<b>Fax Number:</b>	(314)612-2307	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>		
<b>Phone:</b>	314-621-5070	
<b>Email:</b>	USpatents@armstrongteasdale.com, clager@armstrongteasdale.com	
<b>Correspondent Name:</b>	PATENT DOCKET DEPARTMENT ARMSTRONG TEASD	
<b>Address Line 1:</b>	7700 FORSYTH BOULEVARD	
<b>Address Line 2:</b>	SUITE 1800	
<b>Address Line 4:</b>	ST. LOUIS, MISSOURI 63105	
<b>ATTORNEY DOCKET NUMBER:</b>	28744-4890 (161004.2)	
<b>NAME OF SUBMITTER:</b>	NICHOLAS A. KEPPEL	
<b>SIGNATURE:</b>	/Nicholas A. Keppel/	
<b>DATE SIGNED:</b>	07/24/2018	
<b>Total Attachments: 7</b>		
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**PATENT**

**REEL: 046437 FRAME: 0786**

## ASSIGNMENT

This Assignment is made by and among SunEdison Semiconductor Limited, a company organized and existing under the laws of Singapore ("SSL"), MEMC Japan Limited, a company organized and existing under the laws of Japan ("MEMC Japan"), and MEMC Electronic Materials, S.P.A., a company organized and existing under the laws of Italy ("MEMC EM", and collectively with SSL and MEMC Japan, the "Assignors"), and GlobalWafers Co., Ltd., a company organized and existing under the laws of Taiwan and having its registered address at No. 8, Industrial East Road, Science-Based Industrial Park, Hsinchu, Taiwan, R.O.C. (hereinafter referred to as "Assignee");

**WHEREAS**, on December 31, 2016, Assignors entered into certain IP Transfer Agreements with Assignee;

**WHEREAS**, the parties hereto desire to memorialize, *nunc pro tunc*, the assignment and ownership of all Assigned IP (defined below) for, among other things, recordal purposes with certain patent administration bodies, such as the United States Patent and Trademark Office; and

**WHEREAS**, Assignors acknowledge that payment in full from Assignee for all Assigned IP has heretofore been paid.

**NOW, THEREFORE**, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, Assignors have agreed to and do hereby sell, assign and transfer, and confirm the sale, assignment and transfer, unto Assignee all of their rights, title and interest throughout the world, including without limitation, the right to sue and recover for any past infringements, in and to the Listed IP, any other applications (including provisional, non-provisional, divisional, continuing, or reissue applications) based in whole or in part on any Listed IP, any corresponding patent or patent applications filed in any country based in whole or in part on, and/or claiming priority from, any Listed IP, any patents (including extensions thereof) of any country based in whole or in part on, and/or claiming priority from, any Listed IP, and all of the inventions described in the Listed IP and all of the aforementioned patents and patent applications (all of the foregoing, collectively, the "Assigned IP");

**TO BE HELD AND ENJOYED** by said Assignee, its successors and assigns, as fully and entirely as the Assigned IP would have been held and enjoyed by Assignors had no assignment of said interest been made.

EACH ASSIGNOR hereby agrees that it will do, execute and deliver, or will cause to be done, executed and delivered, all such further lawful acts, transfers, assignments and conveyances, powers of attorney and assurances for the better assuring, conveying and confirming unto Assignee, all of such Assignor's rights, title and interest in and to the Assigned IP hereby transferred, assigned and conveyed, as Assignee may reasonably require.

*[Signature Pages Follow]*

IN WITNESS WHEREOF, the undersigned have caused these presents to be executed by their respective officers thereunto duly authorized this 6 day of June, 2018.

Assignor:

SUNEDISON SEMICONDUCTOR LIMITED

By: *Paul Markowitz*

Name: Paul Markowitz

Title: Director of Intellectual Property

STATE OF Missouri

COUNTY OF St. Charles

On this 6 day of June, 2018, before me, a Notary Public, personally appeared Paul Markowitz, an officer of SUNEDISON SEMICONDUCTOR LIMITED, known to me to be the person who executed the within Assignment, and who did state that said instrument was signed on behalf of said company, and acknowledged to me that he executed the same for the purposes therein stated.

IN TESTIMONY WHEREOF, I have hereunto set my hand and seal the date and year last above written.

*Erika Damiani*  
Notary Public

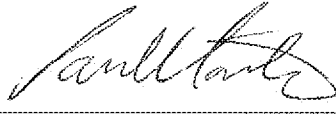
My Commission Expires:

July 20, 2018



Assignor:

**MEMC JAPAN LTD**

By: 

Name: Paul Markowitz


Title: Director of Intellectual Property

STATE OF Missouri

COUNTY OF St. Charles

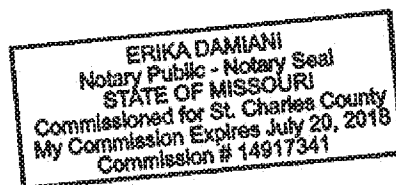
On this 6 day of June, 2018, before me, a Notary Public, personally appeared Paul Markowitz, an officer of MEMC JAPAN LTD, known to me to be the person who executed the within Assignment, and who did state that said instrument was signed on behalf of said company, and acknowledged to me that he executed the same for the purposes therein stated.

IN TESTIMONY WHEREOF, I have hereunto set my hand and seal the date and year last above written.

  
Notary Public

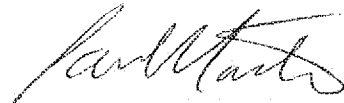
My Commission Expires:

July 20, 2018



Assignor:

MEMC ELECTRONIC MATERIALS S.P.A.

By: 


Name: Paul Markowitz

Title: Director of Intellectual Property

STATE OF Missouri  
COUNTY OF St. Charles

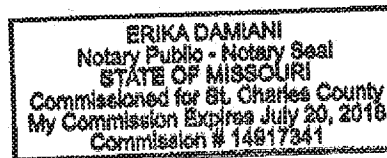
On this 6 day of June, 2016, before me, a Notary Public, personally appeared Paul Markowitz, an officer of MEMC ELECTRONIC MATERIALS S.P.A., known to me to be the person who executed the within Assignment, and who did state that said instrument was signed on behalf of said company, and acknowledged to me that he executed the same for the purposes therein stated.

IN TESTIMONY WHEREOF, I have hereunto set my hand and seal the date and year last above written.

  
Notary Public

My Commission Expires:

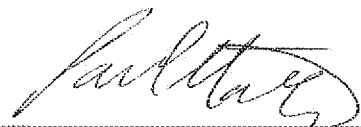
July 20, 2018



For and on behalf of GlobalWafers Co., Ltd.

Assignee:

**GLOBALWAFERS CO., LTD.**

By: 

Name: Paul Markowitz

Title: Director of Intellectual Property

STATE OF Missouri )  
COUNTY OF St. Charles

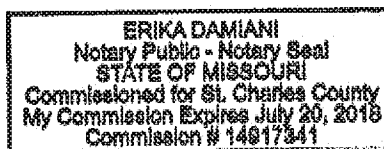
On this 6 day of June, 2018, before me, a Notary Public, personally appeared Paul Markowitz, an officer of GLOBALWAFERS CO., LTD., known to me to be the person who executed the within Assignment, and who did state that said instrument was signed on behalf of said company, and acknowledged to me that he executed the same for the purposes therein stated.

IN TESTIMONY WHEREOF, I have hereunto set my hand and seal the date and year last above written.

  
Notary Public

My Commission Expires:

July 20, 2018





**SCHEDULE A**

TITLE	Country	Application Number	Filing Date	Publication Number	Publication Date	Patent Number	Issue Date
SEMICONDUCTOR ON INSULATOR STRUCTURE COMPRISING A LOW TEMPERATURE FLOWABLE OXIDE LAYER AND METHOD OF MANUFACTURE THEREOF	WO	PCT/US2017/020619	3-Mar-2017	WO 2017/155805	14-Sep-2017		
SEMICONDUCTOR ON INSULATOR STRUCTURE COMPRISING A PLASMA OXIDE LAYER AND METHOD OF MANUFACTURE THEREOF	WO	PCT/US2017/020623	3-Mar-2017	WO 2017/155806	14-Sep-2017		
SEMICONDUCTOR ON INSULATOR STRUCTURE COMPRISING A PLASMA NITRIDE LAYER AND METHOD OF MANUFACTURE THEREOF	WO	PCT/US2017/020634	3-Mar-2017	WO 2017/155808	14-Sep-2017		
SEMICONDUCTOR ON INSULATOR STRUCTURE COMPRISING A SACRIFICIAL LAYER AND METHOD OF MANUFACTURE THEREOF	US	15/450124	6-Mar-2017	2017-0256442	7-Sep-2017		
SEMICONDUCTOR ON INSULATOR STRUCTURE COMPRISING A SACRIFICIAL LAYER AND METHOD OF MANUFACTURE THEREOF	US	15/917123	9-Mar-2018				
METHOD OF MANUFACTURING A SEMICONDUCTOR ON INSULATOR STRUCTURE BY A PRESSURIZED BOND TREATMENT	WO	PCT/US2017/020614	3-Mar-2017	WO 2017/155804	14-Sep-2017		
DIRECT FORMATION OF HEXAGONAL BORON NITRIDE ON SILICON BASED DIELECTRICS	WO	PCT/US2017/030124	28-Apr-2017	WO 2017/196559	16-Nov-2017		
METHODS FOR PRODUCING SINGLE CRYSTAL SILICON INGOTS WITH REDUCED SEED END OXYGEN	US	15/662844	28-Jul-2017	2018-0030615	1-Feb-2018		
SILICON WAFERING PROCESS FLOW	US	09/512111	24-Feb-2000			6294469	25-Sep-2001
PROCESS FOR PRODUCING LOW DEFECT DENSITY, IDEAL OXYGEN PRECIPITATING SILICON	US	09/705092	2-Nov-2000			6555194	29-Apr-2003
PROCESS FOR PRODUCING LOW DEFECT DENSITY, IDEAL OXYGEN PRECIPITATING SILICON	US	10/373899	25-Feb-2003	2004-0025782	12-Feb-2004	6896728	24-May-2005
PROCESS AND APPARATUS FOR PREPARATION OF SILICON CRYSTALS WITH REDUCED METAL CONTENT	JP	2000-554909	15-Dec-2000	2002-518286A	25-Jun-2002	4683725	18-Feb-2011
PROCESS AND APPARATUS FOR PREPARATION OF SILICON CRYSTALS WITH REDUCED METAL CONTENT	DE	99927525.8	14-Jun-1999	1090167	11-Apr-2001	69901183.3-08	3-Apr-2002

**PATENT**

**RECORDED: 07/24/2018**

**REEL: 046437 FRAME: 0793**