PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT5177936

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
MASAKAZU SAGAWA	08/20/2018
TAKAHIRO MORIKAWA	08/28/2018
MOTOYUKI MIYATA	08/08/2018
KAN YASUI	08/30/2018
TOSHIAKI MORITA	08/28/2018

RECEIVING PARTY DATA

Name:	HITACHI POWER SEMICONDUCTOR DEVICE, LTD.
Street Address:	2-2, OMIKA-CHO 5-CHOME
City:	HITACHI-SHI, IBARAKI
State/Country:	JAPAN
Postal Code:	319-1221

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	16153251

CORRESPONDENCE DATA

Fax Number: (703)610-8686

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 703-903-9000
Email: tpurifoy@milesstockbridge.com, ipdocketing@milesstockbridge.com
Correspondent Name: ERIC G. KING
Address Line 1: MILES & STOCKBRIDGE PC
Address Line 2: 1751 PINNACLE DRIVE SUITE 1500
Address Line 4: TYSONS CORNER, VIRGINIA 22102-3833

ATTORNEY DOCKET NUMBER:	T3381-23174US01
NAME OF SUBMITTER:	TYNISHA PURIFOY
SIGNATURE:	/Tynisha Purifoy/
DATE SIGNED:	10/08/2018
Total Attachments: 3	

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ASSIGNMENT
（譲 渡 証）

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar ($1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by Hitachi Power Semiconductor Device, Ltd.
a corporation organized under the laws of Japan,
located at 2-2, Omika-cho 5-chome, Hitachi-shi, Ibaraki 319-1221, Japan,
receipt of which is hereby acknowledged I do hereby sell and assign to sa Hitachi Power Semiconductor Device, Ltd.
its successors and assigns, all my right, title and interest, in and for the United States of America, in and to

SEMICONDUCTOR CHIP AND POWER MODULE, AND MANUFACTURING METHOD OF THE SAME

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted,

to be held and enjoyed by said Hitachi Power Semiconductor Device, Ltd.

its successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Hitachi Power Semiconductor Device, Ltd.

Signed on the date(s) indicated aside our signatures:

INVENTOR(S) （発明者フルネームサイン）	Date Signed （署名日）
1) Masakazu Sagawa (Masakazu SAGAWA)	Aug/20/2018
2) (Takahiro MORIKAWA)	
3) (Motoyuki MIYATA)	
4) (Kan YASUI)	
5) (Toshiaki MORITA)	
6)	
7)	
8)	

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Signed on the date(s) indicated aside our signatures:

INVENTOR(S) （発明者フルネームサイン）	Date Signed （署名日）
1) (Masakazu SAGAWA)	
2) Takahiro Morikawa (Takahiro MORIKAWA)	Aug. 28. 2018
3) (Motoyuki MIYATA)	
4) Kan Yasui (Kan YASUI)	Aug. 30, 2018
5) Toshiaki Morita (Toshiaki MORITA)	Aug. 28. 2018
6)	
7)	
8)	

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1)	(Masakazu SAGAWA)	
2)	(Takahiro MORIKAWA)	
3)	Motoyuki Miyata (Motoyuki MIYATA)	8/8/2018
4)	(Kan YASUI)	
5)	(Toshiaki MORITA)	
6)		
7)		
8)		