

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
FEDERICO SAMBILAY JR.	11/03/2017
BHARADWAJ PUDIPEDDI	11/03/2017
RICHARD A. CANTONG	11/03/2017
JOEVANNI PARAIRO	11/03/2017
RECEIVING PARTY DATA	
Name:	NVXL TECHNOLOGY, INC.
Street Address:	48073 FREMONT BLVD
City:	FREMONT
State/Country:	CALIFORNIA
Postal Code:	94538
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	16024730
CORRESPONDENCE DATA	
Fax Number:	(617)979-8733
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	802-482-5682
Email:	patents@adamsip.com
Correspondent Name:	ADAMS INTELLEX, PLC
Address Line 1:	PO BOX 197
Address Line 4:	HINESBURG, VERMONT 05461
ATTORNEY DOCKET NUMBER:	NVX-005
NAME OF SUBMITTER:	MICHAEL BATTIG/R DEAN ADAMS
SIGNATURE:	/Michael Battig/
DATE SIGNED:	11/09/2018
Total Attachments: 2	
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ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, the person(s) named below (referred to as "INVENTOR" whether singular or plural) has sold, assigned, and transferred and does hereby sell, assign, and transfer to NVXL Technology, Inc., a Delaware corporation, having a place of business at 47929 Fremont Blvd., Fremont, CA 94538 ("ASSIGNEE"), for itself and its successors, transferees, and assignees, the following:

1. The entire worldwide right, title, and interest in all inventions and improvements ("SUBJECT MATTER") that are disclosed in the application for United States Letters Patent entitled: FPGA Functionality Mode Switch-Over ("APPLICATION"), which:

is to be filed herewith;

was filed on June 29, 2017 now bearing U.S. serial number 62/526,477; and

2. The entire worldwide right, title, and interest in and to: (a) the APPLICATION, including any right of priority; (b) any divisional, continuation, continuation-in-part, substitute, renewal, reissue, and other related applications thereto which have been or may be filed in the United States or elsewhere in the world; and (c) any patents which may be granted on the applications set forth in (a) and (b) above.

INVENTOR agrees that ASSIGNEE may apply for and receive patents for SUBJECT MATTER in ASSIGNEE's own name.

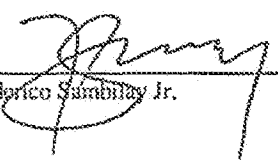
INVENTOR agrees to do the following, when requested, and without further consideration, in order to carry out the intent of this Assignment: (1) execute all oaths, assignments, powers of attorney, applications, and other papers necessary or desirable to fully secure to ASSIGNEE the rights, titles and interests herein conveyed; (2) communicate to ASSIGNEE all known facts relating to the SUBJECT MATTER; and (3) generally do all lawful acts that ASSIGNEE shall consider desirable for securing, maintaining, and enforcing worldwide patent protection relating to the SUBJECT MATTER and for vesting in ASSIGNEE the rights, titles, and interests herein conveyed. INVENTOR further agrees to provide any successor, assign, or legal representative of ASSIGNEE with the benefits and assistance provided to ASSIGNEE hereunder.

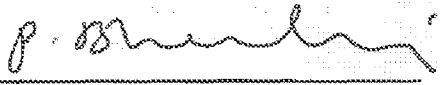
INVENTOR represents that INVENTOR has the rights, titles, and interests to convey as set forth herein, and covenants with ASSIGNEE that the INVENTOR has made or will make hereafter no assignment, grant, mortgage, license, or other agreement affecting the rights, titles, and interests herein conveyed.

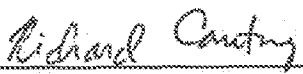
INVENTOR grants the attorney of record the power to insert on this Assignment any further identification which may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office for recordation of this document.

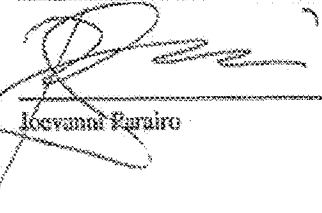
Title of Document: ASSIGNMENT
In Re Application of: Federico Sambilay Jr. et al.
Title: FPGA Functionality Mode Switch-Over
Filed: (if applicable) June 29, 2017
Serial No.: (if applicable) 62/526,477

This Assignment may be executed in one or more counterparts, each of which shall be deemed an original and all of which may be taken together as one and the same Assignment.

First Inventor Signature _____ Date of Signature
 _____, 2017
Federico Sambilay Jr.

Second Inventor Signature _____ Date of Signature
 _____, 2017
Bharadwaj Pudipeddi

Third Inventor Signature _____ Date of Signature
 _____, 2017
Richard A. Cantong

Fourth Inventor Signature _____ Date of Signature
 _____, 2017
Giovanni Parairo