505184640 11/09/2018

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT5231409

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
ABHISHEK VENKATESH	06/14/2017
PRASOONKUMAR SURTI	06/15/2017
SLAWOMIR GRAJEWSKI	06/19/2017
LOUIS FENG	09/15/2018
KAI XIAO	06/20/2017
TOMASZ JANCZAK	10/31/2018
DEVAN BURKE	06/15/2017
TRAVIS SCHLUESSLER	06/15/2017

RECEIVING PARTY DATA

Name:	INTEL CORPORATION	
Street Address:	2200 MISSION COLLEGE BOULEVARD	
City:	SANTA CLARA	
State/Country:	CALIFORNIA	
Postal Code:	95054	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	15483641

CORRESPONDENCE DATA

Fax Number: (202)683-6783

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 202-683-9317

Email: nceron@jordaniplaw.com
Correspondent Name: JORDAN IP LAW, LLC

Address Line 1:12501 PROSPERITY DRIVE, SUITE 401Address Line 4:SILVER SPRING, MARYLAND 20904

ATTORNEY DOCKET NUMBER:	P113748
NAME OF SUBMITTER:	B. DELANO JORDAN
SIGNATURE:	/B. Delano Jordan/

PATENT 505184640 REEL: 047465 FRAME: 0977

DATE SIGNED:	11/09/2018		
Total Attachments: 16	Total Attachments: 16		
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ASSIGNMENT

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we, the undersigned inventor(s):

Abhishek Venkatesh; Prasoonkumar Surti; Slawomir Grajewski; Louis Feng; Kai Xiao; Tomasz Janczak; Devan Burke; Travis T. Schluessler

hereby sell, assign, and transfer to:

Intel Corporation

a corporation of Delaware, having a principal place of business at 2200 Mission College Boulevard, Santa Clara, California, 95054 USA ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all other countries, in and to any and all inventions and improvements that are disclosed in the application for the patent entitled:

GRAPHICS PROCESSOR WITH TILED COMPUTE KERNELS

designated lines below, the filing date and application number of said application when known.)			
which was filed on	April 10, 2017	as	
COUNTRY or Inte	Application Number	15/483,641	and

which has been executed by the undersigned prior hereto or concurrently herewith on the date(s) indicated below,

and in and to said application and all Additional Applications, and all other patent applications that have been or shall be filed in the United States and all other countries and international filing offices on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all other countries and international filing offices on said inventions and improvements; and in and to all rights of priority resulting from the filing of said applications; as used herein "Additional Applications" includes but is not limited to design, utility, utility model, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and national phase applications on said inventions and improvements;

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1	_06/14/2017
Abhishek Venkatesh	Date signed
Prasoonkumar Surti	Date signed
Slawomir Grajewski	Date signed
Louis Feng	Date signed
Kai Xiao	Date signed
Tomasz Janczak	Date signed
Devan Burke	Date signed
Travis T. Schluessler	Date signed

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Prasoonkumar Surti	Date signed
Slawomir Grajewski	Date signed
Louis Feng	Date signed
Kai Xiao	Date signed
Tomasz Janczak	Date signed
	<u>~</u>
Devan Burke	Perasional
rzesan image	Date signed
Travis T. Schluessler	Date signed

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(I hereby authorize and request any attorney having appropriate authority from the assignee to insert on the designated lines below, the filing date and application number of said application when known.)			
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Abhishek Venkatesh	Date signed	
Prasoonkumar Surti	Date signed	
aanir Gazana	6/19/2017	
Slawomir Črajewski	Date signed	
Louis Feng	Date signed	
Kai Xiao	Date signed	
Tomasz Janczak	Date signed	
Devan Burke	Date signed	
Travis T. Schluessier	Date signed	

Page 2 of 2

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	Abhishek Venkatesh	Date signed
	Prasoonkumar Surti	Date signed
	Slawomir Grajewski	Date signed
4		9/15/2018
	Louis Feng	Date signed
· · · · · · · · · · · · · · · · · · ·		
	Kai Xiao	Date signed
	Tomasz Janczak	Date signed
	Devan Burke	Date signed
. Na 18	Devan Burke	Date signed

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GRAPHICS PROCESSOR WITH TILED COMPUTE KERNELS

which was filed on	April 10, 2017	1882	
WINCH WAS INCO ON		XV:	
108	Application Number	15/483/641	1.155115-5155

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Prasoonkumar Surti	Date signed
Slawomir Grajewski	Date signed
Louis Feng	Date signed
Kengr	06-22-227
Kai Xiao	Date signed
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Afformey Docket No.: PXXXXX

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	Tomasz Janczak	Date signed
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Z	T Shire	6/15/17
	Travis T. Schluessler	Date signed

Page 2 of 2