PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2

EPAS ID: PAT5336473

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
LEON SIGAL	01/21/2019
DAVID KADZOV	01/22/2019
NAGASHYAMALA R. DHANWADA	01/21/2019
JAMES DOUGLAS WARNOCK	01/21/2019

RECEIVING PARTY DATA

Name:	INTERNATIONAL BUSINESS MACHINES CORPORATION	
Street Address:	New Orchard Road	
City:	Armonk	
State/Country:	NEW YORK	
Postal Code:	10504	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	16253812

CORRESPONDENCE DATA

Fax Number: (203)517-9615

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

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OTTERSTEDT, ELLENBOGEN & KAMMER, LLP **Correspondent Name:**

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ATTORNEY DOCKET NUMBER:	YOR820160110US01
NAME OF SUBMITTER:	PAUL J. OTTERSTEDT
SIGNATURE:	/PAUL J. OTTERSTEDT/
DATE SIGNED:	01/22/2019
	This document serves as an Oath/Declaration (37 CFR 1.63).

Total Attachments: 8

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PATENT REEL: 048092 FRAME: 0505

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PATENT REEL: 048092 FRAME: 0506

Title of Invention: CHARACTERIZING AND SIMULATING LIBRARY GATES TO ENABLE IDENTIFICATION AND ELIMINATION OF ELECTROMIGRATION VIOLATIONS IN SEMICONDUCTOR CHIPS

As a pelow hamen inventor, I nelectly declare that.		
This declaration is directed to the attached application, or (if following box is checked):		
16253812		
[x] United States application or PCT international application number	 e naimhi	
filed on <u>22-JAN-2019</u> .		

The above-identified application was made or authorized to be made by me.

I believe that I am the original inventor or an original joint inventor of a claimed invention in the application.

I have reviewed and understand the contents of the application, including the claims.

I am aware of the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in 37 CFR Section 1.56.

Whereas, the undersigned inventor(s) has/have made certain inventions, improvements, and discoveries (herein referred to as the "Invention") disclosed in the above-identified patent application and further identified by the IBM Docket Number provided above in the header of this document;

Whereas, International Business Machines Corporation, a corporation of New York having a place of business at Armonk, New York (herein referred to as "IBM"), desires to acquire, and each undersigned inventor desires to grant to IBM, the entire worldwide right, title, and interest in and to the Invention and in and to any and all patent applications and patents directed thereto;

Now, therefore, for good and valuable consideration, the receipt and sufficiency thereof being hereby acknowledged, each undersigned inventor ("ASSIGNOR") hereby sells or has sold, assigns or has assigned, and otherwise transfers or has transferred to IBM (the "ASSIGNEE"), its successors, legal representatives, and assigns, the entire worldwide right, title, and interest in and to the Invention, the above-identified United States patent application, and any and all other patent applications and patents for the Invention which may be applied for or granted therefor in the United States and in all foreign countries and jurisdictions, including all divisions, continuations, reissues, reexaminations, renewals, extensions, counterparts, substitutes, and extensions thereof, and all rights of priority resulting from the filing of such applications and granting of such patents. In addition, each undersigned inventor hereby authorizes and requests the Director of the United States Patent and Trademark Office to issue any United States Patent, and foreign patent authorities to issue any foreign patent, granted for the Invention, to IBM, its successors, legal representatives, and assigns, the entire worldwide right, title, and interest in and to the same to be held and enjoyed by IBM, its successors, legal representatives, and assigns to the full end of the terms for which any and all such patents may be granted, as fully and entirely as would have been held and enjoyed by the undersigned had this Assignment not been made; and each undersigned inventor agrees to execute any and all documents and instruments and perform all lawful acts reasonably related to recording this Assignment or perfecting title to the Invention and all related patents and applications, in IBM, its successors, legal representatives, and assigns, whenever requested by IBM, its successors, legal

IBM DOCKET NUMBER: YOR820160110US01

representatives, or assigns.

(1) Legal Name of Inventor: Leon Sigal

Each undersigned inventor acknowledges their prior and ongoing obligations to sell, assign, and transfer the rights under this Assignment to IBM and is unaware of any reason why they may not have the full and unencumbered right to sell, assign, and transfer the rights hereby sold, assigned, and transferred, and has not executed, and will not execute, any document or instrument in conflict herewith. Each undersigned inventor also hereby grants IBM, its successors, legal representatives, and assigns, the right to insert in this Assignment any further identification (including, but not limited to, patent Application Number) which may be necessary or desirable for recordation of this Assignment. This Assignment is governed by the substantive laws of the State of New York, and any disputes will be resolved in a New York state court or federal court sited in New York.

I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) years, or both.

Signature:	Date: <u>//21</u> /2<>13
(2) Legal Name of Inventor: David Kadzov	
Signature:	Date:
(3) Legal Name of Inventor: Nagashyamala R. Dhanwada	
Signature:	Date:
(4) Legal Name of inventor: James Dougles Warnock	
Signature:	Date:

Page 2 of 2

Title of Invention: CHARACTERIZING AND SIMULATING LIBRARY GATES TO ENABLE IDENTIFICATION AND ELIMINATION OF ELECTROMIGRATION VIOLATIONS IN SEMICONDUCTOR CHIPS

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(1) Legal Name of Inventor: Leon Sigal	
Signature:	Date:
(2) Legal Name of Inventor: David Kadzov	
Signature: Signature:	Date:
(3) Legal Name of Inventor: Nagashyamala R. Dhanw	ada
Signature:	Date:
(4) Legal Name of Inventor: James Douglas Warnock	
Signature:	Date:

Title of Invention: CHARACTERIZING AND SIMULATING LIBRARY GATES TO ENABLE IDENTIFICATION AND ELIMINATION OF ELECTROMIGRATION VIOLATIONS IN SEMICONDUCTOR CHIPS

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[x] United States application or PCT international application number	16253812	
filed on _22-JAN-2019		

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(1) Legal Name of Inventor: Leon Sigal	
Signature:	Date:
(2) Legal Name of Inventor: David Kadzov	
Signature:	Date:
(3) Legal Name of Inventor: Nagashyamala R.	
Signature: D Naghyam	La Ruo January 21, 2019 Date:
(4) Legal Name of Inventor: James Douglas W	⁷ arnock
Signature:	Date:

Title of Invention: CHARACTERIZING AND SIMULATING LIBRARY GATES TO ENABLE IDENTIFICATION AND ELIMINATION OF ELECTROMIGRATION VIOLATIONS IN SEMICONDUCTOR CHIPS

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[x] United States application or PCT international application number 16253812 filed on 22-JAN-2019

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	Signature:	Date:
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	Signature:	Date:
(3)	Legal Name of Inventor: Nagashyamala R. Dhanwada	
	Signature:	Date:
(4)	Legal Name of Inventor: James Douglas Warnock	
	Signature: James Minster	Date: 1/21/19

Page 2 of 2