

PATENT ASSIGNMENT COVER SHEET

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NATURE OF CONVEYANCE:	NUNC PRO TUNC ASSIGNMENT
EFFECTIVE DATE:	12/10/2018

CONVEYING PARTY DATA

Name	Execution Date
ZARBAÑA DIGITAL FUND LLC	12/14/2018

RECEIVING PARTY DATA

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PROPERTY NUMBERS Total: 3

Property Type	Number
Patent Number:	7107386
Patent Number:	7426603
Patent Number:	6886122

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DATE SIGNED:	01/22/2019

Total Attachments: 3

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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Zarbaña Digital Fund LLC, a Delaware limited liability company, with an address at 251 Little Falls Drive, Wilmington, DE 19808 (“*Assignor*”), does hereby sell, assign, transfer, and convey unto Intellectual Ventures Assets 115 LLC, a Delaware limited liability company, with an address at 251 Little Falls Drive, Wilmington, DE 19808 (“*Assignee*”), all of Assignor's right, title, and interest in and to the following (collectively, the “*Assigned Patent Rights*”):

(a) the patents and patent applications listed in the table below (the “*Patents*”);

<u>Patent/ Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
7107386 (10/759376)	US	9/12/2006 (1/15/2004)	MEMORY BUS ARBITRATION USING MEMORY BANK READINESS Stephen Clark Purcell; Scott Kimura
7426603 (11/499015)	US	9/16/2008 (8/4/2006)	Memory bus arbitration using memory bank readiness Clark, Purcell Stephen; Scott, Kimura
FR2790832 (FR9902823)	FR	6/8/2001 (3/8/1999)	METHOD FOR TESTING INTEGRATED CIRCUITS WITH MEMORY ELEMENT ACCESS BARTHEL DOMINIQUE
GB1159628 (GB00909440.0)	GB	12/18/2002 (3/7/2000)	METHOD FOR TESTING INTEGRATED CIRCUITS WITH MEMORY ELEMENT ACCESS BARTHEL DOMINIQUE
6886122 (09/936487)	US	4/26/2005 (1/25/2002)	METHOD FOR TESTING INTEGRATED CIRCUITS WITH MEMORY ELEMENT ACCESS Dominique Barthel

