

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
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EPAS ID: PAT5385411

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
RAVINDAR ATTINENI	01/17/2019
SACHIN MATHUR	02/04/2019
JIAXIANG SHI	01/12/2019
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	INTEL CORPORATION
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<b>City:</b>	SANTA CLARA
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	95054
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
Application Number:	16233554
<b>CORRESPONDENCE DATA</b>	
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<b>NAME OF SUBMITTER:</b>	THOMAS G. ESCHWEILER
<b>SIGNATURE:</b>	/Thomas G. Eschweiler/
<b>DATE SIGNED:</b>	02/21/2019
<b>Total Attachments: 2</b>	
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ASSIGNMENT

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we, the undersigned inventor(s):

**Ravindar Attineni; Sachin Mathur; & Jiaxiang Shi**

hereby sell, assign, and transfer to:

**Intel Corporation**

a corporation of Delaware, having a principal place of business at 2200 Mission College Boulevard, Santa Clara, California, 95054 USA ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all other countries, in and to any and all inventions and improvements that are disclosed in the application for the patent entitled:

**SYSTEMS AND METHODS FOR DYNAMIC PHASE ALIGNMENT OF CLOCKS**

(I hereby authorize and request any attorney having appropriate authority from the assignee to insert on the designated lines below, the filing date and application number of said application when known.)

which was filed on January 4, 2019 as

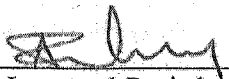
US Application Number 16/233,554 and  
COUNTRY or International Office

which has been executed by the undersigned prior hereto or concurrently herewith on the date(s) indicated below,


and in and to said application and all Additional Applications, and all other patent applications that have been or shall be filed in the United States and all other countries and international filing offices on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all other countries and international filing offices on said inventions and improvements; and in and to all rights of priority resulting from the filing of said applications; as used herein "Additional Applications" includes but is not limited to design, utility, utility model, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and national phase applications on said inventions and improvements;

and agree that said Assignee may apply for and receive a patent or patents for said inventions and improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all Additional Applications, and all other patent applications on any and all said inventions and improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and legal representatives all facts known to the undersigned relating to said inventions and improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or legal representatives in securing and maintaining proper patent protection for said inventions and improvements and for vesting title to said inventions and improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives; and


covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

  
\_\_\_\_\_  
Inventor 1 Ravindar Attineni

17 - Jan - 2019  
Date signed

  
\_\_\_\_\_  
Inventor 2 Sachin Mathur

4 - FEB - 2019  
Date signed

  
\_\_\_\_\_  
Inventor 3 Jiayang Shi

12 - Jan - 2019  
Date signed