

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT5389554

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT
NATURE OF CONVEYANCE:	Corrective Assignment to correct the ASSIGNEE'S NAME previously recorded on Reel 025766 Frame 0872. Assignor(s) hereby confirms the ASSIGNMENTS ASSIGNEE NAME OF JM MGMT. GROUP LTD. LLC.
CONVEYING PARTY DATA	
Name	Execution Date
JAM TECHNOLOGIES, INC	12/07/2007
RECEIVING PARTY DATA	
Name:	JM MGMT. GROUP LTD. LLC
Street Address:	2711 CENTERVILLE ROAD
Internal Address:	SUITE 400
City:	WILMINGTON
State/Country:	DELAWARE
Postal Code:	19808
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	12483906
CORRESPONDENCE DATA	
Fax Number:	(425)679-0580
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Email:	ivrecording@intven.com
Correspondent Name:	INTELLECTUAL VENTURES MANAGEMENT- IP LEGAL
Address Line 1:	3150 139TH AVENUE SE
Address Line 2:	BUILDING 4, FLOOR 3
Address Line 4:	BELLEVUE, WASHINGTON 98005
NAME OF SUBMITTER:	JANICE L. GOEBEL
SIGNATURE:	/Janice L. Goebel/
DATE SIGNED:	02/22/2019
Total Attachments: 27	
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SUBMISSION TYPE:	NEW ASSIGNMENT
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JAM Technologies, Inc.	12/07/2007
RECEIVING PARTY DATA <u>JM MGMT. Group Ltd. LLC</u>	
Name:	JM Electronics Ltd. LLC
Street Address:	2711 Centerville Road
Internal Address:	Suite 400
City:	Wilmington
State/Country:	DELAWARE
Postal Code:	19808
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	12483906
CORRESPONDENCE DATA	
Fax Number:	(206)903-8820
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	(206) 903-8800
Email:	ipdocket-se@dorsey.com
Correspondent Name:	Dorsey & Whitney LLP
Address Line 1:	701 Fifth Avenue
Address Line 2:	Suite 6100
Address Line 4:	Seattle, WASHINGTON 98104-7043
NAME OF SUBMITTER:	Jennifer M. Lane

Total Attachments: 9
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PATENT
REEL: 048415 FRAME: 0300

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PATENT
REEL: 025766 FRAME: 0873
PATENT
REEL: 048415 FRAME: 0301

PATENT OR APPLICATION NO.	COUNTRY	FILING DATE	TITLE OF PATENT AND FIRST NAMED INVENTOR(S)
6,768,375 (10/356,883)	US	7/27/2004 (2/3/2003)	MULTI-REFERENCE HIGH ACCURACY SWITCHING AMPLIFIER EXPANSION LARRY KIRN
6,538,504 (09/980,837)	US	3/25/2003 (11/15/2001)	SWITCHING AMPLIFIER CROSSOVER DISTORTION REDUCTION TECHNIQUE LARRY KIRN
6,538,505 (09/980,966)	US	3/25/2003 (11/15/2001)	DISTORTION REDUCTION TECHNIQUE FOR INDUCTIVE BOOST AMPLIFIER LARRY KIRN
6,563,378 (09/980,662)	US	5/13/2003 (11/15/2001)	DIGITAL AMPLIFIER LINEARIZATION USING ANALOG FEEDBACK LARRY KIRN
6,643,147 (10/167,380)	US	11/4/2003 (6/11/2002)	MODULATION TECHNIQUE FOR FLYBACK CONVERTER LARRY KIRN
6,744,311 (10/128,049)	US	6/1/2004 (4/23/2002)	SWITCHING AMPLIFIER WITH VOLTAGE-MULTIPLYING OUTPUT STAGE LARRY KIRN
6,771,120 (09/929,310)	US	8/3/2004 (8/14/2001)	REFERENCE GENERATION TECHNIQUE FOR MULTIPLE-REFERENCE AMPLIFIER LARRY KIRN
6,778,012 (10/435,851)	US	8/17/2004 (5/12/2003)	POLYPHASE IMPEDANCE TRANSFORMATION AMPLIFIER LARRY KIRN
CN03816260.1	CN	5/13/2003	POLYPHASE IMPEDANCE TRANSFORMATION AMPLIFIER LARRY KIRN
JP2004-504376	JP	5/13/2003	POLYPHASE IMPEDANCE TRANSFORMATION AMPLIFIER LARRY KIRN

PATENT OR APPLICATION NO.	COUNTRY	FILING DATE	TITLE OF PATENT AND FIRST NAMED INVENTOR(S)
KR1020047018372	KR	5/13/2003	POLYPHASE IMPEDANCE TRANSFORMATION AMPLIFIER LARRY KIRN
7,196,575 (10/514,306)	US	3/27/2007 (11/12/2004)	POLYPHASE IMPEDANCE TRANSFORMATION AMPLIFIER LARRY KIRN
6,781,453 (10/128,047)	US	8/24/2004 (4/23/2002)	METHOD OF DETECTING LOAD IMPEDANCE FOR INDUCTIVE BOOST AMPLIFIER LARRY KIRN
6,937,090 (09/941,187)	US	8/30/2005 (8/28/2001)	CHARGE INJECTION REDUCTION TECHNIQUE IN SINGLE AND MULTI-REFERENCE SWITCHING AMPLIFIERS LARRY KIRN
6,989,656 (10/437,318)	US	1/24/2006 (5/13/2003)	FLYBACK CONVERTER LINEARIZATION METHODS AND APPARATUS LARRY KIRN
6,989,657 (10/769,952)	US	1/24/2006 (2/2/2004)	METHOD OF DETECTING SWITCHING POWER SUPPLY OUTPUT CURRENT LARRY KIRN
CN20048000573.0	CN	2/3/2004	IMPROVED METHOD OF DETECTING SWITCHING POWER SUPPLY OUTPUT CURRENT LARRY KIRN
JP2006-503294	JP	2/3/2004	IMPROVED METHOD OF DETECTING SWITCHING POWER SUPPLY OUTPUT CURRENT LARRY KIRN
KR10-2005-7014294	KR	2/3/2004	IMPROVED METHOD OF DETECTING SWITCHING POWER SUPPLY OUTPUT CURRENT LARRY KIRN

PATENT OR APPLICATION NO.	COUNTRY	FILING DATE	TITLE OF PATENT AND FIRST NAMED INVENTOR(S)
EP04707758.1	EP	2/3/2004	IMPROVED METHOD OF DETECTING SWITCHING POWER SUPPLY OUTPUT CURRENT LARRY KIRN
7,116,162 (10/649,035)	US	10/3/2006 (8/27/2003)	REDUCED OUTPUT TOPOLOGY FOR MULTI-REFERENCE SWITCHING AMPLIFIERS LARRY KIRN
7,005,917 (10/405,821)	US	2/28/2006 (4/2/2003)	POWER SUPPLY REJECTION TECHNIQUE FOR SWITCHING AMPLIFIER LARRY KIRN
7,132,886 (10/916,037)	US	11/7/2006 (8/11/2004)	DETECTING LOAD CURRENT IN MULTI-REFERENCE AMPLIFIERS LARRY KIRN
7,142,049 (10/916,131)	US	11/28/2006 (8/11/2004)	MULTI-REFERENCE SWITCHING AMPLIFIER MODULATION METHOD AND APPARATUS LARRY KIRN
7,151,403 (10/916,032)	US	12/19/2006 (8/11/2004)	ADAPTIVE SELF-CALIBRATION METHOD AND APPARATUS LARRY KIRN
7,157,964 (10/916,038)	US	1/2/2007 (8/11/2004)	MULTI-OUTPUT SWITCHING AMPLIFIER LARRY KIRN
7,230,500 (11/168,810)	US	6/12/2007 (6/28/2005)	SYNCHRONOUS DELAY-LINE AMPLIFICATION TECHNIQUE LARRY KIRN
5,610,553 (08/513,780)	US	3/11/1997 (8/31/1995)	SWITCHING AMPLIFIER WITH IMPEDANCE TRANSFORMATION OUTPUT STAGE LARRY KIRN
6,492,868 (09/929,335)	US	12/10/2002 (8/14/2001)	DYNAMIC RANGE ENHANCEMENT TECHNIQUE LARRY KIRN

PATENT OR APPLICATION NO.	COUNTRY	FILING DATE	TITLE OF PATENT AND FIRST NAMED INVENTOR(S)
6,509,793 (09/862,760)	US	1/21/2003 (5/21/2001)	SWITCHING AMPLIFIER RESOLUTION ENHANCEMENT APPARATUS AND METHODS LARRY KIRN
JP2000-619095	JP	5/19/2000	LOAD COMPENSATION TECHNIQUE FOR REACTIVE IMPEDANCE TRANSFORMATION AMPLIFIER OUTPUT STAGE LARRY KIRN
EP00932638.0	EP	5/19/2000	LOAD COMPENSATION TECHNIQUE FOR REACTIVE IMPEDANCE TRANSFORMATION AMPLIFIER OUTPUT STAGE LARRY KIRN
6,636,113 (09/980,983)	US	10/21/2003 (11/15/2001)	LOAD COMPENSATION TECHNIQUE FOR REACTIVE IMPEDANCE TRANSFORMATION AMPLIFIER OUTPUT STAGE LARRY KIRN
10/916,128	US	8/11/2004	LOAD CURRENT SENSING TECHNIQUE LARRY KIRN
11/107,713	US	4/15/2005	TRICKLE-CHARGED AMPLIFIER LARRY KIRN
10/912,211	US	8/5/2004	ADAPTIVE PULSE WIDTH DISCRIMINATION USING AN ASYNCHRONOUS CLOCK LARRY KIRN
11/114,628	US	4/26/2005	SWITCHING CLASS A-B AMPLIFIER LARRY KIRN
11/106,290	US	4/14/2005	SELF-POWERED DIGITAL AUDIO DEVICES LARRY KIRN
11/108,243	US	4/18/2005	SAMPLED SYSTEM AGILITY TECHNIQUE LARRY KIRN

PATENT OR APPLICATION NO.	COUNTRY	FILING DATE	TITLE OF PATENT AND FIRST NAMED INVENTOR(S)
11/483,053	US	7/7/2006	INTEGRATION IMPROVEMENT TECHNIQUE FOR SWITCHING AMPLIFIERS LARRY KIRN
10/649,218	US	8/26/2003	DATA DEMODULATION USING AN ASYNCHRONOUS CLOCK LARRY KIRN
11/344,358	US	1/31/2006	AUTOMATIC VOLUME LIMITER FOR PORTABLE AUDIO DEVICES LARRY KIRN
60/886,746	US	1/26/2007	AUTOMATIC AMPLIFIER EQUALIZATION TECHNIQUE AND APPARATUS LARRY KIRN
60/887,000	US	1/29/2007	AUDIO BASS ENHANCEMENT TECHNIQUE LARRY KIRN
60/887,173	US	1/30/2007	FILTER COMPENSATION TECHNIQUE FOR SWITCHING AMPLIFIERS LARRY KIRN
60/887,394	US	1/31/2007	EMI REDUCTION TECHNIQUE FOR SWITCHING AMPLIFIERS LARRY KIRN
60/887,662	US	2/1/2007	SAMPLING FREQUENCY REDUCTION TECHNIQUE FOR SWITCHING AMPLIFIERS LARRY KIRN
SG200406681-7	SG	5/13/2003	POLYPHASE IMPEDANCE TRANSFORMATION AMPLIFIER LARRY KIRN
SG200504920-0	SG	2/3/2004	IMPROVED METHOD OF DETECTING SWITCHING POWER SUPPLY OUTPUT CURRENT

Initial:
 Date: 2/10/06/2007

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, JAM Technologies, Inc., a Delaware corporation, with an address at P.O. Box 27772, Austin, Texas 78755 ("*Assignor*"), does hereby sell, assign, transfer, and convey unto JM-Electronics-Ltd.-LLC, a Delaware limited liability company, with an address at 2711 Centerville Road, Suite 400, Wilmington, DE 19808 ("*Assignee*"), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the "*Patent Rights*"):

(a) the provisional patent applications, patent applications and patents listed in the table below (the "*Patents*");

(b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, (ii) for which any of the Patents directly or indirectly forms a basis for priority, and/or (iii) that were co-owned applications that directly or indirectly incorporate by reference the Patents;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);

(d) all foreign patents, patent applications, and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

(e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents below and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;

(f) all inventions, invention disclosures, and discoveries described in any item in any of the foregoing categories (a) through (e) and all other rights arising out of such inventions, invention disclosures, and discoveries;

(g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

- (i) damages,
- (ii) injunctive relief, and
- (iii) any other remedies of any kind

for past, current, and future infringement; and

(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

PATENT OR APPLICATION NO.	COUNTRY	FILING DATE	TITLE OF PATENT AND FIRST NAMED INVENTOR(S)
ZL99813224 (CN99813224)	CN	4/27/2005 (11/12/1999)	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN
JP2000-581746	JP	11/12/1999	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN
KR10-0704859-0000 (KR10-2001-7005997)	KR	4/2/2007 (11/12/1999)	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN
MXPA01004786	MX	11/12/1999	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER. LARRY KIRN
DE69935731 (DE69935731)	DE	4/4/2007 (11/12/1999)	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN
EP1131886 (GB99964977.5)	GB	4/4/2007 (11/12/1999)	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN
EP1131886 (FR99964977.5)	FR	4/4/2007 (11/12/1999)	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN
6,535,058 (09/831,595)	US	3/18/2003 (5/11/2001)	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN

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10/912,211	US	8/5/2004	ADAPTIVE PULSE WIDTH DISCRIMINATION USING AN ASYNCHRONOUS CLOCK LARRY KIRN
11/114,628	US	4/26/2005	SWITCHING CLASS A-B AMPLIFIER LARRY KIRN
11/106,290	US	4/14/2005	SELF-POWERED DIGITAL AUDIO DEVICES LARRY KIRN
11/108,243	US	4/18/2005	SAMPLED SYSTEM AGILITY TECHNIQUE LARRY KIRN

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11/344,358	US	1/31/2006	AUTOMATIC VOLUME LIMITER FOR PORTABLE AUDIO DEVICES LARRY KIRN
60/886,746	US	1/26/2007	AUTOMATIC AMPLIFIER EQUALIZATION TECHNIQUE AND APPARATUS LARRY KIRN
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60/887,173	US	1/30/2007	FILTER COMPENSATION TECHNIQUE FOR SWITCHING AMPLIFIERS LARRY KIRN
60/887,394	US	1/31/2007	EMI REDUCTION TECHNIQUE FOR SWITCHING AMPLIFIERS LARRY KIRN
60/887,662	US	2/1/2007	SAMPLING FREQUENCY REDUCTION TECHNIQUE FOR SWITCHING AMPLIFIERS LARRY KIRN
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SG200504920-0	SG	2/3/2004	IMPROVED METHOD OF DETECTING SWITCHING POWER SUPPLY OUTPUT CURRENT

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
JAM Technologies, Inc.	12/07/2007
RECEIVING PARTY DATA	
Name:	JM Electronics Ltd. LLC
Street Address:	2711 Centerville Road
Internal Address:	Suite 400
City:	Wilmington
State/Country:	DELAWARE
Postal Code:	19808
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	12483906
CORRESPONDENCE DATA	
Fax Number:	(206)903-8820
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Total Attachments: 9
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 source=JAM_RevisedAssignment_ExhibitBonly_(signed)#page2.tif
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source=JAM_RevisedAssignment_ExhibitBonly_(signed)#page8.tif
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PATENT
REEL: 025766 FRAME: 0873
PATENT
REEL: 048415 FRAME: 0317

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, JAM Technologies, Inc., a Delaware corporation, with an address at P.O. Box 27772, Austin, Texas 78755 ("**Assignor**"), does hereby sell, assign, transfer, and convey unto JM Electronics Ltd. LLC, a Delaware limited liability company, with an address at 2711 Centerville Road, Suite 400, Wilmington, DE 19808 ("**Assignee**"), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the "**Patent Rights**"):

(a) the provisional patent applications, patent applications and patents listed in the table below (the "**Patents**");

(b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, (ii) for which any of the Patents directly or indirectly forms a basis for priority, and/or (iii) that were co-owned applications that directly or indirectly incorporate by reference the Patents;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);

(d) all foreign patents, patent applications, and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

(e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents below and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;

(f) all inventions, invention disclosures, and discoveries described in any item in any of the foregoing categories (a) through (e) and all other rights arising out of such inventions, invention disclosures, and discoveries;

(g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

- (i) damages,
- (ii) injunctive relief, and
- (iii) any other remedies of any kind

for past, current, and future infringement; and

(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

PATENT OR APPLICATION NO.	COUNTRY	FILING DATE	TITLE OF PATENT AND FIRST NAMED INVENTOR(S)
ZL99813224 (CN99813224)	CN	4/27/2005 (11/12/1999)	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN
JP2000-581746	JP	11/12/1999	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN
KR10-0704859-0000 (KR10-2001-7005997)	KR	4/2/2007 (11/12/1999)	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN
MXPA01004786	MX	11/12/1999	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER. LARRY KIRN
DE69935731 (DE69935731)	DE	4/4/2007 (11/12/1999)	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN
EP1131886 (GB99964977.5)	GB	4/4/2007 (11/12/1999)	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN
EP1131886 (FR99964977.5)	FR	4/4/2007 (11/12/1999)	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN
6,535,058 (09/831,595)	US	3/18/2003 (5/11/2001)	MULTI-REFERENCE, HIGH-ACCURACY SWITCHING AMPLIFIER LARRY KIRN

PATENT OR APPLICATION NO.	COUNTRY	FILING DATE	TITLE OF PATENT AND FIRST NAMED INVENTOR(S)
6,768,375 (10/356,883)	US	7/27/2004 (2/3/2003)	MULTI-REFERENCE HIGH ACCURACY SWITCHING AMPLIFIER EXPANSION LARRY KIRN
6,538,504 (09/980,837)	US	3/25/2003 (11/15/2001)	SWITCHING AMPLIFIER CROSSOVER DISTORTION REDUCTION TECHNIQUE LARRY KIRN
6,538,505 (09/980,966)	US	3/25/2003 (11/15/2001)	DISTORTION REDUCTION TECHNIQUE FOR INDUCTIVE BOOST AMPLIFIER LARRY KIRN
6,563,378 (09/980,662)	US	5/13/2003 (11/15/2001)	DIGITAL AMPLIFIER LINEARIZATION USING ANALOG FEEDBACK LARRY KIRN
6,643,147 (10/167,380)	US	11/4/2003 (6/11/2002)	MODULATION TECHNIQUE FOR FLYBACK CONVERTER LARRY KIRN
6,744,311 (10/128,049)	US	6/1/2004 (4/23/2002)	SWITCHING AMPLIFIER WITH VOLTAGE-MULTIPLYING OUTPUT STAGE LARRY KIRN
6,771,120 (09/929,310)	US	8/3/2004 (8/14/2001)	REFERENCE GENERATION TECHNIQUE FOR MULTIPLE-REFERENCE AMPLIFIER LARRY KIRN
6,778,012 (10/435,851)	US	8/17/2004 (5/12/2003)	POLYPHASE IMPEDANCE TRANSFORMATION AMPLIFIER LARRY KIRN
CN03816260.1	CN	5/13/2003	POLYPHASE IMPEDANCE TRANSFORMATION AMPLIFIER LARRY KIRN
JP2004-504376	JP	5/13/2003	POLYPHASE IMPEDANCE TRANSFORMATION AMPLIFIER LARRY KIRN

PATENT OR APPLICATION NO.	COUNTRY	FILING DATE	TITLE OF PATENT AND FIRST NAMED INVENTOR(S)
KR1020047018372	KR	5/13/2003	POLYPHASE IMPEDANCE TRANSFORMATION AMPLIFIER LARRY KIRN
7,196,575 (10/514,306)	US	3/27/2007 (11/12/2004)	POLYPHASE IMPEDANCE TRANSFORMATION AMPLIFIER LARRY KIRN
6,781,453 (10/128,047)	US	8/24/2004 (4/23/2002)	METHOD OF DETECTING LOAD IMPEDANCE FOR INDUCTIVE BOOST AMPLIFIER LARRY KIRN
6,937,090 (09/941,187)	US	8/30/2005 (8/28/2001)	CHARGE INJECTION REDUCTION TECHNIQUE IN SINGLE AND MULTI-REFERENCE SWITCHING AMPLIFIERS LARRY KIRN
6,989,656 (10/437,318)	US	1/24/2006 (5/13/2003)	FLYBACK CONVERTER LINEARIZATION METHODS AND APPARATUS LARRY KIRN
6,989,657 (10/769,952)	US	1/24/2006 (2/2/2004)	METHOD OF DETECTING SWITCHING POWER SUPPLY OUTPUT CURRENT LARRY KIRN
CN20048000573.0	CN	2/3/2004	IMPROVED METHOD OF DETECTING SWITCHING POWER SUPPLY OUTPUT CURRENT LARRY KIRN
JP2006-503294	JP	2/3/2004	IMPROVED METHOD OF DETECTING SWITCHING POWER SUPPLY OUTPUT CURRENT LARRY KIRN
KR10-2005-7014294	KR	2/3/2004	IMPROVED METHOD OF DETECTING SWITCHING POWER SUPPLY OUTPUT CURRENT LARRY KIRN

PATENT OR APPLICATION NO.	COUNTRY	FILING DATE	TITLE OF PATENT AND FIRST NAMED INVENTOR(S)
EP04707758.1	EP	2/3/2004	IMPROVED METHOD OF DETECTING SWITCHING POWER SUPPLY OUTPUT CURRENT LARRY KIRN
7,116,162 (10/649,035)	US	10/3/2006 (8/27/2003)	REDUCED OUTPUT TOPOLOGY FOR MULTI-REFERENCE SWITCHING AMPLIFIERS LARRY KIRN
7,005,917 (10/405,821)	US	2/28/2006 (4/2/2003)	POWER SUPPLY REJECTION TECHNIQUE FOR SWITCHING AMPLIFIER LARRY KIRN
7,132,886 (10/916,037)	US	11/7/2006 (8/11/2004)	DETECTING LOAD CURRENT IN MULTI-REFERENCE AMPLIFIERS LARRY KIRN
7,142,049 (10/916,131)	US	11/28/2006 (8/11/2004)	MULTI-REFERENCE SWITCHING AMPLIFIER MODULATION METHOD AND APPARATUS LARRY KIRN
7,151,403 (10/916,032)	US	12/19/2006 (8/11/2004)	ADAPTIVE SELF-CALIBRATION METHOD AND APPARATUS LARRY KIRN
7,157,964 (10/916,038)	US	1/2/2007 (8/11/2004)	MULTI-OUTPUT SWITCHING AMPLIFIER LARRY KIRN
7,230,500 (11/168,810)	US	6/12/2007 (6/28/2005)	SYNCHRONOUS DELAY-LINE AMPLIFICATION TECHNIQUE LARRY KIRN
5,610,553 (08/513,780)	US	3/11/1997 (8/31/1995)	SWITCHING AMPLIFIER WITH IMPEDANCE TRANSFORMATION OUTPUT STAGE LARRY KIRN
6,492,868 (09/929,335)	US	12/10/2002 (8/14/2001)	DYNAMIC RANGE ENHANCEMENT TECHNIQUE LARRY KIRN

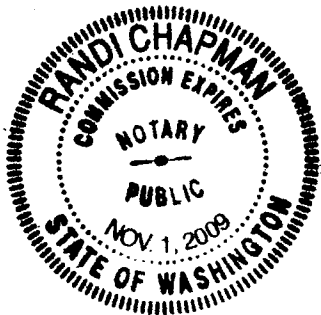
PATENT OR APPLICATION NO.	COUNTRY	FILING DATE	TITLE OF PATENT AND FIRST NAMED INVENTOR(S)
6,509,793 (09/862,760)	US	1/21/2003 (5/21/2001)	SWITCHING AMPLIFIER RESOLUTION ENHANCEMENT APPARATUS AND METHODS LARRY KIRN
JP2000-619095	JP	5/19/2000	LOAD COMPENSATION TECHNIQUE FOR REACTIVE IMPEDANCE TRANSFORMATION AMPLIFIER OUTPUT STAGE LARRY KIRN
EP00932638.0	EP	5/19/2000	LOAD COMPENSATION TECHNIQUE FOR REACTIVE IMPEDANCE TRANSFORMATION AMPLIFIER OUTPUT STAGE LARRY KIRN
6,636,113 (09/980,983)	US	10/21/2003 (11/15/2001)	LOAD COMPENSATION TECHNIQUE FOR REACTIVE IMPEDANCE TRANSFORMATION AMPLIFIER OUTPUT STAGE LARRY KIRN
10/916,128	US	8/11/2004	LOAD CURRENT SENSING TECHNIQUE LARRY KIRN
11/107,713	US	4/15/2005	TRICKLE-CHARGED AMPLIFIER LARRY KIRN
10/912,211	US	8/5/2004	ADAPTIVE PULSE WIDTH DISCRIMINATION USING AN ASYNCHRONOUS CLOCK LARRY KIRN
11/114,628	US	4/26/2005	SWITCHING CLASS A-B AMPLIFIER LARRY KIRN
11/106,290	US	4/14/2005	SELF-POWERED DIGITAL AUDIO DEVICES LARRY KIRN
11/108,243	US	4/18/2005	SAMPLED SYSTEM AGILITY TECHNIQUE LARRY KIRN

PATENT OR APPLICATION NO.	COUNTRY	FILING DATE	TITLE OF PATENT AND FIRST NAMED INVENTOR(S)
11/483,053	US	7/7/2006	INTEGRATION IMPROVEMENT TECHNIQUE FOR SWITCHING AMPLIFIERS LARRY KIRN
10/649,218	US	8/26/2003	DATA DEMODULATION USING AN ASYNCHRONOUS CLOCK LARRY KIRN
11/344,358	US	1/31/2006	AUTOMATIC VOLUME LIMITER FOR PORTABLE AUDIO DEVICES LARRY KIRN
60/886,746	US	1/26/2007	AUTOMATIC AMPLIFIER EQUALIZATION TECHNIQUE AND APPARATUS LARRY KIRN
60/887,000	US	1/29/2007	AUDIO BASS ENHANCEMENT TECHNIQUE LARRY KIRN
60/887,173	US	1/30/2007	FILTER COMPENSATION TECHNIQUE FOR SWITCHING AMPLIFIERS LARRY KIRN
60/887,394	US	1/31/2007	EMI REDUCTION TECHNIQUE FOR SWITCHING AMPLIFIERS LARRY KIRN
60/887,662	US	2/1/2007	SAMPLING FREQUENCY REDUCTION TECHNIQUE FOR SWITCHING AMPLIFIERS LARRY KIRN
SG200406681-7	SG	5/13/2003	POLYPHASE IMPEDANCE TRANSFORMATION AMPLIFIER LARRY KIRN
SG200504920-0	SG	2/3/2004	IMPROVED METHOD OF DETECTING SWITCHING POWER SUPPLY OUTPUT CURRENT

Notarial Certificate

I, the undersigned, hereby certify that the annexed is a true copy of the original document which was recorded in the United States Patent and Trademark Office and that JAM Technologies, Inc., (Assignor) with an address at P.O. Box 27772 Austin, Texas 78755, and JM Electronics Ltd. LLC (Assignee) with an address at 2711 Centerville Road, Suite 400, Wilmington, Delaware 19808, the United States of America.

This 23 day of May, 2007.



Randi Chapman

Randi Chapman, Notary Public