

PATENT ASSIGNMENT COVER SHEET

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 Stylesheet Version v1.2

EPAS ID: PAT5405084

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
JAVIER A. SALCEDO	03/04/2019
SRIVATSAN PARTHASARATHY	03/04/2019
ENRIQUE C. BOSCH	03/05/2019
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City:	LIMERICK
State/Country:	IRELAND
PROPERTY NUMBERS Total: 2	
Property Type	Number
Application Number:	62790822
Application Number:	16291352
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NAME OF SUBMITTER:	DAVID TROSSEN
SIGNATURE:	/David Trossen/
DATE SIGNED:	03/05/2019
Total Attachments: 4	
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ASSIGNMENT

WHEREAS, I am a named inventor, **Javier A. Salcedo, Srivatsan Parthasarathy and Enrique C. Bosch**, of residence listed below, having invented certain new and useful improvements as described in an application for United States Letters Patent entitled "ELECTRICAL OVERSTRESS PROTECTION WITH LOW LEAKAGE CURRENT FOR HIGH VOLTAGE TOLERANT HIGH SPEED INTERFACES," filed on **March 4, 2019**, having Ser. No. **16/291,352** (hereinafter referred to as "Application") and having internal reference number of **APD6806US02**;

WHEREAS, **Analog Devices International Unlimited Company** (hereinafter referred to as "Assignee"), a company organized under the laws of Ireland and residing at Bay F1 Raheen Industrial Estate, Limerick, Ireland, desires to acquire my entire right, title and interest in and to any and all inventions and improvements that are disclosed in the Application (hereinafter referred to as "Invention"), including any Letters Patent that may issue thereon;

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I hereby assign and transfer, and hereby agree to assign and transfer, to Assignee, all right, title and interest in and to the Invention and in and to the Application and all patents which may be granted therefrom, and all related applications (e.g., provisional applications [including by not limited to U.S. Provisional Application No. **62/790,822**, filed **January 10, 2019**], divisional applications, reissues, reexaminations, continuation applications, continuation-in-part applications, extensions thereof, design applications, applications in foreign countries, international applications, national phase applications, and the like). I hereby authorize and request the Commissioner of Patents and Trademarks to issue all patents, for the Invention, or patents resulting therefrom, insofar as my interest is concerned, to Assignee.

I also hereby assign and transfer, and hereby agree to assign and transfer to Assignee, all right, title and interest to the Invention, the Application or any related application throughout the world, including all rights of priority resulting from the filing of the Application or any related application, including but not limited to: the right to file applications and obtain patents, utility models, industrial models and designs for the Invention in Assignee's own name throughout the world, the right to claim priority from the Application or any related application when filing applications and obtaining patents, utility models, industrial models and designs for the Invention throughout the world, and all rights to publish cautionary notices reserving ownership of the Invention and all rights to register the Invention in appropriate registries. I further agree to execute any and all powers of attorney, applications, assignments, declarations, affidavits, and any other papers in connection therewith necessary to perfect such right, title and interest in and to Assignee.

I will communicate to Assignee any facts known to me respecting any improvements to the Invention; and I will sign all lawful papers, execute all rightful assignments, execute all provisional, divisional, continuation, continuation-in-part, reissue and substitute applications,

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ASSIGNMENT

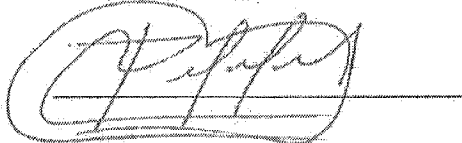
make lawful oaths and declarations; and generally do everything possible to vest title in and to Assignee and to aid Assignee to obtain and enforce proper protection for the Invention in all countries and jurisdictions throughout the world. I irrevocably appoint Analog Devices, Inc. as my attorney-in-fact to execute on my behalf any documents deemed necessary by Analog Devices, Inc. to obtain and enforce proper protection for the Invention in all countries and jurisdictions throughout the world. I agree to testify and cooperate with Assignee in any judicial proceeding, administrative proceeding, post-grant proceeding, or other proceeding concerning the Invention (including litigation with a third party over any patent issued on the Invention), and Assignee agrees to cover my reasonable costs and expenses (if any).

This Assignment shall be binding on the parties' successors, assigns and legal representatives.

Title of Invention: ELECTRICAL OVERSTRESS PROTECTION
WITH LOW LEAKAGE CURRENT FOR
HIGH VOLTAGE TOLERANT HIGH SPEED
INTERFACES

Full name of inventor: Javier A. Salcedo

Inventor's signature:



Date:

March 4, 2019

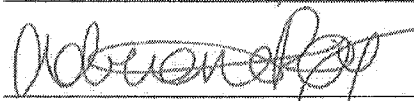
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Title of Invention: ELECTRICAL OVERSTRESS PROTECTION
WITH LOW LEAKAGE CURRENT FOR
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INTERFACES

Full name of inventor: Srivatsan Parthasarathy

Inventor's signature:

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Title of Invention:

ELECTRICAL OVERSTRESS PROTECTION
WITH LOW LEAKAGE CURRENT FOR
HIGH VOLTAGE TOLERANT HIGH SPEED
INTERFACES

Full name of inventor:

Enrique C. Bosch

Inventor's signature:



Date:

03-05-2019

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Witness's Print Name:

MARIA DEL MAR CHAMARRO MARTI

Witness's signature:



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