

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
TEJASVI DAS	12/21/2018
ERIC J. KING	01/18/2019
XIN ZHAO	01/18/2019
XIAOFAN FEI	12/05/2018
JOHANN G. GABORIAU	12/05/2018
LEI ZHU	12/05/2018
JOHN L. MELANSON	12/17/2018
THOMAS HOFF	12/17/2018
RECEIVING PARTY DATA	
Name:	CIRRUS LOGIC INTERNATIONAL SEMICONDUCTOR LTD.
Street Address:	7B NIGHTINGALE WAY
Internal Address:	QUARTERMILE
City:	EDINBURGH
State/Country:	UNITED KINGDOM
Postal Code:	EH3 9EG
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	16190427
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<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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Address Line 4:	AUSTIN, TEXAS 78701
ATTORNEY DOCKET NUMBER:	141841.00989-P3580US00
NAME OF SUBMITTER:	MINDY MAYER

PATENT

SIGNATURE:	/mindy mayer/
DATE SIGNED:	03/05/2019
Total Attachments: 9 source=P3580US00 Assignment#page1.tif source=P3580US00 Assignment#page2.tif source=P3580US00 Assignment#page3.tif source=P3580US00 Assignment#page4.tif source=P3580US00 Assignment#page5.tif source=P3580US00 Assignment#page6.tif source=P3580US00 Assignment#page7.tif source=P3580US00 Assignment#page8.tif source=P3580US00 Assignment#page9.tif	

ASSIGNMENT

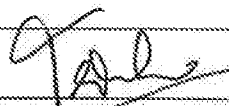
WHEREAS, **TEJASVI DAS**, a citizen of India, residing at 14521 Ballimamore Drive, Austin, Texas 78717, **ERIC J. KING**, a citizen of the United States of America, residing at 2004 Paramount Avenue, Unit A, Austin, Texas 78704, **XIN ZHAO**, a citizen of China, residing at 2600 Golden Gate Park, Austin, Texas 78732, **XIAOFAN FEI**, a citizen of the United States of America, residing at 1912 Trillium Cove, Austin, Texas 78733, **JOHANN G. GABORIAU**, a citizen of France, residing at 360 Nueces Street, #50, Austin, Texas 78701, **LEI ZHU**, a citizen of China, residing at 10832 Pointe View Dr., Austin, Texas 78738, **JOHN L. MELANSON**, a citizen of the United States of America, residing at 901 W. 9th Street, #201, Austin, Texas 78703, and **THOMAS HOFF**, a citizen of the United States of America, residing at 3802 Avenue G, Austin, Texas 78751, hereinafter called the "Assignors", have made the invention described in the United States patent application entitled *Variable Output Resistance in a Playback Path with Open-Loop Pulse-Width Modulation Driver*, Attorney Docket No. 141841.00989-P3580US00, for a full description of which reference is here made to an application for Letters Patent of the United States filed on November 14, 2018 and assigned Application Serial No. 16/190,427; and

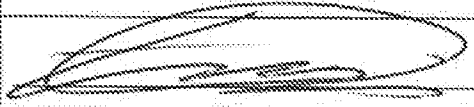
For good and valuable consideration, the receipt and sufficiency of which is acknowledged, I hereby sell, assign, and transfer unto Cirrus Logic International Semiconductor Ltd., a wholly-owned subsidiary of Cirrus Logic, Inc. organized under and pursuant to the laws of Scotland and having its principal place of business at 7B Nightingale Way, Quartermile, Edinburgh, EH3 9EG, United Kingdom, the entire right, title, and interest in and to the invention and all related intellectual property rights (the "Invention") and in and to all applications related to the Invention for Letters Patent in the United States of America, including but not limited to any and all direct and indirect divisions, continuations, continuations-in-part, and reexaminations of said applications and reissues of any resulting patents, and I further authorize and assign all rights to Cirrus Logic International Semiconductor Ltd. to apply for Letters Patent in the United States of America directly in its own name, and to claim priority of the filing date of an application for the earliest-filed patent application(s) for the Invention. Furthermore, for good and valuable consideration, the receipt of which is acknowledged, I hereby also sell, assign, and transfer unto Cirrus Logic International Semiconductor Ltd. the entire right, title, and interest in and to the Invention and in and to all applications related to the Invention for all countries foreign to the United States of America, including but not limited to all rights under any and all international conventions and treaties in respect of the Invention and the applications for Letters Patent, utility models, utility patents, design patents, supplementary protection certificates, patent certificates, and all similar protections in all countries foreign to the United States of America, and I further

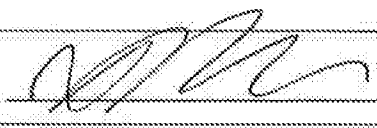
authorize and assign all rights to Cirrus Logic International Semiconductor Ltd. to apply for Letters Patent, utility models, utility patents, and patent certificates in all countries foreign to the United States directly in its own name, and to claim priority of the filing date of an application for the earliest-filed patent application(s) for the Invention under the provisions of any and all laws, international conventions, treaties, or otherwise.

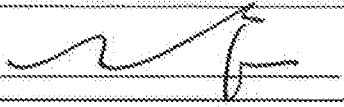
I hereby authorize and request the Commissioner of Patents of the United States of America to issue Letters Patent in the United States of America upon the aforesaid application, and any application related to the aforesaid application in the United States of America, to Cirrus Logic International Semiconductor Ltd., for the sole use and benefit of Cirrus Logic International Semiconductor Ltd., its successors, assigns, and legal representatives, to the full end of the term for which the Letters Patent in the United States of America may be granted, the same as they would have been held and enjoyed by me had this assignment not been made. I hereby authorize and request the equivalent authorities in foreign countries to issue the patents of their respective countries to Cirrus Logic International Semiconductor Ltd.


I agree that, when requested, I will, without charge to Cirrus Logic, Inc. or its subsidiaries, but at its expense, sign all papers, take all rightful oaths, and do all acts which may be necessary, desirable, or convenient for securing and maintaining any and all patent protection (as described above) for the Invention in any and all countries and for respectively vesting title thereto in Cirrus Logic International Semiconductor Ltd., its successors, assigns, and legal representatives or nominees. To the maximum extent allowed by law, I hereby irrevocably designate and appoint Cirrus Logic International Semiconductor Ltd. and its duly authorized officers and agents, or Cirrus Logic International Semiconductor Ltd.'s applicable designee, as my agents and attorneys-in-fact to act for and on my behalf and instead of me to sign all papers, take all rightful oaths, and do all acts described in the foregoing sentence should I refuse or become otherwise unavailable to do so.


NAME OF FIRST INVENTOR	TEJASVI DAS
SIGNATURE OF FIRST INVENTOR	
DATE	12/21/2018

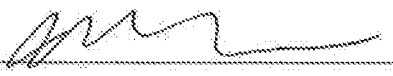
NAME OF SECOND INVENTOR	ERIC J. KING
SIGNATURE OF SECOND INVENTOR	
DATE	JAN 18 2019

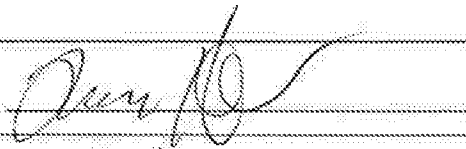
NAME OF THIRD INVENTOR	XIN ZHAO
SIGNATURE OF THIRD INVENTOR	
DATE	JAN 18 2019 JAN 18 2019

NAME OF FOURTH INVENTOR	XIAOFAN FEI
SIGNATURE OF FOURTH INVENTOR	
DATE	DEC 5 2018

NAME OF FIFTH INVENTOR	JOHANN G. GABORIAU
SIGNATURE OF FIFTH INVENTOR	
DATE	12/5/2018

NAME OF SIXTH INVENTOR	LEI ZHU
SIGNATURE OF SIXTH INVENTOR	
DATE	12/05/18

NAME OF SEVENTH INVENTOR	JOHN L. MELANSON
SIGNATURE OF SEVENTH INVENTOR	
DATE	DEC 17 2018

NAME OF EIGHTH INVENTOR	THOMAS HOFF
SIGNATURE OF EIGHTH INVENTOR	
DATE	12/17/2018