PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
SRINIVASAN DASASATHYAN	04/22/2019
PADMINI GOPALAKRISHNAN	04/22/2019
VISHAL TRIPATHI	04/22/2019
VIKAS N. VEDAMURTHY	04/22/2019
SUMIT NAGPAL	04/22/2019

RECEIVING PARTY DATA

Name:	XILINX, INC.
Street Address:	2100 LOGIC DRIVE
City:	SAN JOSE
State/Country:	CALIFORNIA
Postal Code:	95124

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	16397501

CORRESPONDENCE DATA

Fax Number: (408)377-6137

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 408-879-5216 Email: xilinxipl@xilinx.com

Correspondent Name: XILINX, INC.

2100 LOGIC DRIVE Address Line 1: ATTN: LEGAL DEPT. Address Line 2:

SAN JOSE, CALIFORNIA 95124 Address Line 4:

ATTORNEY DOCKET NUMBER:	X-5651 US
NAME OF SUBMITTER:	SUSAN WIENS
SIGNATURE:	/susan wiens/
DATE SIGNED:	04/29/2019
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Total Attachments: 3

PATENT REEL: 049023 FRAME: 0383 source=X_5651_US_Assign_efiled#page1.tif source=X_5651_US_Assign_efiled#page2.tif source=X_5651_US_Assign_efiled#page3.tif

> PATENT REEL: 049023 FRAME: 0384

ASSIGNMENT

For good and valuable consideration, receipt of which is hereby acknowledged, I/we

Sring vasan Dasasathyan of 25 Indraprastha, SBI Colony Transport Rd., Secunderbad, TS 500009, India
Padrnini Gopalakrishnan of 8-2-Plot No 559, Road No 31, Jubilee Hills, Hyderabad, TS 500033, India
Vish al Tripathi of Flat No A710, Aparna Hill Park Avenue, Chandanagar, TS 500050, India
Vika S N. Vedamurthy of 305, A Block, Dollfine Durga County, Madinaguda, Hyderabad, TS 500050, India
Sum it Nagpal of Flat 53, Lotus Block, L & T Serene County, Gachibowli, Hyderabad, TS 500032, India

here by sell, assign and transfer to Xilinx, Inc., a Delaware corporation, having a place of business at 2100 Logic Drive, San Jose, California 95124, its successors and assigns, the entire right, title and interest throughout the world in my/our invention

AUTOMATED ANALYSIS AND OPTIMIZATION OF CIRCUIT DESIGNS

for which I/we will execute a United States patent application on or about the date of this assignment, and all patent applications and patents of every country for said invention, including divisions, reissues, continuations and extensions thereof, and all rights of priority resulting from the filing of said applications; I/we authorize the above-named assignee to apply for patents of foreign countries for said invention, and to claim all rights of priority without further authorization from me; I/we agree to execute all papers useful in connection with said United States and foreign applications, and generally to do everything possible to aid said assignee, its successors, assigns and nominees, at its request and expense, in obtaining and enforcing patents for said invention in all countries; and I/we request the Commissioner of Patents and Trademarks to issue all patents granted for said invention to the above-named assignee, its successors and assigns.

			Srinivasan
(1) WITNESS my hand	SUMIT NA	repa c	
Signature	[Printed Name]	2>-4 Date	xfar-ra
(2) WITNESS my hand	VEENA JOHA [Printed Name]	HR_	
Veera hish Signature	nan .	22/04 Date	119

Executed on 22 April 2019	
	Padmini Gopalalvishvan
	Padmini Gopalakrishnan
(1) WITNESS my hand SUMIT NAC [Printed Name] Signification D. SRINIVA [Printed Name] [Printed Name]	22-Apr-19 Date SAN
[Printed Name]	
0- minu	22-Apr-19
Signature	Date
Executed on 22 nd April 2019	Vishaltripathi Vishal Tripathi
(1) WITNESS my hand SUMIT NAC	22-Apr-19 Date
(2) WITNESS my hand VEENA JOHAR [Printed Name]	
Veen hahran	22-04-2019
Signature	Date .

Executed on 22rd April 2019.
Vikas N. Vedamurthy
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Vishal tripathi. 22/04/2019 Signature Date
Executed on 27 Apr 19 Sunlit Nagpal
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PATENT REEL: 049023 FRAME: 0387

RECORDED: 04/29/2019