

PATENT ASSIGNMENT COVER SHEET

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 Stylesheet Version v1.2

EPAS ID: PAT5537466

SUBMISSION TYPE:	NEW ASSIGNMENT	
NATURE OF CONVEYANCE:	ASSIGNMENT	
CONVEYING PARTY DATA		
Name		Execution Date
INTELLECTUAL VENTURES ASSETS		04/30/2019
RECEIVING PARTY DATA		
Name:	SAMSUNG ELECTRONCS CO., LTD.	
Street Address:	129, SAMSUNG-RO, YEONGTONG-GU, GYEONGGI-DO,	
City:	SUWON-SI	
State/Country:	KOREA, REPUBLIC OF	
Postal Code:	16677	
PROPERTY NUMBERS Total: 27		
Property Type	Number	
Patent Number:	7139213	
Patent Number:	6996658	
Patent Number:	7146452	
Patent Number:	7062581	
Patent Number:	7646760	
Patent Number:	8006024	
Patent Number:	8402197	
Patent Number:	8769181	
Patent Number:	7502029	
Patent Number:	8520016	
Patent Number:	7383149	
Patent Number:	7480588	
Patent Number:	7654736	
Patent Number:	8308359	
Patent Number:	9766135	
Application Number:	15701777	
Patent Number:	7535786	
Patent Number:	7639548	
Patent Number:	8040742	
Patent Number:	7603249	

PATENT

Property Type	Number
Patent Number:	8005641
Patent Number:	8049145
Patent Number:	8497453
Patent Number:	7760570
Patent Number:	8081532
Patent Number:	7720627
Patent Number:	7953573

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	023919
NAME OF SUBMITTER:	INGRID CONTRERAS, TEAMS DEPT.
SIGNATURE:	/Ingrid Contreras/
DATE SIGNED:	05/23/2019

Total Attachments: 5

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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Intellectual Ventures Assets 129 LLC, a Delaware limited liability company, with an address at 251 Little Falls Drive, Wilmington, DE 19808 ("Assignor"), does hereby sell, assign, transfer, and convey unto Samsung Electronics Co., Ltd., a Korean corporation with an address at 129, Samsung-ro, Yeongtong-gu, Suwon-si, Gyeonggi-do, 443-742, Korea ("Assignee"), all of Assignor's right, title, and interest in and to the following (collectively, the "Assigned Patent Rights"):

- (a) the patents and patent applications listed in the table below (the "Patents");

Patent or application no.	Country	Filing Date	Title of Patent and Inventor
7139213	US	2006-11-21	MULTIPLE DATA PATH MEMORIES AND SYSTEMS
(10/844002)		(2004-05-12)	G. R. Mohan Rao

- (b) any future reissues, reexaminations, extensions, continuations, continuing prosecution applications, requests for continuing examinations, divisions, and registrations of any of the Patents;

- (c) rights to apply in any or all countries of the world for future patents, certificates of invention, utility models, industrial design protections, design patent protections, or other future governmental grants or issuances of any type related to the Patents; and

- (d) causes of action and enforcement rights of any kind under, or on account of, any of the Patents and/or any of the items described in either of the foregoing categories (b) or (c), including, without limitation, all causes of action, enforcement rights and all other rights to seek and obtain any other remedies of any kind for past, current and future infringement.

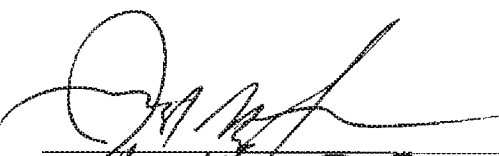
Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all future patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Assigned Patent Rights in the name of Assignee, as the assignee to the entire interest therein. This Assignment of Patent Rights will inure for the benefit of any permitted successors or assigns of Assignee.

Assignor will, at the reasonable request of Assignee, take all reasonable steps necessary and proper, to confirm the assignment to Assignee of the Assigned Patent Rights pursuant to this Assignment of Patent Rights, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining and perfecting the Assigned Patent Rights.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed
on APRIL 30, 2019, to be effective on APRIL 30, 2019.

ASSIGNOR:

INTELLECTUAL VENTURES ASSETS 129 LLC

By: 
Name: Jim Weisberg
Title: AUTHORIZED PERSON

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Intellectual Ventures Assets 124 LLC, a Delaware limited liability company, with an address at 251 Little Falls Drive, Wilmington, DE 19808 ("Assignor"), does hereby sell, assign, transfer, and convey unto Samsung Electronics Co., Ltd., a Korean corporation with an address at 129, Samsung-ro, Yeongtong-gu, Suwon-si, Gyeonggi-do, 443-742, Korea ("Assignee"), all of Assignor's right, title, and interest in and to the following (collectively, the "Assigned Patent Rights"):

(a) the patents and patent applications listed in the table below (the "Patents");

Patent or application no	Country	Filing Date	Title of Patent and Inventor
7480588 (11/708408)	US	2009-01-20 (2007-02-20)	Semiconductor device having variable parameter selection based on temperature and test method Darryl G. Walker
7883573 (12/753411)	US	2011-05-31 (2010-04-02)	SEMICONDUCTOR DEVICE HAVING VARIABLE PARAMETER SELECTION BASED ON TEMPERATURE AND TEST METHOD Darryl G. Walker
7883149 (11/637280)	US	2008-06-03 (2006-12-12)	Semiconductor device having variable parameter selection based on temperature and test method Darryl G. Walker
7854736 (12/315521)	US	2010-02-02 (2006-12-04)	Semiconductor device having variable parameter selection based on temperature and test method Darryl Walker
8040742 (12/635533)	US	2011-10-18 (2009-12-10)	SEMICONDUCTOR DEVICE HAVING VARIABLE PARAMETER SELECTION BASED ON TEMPERATURE AND TEST METHOD Darryl G. Walker
8049145 (11/708174)	US	2011-11-01 (2007-02-20)	SEMICONDUCTOR DEVICE HAVING VARIABLE PARAMETER SELECTION BASED ON TEMPERATURE AND TEST METHOD Darryl Walker
9766135 (13/490890)	US	2017-09-19 (2012-08-07)	Semiconductor device having variable parameter selection based on temperature and test method Darryl G. Walker
8005641 (12/540446)	US	2011-08-23 (2009-05-13)	TEMPERATURE SENSING CIRCUIT WITH HYSTERESIS AND TIME DELAY Darryl G. Walker
8081532 (12/793845)	US	2011-12-20 (2010-06-04)	SEMICONDUCTOR DEVICE HAVING VARIABLE PARAMETER SELECTION BASED ON TEMPERATURE AND TEST METHOD Darryl G. Walker
7502029 (11/233479)	US	2009-03-10 (2006-01-17)	Instruction Folding Mechanism, Method For Performing The Same And Pixel Processing System Employing The Same R-ming Hsu
8520018 (12/400127)	US	2013-08-27 (2009-03-09)	Instruction Folding Mechanism, Method For Performing The Same And Pixel Processing System Employing The Same R-ming Hsu
7639546 (12/391764)	US	2009-12-29 (2009-02-24)	Semiconductor Device Having Variable Parameter Selection Based on Temperature and Test Method

			Darryl G. Walker
7783870 (11/708793)	US	2010-07-20 (2007-02-21)	SEMICONDUCTOR DEVICE HAVING VARIABLE PARAMETER SELECTION BASED ON TEMPERATURE AND TEST METHOD Darryl Walker
7720627 (12/150081)	US	2010-05-18 (2008-04-24)	Semiconductor device having variable parameter selection based on temperature and test method Darryl Walker
8497453 (13/235754)	US	2013-07-30 (2011-09-19)	Semiconductor device having variable parameter selection based on temperature and test method Darryl G. Walker
8006024 (12/843385)	US	2011-08-23 (2009-12-21)	Multi-port system and method for routing a data element within an interconnection fabric Nathan J. Dohm
7603249 (11/708184)	US	2009-10-13 (2007-02-20)	Semiconductor Device Having Variable Parameter Selection Based on Temperature and Test Method Darryl G. Walker
8308359 (12/888682)	US	2012-11-13 (2010-01-16)	SEMICONDUCTOR DEVICE HAVING VARIABLE PARAMETER SELECTION BASED ON TEMPERATURE AND TEST METHOD Darryl G. Walker
6996658 (10/152658)	US	2008-02-07 (2002-05-21)	Multi-port system and method for routing a data element within an interconnection fabric Nathan J. Dohm
7646760 (10/945633)	US	2010-01-12 (2004-09-21)	Multi-port system and method for routing a data element within an interconnection fabric David E. Mayhew
7535786 (11/708185)	US	2009-06-19 (2007-02-20)	Semiconductor device having variable parameter selection based on temperature and test method Darryl G. Walker
8402197 (13/215011)	US	2013-03-19 (2011-08-22)	Multi-port system and method for routing a data element within an interconnection fabric Lynne M. Brocco
8769181 (13/846515)	US	2014-07-01 (2013-03-18)	Multi-port system and method for routing a data element within an interconnection fabric Todd R. Comins
7082581 (10/945615)	US	2006-06-13 (2004-09-21)	Interconnection fabric enumeration Nathan J. Dohm
7148452 (11/271273)	US	2006-12-05 (2005-11-12)	Multi-port system and method for routing a data element within an interconnection fabric Nathan J. Dohm
(15/701777)	US	(2017-09-12)	Semiconductor device having variable parameter selection based on temperature and test method Darryl G. Walker

(b) any future reissues, reexaminations, extensions, continuations, continuing prosecution application, requests for continuing examinations, divisions, and registrations of any of the Patents;

(c) rights to apply in any or all countries of the world for future patents, certificates of invention, utility models, industrial design protections, design patent protections, or other future governmental grants or issuances of any type related to the Patents; and

(d) causes of action and enforcement rights of any kind under, or on account of, any of the Patents and/or any of the items described in either of the foregoing categories (b) or (c), including, without limitation, all causes of action, enforcement rights and all other rights to seek and obtain any other remedies of any kind for past, current and future infringement.

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all future patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Assigned Patent Rights in the name of Assignee, as the assignee to the entire interest therein. This Assignment of Patent Rights will inure for the benefit of any permitted successors or assigns of Assignee.

Assignor will, at the reasonable request of Assignee, take all reasonable steps necessary and proper, to confirm the assignment to Assignee of the Assigned Patent Rights pursuant to this Assignment of Patent Rights, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining and perfecting the Assigned Patent Rights.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed
on APRIL 30, 2019, to be effective on APRIL 30, 2019.

ASSIGNOR:

INTELLECTUAL VENTURES ASSETS 124 LLC

By: 

Name: JIM WESTFIELD

Title: AUTHORIZED PERSON