## 505645125 08/28/2019

# PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

#### **CONVEYING PARTY DATA**

Name	Execution Date
CAVIUM, LLC	08/23/2019

## **RECEIVING PARTY DATA**

Name:	MARVELL SEMICONDUCTOR, INC.	
Street Address:	5488 MARVELL LANE	
City:	SANTA CLARA	
State/Country:	Intry: CALIFORNIA	
Postal Code:	tal Code: 95054	

## **PROPERTY NUMBERS Total: 2**

Property Type	Number
Application Number:	62770293
Application Number:	16209709

### **CORRESPONDENCE DATA**

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ATTORNEY DOCKET NUMBER:	MP12850PR_MP12850
NAME OF SUBMITTER:	KRISTINA FAY
SIGNATURE:	/KRISTINA FAY/
DATE SIGNED:	08/28/2019

#### **Total Attachments: 3**

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PATENT 505645125 REEL: 050203 FRAME: 0149

PATENT

# ASSIGNMENT Corporate

WHEREAS Cavium, LLC, a California corporation, having a principal place of business at 5488 Marvell Lane, Santa Clara, CA 95054 (hereafter the "Assignor"), is the owner of the patents, including utility models and other similar registrations, or patent applications identified below (hereafter referred to as the "Patent Application(s)").

Marvell Semiconductor, Inc., a corporation of California, having a place of business at 5488 Marvell Lane, Santa Clara, CA 95054 (hereafter the "Assignee"), desires to acquire all of the rights to the Patent Application(s).

THEREFORE, in consideration of the sum of One Dollar (\$1.00) US or equivalent and other good and valuable consideration paid to it, the receipt and sufficiency of which hereby is acknowledged, Assignor hereby sells, assigns and transfers to the Assignee the entire rights, title, and interest, together with all priority rights, in and to the Patent Application(s), and all inventions described and claimed therein, in any and all patents including utility models and other similar registrations granted from the Patent Application(s) and from patents or patent applications claiming priority directly or indirectly from the Patent Application(s), including but not limited to any and all reissues, renewals, reexaminations, divisions, and continuations in whole and in part, and foreign counterparts of the foregoing including national stage applications to the full end of the term or terms for which such patents issue in the United States, and/or any foreign countries, such entire rights, title and interest to be held and enjoyed by the above-named Assignee the same as they would have been held and enjoyed by the Assignor had this assignment not been made, including the right to sue and recover damages for past, present and future infringement.

Marvell Reference No.	Application No.	Filing Date	Title
MP12850FR	62/770,293	November 21, 2018	Glitch-Free PLL Multiplexer
MP12850	16/209,709	December 4, 2018	Glitch-Free PLL Multiplexer

Assignor hereby covenants and agrees to execute and deliver, at the request of Assignee, such further instruments of transfer and assignment and to take any other action as such Assignee may reasonably request to more effectively consummate the assignment contemplated by this agreement. Specifically, Assignor agrees to, at Assignee's expense, execute, acknowledge and deliver such further

documents, instruments, conveyances and assurances and take such further actions as may be reasonably required to register in the name of Assignee the assignment of the patents in any appropriate governmental agency or registrar.

The undersigned representatives of the Assignor and the Assignee authorize and request that the attorneys of record in any of the Patent Application(s) insert in this assignment the filing date(s) and application number(s) of the Patent Application(s) when officially known.

IN WITNESS WHEREOF, executed by the Assignor's undersigned representative on the date following the name of the Assignor's undersigned representative, which date shall be the effective date of this assignment.

CAVIUM, LLC
By: ////////
Name: Mitchell Gaynor
Title: Director, EVP, CALO & Secretary
Date: <u>August 23, 2019</u>
<i>*</i>
MARVELL SEMICONDUCTOR, INC.
By:
Name: Kelvin M. Vivian
Title: Director, Assistant General Counsel, IP Development
Date: <u>AUGUST</u> 25 2019

**RECORDED: 08/28/2019**