

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SHIVAM PRIYADARSHI	09/05/2019
YUSUF CAGATAY TEKME	09/05/2019
RODNEY WAYNE SMITH	09/05/2019
VIGNYAN REDDY KOTHINTI NARESH	09/05/2019
RECEIVING PARTY DATA	
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PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	16562591
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NAME OF SUBMITTER:	KAT M. KENNY
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DATE SIGNED:	09/06/2019
Total Attachments: 8	
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I Shivam PRIYADARSHI ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "MINIMIZING TRAVERSAL OF A PROCESSOR REORDER BUFFER (ROB) FOR REGISTER RENAME MAP TABLE (RMT) STATE RECOVERY FOR INTERRUPTED INSTRUCTION RECOVERY IN A PROCESSOR" ("APPLICATION"), which:

will be filed. ASSIGNOR hereby authorizes, and requests, ASSIGNEE'S legal representative, Microsoft Technology Licensing, LLC, One Microsoft Way, Redmond, Washington 98052 (ASSIGNEE'S LEGAL REPRESENTATIVES), to insert here in parentheses (filed on _____ and given Application No. _____ by the following Office US - United States Patent and Trademark Office) this APPLICATION's Application No., filing date, and Office, when known;

was filed on _____ and was given Application No. _____ by the following Office "_____";

Microsoft Technology Licensing, LLC, a Washington limited liability company, on behalf of itself and its successors and assigns ("ASSIGNEE"), is entitled to, and is desirous of acquiring, the entire and exclusive rights, title and interest, including the right of priority, in the INVENTION and the APPLICATION (and all other applications and patents derived therefrom, such as continuing applications, in whole or in part, in and for the United States, its territories, and all foreign countries ("APPLICATION DERIVATIVES"));

ASSIGNOR hereby authorizes, and requests, ASSIGNEE'S LEGAL REPRESENTATIVES to insert the Application No., filing date, and Office into the following table for any APPLICATION DERIVATIVES after they are known;

Application No.	Filing Date	Office

For good and valuable consideration, the receipt of which is hereby acknowledged by the ASSIGNOR, the ASSIGNOR hereby sells, assigns and transfers to the ASSIGNEE, ASSIGNOR's entire and exclusive rights, title and interest, including the right of priority, in the INVENTION and the APPLICATION (and all other applications and patents derived therefrom, such as continuing applications, in whole or in part, in and for the United States, its territories, and all foreign countries ("APPLICATION DERIVATIVES"));

ASSIGNOR agrees to execute all instruments and documents required for the making and prosecution of the APPLICATION (and APPLICATION DERIVATIVES), for litigation regarding letters patent derived therefrom, and for the purpose of protecting and perfecting title to the APPLICATION (and APPLICATION DERIVATIVES).

<u>Shivam</u>	<u>03/05/2015</u>
Inventor's Signature	Date
<u>Shivam PRIYADARSHI</u>	<u></u>
Printed Name in English	Printed Name in Native Language (if other than English)

I Yusuf Cagatay TEKMEN ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "MINIMIZING TRAVERSAL OF A PROCESSOR REORDER BUFFER (ROB) FOR REGISTER RENAME MAP TABLE (RMT) STATE RECOVERY FOR INTERRUPTED INSTRUCTION RECOVERY IN A PROCESSOR" ("APPLICATION"), which:

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	<u>09.05.2019</u>
Inventor's Signature	Date
<u>Yusuf Cagatay TEKME</u>	
Printed Name in English	Printed Name in Native Language (if other than English)

I Rodney Wayne SMITH ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "MINIMIZING TRAVERSAL OF A PROCESSOR REORDER BUFFER (ROB) FOR REGISTER RENAME MAP TABLE (RMT) STATE RECOVERY FOR INTERRUPTED INSTRUCTION RECOVERY IN A PROCESSOR" ("APPLICATION"), which:

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<u><i>Rodney Wayne Smith</i></u>	<u>9/5/2019</u>
Inventor's Signature	Date
<u>Rodney Wayne SMITH</u>	<u></u>
Printed Name in English	Printed Name in Native Language (if other than English)

I Vignyan Reddy KOTHINTI NARESH ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "MINIMIZING TRAVERSAL OF A PROCESSOR REORDER BUFFER (ROB) FOR REGISTER RENAME MAP TABLE (RMT) STATE RECOVERY FOR INTERRUPTED INSTRUCTION RECOVERY IN A PROCESSOR" ("APPLICATION"), which:

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	<u>09/05/2019</u>
Inventor's Signature	Date
<u>Vignyan Reddy KOTHINTI NARESH</u>	
Printed Name in English	Printed Name in Native Language (if other than English)