505662054 09/09/2019

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
ANDREA MIELE	09/09/2019

RECEIVING PARTY DATA

Name:	NVIDIA CORPORATION
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State/Country:	CALIFORNIA
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PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	16559424

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ATTORNEY DOCKET NUMBER:	0112912-050US0
NAME OF SUBMITTER:	PHILIP H. ALBERT
SIGNATURE:	/Philip H. Albert/
DATE SIGNED:	09/09/2019
	This document serves as an Oath/Declaration (37 CFR 1.63).

Total Attachments: 4

source=2019-09-06 19-SC-0204US01 (0112912-050US0) Assignment and Declaration (Signed)#page1.tif source=2019-09-06 19-SC-0204US01 (0112912-050US0) Assignment and Declaration (Signed)#page2.tif source=2019-09-06 19-SC-0204US01 (0112912-050US0) Assignment and Declaration (Signed)#page3.tif source=2019-09-06 19-SC-0204US01 (0112912-050US0) Assignment and Declaration (Signed)#page4.tif

PATENT REEL: 050317 FRAME: 0819 505662054

U.S. Patent Appln. No. 16/559,424

Attorney Docket No. 0112912-050US0

Filing Date September 3, 2019

Client Docket No. 19-SC-0204US01

PARTIES TO THE ASSIGNMENT

<u>Assignor</u>

Andrea Miele 2788 San Tomas Expressway Santa Clara, CA 95051

Assignee:

NVIDIA Corporation 2788 San Tomas Expressway Santa Clara, CA 95051

AGREEMENT

WHEREAS, ASSIGNOR(S) (listed above) are inventor(s) of an invention entitled PERFORMING CYCLIC REDUNDANCY CHECKS USING PARALLEL COMPUTING ARCHITECTURES (Invention) for which a non-provisional application for United States Letters Patent (Application): was filed on September 3, 2019, and accorded U.S. Application No. 16/559,424; or will be filed concurrently with the submission of this executed PATENT ASSIGNMENT for recordation.

ASSIGNOR(S) hereby authorizes and requests ASSIGNEE'S legal representatives, of Davis Wright Tremaine LLP, associated with Customer No. 119055, to insert in the header above and here in parentheses (U.S. Patent Application No. 16/559,424, filed September 3, 2019) this application's U.S. application number and filing date, when known.

WHEREAS, ASSIGNEE, a corporation of the State of Delaware, is desirous of acquiring the entire right, title and interest in and to the Invention and in and to any letters patent that may be granted therefor in the United States and in any and all foreign countries.

NOW, THEREFORE, in exchange for good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNOR(S) hereby sell, assign and transfer unto ASSIGNEE its successors and assigns, the entire right, title and interest in and to said Invention and any improvements thereto, said Application and any and all letters patent which may be granted for

> Page 1 of 4 PATENT REEL: 050317 FRAME: 0820

U.S. Patent Appln. No. 16/559,424

Attorney Docket No. 0112912-050US0

Filing Date September 3, 2019

Client Docket No. 19-SC-0204US01

Title of Invention: PERFORMING CYCLIC REDUNDANCY CHECKS USING PARALLEL COMPUTING

ARCHITECTURES

Inventor: Andrea Miele

said Invention in the United States of America and its territorial possessions and in any and all

foreign countries, and in any and all provisional, divisions, reissues, re-examinations and

continuations thereof, including the right to file foreign applications directly in the name of

ASSIGNEE and to claim priority rights deriving from said Application to which said foreign

applications are entitled by virtue of international convention, treaty or otherwise, said Invention,

Application and all letters patent on said Invention to be held and enjoyed by ASSIGNEE and its

successors and assigns for their use and benefit and of their successors and assigns as fully and

entirely as the same would have been held and enjoyed by ASSIGNOR(S) had this assignment,

transfer and sale not been made. ASSIGNOR(S) hereby authorize and request the Commissioner

of Patents and Trademarks, as well as all other applicable patent offices throughout the world, to

issue all letters patent on said Invention to ASSIGNEE. ASSIGNOR(S) agree to execute all

instruments and documents required for the making and prosecution of applications for United

States and foreign letters patent on said Invention, for litigation regarding said letters patent, or

for the purpose of protecting title to said Invention or letters patent therefor.

AND ASSIGNOR(S) DOES HEREBY sell, assign, transfer, and convey to ASSIGNEE, its

successors, legal representatives, and assigns all claims for damages and all remedies arising out

of any violation of the rights assigned hereby that may have accrued prior to the date of

assignment to ASSIGNEE, or may accrue hereafter, including, but not limited to, the right to sue

for, collect, and retain damages for past infringements of said letters patent before or after

issuance.

PATENT REEL: 050317 FRAME: 0821

U.S. Patent Appln. No. 16/559,424

Attorney Docket No. 0112912-050US0

Filing Date September 3, 2019

Client Docket No. 19-SC-0204US01

Title of Invention: PERFORMING CYCLIC REDUNDANCY CHECKS USING PARALLEL COMPUTING

ARCHITECTURES

Inventor: Andrea Miele

AND ASSIGNOR(S) DOES HEREBY covenant and agree that ASSIGNOR(S) will communicate

to ASSIGNEE, its successors, legal representatives and assigns, any facts known to ASSIGNOR

respecting the Invention or said Application, and testify in any legal proceeding, assist in the

preparation of any other patent property relating to the Application and the Invention or any

improvements made thereto, sign/execute all lawful papers, provide all requested documents,

execute and make all rightful oaths and/or declarations in connection with the Application and

the Invention including any improvements made thereto, any patent applications filed therefrom,

and any continuing application filed from any of the aforementioned applications, and generally

do everything possible to aid ASSIGNEE, its successors, legal representatives and assigns, to obtain

and enforce proper patent protection for the Invention in all countries. These provisions are

binding upon ASSIGNOR's heirs, legal representatives, administrators, and assigns.

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PATENT

REEL: 050317 FRAME: 0822

Client Docket No. <u>19 SC-9204US91</u> S USING PARALLEL COMPUTING
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RECORDED: 09/09/2019