

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
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EPAS ID: PAT5391856

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
PAKAL TECHNOLOGIES, LLC	12/21/2018
RECEIVING PARTY DATA	
Name:	PAKAL TECHNOLOGIES, INC.
Street Address:	220 JACKSON STREET
Internal Address:	SUITE 2000
City:	SAN FRANCISCO
State/Country:	CALIFORNIA
Postal Code:	94111
PROPERTY NUMBERS Total: 17	
Property Type	Number
Patent Number:	8878238
Patent Number:	8569117
Patent Number:	8742456
Patent Number:	8803191
Patent Number:	8878237
Patent Number:	9306048
Patent Number:	9082648
Patent Number:	9806152
Patent Number:	9391184
Patent Number:	RE47072
Patent Number:	9806181
Patent Number:	10181509
Patent Number:	9935188
Patent Number:	7705368
Application Number:	16210762
Application Number:	15910482
Application Number:	15632169
CORRESPONDENCE DATA	

Fax Number: (650)396-5550

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 6509658005

Email: sandy@patentlawgroup.com

Correspondent Name: PATENT LAW GROUP LLP

Address Line 1: PO BOX 61839

Address Line 4: SUNNYVALE, CALIFORNIA 94088

NAME OF SUBMITTER:	BRIAN D. OGONOWSKY
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SIGNATURE:	/Brian D Ogonowsky/
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DATE SIGNED:	02/25/2019
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Total Attachments: 5

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PATENT ASSIGNMENT AGREEMENT

This PATENT ASSIGNMENT AGREEMENT ("**Patent Assignment**"), dated as of ^{December} ~~November~~ 21, 2018, is made by and between Pakal Technologies, LLC ("**Assignor**"), a Delaware limited liability company, located at 583 Dolores St., San Francisco, CA 94110, and Pakal Technologies, Inc. ("**Assignee**"), a Delaware corporation, located at 220 Jackson Street, Suite 2000, San Francisco, CA.

NOW THEREFORE, Assignor and Assignee agree as follows:

1. Assignment. For good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, Assignor hereby irrevocably conveys, transfers, and assigns to Assignee, and Assignee hereby accepts, all of Assignor's right, title, and interest in and to the following:

(a) the patents and patent applications set forth in Schedule 1 hereto and all issuances, divisions, continuations, continuations-in-part, reissues, extensions, reexaminations, and renewals thereof;

(b) all rights of any kind whatsoever of Assignor accruing under any of the foregoing provided by applicable law of any jurisdiction, by international treaties and conventions, and otherwise throughout the world;

(c) any and all royalties, fees, income, payments, and other proceeds now or hereafter due or payable with respect to any and all of the foregoing; and

(d) any and all claims and causes of action with respect to any of the foregoing, whether accruing before, on, or after the date hereof, including all rights to and claims for damages, restitution, and injunctive and other legal and equitable relief for past, present, and future infringement, misappropriation, violation, misuse, breach, or default, with the right but no obligation to sue for such legal and equitable relief and to collect, or otherwise recover, any such damages.

2. Recordation. Assignor hereby authorizes the Commissioner for Patents in the United States Patent and Trademark Office to record and register this Patent Assignment upon request by Assignee.

3. Successors and Assigns. This Patent Assignment shall be binding upon and shall inure to the benefit of the parties hereto and their respective successors and assigns.

[SIGNATURE PAGE FOLLOWS]

ACKNOWLEDGMENT

A notary public or other officer completing this certificate verifies only the identity of the individual who signed the document to which this certificate is attached, and not the truthfulness, accuracy, or validity of that document.

State of California
County of SAN FRANCISCO

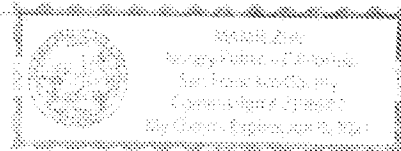
On December 21, 2018 before me, MAMIE ZHU (NOTARY PUBLIC)
(insert name and title of the officer)

personally appeared BENJAMIN BROWN QUINONES
who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

Signature _____ (Seal)



IN WITNESS WHEREOF, Assignor has duly executed and delivered this Patent Assignment as of the date first above written.

Pakal Technologies LLC

By: [Signature]

Name: Benjamin Quinonez

Title: Managing Member

ACKNOWLEDGMENT

STATE OF [STATE] California)
)SS.

COUNTY OF [COUNTY] San Francisco)

On the 21st [ORDINAL NUMBER] day of [MONTH], [YEAR], before me personally appeared [SIGNATORY NAME], personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the foregoing instrument, who, being duly sworn, did depose and say that [he/she] executed the same [in [his/her] authorized capacity as the [SIGNATORY TITLE] of [SELLER], the [TYPE OF ENTITY] described], and acknowledged the instrument to be [[his/her] free act and deed/the free act and deed of [SELLER]] for the uses and purposes mentioned in the instrument.

Notary Public

Printed Name:

My Commission Expires: [DATE]

SCHEDULE 1

ASSIGNED PATENTS AND PATENT APPLICATIONS

Patents

Title	Jurisdiction	Patent Number	Issue Date
INSULATED GATE TURN-OFF DEVICE WITH TURN-OFF TRANSISTOR	United States	RE47072	October 2, 2018
INSULATED GATE TURN-OFF DEVICE WITH TURN-OFF SCHOTTKY-BARRIER MOSFET	United States	9935188	April 3, 2018
VERTICAL INSULATED GATE TURN-OFF THYRISTOR WITH INTERMEDIATE P+ LAYER IN P-BASE	United States	9806152	October 31, 2017
INSULATED GATE POWER DEVICE USING A MOSFET FOR TURNING OFF	United States	9806181	October 31, 2017
INSULATED GATE TURN-OFF DEVICE WITH TURN-OFF TRANSISTOR	United States	9391184	July 12, 2016
DUAL DEPTH TRENCH-GATED MOS-CONTROLLED THYRISTOR WITH WELL-DEFINED TURN-ON CHARACTERISTICS	United States	9306048	April 5, 2016
VERTICAL INSULATED-GATE TURN-OFF DEVICE HAVING A PLANAR GATE	United States	9082648	July 14, 2015
ACTIVE EDGE STRUCTURES PROVIDING UNIFORM CURRENT FLOW IN INSULATED GATE TURN-OFF THYRISTORS	United States	8878237	November 4, 2014
MCT DEVICE WITH BASE-WIDTH-DETERMINED LATCHING AND NON-LATCHING STATES	United States	8878238	November 4, 2014
SYSTEMS, DEVICES, AND METHODS WITH INTEGRABLE FET-CONTROLLED LATERAL THYRISTORS	United States	8803191	August 12, 2014
SYSTEMS AND METHODS INTEGRATING TRENCH-GATED THYRISTOR WITH TRENCH-GATED RECTIFIER	United States	8569117	October 29, 2013
INSULATED GATE TYPE THYRISTOR	United States	7705368	April 27, 2010

Patent Applications

Title	Jurisdiction	Application Number	Filing Date
INSULATED GATE TURN-OFF DEVICE HAVING LOW CAPACITANCE AND LOW SATURATION CURRENT	United States	15910482	March 2, 2018
INSULATED GATE TURN-OFF DEVICE WITH HOLE INJECTOR FOR FASTER TURN OFF	United States	15632169	June 23, 2017
INSULATED GATE POWER DEVICES WITH REDUCED CARRIER INJECTION IN TERMINATION AREA	United States	15228788	August 4, 2016