

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT5733189

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
JEROME F. DULUK, JR.	09/17/2019
GREGORY SCOTT PALMER	09/17/2019
JONATHON STUART RAMSEY EVANS	09/17/2019
SHAILENDRA SINGH	09/17/2019
SAMUEL H. DUNCAN	09/18/2019
WISHWESH ANIL GANDHI	09/19/2019
LACKY V. SHAH	09/17/2019
ERIC ROCK	09/18/2019
FEIQI SU	09/17/2019
JAMES LEROY DEMING	09/18/2019
ALAN MENEZES	09/18/2019
PRANAV VAIDYA	09/18/2019
PRAVEEN JOGINIPALLY	09/20/2019
TIMOTHY JOHN PURCELL	09/21/2019
MANAS MANDAL	09/17/2019

RECEIVING PARTY DATA

Name:	NVIDIA CORPORATION
Street Address:	2701 SAN TOMAS EXPRESSWAY
City:	SANTA CLARA
State/Country:	CALIFORNIA
Postal Code:	95050

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	16562361

CORRESPONDENCE DATA

Fax Number: (408)715-1201

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 4087151211

PATENT

Email: jranieri@artegislaw.com, Algdocketing@artegislaw.com
Correspondent Name: ARTEGIS LAW GROUP LLC
Address Line 1: 710 LAKEWAY DRIVE, SUITE 185
Address Line 4: SUNNYVALE, CALIFORNIA 94085

ATTORNEY DOCKET NUMBER: NVDA19SC0036US2

NAME OF SUBMITTER: JOHN C. CAREY

SIGNATURE: /John C. Carey/

DATE SIGNED: 09/23/2019

This document serves as an Oath/Declaration (37 CFR 1.63).

Total Attachments: 32

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ASSIGNMENT FOR APPLICATION FOR PATENT WITH DECLARATION STATEMENT

WHEREAS:

Names and Addresses of Inventors:

1)	Jerome F. Duluk, Jr. Palo Alto, CA	2)	Gregory Scott Palmer Cedar Park, TX
3)	Jonathon Stuart Ramsey Evans Santa Clara, CA	4)	Shailendra Singh Fremont, CA
5)	Samuel H. Duncan Arlington, MA	6)	Wishwesh Anil Gandhi Sunnyvale, CA
7)	Lacky V. Shah Los Altos, CA	8)	Eric Rock San Jose, CA
9)	Feiqi Su Cupertino, CA	10)	James Leroy Deming Madison, AL
11)	Alan Menezes San Jose, CA	12)	Pranav Vaidya San Jose, CA
13)	Praveen Joginipally San Jose, CA	14)	Timothy John Purcell Melbourne, Victoria Australia
15)	Manas Mandal Palo Alto, CA		

(hereinafter referred to as Assignors), have invented a certain invention entitled:

TECHNIQUES FOR CONFIGURING A PROCESSOR TO FUNCTION AS MULTIPLE, SEPARATE PROCESSORS

enclosed herewith or for which application for Letters Patent in the United States was filed on
September 5, 2019 under Serial No. 16/562,361; and

WHEREAS, **NVIDIA CORPORATION**, a corporation of the State of Delaware, having a place of business at 2701 San Tomas Expressway, Santa Clara, California 95050 (hereinafter referred to as Assignee), is desirous of acquiring the entire right, title and interest in and to said application (hereinafter referred to as Application), and the invention disclosed therein (hereinafter referred to as Invention), and in and to all embodiments of the Invention, heretofore conceived, made or discovered by said Assignors, and in and to any and all patents, inventor's certificates and other forms of protection (hereinafter referred to as Patents) thereon granted in any and all countries and groups of countries.

NOW, THEREFORE, in consideration of good and valuable consideration acknowledged by said Assignors to have been received in full from said Assignee:

1. Said Assignors hereby sell, assign, transfer and convey to Assignee the full and exclusive right, title and interest (a) in and to said Application and said Invention; (b) in and to all rights to apply for patents on said Invention in any and all countries pursuant to the International Convention for the Protection of Industrial Property or otherwise; (c) in and to any and all Applications filed and any and all Patents granted on said Invention in any and all countries and groups of countries, including each and every Application filed and each and every Patent granted on any application which is a conventional, division, substitution, or continuation of said Application; and (d) in and to each and every reissue or extension of any of said Patents.

2. Said Assignors hereby covenant and agree to cooperate with said Assignee to enable said Assignee to enjoy to the fullest extent the right, title and interest to said Invention herein conveyed in any and all countries and groups of countries. Such cooperation by said Assignors shall include prompt production of pertinent facts and documents, giving testimony, execution of petitions, oaths, specifications, declarations or other papers, and other assistance all to the extent deemed necessary or desirable by said Assignee (a) for perfecting in said Assignee the right, title and interest herein conveyed; (b) for prosecuting any of said applications; (c) for filing and prosecuting substitute, conventional, divisional, continuing or additional applications covering said Invention; (d) for filing and prosecuting applications for reissuance of any of said Patents; (e) for interference or other priority proceedings involving said Invention; and (f) for legal proceedings involving said Invention and any application therefore and any Patents granted thereon, including without limitation opposition proceedings, cancellation proceedings, priority contests, public use proceedings, infringement actions and court actions; provided, however, that the expense incurred by said Assignors in providing such cooperation shall be paid for by said Assignee.

3. The term and covenants of this agreement shall inure to the benefit of said Assignee, its successors, assigns and other legal representatives, and shall be binding upon said Assignors, their respective heirs, legal representatives and assigns.

4. Said Assignors hereby warrant and represent that they have not entered and will not enter into any assignment, contract, or understanding in conflict herewith.

5. In the event that the filing date and/or Application No. of said Invention are not entered at the time Assignors execute this document, and if such information is deemed necessary, Assignors hereby authorize and request the attorney/agent(s) of **Artegis Law Group, LLP**, to insert the filing date and/or Application No. of said application where needed.

6. This declaration is directed to the attached application, or (if following box is checked):

☒ United States application or PCT international application number
16/562,361 filed on September 5, 2019.

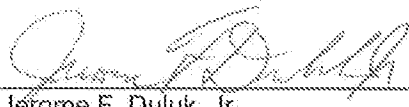
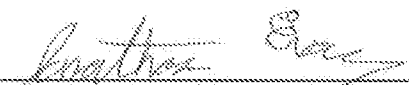
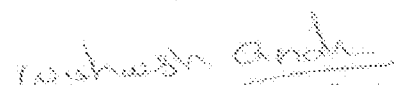
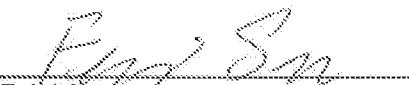
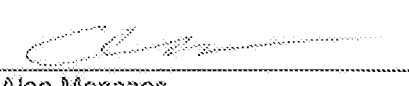
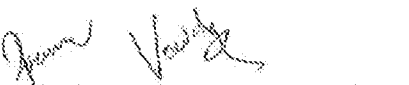
As a below named inventor, I hereby declare that:

The above-identified application was made or authorized to be made by me;

I believe that I am the original inventor or an original joint inventor of a claimed invention in the application; and

I acknowledge that any willful false statement made in this declaration is punishable under section 1001 of title 18 by fine or imprisonment of not more than 5 years, or both.

IN WITNESS WHEREOF, the said Assignors have executed and delivered this instrument to said Assignee on the dates indicated below.

- 1) Sep 17, 2019 (DATE) 
Jerome F. Duluk, Jr.
- 2) _____ (DATE) _____
Gregory Scott Palmer
- 3) 9/17/19 (DATE) 
Jonathon Stuart Ramsey Evans
- 4) _____ (DATE) _____
Shailendra Singh
- 5) _____ (DATE) _____
Samuel H. Duncan
- 6) 9/19/2019 (DATE) 
Wishwesh Anil Gandhi
- 7) _____ (DATE) _____
Lucky V. Shah
- 8) _____ (DATE) _____
Eric Rock
- 9) 9/17/19 (DATE) 
Feiqi Su
- 10) _____ (DATE) _____
James Leroy Deming
- 11) 9/18/19 (DATE) 
Alan Menezes
- 12) 9/18/19 (DATE) 
Pranav Vaidya
- 13) _____ (DATE) _____
Praveen Joginipally

14) _____ (DATE)

Timothy John Purcell

15) Sep 17, 2019 (DATE)

M. Mandal
Manas Mandal

ASSIGNMENT FOR APPLICATION FOR PATENT WITH DECLARATION STATEMENT

WHEREAS:

Names and Addresses of Inventors:

1)	Jerome F. Duluk, Jr. Palo Alto, CA	2)	Gregory Scott Palmer Cedar Park, TX
3)	Jonathon Stuart Ramsey Evans Santa Clara, CA	4)	Shailendra Singh Fremont, CA
5)	Samuel H. Duncan Arlington, MA	6)	Wishwesh Anil Gandhi Sunnyvale, CA
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13)	Praveen Joginipally San Jose, CA	14)	Timothy John Purcell Melbourne, Victoria Australia
15)	Manas Mandal Palo Alto, CA		

(hereinafter referred to as Assignors), have invented a certain invention entitled:

TECHNIQUES FOR CONFIGURING A PROCESSOR TO FUNCTION AS MULTIPLE, SEPARATE PROCESSORS

enclosed herewith or for which application for Letters Patent in the United States was filed on
September 5, 2019 under Serial No. 16/562,361; and

WHEREAS, **NVIDIA CORPORATION**, a corporation of the State of Delaware, having a place of business at 2701 San Tomas Expressway, Santa Clara, California 95050 (hereinafter referred to as Assignee), is desirous of acquiring the entire right, title and interest in and to said application (hereinafter referred to as Application), and the invention disclosed therein (hereinafter referred to as Invention), and in and to all embodiments of the Invention, heretofore conceived, made or discovered by said Assignors, and in and to any and all patents, inventor's certificates and other forms of protection (hereinafter referred to as Patents) thereon granted in any and all countries and groups of countries.

NOW, THEREFORE, in consideration of good and valuable consideration acknowledged by said Assignors to have been received in full from said Assignee:

1. Said Assignors hereby sell, assign, transfer and convey to Assignee the full and exclusive right, title and interest (a) in and to said Application and said Invention; (b) in and to all rights to apply for patents on said Invention in any and all countries pursuant to the International Convention for the Protection of Industrial Property or otherwise; (c) in and to any and all Applications filed and any and all Patents granted on said Invention in any and all countries and groups of countries, including each and every Application filed and each and every Patent granted on any application which is a conventional, division, substitution, or continuation of said Application; and (d) in and to each and every reissue or extension of any of said Patents.

2. Said Assignors hereby covenant and agree to cooperate with said Assignee to enable said Assignee to enjoy to the fullest extent the right, title and interest to said Invention herein conveyed in any and all countries and groups of countries. Such cooperation by said Assignors shall include prompt production of pertinent facts and documents, giving testimony, execution of petitions, oaths, specifications, declarations or other papers, and other assistance all to the extent deemed necessary or desirable by said Assignee (a) for perfecting in said Assignee the right, title and interest herein conveyed; (b) for prosecuting any of said applications; (c) for filing and prosecuting substitute, conventional, divisional, continuing or additional applications covering said Invention; (d) for filing and prosecuting applications for reissuance of any of said Patents; (e) for interference or other priority proceedings involving said Invention; and (f) for legal proceedings involving said Invention and any application therefore and any Patents granted thereon, including without limitation opposition proceedings, cancellation proceedings, priority contests, public use proceedings, infringement actions and court actions; provided, however, that the expense incurred by said Assignors in providing such cooperation shall be paid for by said Assignee.

3. The term and covenants of this agreement shall inure to the benefit of said Assignee, its successors, assigns and other legal representatives, and shall be binding upon said Assignors, their respective heirs, legal representatives and assigns.

4. Said Assignors hereby warrant and represent that they have not entered and will not enter into any assignment, contract, or understanding in conflict herewith.

5. In the event that the filing date and/or Application No. of said Invention are not entered at the time Assignors execute this document, and if such information is deemed necessary, Assignors hereby authorize and request the attorney/agent(s) of **Artegis Law Group, LLP**, to insert the filing date and/or Application No. of said application where needed.

6. This declaration is directed to the attached application, or (if following box is checked):

☒ United States application or PCT international application number
16/562,361 filed on September 5, 2019

As a below named inventor, I hereby declare that:

The above-identified application was made or authorized to be made by me;

I believe that I am the original inventor or an original joint inventor of a claimed invention in the application; and

I acknowledge that any willful false statement made in this declaration is punishable under section 1001 of title 18 by fine or imprisonment of not more than 5 years, or both.

IN WITNESS WHEREOF, the said Assignors have executed and delivered this instrument to said Assignee on the dates indicated below.

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Praveen Joginipally

- 14) _____ (DATE) _____
Timothy John Purcell
- 15) _____ (DATE) _____
Manas Mandal

ASSIGNMENT FOR APPLICATION FOR PATENT WITH DECLARATION STATEMENT

WHEREAS:

Names and Addresses of Inventors:

1)	Jerome F. Duluk, Jr. Palo Alto, CA	2)	Gregory Scott Palmer Cedar Park, TX
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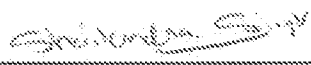
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(hereinafter referred to as Assignors), have invented a certain invention entitled:

TECHNIQUES FOR CONFIGURING A PROCESSOR TO FUNCTION AS MULTIPLE, SEPARATE PROCESSORS

enclosed herewith or for which application for Letters Patent in the United States was filed on
September 5, 2019 under Serial No. 16/562,361; and

WHEREAS, **NVIDIA CORPORATION**, a corporation of the State of Delaware, having a place of business at 2701 San Tomas Expressway, Santa Clara, California 95050 (hereinafter referred to as Assignee), is desirous of acquiring the entire right, title and interest in and to said application (hereinafter referred to as Application), and the invention disclosed therein (hereinafter referred to as Invention), and in and to all embodiments of the Invention, heretofore conceived, made or discovered by said Assignors, and in and to any and all patents, inventor's certificates and other forms of protection (hereinafter referred to as Patents) thereon granted in any and all countries and groups of countries.

NOW, THEREFORE, in consideration of good and valuable consideration acknowledged by said Assignors to have been received in full from said Assignee:

1. Said Assignors hereby sell, assign, transfer and convey to Assignee the full and exclusive right, title and interest (a) in and to said Application and said Invention; (b) in and to all rights to apply for patents on said Invention in any and all countries pursuant to the International Convention for the Protection of Industrial Property or otherwise; (c) in and to any and all Applications filed and any and all Patents granted on said Invention in any and all countries and groups of countries, including each and every Application filed and each and every Patent granted on any application which is a conventional, division, substitution, or continuation of said Application; and (d) in and to each and every reissue or extension of any of said Patents.

2. Said Assignors hereby covenant and agree to cooperate with said Assignee to enable said Assignee to enjoy to the fullest extent the right, title and interest to said Invention herein conveyed in any and all countries and groups of countries. Such cooperation by said Assignors shall include prompt production of pertinent facts and documents, giving testimony, execution of petitions, oaths, specifications, declarations or other papers, and other assistance all to the extent deemed necessary or desirable by said Assignee (a) for perfecting in said Assignee the right, title and interest herein conveyed; (b) for prosecuting any of said applications; (c) for filing and prosecuting substitute, conventional, divisional, continuing or additional applications covering said Invention; (d) for filing and prosecuting applications for reissuance of any of said Patents; (e) for interference or other priority proceedings involving said Invention; and (f) for legal proceedings involving said Invention and any application therefore and any Patents granted thereon, including without limitation opposition proceedings, cancellation proceedings, priority contests, public use proceedings, infringement actions and court actions; provided, however, that the expense incurred by said Assignors in providing such cooperation shall be paid for by said Assignee.

3. The term and covenants of this agreement shall inure to the benefit of said Assignee, its successors, assigns and other legal representatives, and shall be binding upon said Assignors, their respective heirs, legal representatives and assigns.

4. Said Assignors hereby warrant and represent that they have not entered and will not enter into any assignment, contract, or understanding in conflict herewith.

5. In the event that the filing date and/or Application No. of said Invention are not entered at the time Assignors execute this document, and if such information is deemed necessary, Assignors hereby authorize and request the attorney/agent(s) of **Artegis Law Group, LLP**, to insert the filing date and/or Application No. of said application where needed.

6. This declaration is directed to the attached application, or (if following box is checked):

☒ United States application or PCT international application number
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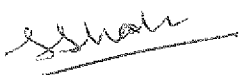
As a below named inventor, I hereby declare that:

The above-identified application was made or authorized to be made by me;

I believe that I am the original inventor or an original joint inventor of a claimed invention in the application; and

I acknowledge that any willful false statement made in this declaration is punishable under section 1001 of title 18 by fine or imprisonment of not more than 5 years, or both.

IN WITNESS WHEREOF, the said Assignors have executed and delivered this instrument to said Assignee on the dates indicated below.

- 1) _____ (DATE) _____
Jerome F. Duluk, Jr.
- 2) _____ (DATE) _____
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- 3) _____ (DATE) _____
Jonathon Stuart Ramsey Evans
- 4) _____ (DATE) _____
Shailendra Singh
- 5) _____ (DATE) _____
Samuel H. Duncan
- 6) _____ (DATE) _____
Wishwesh Anil Gandhi
- 7) 9/17/19 (DATE) 
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Alan Menezes
- 12) _____ (DATE) _____
Pranav Vaidya
- 13) _____ (DATE) _____
Praveen Joginipally

14) _____ (DATE) _____
Timothy John Purcell

15) _____ (DATE) _____
Manas Mandal

ASSIGNMENT FOR APPLICATION FOR PATENT WITH DECLARATION STATEMENT

WHEREAS:

Names and Addresses of Inventors:

1)	Jerome F. Duluk, Jr. Palo Alto, CA	2)	Gregory Scott Palmer Cedar Park, TX
3)	Jonathon Stuart Ramsey Evans Santa Clara, CA	4)	Shailendra Singh Fremont, CA
5)	Samuel H. Duncan Arlington, MA	6)	Wishwesh Anil Gandhi Sunnyvale, CA
7)	Lacky V. Shah Los Altos, CA	8)	Eric Rock San Jose, CA
9)	Feiqi Su Cupertino, CA	10)	James Leroy Deming Madison, AL
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15)	Manas Mandal Palo Alto, CA		

(hereinafter referred to as Assignors), have invented a certain invention entitled:

TECHNIQUES FOR CONFIGURING A PROCESSOR TO FUNCTION AS MULTIPLE, SEPARATE PROCESSORS

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As a below named inventor, I hereby declare that:

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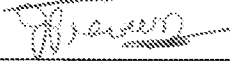
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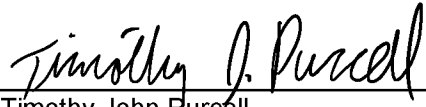
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- 14) 21-Sep-2017 (DATE) 
Timothy John Purcell
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Manas Mandal