

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
 Stylesheet Version v1.2

EPAS ID: PAT5767664

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
JEFFREY ALLEN MAY	08/02/2017
ERIC J. KING	07/14/2017
FIRAS AZRAI	07/14/2017
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	CIRRUS LOGIC INTERNATIONAL SEMICONDUCTOR LTD.
<b>Street Address:</b>	7B NIGHTINGALE WAY
<b>Internal Address:</b>	QUARTERMILE
<b>City:</b>	EDINBURGH
<b>State/Country:</b>	UNITED KINGDOM
<b>Postal Code:</b>	EH3 9EG
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	16600621
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(512)391-2173
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	512.236.2023
<b>Email:</b>	bprewitt@docket@jw.com
<b>Correspondent Name:</b>	JACKSON WALKER L.L.P.
<b>Address Line 1:</b>	100 CONGRESS AVENUE
<b>Address Line 2:</b>	SUITE 1100
<b>Address Line 4:</b>	AUSTIN, TEXAS 78701
<b>ATTORNEY DOCKET NUMBER:</b>	141841.01251-P3098US01
<b>NAME OF SUBMITTER:</b>	MINDY MAYER
<b>SIGNATURE:</b>	/mindy mayer/
<b>DATE SIGNED:</b>	10/14/2019
<b>Total Attachments: 4</b>	
source=P3098US00Assignment#page1.tif	
source=P3098US00Assignment#page2.tif	

source=P3098US00Assignment#page3.tif

source=P3098US00Assignment#page4.tif

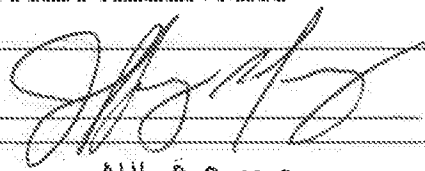
## ASSIGNMENT


WHEREAS, JEFFREY ALLEN MAY, a citizen of the United States of America, residing at 654 Tom Sawyer Road, Dripping Springs, Texas 78620, ERIC J. KING, a citizen of the United States of America, residing at 2004 Paramount Avenue, Unit A, Austin, Texas 78704, and FIRAS AZRAI, a citizen of Jordan, residing at 9525 N. Capital of Texas Highway #222, Austin, Texas 78759, hereinafter called the "Assignors", have made the invention described in the United States patent application entitled *Multi-Phase Power Converter System Using Multiple Amplifier Integrated Circuits*, Attorney Docket No. 141841.00601-P3098US00, for a full description of which reference is here made to an application for Letters Patent of the United States filed on May 1, 2017 and assigned Application Serial No. 15/582,861; and

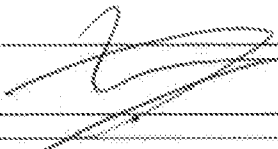
For good and valuable consideration, the receipt and sufficiency of which is acknowledged, I hereby sell, assign, and transfer unto Cirrus Logic International Semiconductor Ltd., a wholly-owned subsidiary of Cirrus Logic, Inc. organized under and pursuant to the laws of Scotland and having its principal place of business at 7B Nightingale Way, Quartermile, Edinburgh, EH3 9EG, United Kingdom, the entire right, title, and interest in and to the invention and all related intellectual property rights (the "Invention") and in and to all applications related to the Invention for Letters Patent in the United States of America, including but not limited to any and all direct and indirect divisions, continuations, continuations-in-part, and reexaminations of said applications and reissues of any resulting patents, and I further authorize and assign all rights to Cirrus Logic International Semiconductor Ltd. to apply for Letters Patent in the United States of America directly in its own name, and to claim priority of the filing date of an application for the earliest-filed patent application(s) for the Invention. Furthermore, for good and valuable consideration, the receipt of which is acknowledged, I hereby also sell, assign, and transfer unto Cirrus Logic International Semiconductor Ltd. the entire right, title, and interest in and to the Invention and in and to all applications related to the Invention for all countries foreign to the United States of America, including but not limited to all rights under any and all international conventions and treaties in respect of the Invention and the applications for Letters Patent, utility models, utility patents, design patents, supplementary protection certificates, patent certificates, and all similar protections in all countries foreign to the United States of America, and I further authorize and assign all rights to Cirrus Logic International Semiconductor Ltd. to apply for Letters Patent, utility models, utility patents, and patent certificates in all countries foreign to the United States directly in its own name, and to claim priority of the filing date of an application for the earliest-filed patent application(s) for the Invention under the provisions of any and all laws, international conventions, treaties, or otherwise.

I hereby authorize and request the Commissioner of Patents of the United States of America to issue Letters Patent in the United States of America upon the aforesaid application, and any application related to the aforesaid application in the United States of America, to Cirrus Logic International Semiconductor Ltd., for the sole use and benefit of Cirrus Logic International Semiconductor Ltd., its successors, assigns, and legal representatives, to the full end of the term for which the Letters Patent in the United States of America may be granted, the same as they would have been held and enjoyed by me had this assignment not been made. I hereby authorize and request the equivalent authorities in foreign countries to issue the patents of their respective countries to Cirrus Logic International Semiconductor Ltd.

I agree that, when requested, I will, without charge to Cirrus Logic, Inc. or its subsidiaries, but at its expense, sign all papers, take all rightful oaths, and do all acts which may be necessary, desirable, or convenient for securing and maintaining any and all patent protection (as described above) for the Invention in any and all countries and for respectively vesting title thereto in Cirrus Logic International Semiconductor Ltd., its successors, assigns, and legal representatives or nominees. To the maximum extent allowed by law, I hereby irrevocably designate and appoint Cirrus Logic International Semiconductor Ltd. and its duly authorized officers and agents, or Cirrus Logic International Semiconductor Ltd.'s applicable designee, as my agents and attorneys-in-fact to act for and on my behalf and instead of me to sign all papers, take all rightful oaths, and do all acts described in the foregoing sentence should I refuse or become otherwise unavailable to do so.

NAME OF FIRST INVENTOR	JEFFREY ALLEN MAY
SIGNATURE OF FIRST INVENTOR	
DATE	AUG 02 2017

NAME OF SECOND INVENTOR	ERIC J. KING
SIGNATURE OF SECOND INVENTOR	
DATE	JUL 14 2017

NAME OF THIRD INVENTOR	FIRAS AZRAI
SIGNATURE OF THIRD INVENTOR	
DATE	JUL 14 2017