505799217 12/02/2019

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT5846046

SUBMISSION TYPE:	NEW ASSIGNMENT
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NATURE OF CONVEYANCE: ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
MARC J. LOINAZ	09/08/2019

RECEIVING PARTY DATA

Name:	ANACAPA SEMICONDUCTOR, INC.
Street Address:	171 MAIN ST., #112
City:	LOS ALTOS
State/Country:	CALIFORNIA
Postal Code:	94022

PROPERTY NUMBERS Total: 8

Property Type	Number
Application Number:	16265322
Application Number:	16265364
Application Number:	16265400
Application Number:	16424415
Application Number:	62625295
Application Number:	62625299
Application Number:	62625298
Application Number:	62676784

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 6508511082
Email: john@s2law.com
Correspondent Name: JOHN STATTLER
Address Line 1: 6000 ALPINE RD

Address Line 4: PORTOLA VALLEY, CALIFORNIA 94028

ATTORNEY DOCKET NUMBER:	ANAC
NAME OF SUBMITTER:	JOHN STATTLER
SIGNATURE:	/john stattler/

PATENT 505799217 REEL: 051154 FRAME: 0841

DATE SIGNED:	12/02/2019
Total Attachments: 14	
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PATENT REEL: 051154 FRAME: 0842

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PATENT APPLICATION

Marc J. Loinaz

Serial No.: 62/676,784

Filing Date: 05/25/2018

For: TIME SYNCHRONIZED SENSOR NETWORK

SOLE INVENTOR TO CORPORATION ASSIGNMENT

WHEREAS, the undersigned Inventor has invented certain new and useful improvements in:

TIME SYNCHRONIZED SENSOR NETWORK

and has filed a United States provisional application disclosing and identifying the invention:

- On the date specified in the accompanying Declaration for Patent Application.
- Said application having Serial Number 62/676,784 and filed on May 25, 2018.

WHEREAS Anacapa Semiconductor, Inc. (hereinafter termed "Assignee"), a corporation of the State of California, having a place of business at 171 Main Street, #112, Los Altos, California, 94022 wishes to acquire the entire right, title and interest in and to said application and the invention disclosed therein, and in and to all embodiments of the invention, heretofore conceived, made or discovered by said Inventor (all collectively hereinafter termed "said invention"), and in and to any and all patents, certificates of invention and other forms of protection thereon (hereinafter termed "patents") applied for or granted in the United States and/or other countries.

NOW THEREFORE, for good and valuable consideration acknowledged by said Inventor to have been received in full from said Assignee:

1. Said Inventor does hereby sell, assign, transfer and convey to said Assignee, the entire right, title and interest (a) in and to said application and said invention; (b) in and to all rights to apply in any or all countries of the world for patents, certificates of inventions or other governmental grants on said invention, including the right to apply for patents pursuant to the International Convention for the Protection of Industrial Property or pursuant to any other convention, treaty, agreement or understanding; (c) in and to any and all applications filed and

- 2. Said Inventor hereby covenants and agrees to cooperate with said Assignee to enable said Assignee to enjoy to the fullest extent the right, title and interest herein conveyed in the United States and other countries. Such cooperation by said Inventor shall include prompt production of pertinent facts and documents, giving of testimony, executing of petitions, oaths, specifications, declarations or other papers, and other assistance all to the extent deemed necessary or desirable by said Assignee (a) for perfecting in said Assignee the right, title and interest herein conveyed; (b) for complying with any duty of disclosure; (c) for prosecuting any of said applications; (d) for filing and prosecuting substitute, divisional, continuing or additional applications covering said invention; (e) for filing and prosecuting applications for reissue of any of said patents; (f) for interference or other priority proceedings involving said invention; and (g) for legal proceedings involving said invention and any applications therefore and any patents granted thereon, including without limitation opposition proceedings, cancellation proceedings, priority contests, public use proceedings, reexamination proceedings, compulsory licensing proceedings, infringement actions and court actions; provided, however, that the expense incurred by said Inventor in providing such cooperation shall be paid for by said Assignee.
- 3. The terms and covenants of this Assignment shall inure to the benefit of said Assignee, its successors, assigns and other legal representatives, and shall be binding upon said Inventor, his/her heirs, legal representatives, and assigns.
- 4. Said Inventor hereby warrants and represents that he/she has not entered and will not enter into any assignment, contract, or understanding in conflict herewith.

(1) aune s. Hanny	Date: 9/8/2019
Marc J. Loinaz	

In the application of:

PATENT APPLICATION

Marc J. Loinaz

Serial No.: 62/625,299

Filing Date: 02/01/2018

For: APPLICATIONS FOR A TIME COHERENT

NETWORK FOR PRECISION SENSING

SOLE INVENTOR TO CORPORATION ASSIGNMENT

WHEREAS, the undersigned Inventor has invented certain new and useful improvements in:

APPLICATIONS FOR A TIME COHERENT NETWORK FOR PRECISION SENSING and has filed a United States provisional application disclosing and identifying the invention:

- On the date specified in the accompanying Declaration for Patent Application.
- Said application having Serial Number 62/625,299 and filed on February 1, 2018.

WHEREAS Anacapa Semiconductor, Inc. (hereinafter termed "Assignee"), a corporation of the State of California, having a place of business at 171 Main Street, #112, Los Altos, California, 94022 wishes to acquire the entire right, title and interest in and to said application and the invention disclosed therein, and in and to all embodiments of the invention, heretofore conceived, made or discovered by said Inventor (all collectively hereinafter termed "said invention"), and in and to any and all patents, certificates of invention and other forms of protection thereon (hereinafter termed "patents") applied for or granted in the United States and/or other countries.

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In the application of:

PATENT APPLICATION

Marc J. Loinaz

Serial No.: 62/625,298

Filing Date: 02/01/2018

For: A Time Coherent Network

SOLE INVENTOR TO CORPORATION ASSIGNMENT

WHEREAS, the undersigned Inventor has invented certain new and useful improvements in:

A Time Coherent Network

and has filed a United States provisional patent application disclosing and identifying the invention:

- On the date specified in the accompanying Declaration for Patent Application.
- Said provisional application having Serial Number 62/625,298 and filed on February 1, 2018.

WHEREAS Anacapa Semiconductor, Inc. (hereinafter termed "Assignee"), a corporation of the State of California, having a place of business at 171 Main Street, #112, Los Altos, California, 94022 wishes to acquire the entire right, title and interest in and to said application and the invention disclosed therein, and in and to all embodiments of the invention, heretofore conceived, made or discovered by said Inventor (all collectively hereinafter termed "said invention"), and in and to any and all patents, certificates of invention and other forms of protection thereon (hereinafter termed "patents") applied for or granted in the United States and/or other countries.

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- 4. Said Inventor hereby warrants and represents that he/she has not entered and will not enter into any assignment, contract, or understanding in conflict herewith.

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PATENT APPLICATION

Marc J. Loinaz

Serial No.: 62/625,295

Filing Date: 02/01/2018

For: HIGH PRECISION MULTI-CHIP CLOCK

SYNCHRONIZATION

SOLE INVENTOR TO CORPORATION ASSIGNMENT

WHEREAS, the undersigned Inventor has invented certain new and useful improvements in:

HIGH PRECISION MULTI-CHIP CLOCK SYNCHRONIZATION

and has filed a United States provisional application disclosing and identifying the invention:

- On the date specified in the accompanying Declaration for Patent Application.
- Said provisional application having Serial Number 62/625,295 and filed on February 1, 2018.

WHEREAS Anacapa Semiconductor, Inc. (hereinafter termed "Assignee"), a corporation of the State of California, having a place of business at 171 Main Street, #112, Los Altos, California, 94022 wishes to acquire the entire right, title and interest in and to said application and the invention disclosed therein, and in and to all embodiments of the invention, heretofore conceived, made or discovered by said Inventor (all collectively hereinafter termed "said invention"), and in and to any and all patents, certificates of invention and other forms of protection thereon (hereinafter termed "patents") applied for or granted in the United States and/or other countries.

NOW THEREFORE, for good and valuable consideration acknowledged by said Inventor to have been received in full from said Assignee:

1. Said Inventor does hereby sell, assign, transfer and convey to said Assignee, the entire right, title and interest (a) in and to said application and said invention; (b) in and to all rights to apply in any or all countries of the world for patents, certificates of inventions or other governmental grants on said invention, including the right to apply for patents pursuant to the International Convention for the Protection of Industrial Property or pursuant to any other convention, treaty, agreement or understanding; (c) in and to any and all applications filed and

- 2. Said Inventor hereby covenants and agrees to cooperate with said Assignee to enable said Assignee to enjoy to the fullest extent the right, title and interest herein conveyed in the United States and other countries. Such cooperation by said Inventor shall include prompt production of pertinent facts and documents, giving of testimony, executing of petitions, oaths, specifications, declarations or other papers, and other assistance all to the extent deemed necessary or desirable by said Assignee (a) for perfecting in said Assignee the right, title and interest herein conveyed; (b) for complying with any duty of disclosure; (c) for prosecuting any of said applications; (d) for filing and prosecuting substitute, divisional, continuing or additional applications covering said invention; (e) for filing and prosecuting applications for reissue of any of said patents; (f) for interference or other priority proceedings involving said invention; and (g) for legal proceedings involving said invention and any applications therefore and any patents granted thereon, including without limitation opposition proceedings, cancellation proceedings, priority contests, public use proceedings, reexamination proceedings, compulsory licensing proceedings, infringement actions and court actions; provided, however, that the expense incurred by said Inventor in providing such cooperation shall be paid for by said Assignee.
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- 4. Said Inventor hereby warrants and represents that he/she has not entered and will not enter into any assignment, contract, or understanding in conflict herewith.

(1)	Organic J. Marinant	Date:	9/2/2019
	Marc J. Loinaz		

In the application of:

Marc J. Loinaz

Serial No.: 16/424,415

Filing Date: 05/28/2019

For: Fluorescence Lifetime Imaging (FLIM) and Flow cytometry Applications For A Time

SYNCHRONIZED SENSOR NETWORK

PATENT APPLICATION

Examiner: <Not yet assigned>

Group Art Unit: 2696

SOLE INVENTOR TO CORPORATION ASSIGNMENT

WHEREAS, the undersigned Inventor has invented certain new and useful improvements in:

FLUORESCENCE LIFETIME IMAGING (FLIM) AND FLOW CYTOMETRY APPLICATIONS FOR A TIME SYNCHRONIZED SENSOR NETWORK

and has executed a declaration or oath for an application for a United States patent disclosing and identifying the invention:

- On the date specified in the accompanying Declaration for Patent Application.
- Said application having Serial Number 16/424,415 and filed on May 28, 2019.

WHEREAS Anacapa Semiconductor, Inc. (hereinafter termed "Assignee"), a corporation of the State of California, having a place of business at 171 Main Street, #112, Los Altos, California, 94022 wishes to acquire the entire right, title and interest in and to said application and the invention disclosed therein, and in and to all embodiments of the invention, heretofore conceived, made or discovered by said Inventor (all collectively hereinafter termed "said invention"), and in and to any and all patents, certificates of invention and other forms of protection thereon (hereinafter termed "patents") applied for or granted in the United States and/or other countries.

NOW THEREFORE, for good and valuable consideration acknowledged by said Inventor to have been received in full from said Assignee:

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- 2. Said Inventor hereby covenants and agrees to cooperate with said Assignee to enable said Assignee to enjoy to the fullest extent the right, title and interest herein conveyed in the United States and other countries. Such cooperation by said Inventor shall include prompt production of pertinent facts and documents, giving of testimony, executing of petitions, oaths, specifications, declarations or other papers, and other assistance all to the extent deemed necessary or desirable by said Assignee (a) for perfecting in said Assignee the right, title and interest herein conveyed; (b) for complying with any duty of disclosure; (c) for prosecuting any of said applications; (d) for filing and prosecuting substitute, divisional, continuing or additional applications covering said invention; (e) for filing and prosecuting applications for reissue of any of said patents; (f) for interference or other priority proceedings involving said invention; and (g) for legal proceedings involving said invention and any applications therefore and any patents granted thereon, including without limitation opposition proceedings, cancellation proceedings, priority contests, public use proceedings, reexamination proceedings, compulsory licensing proceedings, infringement actions and court actions; provided, however, that the expense incurred by said Inventor in providing such cooperation shall be paid for by said Assignee.
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- 4. Said Inventor hereby warrants and represents that he/she has not entered and will not enter into any assignment, contract, or understanding in conflict herewith.

(1)	Mary & Harry	Date: 9/8/2019	
3 J	Marc J. Loinaz		

In the application of:

Marc J. Loinaz

Serial No.: 16/265,400

Filing Date: 02/01/2019

For: Applications for a Time Coherent

NETWORK FOR PRECISION SENSING

PATENT APPLICATION

Examiner: <Not yet assigned>

Group Art Unit: <Not yet assigned>

SOLE INVENTOR TO CORPORATION ASSIGNMENT

WHEREAS, the undersigned Inventor has invented certain new and useful improvements in:

APPLICATIONS FOR A TIME COHERENT NETWORK FOR PRECISION SENSING and has executed a declaration or oath for an application for a United States patent disclosing and identifying the invention:

- On the date specified in the accompanying Declaration for Patent Application.
- Said application having Serial Number 16/265,400 and filed on February 1, 2019.

WHEREAS Anacapa Semiconductor, Inc. (hereinafter termed "Assignee"), a corporation of the State of California, having a place of business at 171 Main Street, #112, Los Altos, California, 94022 wishes to acquire the entire right, title and interest in and to said application and the invention disclosed therein, and in and to all embodiments of the invention, heretofore conceived, made or discovered by said Inventor (all collectively hereinafter termed "said invention"), and in and to any and all patents, certificates of invention and other forms of protection thereon (hereinafter termed "patents") applied for or granted in the United States and/or other countries.

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- 4. Said Inventor hereby warrants and represents that he/she has not entered and will not enter into any assignment, contract, or understanding in conflict herewith.

(I)	Whore I Thomas	Date: 9/8/2009	e e
V. Airmini	Marc J. Loinaz		

In the application of:

Marc J. Loinaz

Serial No.: 16/265,322

Filing Date: 02/01/2019

For: HIGH PRECISION MULTI-CHIP CLOCK

SYNCHRONIZATION

PATENT APPLICATION

Examiner: <Not yet assigned>

Group Art Unit: 2676

SOLE INVENTOR TO CORPORATION ASSIGNMENT

WHEREAS, the undersigned Inventor has invented certain new and useful improvements in:

HIGH PRECISION MULTI-CHIP CLOCK SYNCHRONIZATION

and has executed a declaration or oath for an application for a United States patent disclosing and identifying the invention:

- On the date specified in the accompanying Declaration for Patent Application.
- Said application having Serial Number 16/265,322 and filed on February 1, 2019.

WHEREAS Anacapa Semiconductor, Inc. (hereinafter termed "Assignee"), a corporation of the State of California, having a place of business at 171 Main Street, #112, Los Altos, California, 94022 wishes to acquire the entire right, title and interest in and to said application and the invention disclosed therein, and in and to all embodiments of the invention, heretofore conceived, made or discovered by said Inventor (all collectively hereinafter termed "said invention"), and in and to any and all patents, certificates of invention and other forms of protection thereon (hereinafter termed "patents") applied for or granted in the United States and/or other countries.

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