PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT5899185

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
KEVIN NEAL MAGILL	01/07/2020
ERIC FRANCIS ROBINSON	01/07/2020
DEREK BACHAND	01/07/2020
JASON PANAVICH	01/07/2020
MICHAEL P. WILSON	01/07/2020
MICHAEL B. MITCHELL	01/07/2020

RECEIVING PARTY DATA

Name:	MICROSOFT TECHNOLOGY LICENSING, LLC
Street Address:	ONE MICROSOFT WAY
City:	REDMOND
State/Country:	WASHINGTON
Postal Code:	98052

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	16736865

CORRESPONDENCE DATA

Fax Number: (877)812-1249

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone:	919-238-2300
Email:	usdocket@microsoft.com
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Address Line 1:	106 PINEDALE SPRINGS WAY
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ATTORNEY DOCKET NUMBER:	7006-035
NAME OF SUBMITTER:	KAT M. KENNY
SIGNATURE:	/Kat M. Kenny/
DATE SIGNED:	01/08/2020

Total Attachments: 12

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I <u>Kevin Neal MAGILL</u> ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "<u>PROVIDING DYNAMIC SELECTION OF CACHE COHERENCE PROTOCOLS IN PROCESSOR-BASED DEVICES</u>" ("APPLICATION"), which:

☒ will be filed. ASSIGNOR hereby authorizes, and requests, ASSIGNEE'S legal representative, Microsoft Technology Licensing, LLC, One Microsoft Way, Redmond, Washington 98052 (ASSIGNEE'S LEGAL REPRESENTATIVES), to insert here in parentheses (filed on _____ and given Application No. _____ by the following Office <u>US – United States Patent and Trademark Office</u>) this APPLICATION's Application No., filing date, and Office, when known;

☐ was filed on _____ and was given Application No _____ by the following Office "_____";
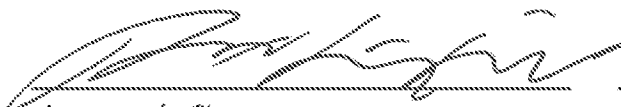
Microsoft Technology Licensing, LLC, a Washington limited liability company, on behalf of itself and its successors and assigns ("ASSIGNEE"), is entitled to, and is desirous of acquiring, the entire and exclusive rights, title and interest, including the right of priority, in the INVENTION and the APPLICATION (and all other applications and patents derived therefrom, such as continuing applications, in whole or in part, in and for the United States, its territories, and all foreign countries ("APPLICATION DERIVATIVES"));

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Application No.	Filing Date	Office

For good and valuable consideration, the receipt of which is hereby acknowledged by the ASSIGNOR, the ASSIGNOR hereby sells, assigns and transfers to the ASSIGNEE, ASSIGNOR's entire and exclusive rights, title and interest, including the right of priority, in the INVENTION and the APPLICATION (and all other applications and patents derived therefrom, such as continuing applications, in whole or in part, in and for the United States, its territories, and all foreign countries ("APPLICATION DERIVATIVES"));

ASSIGNOR agrees to execute all instruments and documents required for the making and prosecution of the APPLICATION (and APPLICATION DERIVATIVES), for litigation regarding letters patent derived therefrom, and for the purpose of protecting and perfecting title to the APPLICATION (and APPLICATION DERIVATIVES).

(signature) 1/7/2020	
Inventor's Signature	Date
Kevin Neal MAGILL	
Printed Name in English	Printed Name in Native Language (if other than English)

I Eric Francis ROBINSON ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "PROVIDING DYNAMIC SELECTION OF CACHE COHERENCE PROTOCOLS IN PROCESSOR-BASED DEVICES" ("APPLICATION"), which:

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Eric Francis Robinson   _Jan 7, 2020_
Inventor's Signature   Date

Eric Francis ROBINSON
Printed Name in English   Printed Name in Native Language
                                            (if other than English)

**PATENT
REEL: 051444 FRAME: 0707**

I Derek BACHAND ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "PROVIDING DYNAMIC SELECTION OF CACHE COHERENCE PROTOCOLS IN PROCESSOR-BASED DEVICES" ("APPLICATION"), which:

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Signature	Jan 7, 2020
Inventor's Signature	Date
Derek BACHAND	
Printed Name in English	Printed Name in Native Language (if other than English)

**PATENT
REEL: 051444 FRAME: 0709**

I Jason PANAVICH ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "PROVIDING DYNAMIC SELECTION OF CACHE COHERENCE PROTOCOLS IN PROCESSOR-BASED DEVICES" ("APPLICATION"), which:

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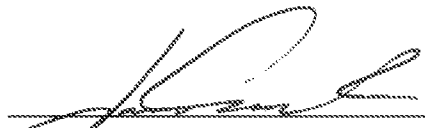
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Inventor's Signature	1/7/20 _Date_
Jason PANAVICH Printed Name in English	 Printed Name in Native Language (if other than English)

I Michael P. WILSON ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "PROVIDING DYNAMIC SELECTION OF CACHE COHERENCE PROTOCOLS IN PROCESSOR-BASED DEVICES" ("APPLICATION"), which:

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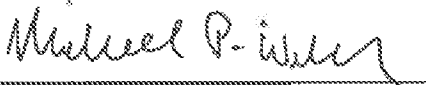
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Michael P. Wilson	1/7/2020
Inventor's Signature	Date
Michael P. WILSON	
Printed Name in English	Printed Name in Native Language (if other than English)

I Michael B. MITCHELL ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "PROVIDING DYNAMIC SELECTION OF CACHE COHERENCE PROTOCOLS IN PROCESSOR-BASED DEVICES" ("APPLICATION"), which:

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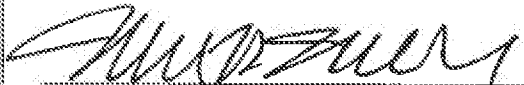☐ was filed on _____ and was given Application No _____ by the following Office "_____";

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_____   1/7/2020
Inventor's Signature   Date

Michael B. MITCHELL
Printed Name in English   Printed Name in Native Language
                                                          (if other than English)