

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
 Stylesheet Version v1.2

EPAS ID: PAT5937322

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
HENG YANG	01/10/2020
DAVID PRITCHARD	01/10/2020
KAI SUN	01/14/2020
HONGRU REN	01/16/2020
NEHA NAYYAR	01/21/2020
MANJUNATHA PRABHU	01/15/2020
ELIZABETH STREHLOW	01/09/2020
SALVATORE CIMINO	01/29/2020
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	GLOBALFOUNDRIES INC.
<b>Street Address:</b>	PO BOX 309
<b>Internal Address:</b>	UGLAND HOUSE
<b>City:</b>	GRAND CAYMAN
<b>State/Country:</b>	CAYMAN ISLANDS
<b>Postal Code:</b>	KY1-1104
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
Application Number:	16776711
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(713)434-6671
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	7134346677
<b>Email:</b>	mpaul@amersoniplaw.com
<b>Correspondent Name:</b>	J. MIKE AMERSON
<b>Address Line 1:</b>	2450 FONDREN ROAD, SUITE 260
<b>Address Line 4:</b>	HOUSTON, TEXAS 77063
<b>ATTORNEY DOCKET NUMBER:</b>	2162.295500/LMLT230B
<b>NAME OF SUBMITTER:</b>	J. MIKE AMERSON

<b>SIGNATURE:</b>	/J. Mike Amerson/
<b>DATE SIGNED:</b>	01/30/2020
	This document serves as an Oath/Declaration (37 CFR 1.63).
<b>Total Attachments: 8</b> source=Declaration_and_Assignment#page1.tif source=Declaration_and_Assignment#page2.tif source=Declaration_and_Assignment#page3.tif source=Declaration_and_Assignment#page4.tif source=Declaration_and_Assignment#page5.tif source=Declaration_and_Assignment#page6.tif source=Declaration_and_Assignment#page7.tif source=Declaration_and_Assignment#page8.tif	

**COMBINED DECLARATION and ASSIGNMENT (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION  
USING AN APPLICATION DATA SHEET (37 CFR 1.76)**

<b>Title of Invention</b>	VERTICAL TRANSISTOR DEVICE WITH SOURCE/DRAIN REGIONS COMPRISING A TWO- DIMENSIONAL (2D) MATERIAL AND METHODS OF MAKING SUCH VERTICAL TRANSISTOR DEVICES
-------------------------------	---

**DECLARATION**

As the below named inventor, I hereby declare that:

This declaration  
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The attached application, or

United States application or PCT international application number \_\_\_\_\_ filed on  
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**ASSIGNMENT**

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If any portion of this Combined Declaration or Assignment is held unenforceable, then remaining portions hereof shall nevertheless remain in full force and effect.

**LEGAL NAME OF INVENTOR**

Inventor: HENG YANG

Date: 01/10/2020

Signature: \_\_\_\_\_

Note: An application data sheet (PFD/SB/14 or equivalent), including naming the entire inventive entity, must accompany this form. Use an additional form for each additional inventor.

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**Title of  
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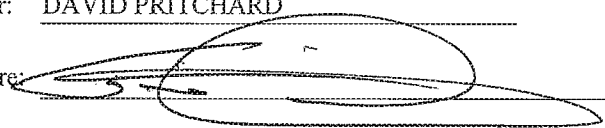
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**LEGAL NAME OF INVENTOR**

Inventor: DAVID PRITCHARD

Date: 14 JAN 2020

Signature: 

Note: An application data sheet (PTO/SB/14 or equivalent), including naming the entire inventive entity, must accompany this form. Use an additional form for each additional inventor.

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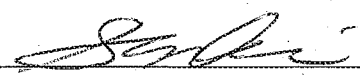
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**LEGAL NAME OF INVENTOR**

Inventor: KAI SUN

Date: 01/14/2020

Signature: 

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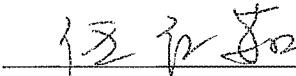
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**LEGAL NAME OF INVENTOR**

Inventor: HONGRU REN

Date: Jan. 16, 2020

Signature: 

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**LEGAL NAME OF INVENTOR**

Inventor: NEHA NAYYAR

Date: 01-21-2020

Signature: Neha Nayar

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**PATENT**

**REEL: 051069 FRAME: 0423**

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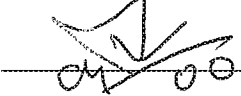
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**LEGAL NAME OF INVENTOR**

Inventor: MANJUNATHA PRABHU

Date: 01/15/2020

Signature: 

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**LEGAL NAME OF INVENTOR**

Inventor: ELIZABETH STREHLOW

Date: 1/9/20

Signature: Elizabeth Strehlow

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Inventor: SALVATORE CIMINO

Date: 01/29/2020

Signature: Salvatore Cimino

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**PATENT**

**RECORDED: 01/30/2020**

**REEL: 051669 FRAME: 0426**