505903685 02/06/2020

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
SADEGH YAZDANSHENAS	01/31/2020
TIM VANDERHOEK	01/31/2020

RECEIVING PARTY DATA

Name:	INTEL CORPORATION	
Street Address:	2200 MISSION COLLEGE BOULEVARD	
City:	SANTA CLARA	
State/Country:	CALIFORNIA	
Postal Code:	95054	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	16729256

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	AC4969-US (INTL:0473)	
NAME OF SUBMITTER:	MATTHEW G. OSTERHAUS	
SIGNATURE:	/Matthew G. Osterhaus/	
DATE SIGNED:	02/06/2020	

Total Attachments: 2

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PATENT 505903685 REEL: 051839 FRAME: 0164

Attorney Docket No.: AC4969-US/INTL:0473

ASSIGNMENT

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we, the undersigned inventor(s):

Sadegh Yazdanshenas Tim Vanderhoek

hereby sell, assign, and transfer to:

Intel Corporation

a corporation of Delaware, having a principal place of business at 2200 Mission College Boulevard, Santa Clara, California, 95054 USA ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all other countries, in and to any and all inventions and improvements that are disclosed in the application for the patent entitled:

EFFICIENT LOGIC BLOCKS ARCHITECTURES FOR DENSE MAPPING OF MULTIPLIERS

(I hereby authorize and request any attorney having appropriate authority from the assignee to insert on the designated lines below, the filing date and application number of said application when known.)

which was filed on December 27, 2019 as

US Application Number 16/729,256 and COUNTRY or International Office

which has been executed by the undersigned prior hereto or concurrently herewith on the date(s) indicated below.

and in and to said application and all Additional Applications, and all other patent applications that have been or shall be filed in the United States and all other countries and international filing offices on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all other countries and international filing offices on said inventions and improvements; and in and to all rights of priority resulting from the filing of said applications; as used herein "Additional Applications" includes but is not limited to design, utility, utility model, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and national phase applications on said inventions and improvements;

and agree that said Assignee may apply for and receive a patent or patents for said inventions and improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all Additional Applications, and all other patent applications on any and all said inventions and improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and legal representatives all facts known to the undersigned relating to said inventions and improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or legal representatives in securing and

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maintaining proper patent protection for said inventions and improvements and for vesting title to said inventions and improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Sadegh Yazdanshenas

1 / 31 / 2020 Date signed

2020-01-31 Date signed