

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
TAMIRAS PER PTE. LTD., LLC	11/26/2019
RECEIVING PARTY DATA	
Name:	INTELLECTUAL VENTURES ASSETS 158 LLC
Street Address:	251 LITTLE FALLS DRIVE
City:	WILMINGTON
State/Country:	DELAWARE
Postal Code:	19808
PROPERTY NUMBERS Total: 71	
Property Type	Number
Patent Number:	5819058
Patent Number:	5847701
Patent Number:	5987624
Patent Number:	6005544
Patent Number:	6011538
Patent Number:	6028571
Patent Number:	6046738
Patent Number:	6108041
Patent Number:	6133957
Patent Number:	6157376
Patent Number:	6232952
Patent Number:	6268848
Patent Number:	6272193
Patent Number:	6430240
Patent Number:	6459426
Patent Number:	6765563
Patent Number:	5887165
Patent Number:	5844576
Patent Number:	9773244
Patent Number:	5907831

Property Type	Number
Patent Number:	6178407
Patent Number:	7203656
Patent Number:	8676619
Patent Number:	5867479
Patent Number:	6091704
Patent Number:	6185126
Patent Number:	6026134
Patent Number:	6560306
Patent Number:	6839394
Patent Number:	6294962
Patent Number:	5732027
Patent Number:	5864506
Patent Number:	6154207
Patent Number:	5995257
Patent Number:	6005997
Patent Number:	6078596
Patent Number:	5835599
Patent Number:	6087224
Patent Number:	5945732
Patent Number:	6190939
Patent Number:	5869353
Patent Number:	6282210
Patent Number:	6310392
Patent Number:	6222737
Patent Number:	6360433
Patent Number:	7038698
Patent Number:	7091991
Patent Number:	7154506
Patent Number:	7190371
Patent Number:	6766315
Patent Number:	5893162
Patent Number:	6228157
Patent Number:	6503559
Patent Number:	5815444
Patent Number:	5953244
Patent Number:	5962890
Application Number:	09738217
Application Number:	60020249

Property Type	Number
Application Number:	09699959
Application Number:	11045754
Application Number:	11784363
Application Number:	09329143
Application Number:	10790991
Application Number:	12953784
Application Number:	60066152
Application Number:	09633297
Application Number:	09527134
Application Number:	60011356
Application Number:	60083856
Application Number:	60093458
Application Number:	60087893

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	11206-001GEN
NAME OF SUBMITTER:	LAWRENCE A. AARONSON
SIGNATURE:	/Lawrence A. Aaronson/
DATE SIGNED:	02/08/2020

Total Attachments: 18

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ASSIGNMENT OF RIGHTS IN CERTAIN ASSETS

For good and valuable consideration, the receipt of which is hereby acknowledged, Tamiras Per Pte. Ltd., LLC, a Delaware limited liability company, having an address at 251 Little Falls Drive, Wilmington, DE 19808 (“*Assignor*”), does hereby sell, assign, transfer, and convey unto Intellectual Ventures Assets 158 LLC, a Delaware limited liability company, having an address at 251 Little Falls Drive, Wilmington, DE 19808 (“*Assignee*”), or its designees, the right, title, and interest in and to any and all of the following provisional patent applications, patent applications, patents, and other governmental grants or issuances of any kind (the “*Certain Assets*”):

<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
CA2282409 (CA2282409)	CA	2/10/2009 (2/27/1998)	Instruction Compression And Decompression System And Method For A Processor Miller, Richard G.
(EP98908805.9)	EP	(2/27/1998)	Instruction Compression And Decompression System And Method For A Processor Miller, Richard G.
JP3833269 (JP10-537920)	JP	7/28/2006 (2/27/1998)	Instruction Compression And Decompression System And Method For A Processor Miller, Richard G.
5819058 (08/808735)	US	10/6/1998 (2/28/1997)	Instruction Compression And Decompression System And Method For A Processor Miller, Richard G.
(PCT/US1998/003954)	WO	(2/27/1998)	Instruction Compression And Decompression System And Method For A Processor Miller, Richard G.
KR10-0567937 (KR10-1999-7007883)	KR	3/30/2006 (2/27/1998)	Instruction Compression And Decompression System And Method For A Processor Miller, Richard G.
5847701 (08/872774)	US	12/8/1998 (6/10/1997)	Method And Apparatus Implemented In A Computer System For Determining The Frequency Used By A Graphics Source For Generating An Analog Display Signal Eglit, Alexander Julian

<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
(JP10-162106)	JP	(6/10/1998)	Method And Apparatus For Automatically Determining Signal Parameters Of An Analog Display Signal Received By A Display Unit Of A Computer System Eglit, Alexander Julian
TWI123426 (TW087106266)	TW	12/1/2000 (4/23/1998)	A Method And Apparatus For Automatically Determining Signal Parameters Of An Analog Display Signal Received By A Display Unit Of A Computer System Eglit, Alexander Julian
5987624 (08/872764)	US	11/16/1999 (6/10/1997)	Method And Apparatus For Automatically Determining Signal Parameters Of An Analog Display Signal Received By A Display Unit Of A Computer System Eglit, Alexander Julian
KR10-0339765 (KR10-1998-0022456)	KR	5/24/2002 (6/10/1998)	Method And Apparatus For Automatically Determining Signal Parameters Of An Analog Display Signal Received By A Display Unit Of A Computer System Eglit, Alexander Julian
6005544 (09/023815)	US	12/21/1999 (2/13/1998)	Digital Display Unit In A Computer System For Enabling A User To Conveniently Select A Desired Monitor Mode For Displaying Images Encoded In A Received Analog Display Signal Eglit, Alexander Julian
6011538 (08/877708)	US	1/4/2000 (6/18/1997)	Method And Apparatus For Displaying Images When An Analog-To-Digital Converter In A Digital Display Unit Is Unable To Sample An Analog Display Signal At A Desired High Sampling Frequency Eglit, Alexander Julian
6028571 (09/035261)	US	2/22/2000 (3/5/1998)	Digital Display Unit In A Computer System With An Improved Method And Apparatus For Determining A Source Mode Using Which A Received Analog Display Signal Was Generated Eglit, Alexander Julian
6046738 (08/909825)	US	4/4/2000 (8/12/1997)	Method And Apparatus For Scanning A Digital Display Screen Of A Computer Screen At A Horizontal Scanning Frequency Lower Than The Origin Frequency Of A Display Signal

<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
			Eglit, Alexander Julian
EP0945016 (EP98949786.2)	EP	12/10/2008 (10/6/1998)	High-Definition Television Signal Processing For Transmitting And Receiving A Television Signal In A Manner Compatible With The Present System Faroudja, Yves C.
6108041 (08/948539)	US	8/22/2000 (10/10/1997)	High-Definition Television Signal Processing For Transmitting And Receiving A Television Signal In A Manner Compatible With The Present System Faroudja, Yves C.
(PCI/US1998/021072)	WO	(10/6/1998)	High-Definition Television Signal Processing For Transmitting And Receiving A Television Signal In A Manner Compatible With The Present System Faroudja, Yves C.
JP4294100 (JP11-522276)	JP	4/17/2009 (10/6/1998)	High-Definition Television Signal Processing For Transmitting And Receiving A Television Signal In A Manner Compatible With The Present System Faroudja, Yves C.
DE69840322.3 (DE69840322.3)	DE	12/10/2008 (10/6/1998)	High-Definition Television Signal Processing For Transmitting And Receiving A Television Signal In A Manner Compatible With The Present System Faroudja, Yves C.
FR0945016 (FR98949786.2)	FR	12/10/2008 (10/6/1998)	High-Definition Television Signal Processing For Transmitting And Receiving A Television Signal In A Manner Compatible With The Present System Faroudja, Yves C.
GB0945016 (GB98949786.2)	GB	12/10/2008 (10/6/1998)	High-Definition Television Signal Processing For Transmitting And Receiving A Television Signal In A Manner Compatible With The Present System Faroudja, Yves C.
DE69832963.5 (DE69832963.5)	DE	12/28/2005 (10/8/1998)	Adaptive Diagonal Interpolation For Image Resolution Enhancement Campbell, Jack J.

<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
EP0951693 (EP98953331.0)	EP	12/28/2005 (10/8/1998)	Adaptive Diagonal Interpolation For Image Resolution Enhancement Campbell, Jack J.
6133957 (08/953840)	US	10/17/2000 (10/14/1997)	Adaptive Diagonal Interpolation For Image Resolution Enhancement Campbell, Jack J.
(PCT/US1998/021282)	WO	(10/8/1998)	Adaptive Diagonal Interpolation For Image Resolution Enhancement Campbell, Jack J.
FR0951693 (FR98953331.0)	FR	12/28/2005 (10/8/1998)	Adaptive Diagonal Interpolation For Image Resolution Enhancement Campbell, Jack J.
GB0951693 (GB98953331.0)	GB	12/28/2005 (10/8/1998)	Adaptive Diagonal Interpolation For Image Resolution Enhancement Campbell, Jack J.
JP4212657 (JP11-522406)	JP	11/7/2008 (10/8/1998)	Adaptive Diagonal Interpolation For Image Resolution Enhancement Campbell, Jack J.
6157376 (09/164081)	US	12/5/2000 (9/30/1998)	Method And Apparatus For Generating A Target Clock Signal Having A Frequency Of X/Y Times The Frequency Of A Reference Clock Signal Eglit, Alexander Julian
6232952 (09/164080)	US	5/15/2001 (9/30/1998)	Method And Apparatus For Comparing Frequently The Phase Of A Target Clock Signal With The Phase Of A Reference Clock Signal Enabling Quick Synchronization Eglit, Alexander Julian
(09/738217)	US	(12/15/2000)	Method And Apparatus For Comparing Frequently The Phase Of A Target Clock Signal With The Phase Of A Reference Clock Signal Enabling Quick Synchronization Eglit, Alexander Julian

<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
6268848 (09/177897)	US	7/31/2001 (10/23/1998)	Method And Apparatus Implemented In An Automatic Sampling Phase Control System For Digital Monitors Eglit, Alexander Julian
DE60033606.9 (DE60033606.9)	DE	2/28/2007 (3/22/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
EP1087564 (EP00302304.1)	EP	2/28/2007 (3/22/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
FR1087564 (FR00302304.1)	FR	2/28/2007 (3/22/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
GB1087564 (GB00302304.1)	GB	2/28/2007 (3/22/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
JP4663850 (JP2000-180219)	JP	1/14/2011 (6/15/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
KR10-0400186 (KR10-2000-0017828)	KR	9/19/2003 (4/6/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
TWI149369 (TW089105499)	TW	1/21/2002 (3/24/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
6272193 (09/406332)	US	8/7/2001 (9/27/1999)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
6430240 (09/866681)	US	8/6/2002 (5/30/2001)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian

<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
NL1087564 (NL00302304.1)	NL	2/28/2007 (3/22/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
6459426 (09/135216)	US	10/1/2002 (8/17/1998)	Monolithic Integrated Circuit Implemented In A Digital Display Unit For Generating Digital Data Elements From An Analog Display Signal Received At High Frequencies Eglit, Alexander Julian
6765563 (10/109891)	US	7/20/2004 (4/1/2002)	Monolithic Integrated Circuit Implemented In A Digital Display Unit For Generating Digital Data Elements From An Analog Display Signal Received At High Frequencies Eglit, Alexander Julian
(EP97931306.1)	EP	(6/20/1997)	Dynamically Reconfigurable Hardware System For Real-Time Control Of Processes Hunter, Ian W.
(JP10-503398)	JP	(6/20/1997)	Dynamically Reconfigurable Hardware System For Real-Time Control Of Processes Hunter, Ian W.
5887165 (08/879923)	US	3/23/1999 (6/20/1997)	Dynamically Reconfigurable Hardware System For Real-Time Control Of Processes Martel, Sylvain
(60/020249)	US	(6/21/1996)	Dynamically Reconfigurable Hardware System For Real-Time Control Of Processes Martel, Sylvain
(PCT/US1997/010763)	WO	(6/20/1997)	Dynamically Reconfigurable Hardware System For Real-Time Control Of Processes Hunter, Ian W.
(EP97953455.9)	EP	(12/24/1997)	Tiled Linear Host Texture Storage Mcdonald, Timothy J.
(JP10-530229)	JP	(12/24/1997)	Tiled Linear Host Texture Storage Mcdonald, Timothy J.

<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
(JP2008-172680)	JP	(12/24/1997)	Tiled Linear Host Texture Storage McDonald, Timothy J.
(KR10-1999-7005847)	KR	(12/24/1997)	Tiled Linear Host Texture Storage McDonald, Timothy J.
TWI104018 (TW086120013)	TW	6/21/1999 (12/30/1997)	Tiled Linear Host Texture Storage McDonald, Timothy J.
5844576 (08/773921)	US	12/1/1998 (12/30/1996)	Tiled Linear Host Texture Storage McDonald, Timothy J.
(PCT/US1997/023910)	WO	(12/24/1997)	Tiled Linear Host Texture Storage McDonald, Timothy J.
9773244 (14/446057)	US	9/26/2017 (7/29/2014)	Tracking Points Related To A User Account For Redemption Lotvin, Mikhail
5907831 (08/826550)	US	5/25/1999 (4/4/1997)	Computer Apparatus And Methods Supporting Different Categories Of Users Lotvin, Mikhail
6178407 (09/294761)	US	1/23/2001 (4/19/1999)	Computer Apparatus And Methods Supporting Different Categories Of Users Lotvin, Mikhail
(09/699959)	US	(10/30/2000)	Computer Apparatus And Methods Supporting Different Categories Of Users Lotvin, Mikhail
7203656 (10/184440)	US	4/10/2007 (6/28/2002)	Computer Apparatus And Methods Supporting Different Categories Of Users Lotvin, Mikhail
(11/045754)	US	(1/28/2005)	Computer Systems And Methods Supporting On-Line Interaction With Content, Purchasing, And Searching Lotvin, Mikhail

<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
(11/784363)	US	(4/6/2007)	A System And Method For Rewarding Users For Accessing Third Party Internet Content Lotvin, Mikhail
(09/329143)	US	(6/9/1999)	Computer Systems And Methods Supporting On-Line Interaction With Content, Purchasing, And Searching Lotvin, Mikhail
(10/790991)	US	(3/2/2004)	Computer Systems And Methods Supporting On-Line Interaction With Content, Purchasing, And Searching Lotvin, Mikhail
8676619 (12/953641)	US	3/18/2014 (11/24/2010)	Online System Enabling Control Of Educational And Advertising Content Lotvin, Mikhail
(12/953784)	US	(11/24/2010)	Computer Systems And Methods Supporting On-Line Interaction With Content, Purchasing, And Searching Lotvin, Mikhail
5867479 (08/884650)	US	2/2/1999 (6/27/1997)	Digital Multi-Channel Demultiplexer/Multiplex (Mcd/M Architecture) Thomas C. Butash
6091704 (09/241313)	US	7/18/2000 (2/1/1999)	Digital Multi-Channel Demultiplexer/Multiplex (Mcd/M Architecture) Thomas C. Butash
6185126 (08/805890)	US	2/6/2001 (3/3/1997)	Self-Initializing Ram-Based Programmable Device T. J. Rodgers
6026134 (08/878714)	US	2/15/2000 (6/19/1997)	Phase Locked Loop (Pll) With Linear Parallel Sampling Phase Detector Michael L. Duffy
6560306 (09/465565)	US	5/6/2003 (12/17/1999)	Phase Locked Loop (Pll) With Linear Parallel Sampling Phase Detector Michael L. Duffy

<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
6839394 (09/015989)	US	1/4/2005 (1/30/1998)	Frequency Difference Detector With Hysteresis Michael L. Duffy
(60/066152)	US	(11/19/1997)	Frequency Difference Detector With Hysteresis Michael L. Duffy
6294962 (09/207912)	US	9/25/2001 (12/9/1998)	Circuit(S), Architecture And Method(S) For Operating And/Or Tuning A Ring Oscillator Monte F. Mar
JP4067036 (JP09-359578)	JP	1/18/2008 (12/26/1997)	Output Buffer Circuit Mathew R. Arcoleo
KR10-0519253 (KR10-1997-0063666)	KR	9/28/2005 (11/28/1997)	Memory Having Selectable Output Mathew R. Arcoleo
5732027 (08/777488)	US	3/24/1998 (12/30/1996)	Memory Having Selectable Output Strength Mathew R. Arcoleo
5864506 (09/006772)	US	1/26/1999 (1/14/1998)	Memory Having Selectable Output Strength Mathew R. Arcoleo
6154207 (08/362318)	US	11/28/2000 (12/22/1994)	Interactive Language Editing In A Network Based Video On Demand System Robert D. Farris
5995257 (08/950452)	US	11/30/1999 (10/15/1997)	Add/Drop Multiplexer For Multiwavelength Pulse Oriented Optical Communications Systems William J. Johnson
MX215202 (MX00/006554)	MX	7/11/2003 (12/30/1998)	Long-Haul Terrestrial Optical Fiber Link Having Low-Power Optical Line Amplifiers With Integrated Dispersion Compensation Modules Andrew N. Robinson
(CA2316857)	CA	(12/30/1998)	A Long-Haul Terrestrial Optical Fiber Link Having Low-Power Optical Line Amplifiers With Integrated Dispersion Compensation Modules Andrew N. Robinson

<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
(EP98964322.6)	EP	(12/30/1998)	A Long-Haul Terrestrial Optical Fiber Link Having Low-Power Optical Line Amplifiers With Integrated Dispersion Compensation Modules Andrew N. Robinson
(HK01100976.7)	HK	(2/12/2001)	A Long-Haul Terrestrial Optical Fiber Link Having Low-Power Optical Line Amplifiers With Integrated Dispersion Compensation Modules Andrew N. Robinson
6005997 (09/001829)	US	12/21/1999 (12/31/1997)	Long-Haul Terrestrial Optical Fiber Link Having Low-Power Optical Line Amplifiers With Integrated Dispersion Compensation Modules Andrew N. Robinson
(PCT/US1998/027713)	WO	(12/30/1998)	A Long-Haul Terrestrial Optical Fiber Link Having Low-Power Optical Line Amplifiers With Integrated Dispersion Compensation Modules Andrew N. Robinson
6078596 (08/883548)	US	6/20/2000 (6/26/1997)	Method And System Of Sonet Line Trace Glenn Wellbrock
DE69728465.4 (DE69728465.4)	DE	4/7/2004 (4/15/1997)	Multi-Cycle Non-Parallel Data Encryption Engine Mark L. Buer
EP0802653 (EP97302549.7)	EP	4/7/2004 (4/15/1997)	Multi-Cycle Non-Parallel Data Encryption Engine Mark L. Buer
FR0802653 (FR97302549.7)	FR	4/7/2004 (4/15/1997)	Multi-Cycle Non-Parallel Data Encryption Engine Mark L. Buer
GB0802653 (GB97302549.7)	GB	4/7/2004 (4/15/1997)	Multi-Cycle Non-Parallel Data Encryption Engine Mark L. Buer
(JP09-097311)	JP	(4/15/1997)	Encrypting Or Decrypting Data And Methods To Protect The Data Transmission Equipment Mark L. Buer

<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
5835599 (08/632611)	US	11/10/1998 (4/15/1996)	Muti-Cycle Non-Parallel Data Encryption Engine Mark Leonard Buer
DE69924338.6 (DE69924338.6)	DE	3/23/2005 (3/29/1999)	Manufacture Of Trench-Gate Semiconductor Devices Jikui Luo
EP0996969 (EP99907817.3)	EP	3/23/2005 (3/29/1999)	Manufacture Of Trench-Gate Semiconductor Devices Jikui Luo
FR0996969 (FR99907817.3)	FR	3/23/2005 (3/29/1999)	Manufacture Of Trench-Gate Semiconductor Devices Jikui Luo
(GB9808234.0)	GB	(4/17/1998)	Manufacture Of Trench-Gate Semiconductor Devices Jikui Luo
GB0996969 (GB99907817.3)	GB	3/23/2005 (3/29/1999)	Manufacture Of Trench-Gate Semiconductor Devices Jikui Luo
(JP11-552665)	JP	(3/29/1999)	Trench Gate Semiconductor Device Manufacturing Method Jikui Luo
KR10-0538603 (KR10-1999-7011981)	KR	12/16/2005 (3/29/1999)	Manufacture Of Trench-Gate Semiconductor Devices Jikui Luo
6087224 (09/292406)	US	7/11/2000 (4/15/1999)	Manufacture Of Trench-Gate Semiconductor Devices Jikui Luo
(PCT/IB1999/000537)	WO	(3/29/1999)	Manufacture Of Trench-Gate Semiconductor Devices Jikui Luo
5945732 (08/815537)	US	8/31/1999 (3/12/1997)	Apparatus And Method Of Manufacturing A Warp Resistant Thermally Conductive Integrated Circuit Package Carmen D. Burns
6190939 (09/115293)	US	2/20/2001 (7/14/1998)	Method Of Manufacturing A Warp Resistant Thermally Conductive Circuit Package Carmen D. Burns

<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
5869353 (08/971499)	US	2/9/1999 (11/17/1997)	Modular Panel Stacking Process Aaron Uri Levy
(09/633297)	US	(8/3/2000)	Modular Panel Stacking Process Aaron Uri Levy
6282210 (09/133297)	US	8/28/2001 (8/12/1998)	Clock Driver With Instantaneously Selectable Phase And Method For Use In Data Communication Systems Russell Rapport
6310392 (09/221350)	US	10/30/2001 (12/28/1998)	Stacked Micro Ball Grid Array Packages Carmen D. Burns
6222737 (09/298664)	US	4/24/2001 (4/23/1999)	Universal Package And Method Of Forming The Same Andrew C. Ross
(09/527134)	US	(3/16/2000)	Unable To Verify Unable To Verify
6360433 (09/664938)	US	3/26/2002 (9/19/2000)	Universal Package And Method Of Forming The Same Andrew C. Ross
(PCT/US2000/004988)	WO	(2/25/2000)	Universal Package And Method Of Forming The Same Andrew C. Ross
(AU21892/97)	AU	(2/7/1997)	3d Stereo Browser For The Internet Charles S. Palm
(EP97914758.4)	EP	(2/7/1997)	3d Stereo Browser For The Internet Charles S. Palm
(JP09-530197)	JP	(2/7/1997)	3d Stereo Browser For The Internet Charles S. Palm
7038698 (09/117219)	US	5/2/2006 (2/7/1997)	3d Stereo Browser For The Internet Brandie D. Lynn

<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
7091991 (10/839581)	US	8/15/2006 (5/5/2004)	3d Stereo Browser For The Internet Charles S. Palm
7154506 (10/839584)	US	12/26/2006 (5/5/2004)	3d Stereo Browser For The Internet Charles S. Palm
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6766315 (09/704234)	US	7/20/2004 (4/30/1999)	Method And Apparatus For Simultaneously Accessing A Plurality Of Dispersed Databases Timothy G. Bratsos
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5893162 (08/796085)	US	4/6/1999 (2/5/1997)	Method And Apparatus For Allocation And Management Of Shared Memory With Data In Memory Stored As Multiple Linked Lists Joseph C. Lau
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(BR9906580)	BR	(5/28/1999)	Neonanoplasts And Microemulsion Technology For Inks And Ink Jet Printing Ronald Sinclair Nohr
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(JP2000-552206)	JP	(5/28/1999)	Neonanoplasts And Microemulsion Technology For Inks And Ink Jet Printing Ronald Sinclair Nohr
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<u>Patent or Application No.</u>	<u>Country</u>	<u>Grant Date Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
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DE69730305.5 (DE69730305.5)	DE	8/18/2004 (12/23/1997)	Serial Access System Semiconductor Storage Device Capable Of Reducing Access Time And Consumption Current Yoshiji Ohta
EP0851424 (EP97310490.4)	EP	8/18/2004 (12/23/1997)	Serial Access System Semiconductor Storage Device Capable Of Reducing Access Time And Consumption Current Yoshiji Ohta
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JP3897388 (JP08-349350)	JP	1/5/2007 (12/27/1996)	Serial Access Type Semiconductor Memory Device Yoshiji Ohta

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(KR10-1997-0074202)	KR	(12/26/1997)	Serial Access Type Semiconductor Memory Device Yoshiji Ohta
TWI104927 (TW086119331)	TW	7/1/1999 (12/19/1997)	Serial Access System Semiconductor Storage Device Capable Of Reducing Access Time And Consumption Current Yoshiji Ohta
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KR10-0262841 (KR10-1997-0065238)	KR	5/8/2000 (11/27/1997)	Semiconductor Memory Device Mikiro Okada
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(JP08-228902)	JP	(8/29/1996)	Non-Volatile Semiconductor Storage Device Shinichi Sato
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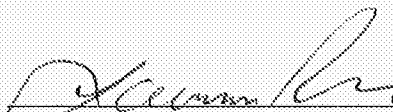
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DATED this 26th day of November 2019.

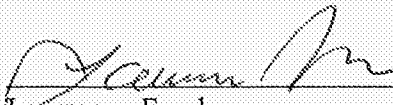
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Tamiras Per Pte. Ltd., LLC

By: 
Name: Lawrence Froeber
Title: Chief Financial Officer

ASSIGNEE:

Intellectual Ventures Assets 158 LLC

By: 
Name: Lawrence Froeber
Title: Chief Financial Officer