

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT6060504

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
ANDREW J. WALKER	04/13/2020
DAFNA BEERY	03/25/2020
PETER CUEVAS	04/07/2020
AMITAY LEVI	03/25/2020
RECEIVING PARTY DATA	
Name:	SPIN MEMORY, INC.
Street Address:	45500 NORTHPORT LOOP WEST
City:	FREMONT
State/Country:	CALIFORNIA
Postal Code:	94538
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	16828879
CORRESPONDENCE DATA	
Fax Number:	(408)971-4660
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	(408) 971-2573
Email:	emma@zilkakotab.com
Correspondent Name:	ZILKA-KOTAB, PC
Address Line 1:	1155 N. 1ST ST.
Address Line 2:	SUITE 105
Address Line 4:	SAN JOSE, CALIFORNIA 95112
ATTORNEY DOCKET NUMBER:	SPINP070/SP00162US001
NAME OF SUBMITTER:	RONALD B. FEECE
SIGNATURE:	/Ronald B. Feece/
DATE SIGNED:	04/14/2020
	This document serves as an Oath/Declaration (37 CFR 1.63).
Total Attachments: 5	

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COMBINED DECLARATION (37 C.F.R. 1.63) AND ASSIGNMENT FOR UTILITY APPLICATION USING AN APPLICATION DATA SHEET (37 C.F.R. 1.76)

Title of Invention: **DRAM WITH SELECTIVE EPITAXIAL TRANSISTOR AND BURIED BITLINE**

As a below named and undersigned inventor, I hereby declare that:

This declaration is directed to the attached application, or (if following box is checked):

United States application or PCT international application number 16/828,879, filed on March 24, 2020

The above-identified application was made or authorized to be made by me.

I believe that I am the original inventor or an original joint inventor of a claimed invention in the application.

I hereby state that I have reviewed and understand the contents of the above-identified application, including the claims.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, CFR § 1.56.

Whereas, I the undersigned inventor have invented certain new and useful improvements as set forth in the above-identified patent application and further identified by the Attorney Docket Number provided above in the header of this document.

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I the undersigned inventor hereby:

- 1) Sell, assign and transfer to **Spin Memory, Inc.**, a U.S. corporation having a place of business at 45500 Northport Loop West, Fremont, California 94538, (hereinafter referred to as "ASSIGNEE"), the entire right, title and interest in any and all improvements and inventions disclosed in, application(s) based upon, and Patent(s) (including foreign patents) granted upon the information which is disclosed in the above referenced application.
- 2) Authorize and request the Commissioner of Patents to issue any and all Letters Patents resulting from said application or any division(s), continuation(s), substitutes(s) or reissue(s) thereof to the ASSIGNEE.
- 3) Agree to execute all papers and documents and, entirely at the ASSIGNEE's expense, perform any acts which are reasonably necessary in connection with the prosecution of said application, as well as any derivative and applications thereof, foreign applications based thereon, and/or the enforcement of patents resulting from such applications.
- 4) Agree that the terms, covenants and conditions of this assignment shall inure to the benefit of the Assignee, its successors, assigns and other legal representative, and shall be binding upon the inventor(s), as well as the inventor's heirs, legal representatives and assigns.
- 5) Warrant and represent that I have not entered, and will not enter into any assignment, contract, or understanding that conflicts with this assignment.

I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) years, or both.

Signed on the date indicated beside my signature.

Legal Name of Inventor: Andrew J. Walker

Signature: 

Date: April 13, 2020

COMBINED DECLARATION (37 C.F.R. 1.63) AND ASSIGNMENT FOR UTILITY APPLICATION USING AN APPLICATION DATA SHEET (37 C.F.R. 1.76)

Title of Invention: DRAM WITH SELECTIVE EPITAXIAL TRANSISTOR AND BURIED BITLINE

As a below named and undersigned inventor, I hereby declare that:

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Signed on the date indicated beside my signature.

Legal Name of Inventor: Dafna Beery

✱

Signature: 

Date: 3/25/20

**COMBINED DECLARATION (37 C.F.R. 1.63) AND ASSIGNMENT FOR UTILITY
APPLICATION USING AN APPLICATION DATA SHEET (37 C.F.R. 1.76)**

Title of Invention: DRAM WITH SELECTIVE EPITAXIAL TRANSISTOR AND BURIED BITLINE

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Signed on the date indicated beside my signature.

Legal Name of Inventor: Peter Cuevas

Signature: _____

Date: 4/7/20

COMBINED DECLARATION (37 C.F.R. 1.63) AND ASSIGNMENT FOR UTILITY APPLICATION USING AN APPLICATION DATA SHEET (37 C.F.R. 1.76)

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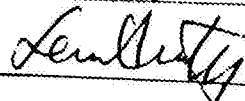
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Signed on the date indicated beside my signature.

Legal Name of Inventor: Amitay Levi

Signature: 

Date: 3/25/2020