506062011 05/15/2020

PATENT ASSIGNMENT COVER SHEET

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NATURE OF CONVEYANCE:	EXECUTIVE ORDER 9424, CONFIRMATORY LICENSE

CONVEYING PARTY DATA

Name	Execution Date
UNIVERSITY OF MINNESOTA	11/16/2018

RECEIVING PARTY DATA

Name:	NATIONAL SCIENCE FOUNDATION
Street Address:	2415 EISENHOWER AVENUE
Internal Address:	ROOM W 18000
City:	ALEXANDRIA
State/Country:	VIRGINIA
Postal Code:	22314

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	16165713

CORRESPONDENCE DATA

Fax Number: (703)292-9041

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Email: nsfpatents@nsf.gov

Correspondent Name: NATIONAL SCIENCE FOUNDATION

Address Line 1: 2415 EISENHOWER AVENUE

Address Line 2: ROOM W 18000

Address Line 4: ALEXANDRIA, VIRGINIA 22314

NAME OF SUBMITTER:	DANA THIBODEAU
SIGNATURE:	/DMT/
DATE SIGNED:	05/15/2020

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PATENT 506062011 REEL: 052679 FRAME: 0192

INVENTION: 20180024 EIR:1450401-17-0081

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Invention Title: Parallel Computing Using Stochastic Circuits and Deterministic Shuffling
Networks .
Inventor(s): Kiarash Bazargan, David J Lilja, Scheil Mchajer, M arcRiedel, Zhiheng Wang, Sayed
Abdolrasoul Faraji
U.S. Filing/Issue Date: 10/19/2018
Patent or Application Serial No.: 16/165,713
Grant/Contract Number(s): CCF1408123
Foreign Applications filed/intended in (countries):
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By Rebecca Gerber (Institutional Business Official) (Signature)
Title Associate Director
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At McNamara Alumni Center, Suite 280 200 Oak Street S.E. Minneapolis, MN 55455 (Business Address)

PATENT REEL: 052679 FRAME: 0193

RECORDED: 05/15/2020