506103152 06/11/2020

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
ROBERT W STRONG	11/25/2019
JAMES RUANE	11/20/2019

RECEIVING PARTY DATA

Name:	MICRON TECHNOLOGY, INC.
Street Address:	8000 SO. FEDERAL WAY
City:	BOISE
State/Country:	IDAHO
Postal Code:	83716-9632

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	16694538

CORRESPONDENCE DATA

Fax Number: (612)339-3061

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 612-373-6900
Email: request@slwip.com

Correspondent Name: SCHWEGMAN LUNDBERG & WOESSNER, P.A.

Address Line 1: P.O. BOX 2938

Address Line 4: MINNEAPOLIS, MINNESOTA 55402-0938

ATTORNEY DOCKET NUMBER:	303.I89US1
NAME OF SUBMITTER:	JULIE A. NELSON
SIGNATURE:	/ Julie A. Nelson /
DATE SIGNED:	06/11/2020

Total Attachments: 5

source=303l89US1_assignment_signed_#page1.tif source=303l89US1_assignment_signed_#page2.tif source=303l89US1_assignment_signed_#page3.tif source=303l89US1_assignment_signed_#page4.tif source=303l89US1_assignment_signed_#page5.tif

> PATENT REEL: 052912 FRAME: 0661

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RECORDATION FORM COVER SHEET PATENTS ONLY

Atty Ref/Docket No.: 303.189US1 Patent and Trademark Office To the Director of the U.S. Patent and Trademark Office: Please record the attached original documents or copy thereof. 2. Name and address of receiving party(ies): 1. Name of conveying party(ies): Name: Micron Technology, Inc. Robert W. Strong, James Ruane Street Address: 8000 So. Federal Way Additional name(s) of conveying party(ies) attached? City: <u>Boise</u> State: <u>ID</u> Zip: <u>83716-9632</u> []Yes [X]No Country: United States of America 3. Nature of conveyance: Additional name(s) & address(es) attached? []Yes [X]No [X] Assignment [] Merger [] Security Agreement [] Change of Name [] Other Execution Date: November 25, 2019, November 20, <u>201</u>9 4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application is: A. Patent Application No.(s) B. Patent No.(s) Serial No. 16/694,538 Additional numbers attached? []Yes [X]No 6. Total number of applications and patents involved: 1 5. Name and address of party to whom correspondence concerning document should be mailed: 7. Total fee (37 CFR 3.41):\$ <u>0.00</u> Name: Garrett M. Hall [Enclosed] [Authorized to be charged to deposit account Address: 19-0743 Schwegman Lundberg & Woessner, P.A. P.O. Box 2938 8. Please charge any additional fees or credit any over Minneapolis, Minnesota 55402 payments to our Deposit Account No.: 19-0743 DO NOT USE THIS SPACE 9. Statement and signature. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Garrett Hall/
Signature 6/11/20 Garrett M. Hall/Reg. No. 67,888 Date Name of Person Signing Total number of pages including cover sheet: 5 Mail documents to be recorded with required cover sheet information to: **Commissioner of Patents and Trademarks**

Mail Stop Assignment Recordation Services P.O. Box 1450

Alexandria, VA 22313-1450

ASSIGNMENT

WHEREAS, Robert W. Strong and James Ruane (hereinafter the "Undersigned") have made one or more inventions and other subject matter (hereinafter collectively referred to as the "Invention"); as described in the patent application filed on November 25, 2019, assigned US application serial number 16/694,538 and titled SECURE REPLACEABLE VERIFICATION KEY ARCHITECTURE IN A MEMORY SUB-SYSTEM and as described in a provisional patent application filed on May 21, 2019 assigned United States application serial number 62/850,990, and titled SECURE REPLACEABLE VERIFICATION KEY ARCHITECTURE IN A MEMORY SUB-SYSTEM.

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency, and adequacy of which are hereby acknowledged by the Undersigned, the Undersigned do hereby irrevocably and unconditionally:

CONVEY, ASSIGN, AND TRANSFER to Micron Technology, Inc. (the "Assignee"), a corporation of the State of Delaware, having a place of business at 8000 So. Federal Way, Boise, ID 83716-9632, the Undersigned's entire right, title, and interest for the United States and all foreign countries and jurisdictions in and to:

the Invention which is disclosed in the above-identified application or applications;

such application or applications, and all divisional, continuing (including continuation-in-part), substitute, renewal, reissue, and all other applications for a patent or patents which have been or shall be filed in the United States (including all provisional and non-provisional applications), and in all foreign countries and jurisdictions based in whole or in part on any of such Invention (including any application for a utility model or an innovation patent application);

all original and reissued patents which have been or shall be issued in the United States and all foreign countries and jurisdictions based in whole or in part on any of such Invention;

including the right to claim priority to the above-identified patent application or applications in relation to subject matter based in whole or in part on the above-identified patent application or applications and any of the foregoing including the right to file foreign applications under the provisions of any convention or treaty;

and including the right to all causes of action, remedies, and other enforcement rights related to the above-identified application or applications, including without limitation the right to sue for past, present, or future infringement, misappropriation, or violation of any and all rights related to the above-identified patent application or applications and any of the foregoing, including the right to obtain and collect damages for past, present, or future infringement;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such Invention to the Assignee;

AUTHORIZE AND REQUEST that any attorney associated with U.S. Patent and Trademark Office (USPTO) Customer No. 73115 may (directly or through his/her designee) delete, insert, or alter any

PATENT REEL: 052912 FRAME: 0663 Assignment Docket No: 303.I89US1
Assignors: Robert W. Strong et al. Client Ref. No. 2019-0645.00/US

Title: SECURE REPLACEABLE VERIFICATION KEY ARCHITECTURE IN A MEMORY SUB-SYSTEM

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information related to the above-identified patent application or applications or any of the foregoing, after execution of this Assignment;

WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or shall be made to others by the Undersigned, and that the full right to convey the same as herein expressed is possessed by the Undersigned;

COVENANT, that when requested and without compensation, but at the expense of the Assignee, in order to carry out in good faith the intent and purpose of this Assignment, the Undersigned shall (1) execute all provisional, non-provisional, divisional, continuing (including continuation-in-part), substitute, renewal, reissue, and all other patent applications for the Invention; (2) execute all rightful oaths, declarations, assignments, powers of attorney and other papers for the Invention; (3) communicate to the Assignee all facts known to the Undersigned relating to the Invention and the history thereof; (4) cooperate with the Assignee in any interference, reexamination, reissue, opposition, dispute, or litigation involving any of the applications or patents for the Invention; and (5) take such further actions as the Assignee shall reasonably consider necessary or desirable for vesting title to such Invention in the Assignee, or for securing, maintaining and enforcing proper patent protection for the Invention;

COVENANT, that should any provision of this agreement be held unenforceable by an authority of competent jurisdiction, such a ruling shall not affect the validity and enforceability of the remaining provisions.

THIS AGREEMENT IS TO BE BINDING on the heirs, assigns, representatives, and successors of the Undersigned, and is to extend to the benefit of the successors, assigns, and nominees of the Assignee.

AGREED as of the date of my signature below:

PATENT REEL: 052912 FRAME: 0664 Assignment

Docket No: 303.189US1 Cliem Ref. No. 2019-0645.00/US

Assigners. Robert W. Strong et al.
Tide: SECURE REPLACEABLE VERIFICATION KEY ARCHITECTURE IN A MEMORY SUB-SYSTEM

Assignor:

(Signature): ≤

Name: Robert W. Strong

City/State or Country: Folsom, CA

Date:

11-25-2019

PATENT REEL: 052912 FRAME: 0665

Docket No: 303.I89US1 Assignment Assignors: Robert W. Strong et al.
Title: SECURE REPLACEABLE VERIFICATION KEY ARCHITECTURE IN A MEMORY SUB-SYSTEM Client Ref. No. 2019-0645.00/US

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Assignor:		
(Signature):	/ James Ruane /	
	Name: <u>James Ruane</u> City/State or Country: <u>San Jose, CA</u>	
Date:	11/20/2019	

PATENT REEL: 052912 FRAME: 0666

RECORDED: 06/11/2020