

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT6109254

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
CAVIUM INTERNATIONAL	12/31/2019

RECEIVING PARTY DATA

Name:	MARVELL ASIA PTE, LTD.
Street Address:	TAI SENG CENTRE, 3 IRVING ROAD #10-01
City:	SINGAPORE
State/Country:	SINGAPORE
Postal Code:	369522

PROPERTY NUMBERS Total: 992

Property Type	Number
Application Number:	12604343
Application Number:	12604351
Application Number:	13965635
Application Number:	12604323
Application Number:	12604358
Application Number:	12563938
Application Number:	12977844
Application Number:	12118664
Application Number:	13168251
Application Number:	13409061
Application Number:	13190419
Application Number:	13039099
Application Number:	09863081
Application Number:	09862384
Application Number:	10993107
Application Number:	11198070
Application Number:	11738864
Application Number:	11006329
Application Number:	11474158
Application Number:	11501324

PATENT

Property Type	Number
Application Number:	11508593
Application Number:	11607416
Application Number:	11405346
Application Number:	11442485
Application Number:	11451220
Application Number:	11405646
Application Number:	11676946
Application Number:	11678054
Application Number:	12247833
Application Number:	12118650
Application Number:	12139447
Application Number:	12329514
Application Number:	13190288
Application Number:	12369702
Application Number:	12563927
Application Number:	13111785
Application Number:	13790333
Application Number:	13171380
Application Number:	13107659
Application Number:	13350624
Application Number:	13625820
Application Number:	15228161
Application Number:	13346534
Application Number:	13413029
Application Number:	14843394
Application Number:	13523826
Application Number:	13966208
Application Number:	13671132
Application Number:	14634605
Application Number:	16114084
Application Number:	10699761
Application Number:	10695166
Application Number:	11702348
Application Number:	10816409
Application Number:	11148988
Application Number:	10926699
Application Number:	11090109
Application Number:	12217962

Property Type	Number
Application Number:	12613627
Application Number:	11205615
Application Number:	11247805
Application Number:	11303364
Application Number:	11438177
Application Number:	12941039
Application Number:	13974551
Application Number:	11510934
Application Number:	11644598
Application Number:	12290181
Application Number:	12960481
Application Number:	13398748
Application Number:	14459260
Application Number:	14180082
Application Number:	14961797
Application Number:	15683618
Application Number:	14961810
Application Number:	14995141
Application Number:	15011384
Application Number:	15065753
Application Number:	15853504
Application Number:	15063468
Application Number:	15083041
Application Number:	14858918
Application Number:	15276250
Application Number:	16057609
Application Number:	14858902
Application Number:	16112583
Application Number:	16365510
Application Number:	14961802
Application Number:	14961806
Application Number:	15222742
Application Number:	15063474
Application Number:	15478046
Application Number:	15398583
Application Number:	15669717
Application Number:	15451920
Application Number:	15620618

Property Type	Number
Application Number:	15478051
Application Number:	16354512
Application Number:	16354525
Application Number:	16354542
Application Number:	16419585
Application Number:	16419625
Application Number:	16537008
Application Number:	16514844
Application Number:	16171298
Application Number:	16362479
Application Number:	16362494
Application Number:	16362500
Application Number:	16143254
Application Number:	16162751
Application Number:	16419643
Application Number:	16554000
Application Number:	16554023
Application Number:	16554036
Application Number:	62868778
Application Number:	15175516
Application Number:	12789728
Application Number:	12818549
Application Number:	12857080
Application Number:	12960474
Application Number:	13235283
Application Number:	15364030
Application Number:	15276259
Application Number:	08971387
Application Number:	08972117
Application Number:	09030865
Application Number:	09104731
Application Number:	09386109
Application Number:	09249873
Application Number:	09257803
Application Number:	09280506
Application Number:	09281369
Application Number:	09282341
Application Number:	09328738

Property Type	Number
Application Number:	09346793
Application Number:	10752390
Application Number:	10166570
Application Number:	09587689
Application Number:	11765724
Application Number:	09755642
Application Number:	11070858
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Application Number:	10025512
Application Number:	09938166
Application Number:	10120266
Application Number:	10284273
Application Number:	10285226
Application Number:	11098831
Application Number:	09992637
Application Number:	10025509
Application Number:	10068677
Application Number:	10052054
Application Number:	10086665
Application Number:	11422373
Application Number:	10469467
Application Number:	10469529
Application Number:	10092328
Application Number:	10234895
Application Number:	10186291
Application Number:	10600779
Application Number:	10200393
Application Number:	10200487
Application Number:	11608634
Application Number:	10212425
Application Number:	10213237
Application Number:	11845688
Application Number:	10241153
Application Number:	11682199
Application Number:	10263858
Application Number:	10295976
Application Number:	10375643
Application Number:	10387151

Property Type	Number
Application Number:	11040266
Application Number:	10397083
Application Number:	10410773
Application Number:	10411909
Application Number:	10411944
Application Number:	10411967
Application Number:	10411943
Application Number:	10411945
Application Number:	10417523
Application Number:	10423499
Application Number:	11283210
Application Number:	10445695
Application Number:	10601534
Application Number:	10619719
Application Number:	12389308
Application Number:	10620040
Application Number:	10889551
Application Number:	10889256
Application Number:	10889259
Application Number:	12198644
Application Number:	10889337
Application Number:	10889635
Application Number:	10889588
Application Number:	10894827
Application Number:	12427966
Application Number:	10889267
Application Number:	10889255
Application Number:	10894492
Application Number:	10895175
Application Number:	10674943
Application Number:	10894689
Application Number:	12267188
Application Number:	10894491
Application Number:	12191890
Application Number:	10894536
Application Number:	10894587
Application Number:	12502973
Application Number:	10894629

Property Type	Number
Application Number:	12189502
Application Number:	10894726
Application Number:	10664548
Application Number:	10894595
Application Number:	10894627
Application Number:	10683622
Application Number:	10696471
Application Number:	10719077
Application Number:	10723004
Application Number:	10894586
Application Number:	12580169
Application Number:	10894547
Application Number:	12697994
Application Number:	13177314
Application Number:	10894597
Application Number:	12141519
Application Number:	10894579
Application Number:	12189497
Application Number:	10894546
Application Number:	10798527
Application Number:	10798468
Application Number:	10894978
Application Number:	10797977
Application Number:	11748160
Application Number:	10806691
Application Number:	10894732
Application Number:	10848485
Application Number:	10894917
Application Number:	12473150
Application Number:	10847756
Application Number:	10878595
Application Number:	29209313
Application Number:	10894529
Application Number:	10927170
Application Number:	11933860
Application Number:	10931851
Application Number:	12407153
Application Number:	10931850

Property Type	Number
Application Number:	10935919
Application Number:	10940355
Application Number:	10956955
Application Number:	10956501
Application Number:	13604261
Application Number:	10956502
Application Number:	10957465
Application Number:	10956718
Application Number:	10956717
Application Number:	10961463
Application Number:	12031585
Application Number:	10963287
Application Number:	10994829
Application Number:	11002728
Application Number:	11005490
Application Number:	11015343
Application Number:	11018055
Application Number:	11024002
Application Number:	14159210
Application Number:	11037922
Application Number:	11039189
Application Number:	11346106
Application Number:	11355622
Application Number:	11097024
Application Number:	11100063
Application Number:	11220899
Application Number:	11221408
Application Number:	11224728
Application Number:	14040323
Application Number:	14337759
Application Number:	15134919
Application Number:	11335189
Application Number:	11225373
Application Number:	12574263
Application Number:	11218727
Application Number:	11126988
Application Number:	11132020
Application Number:	11140891

Property Type	Number
Application Number:	11201036
Application Number:	11222592
Application Number:	11223693
Application Number:	11222936
Application Number:	11252524
Application Number:	11257693
Application Number:	11291430
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Application Number:	11318116
Application Number:	11328358
Application Number:	11343596
Application Number:	11357515
Application Number:	11362690
Application Number:	11363365
Application Number:	11376703
Application Number:	11376955
Application Number:	11378838
Application Number:	11396748
Application Number:	11430704
Application Number:	11453500
Application Number:	11477161
Application Number:	11491357
Application Number:	11493978
Application Number:	11461387
Application Number:	11502282
Application Number:	11469377
Application Number:	11522672
Application Number:	11533873
Application Number:	11555180
Application Number:	13241012
Application Number:	11555123
Application Number:	11560317
Application Number:	11565444
Application Number:	11615329
Application Number:	11669573
Application Number:	11669775
Application Number:	12684054
Application Number:	11686417

Property Type	Number
Application Number:	11742282
Application Number:	11694698
Application Number:	12598544
Application Number:	11752212
Application Number:	11761279
Application Number:	11762578
Application Number:	13330085
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Application Number:	13190165
Application Number:	12111057
Application Number:	11842830
Application Number:	11849122
Application Number:	11864651
Application Number:	13026007
Application Number:	11982391
Application Number:	13311244
Application Number:	11982433
Application Number:	12052130
Application Number:	11986975
Application Number:	11986970
Application Number:	11963566
Application Number:	12751955
Application Number:	11966857
Application Number:	12019094
Application Number:	12034913
Application Number:	12392004
Application Number:	12037847
Application Number:	12039604
Application Number:	12082246
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Application Number:	12988348
Application Number:	12135973
Application Number:	12214789
Application Number:	12489241
Application Number:	13610095
Application Number:	12165464
Application Number:	12505430
Application Number:	12536336

Property Type	Number
Application Number:	12539156
Application Number:	13758543
Application Number:	12229617
Application Number:	12547414
Application Number:	12200649
Application Number:	12201304
Application Number:	12201359
Application Number:	12586169
Application Number:	12277129
Application Number:	13905711
Application Number:	14516807
Application Number:	12410294
Application Number:	12556064
Application Number:	13273008
Application Number:	12412050
Application Number:	12411176
Application Number:	12615152
Application Number:	12423626
Application Number:	12415873
Application Number:	12315080
Application Number:	12552222
Application Number:	12731065
Application Number:	13711380
Application Number:	12410277
Application Number:	12551302
Application Number:	12774574
Application Number:	12471331
Application Number:	13447652
Application Number:	12572835
Application Number:	13856848
Application Number:	12544658
Application Number:	13221737
Application Number:	12859690
Application Number:	12572810
Application Number:	12651270
Application Number:	13111475
Application Number:	12691629
Application Number:	12695648

Property Type	Number
Application Number:	13713650
Application Number:	14488724
Application Number:	12730139
Application Number:	13770828
Application Number:	12759316
Application Number:	13644203
Application Number:	13090882
Application Number:	12769463
Application Number:	14452873
Application Number:	15192214
Application Number:	15499177
Application Number:	14452878
Application Number:	12774608
Application Number:	12795563
Application Number:	12826105
Application Number:	13489016
Application Number:	12914852
Application Number:	12848184
Application Number:	13216598
Application Number:	12901409
Application Number:	12902020
Application Number:	12901994
Application Number:	12941621
Application Number:	12964295
Application Number:	13325943
Application Number:	14629770
Application Number:	13326091
Application Number:	14026293
Application Number:	14746674
Application Number:	13369652
Application Number:	13968523
Application Number:	13369562
Application Number:	13397550
Application Number:	13397303
Application Number:	13396824
Application Number:	13369579
Application Number:	13370784
Application Number:	14639476

Property Type	Number
Application Number:	13029018
Application Number:	13035114
Application Number:	13168395
Application Number:	13087817
Application Number:	13888851
Application Number:	13067051
Application Number:	14480469
Application Number:	13067050
Application Number:	13105401
Application Number:	13168450
Application Number:	13168323
Application Number:	14632448
Application Number:	13067880
Application Number:	14329843
Application Number:	13181270
Application Number:	13181251
Application Number:	13193462
Application Number:	13565271
Application Number:	13565784
Application Number:	13664015
Application Number:	13831487
Application Number:	13686269
Application Number:	14570626
Application Number:	13565389
Application Number:	13565406
Application Number:	15145052
Application Number:	13565727
Application Number:	13599276
Application Number:	14138931
Application Number:	13565767
Application Number:	14082365
Application Number:	13565741
Application Number:	13565743
Application Number:	13565746
Application Number:	13565749
Application Number:	14592384
Application Number:	13565775
Application Number:	13831232

Property Type	Number
Application Number:	13831191
Application Number:	13565735
Application Number:	13565736
Application Number:	13565459
Application Number:	14230698
Application Number:	13565422
Application Number:	13901220
Application Number:	14660132
Application Number:	13843353
Application Number:	14583968
Application Number:	13220249
Application Number:	13238485
Application Number:	14036920
Application Number:	13433146
Application Number:	13397002
Application Number:	13272975
Application Number:	14828884
Application Number:	13272937
Application Number:	13274767
Application Number:	13277613
Application Number:	13281059
Application Number:	13280756
Application Number:	14466384
Application Number:	13280738
Application Number:	14023953
Application Number:	13281052
Application Number:	14492557
Application Number:	13280841
Application Number:	13280768
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Application Number:	14747790
Application Number:	13283252
Application Number:	13284289
Application Number:	13285773
Application Number:	14670934
Application Number:	13403697
Application Number:	13285629

Property Type	Number
Application Number:	13303885
Application Number:	14863484
Application Number:	13447521
Application Number:	13481122
Application Number:	13790395
Application Number:	13790471
Application Number:	13790499
Application Number:	13902427
Application Number:	13790846
Application Number:	13594343
Application Number:	13644535
Application Number:	13649391
Application Number:	13654308
Application Number:	14062418
Application Number:	13660928
Application Number:	14058887
Application Number:	13924107
Application Number:	13852885
Application Number:	13852872
Application Number:	13678108
Application Number:	13678121
Application Number:	13678398
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Application Number:	13678055
Application Number:	13678086
Application Number:	13678161
Application Number:	13678135
Application Number:	13678378
Application Number:	13678385
Application Number:	13678377
Application Number:	13678100
Application Number:	13678367
Application Number:	14749964
Application Number:	13678145
Application Number:	13830717
Application Number:	13678418
Application Number:	13678397

Property Type	Number
Application Number:	13678406
Application Number:	13762715
Application Number:	13689607
Application Number:	13782673
Application Number:	13834838
Application Number:	14100856
Application Number:	13755770
Application Number:	14194567
Application Number:	15140424
Application Number:	14142612
Application Number:	13830492
Application Number:	13831415
Application Number:	13830395
Application Number:	14207928
Application Number:	14207933
Application Number:	13841743
Application Number:	13896798
Application Number:	13844451
Application Number:	13840867
Application Number:	14922449
Application Number:	13843992
Application Number:	13843308
Application Number:	13843782
Application Number:	14201692
Application Number:	13902453
Application Number:	13906207
Application Number:	13921090
Application Number:	13922911
Application Number:	13949095
Application Number:	13954661
Application Number:	14200939
Application Number:	14217935
Application Number:	14015929
Application Number:	14014803
Application Number:	14015601
Application Number:	14186978
Application Number:	14186913
Application Number:	14015248

Property Type	Number
Application Number:	14015607
Application Number:	14142623
Application Number:	14142631
Application Number:	14142643
Application Number:	14676791
Application Number:	14142653
Application Number:	14034200
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Application Number:	14036709
Application Number:	14038156
Application Number:	14038383
Application Number:	14038189
Application Number:	15475718
Application Number:	14038225
Application Number:	14040431
Application Number:	14040577
Application Number:	14040563
Application Number:	14040532
Application Number:	14039846
Application Number:	14041961
Application Number:	14042111
Application Number:	14042403
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Application Number:	14088082
Application Number:	14151748
Application Number:	14106841
Application Number:	14106827
Application Number:	14140503
Application Number:	14140488
Application Number:	14140494

Property Type	Number
Application Number:	14140585
Application Number:	14141076
Application Number:	14141096
Application Number:	14141104
Application Number:	14142511
Application Number:	15446259
Application Number:	15446297
Application Number:	15923851
Application Number:	14142497
Application Number:	14141987
Application Number:	14141681
Application Number:	14143586
Application Number:	14144260
Application Number:	14144270
Application Number:	15167704
Application Number:	15991810
Application Number:	14145374
Application Number:	14145918
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Application Number:	14171108
Application Number:	14171290
Application Number:	14186743
Application Number:	15601493
Application Number:	14630554
Application Number:	14191163
Application Number:	14192100

Property Type	Number
Application Number:	14194049
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Application Number:	14193899
Application Number:	14194038
Application Number:	14193658
Application Number:	14201594
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Application Number:	14252390
Application Number:	14252299
Application Number:	14252354
Application Number:	14252293
Application Number:	14288121
Application Number:	14264927
Application Number:	14264957
Application Number:	14300552
Application Number:	14496916
Application Number:	14537758
Application Number:	15041892
Application Number:	14289533
Application Number:	14289548
Application Number:	15675336
Application Number:	14723858
Application Number:	14302351
Application Number:	15202428
Application Number:	16019780
Application Number:	14302706
Application Number:	14309659
Application Number:	15457970
Application Number:	14309726
Application Number:	14309802
Application Number:	15457959
Application Number:	14309789

Property Type	Number
Application Number:	15653021
Application Number:	14309773
Application Number:	14309763
Application Number:	14309739
Application Number:	14309679
Application Number:	14309650
Application Number:	14309633
Application Number:	14309619
Application Number:	14309603
Application Number:	14312130
Application Number:	14325841
Application Number:	15703638
Application Number:	14663912
Application Number:	14328951
Application Number:	14328923
Application Number:	14331105
Application Number:	14339086
Application Number:	14724273
Application Number:	14447732
Application Number:	14464535
Application Number:	14470309
Application Number:	14470337
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Application Number:	16232882
Application Number:	14493865
Application Number:	14494229
Application Number:	15672082
Application Number:	14493897
Application Number:	14493953
Application Number:	14498580

Property Type	Number
Application Number:	14716689
Application Number:	14870675
Application Number:	14514708
Application Number:	14514596
Application Number:	14521333
Application Number:	14521354
Application Number:	15969681
Application Number:	14521359
Application Number:	14521367
Application Number:	14530388
Application Number:	14667485
Application Number:	14536937
Application Number:	14932791
Application Number:	14538484
Application Number:	14540414
Application Number:	14540175
Application Number:	14540436
Application Number:	14540379
Application Number:	14542350
Application Number:	14542039
Application Number:	14542298
Application Number:	14541807
Application Number:	14541902
Application Number:	14541882
Application Number:	14541977
Application Number:	14616580
Application Number:	14542060
Application Number:	14542136
Application Number:	15890921
Application Number:	14542118
Application Number:	14542188
Application Number:	14542202
Application Number:	14672787
Application Number:	14541952
Application Number:	14940982
Application Number:	14541841
Application Number:	14542485
Application Number:	16132424

Property Type	Number
Application Number:	14542383
Application Number:	14541726
Application Number:	14541616
Application Number:	14541498
Application Number:	14541685
Application Number:	14541971
Application Number:	14542528
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Application Number:	14617610
Application Number:	14617546
Application Number:	14617645
Application Number:	14632200
Application Number:	14638266
Application Number:	14638194
Application Number:	14637533
Application Number:	15065799

Property Type	Number
Application Number:	14662405
Application Number:	14664680
Application Number:	14667568
Application Number:	15862509
Application Number:	16126644
Application Number:	14667488
Application Number:	14672025
Application Number:	14672006
Application Number:	14671900
Application Number:	14671983
Application Number:	14671993
Application Number:	14672016
Application Number:	14673835
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Application Number:	16696789
Application Number:	16694481
Application Number:	16699520
Application Number:	11949755
Application Number:	12176750
Application Number:	11617716

CORRESPONDENCE DATA

Fax Number: (248)641-0270

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 248-641-1600

Email: sstevens@hdp.com

Correspondent Name: HARNESS, DICKEY & PIERCE, P.L.C.

Address Line 1: P.O. BOX 828

Address Line 4: BLOOMFIELD HILLS, MICHIGAN 48303

ATTORNEY DOCKET NUMBER:	5059-500027
NAME OF SUBMITTER:	STEPHANIE STEVENS
SIGNATURE:	/Stephanie Stevens/
DATE SIGNED:	05/15/2020

Total Attachments: 393

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ASSIGNMENT AND QUITCLAIM TRANSFER

WHEREAS, Cavium International, a corporation organized under the laws of Cayman Islands, and with offices at c/o Estera Trust (Cayman) Limited, PO Box 1350, Clifton House, 75 Fort Street, Grand Cayman KY1-1108, Cayman Islands (hereinafter "**Assignor**") owns the patents and patent applications listed in **Exhibit A** attached hereto and incorporated herein by this reference (hereinafter "**Group A**") and may have an ownership interest in some or all of the patents and patent applications listed in **Exhibit B** attached hereto and incorporated herein by this reference (hereinafter "**Group B**");

WHEREAS, Marvell Asia Pte, Ltd., a corporation organized under the laws of Singapore, and with offices at Tai Seng Centre, 3 Irving Road, #10-01 Singapore 369522, a limited liability corporation (hereinafter "**Assignee**"), desires to acquire the Assignor's ownership interest in, to and under the **Group A** and the **Group B**; and

WHEREAS, Assignor has executed on an effective date of December 31, 2019 (hereinafter "**Effective Date**") that certain Transfer and Assignment Agreement assigning, among other things, all interest in and to the **Group A** and transferring by quitclaim its ownership interest, if any, in and to the **Group B** to Assignee (the "**Purchase Agreement**").

NOW, THEREFORE, for consideration of one dollar (\$1.00) and other good and valuable consideration paid by Assignee to Assignor, the receipt and sufficiency of which hereby is acknowledged, Assignor does hereby sell, assign and transfer to Assignee its entire interest in and to the **Group A**, including all divisions, continuations, reexaminations, reissues, and foreign counterparts of the applications and patent registrations for the **Group A** (and the right to claim priority and the right to apply for any of the foregoing); including assignment of any and all provisional applications that are relied upon for priority; all rights to causes of action and remedies related thereto (including, without limitation, the right to sue for past, present or future infringement, misappropriation or violation of rights related to the foregoing); and any and all other rights and interests arising out of, in connection with or in relation to the **Group A**.

FURTHER, for consideration of one dollar (\$1.00) and other good and valuable consideration paid by Assignee to Assignor, the receipt and sufficiency of which hereby is acknowledged, Assignor does hereby **quitclaim** sell, assign and transfer to Assignee its ownership interest, if any, in and to the **Group B**, including all divisions, continuations, reexaminations, reissues, and foreign counterparts of the applications and patent registrations for the **Group B** (and the right to claim priority and the right to apply for any of the foregoing); including assignment of any and all provisional applications that are relied upon for priority; all rights to causes of action and remedies related thereto (including, without limitation, the right to sue for past, present or future infringement, misappropriation or violation of rights related to the foregoing); and any and all other rights and interests arising out of, in connection with or in relation to the **Group B**.

FURTHER, nothing contained herein shall be deemed to alter or amend the terms and provisions of the Purchase Agreement and in the event of any conflict between the terms and provisions of this Assignment and the Purchase Agreement, the terms and provisions of the Purchase Agreement shall be deemed to govern and be controlling in all circumstances. This Assignment is executed pursuant to the Purchase Agreement and is entitled to the benefits and subject to the

provisions thereof and shall bind and inure to the benefit of the parties thereto and their respective successors and assigns.

FURTHER, Assignor hereby covenants and agrees to execute and deliver, at the request of Assignee, such further instruments of transfer and assignment and to take any other action as such Assignee may reasonably request to more effectively consummate the assignments contemplated by this Assignment. Specifically, Assignor agrees to, at Assignee's expense, execute, acknowledge and deliver such further documents, instruments, conveyances and assurances and take such further actions as may be reasonably required to register in the name of Assignee the assignment of any of the Patents in **Group A** and/or the **Group B** in any appropriate governmental agency or registrar.

IN WITNESS WHEREOF, Assignor and Assignee have executed and delivered this Patent Assignment by their duly authorized representatives at 11:58 pm PST on the **Effective Date**.

ASSIGNOR:

Cavium International,
a corporation organized under the laws of Cayman Islands

By: _____

Philip Anderson
Director

ASSIGNEE:

Marvell Asia Pte, Ltd.,
a corporation organized under the laws of Singapore

By: _____

Steven Parker
Director

provisions thereof and shall bind and inure to the benefit of the parties thereto and their respective successors and assigns.

FURTHER, Assignor hereby covenants and agrees to execute and deliver, at the request of Assignee, such further instruments of transfer and assignment and to take any other action as such Assignee may reasonably request to more effectively consummate the assignments contemplated by this Assignment. Specifically, Assignor agrees to, at Assignee's expense, execute, acknowledge and deliver such further documents, instruments, conveyances and assurances and take such further actions as may be reasonably required to register in the name of Assignee the assignment of any of the Patents in **Group A** and/or the **Group B** in any appropriate governmental agency or registrar.

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By: _____

Philip Anderson
Director

ASSIGNEE:

Marvell Asia Pte, Ltd.,
a corporation organized under the laws of Singapore

By: _____

Steven Parker
Director

Exhibit A - 3A CI TO MAPL Group A

Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
AQUA.P102	AQUA.P102C1	US	12/604,343				Unknown	
AQUA.P103	AQUA.P103C1	US	12/604,351				Unknown	
AQUA.P103	AQUA.P103C2	US	13/965,635				Unknown	
AQUA.P104	AQUA.P104C1	US	12/604,323				Unknown	
AQUA.P105	AQUA.P105C1	US	12/604,358				Unknown	
AQUA.P106	AQUA.P106C1	US	12/563,938				Unknown	
AQUA.P106	AQUA.P106CIP1	US	12/977,844				Unknown	
AQUA.P108	AQUA.P108C1	US	12/118,664				Unknown	
AQUA.P108	AQUA.P108C2	US	13/168,251				Unknown	
AQUA.P109	AQUA.P109	US	13/409,061				Unknown	
AQUA.P110	AQUA.P110	US	13/190,419				Unknown	
AQUA.P111	AQUA.P111	US	13/039,099				Unknown	
AQUA.P115	AQUA.P115	US	09/863,081				Unknown	
AQUA.P116	AQUA.P116	US	09/862,384				Unknown	
AQUA.P117	AQUA.P117	US	10/993,107				Unknown	
AQUA.P117	AQUA.P117CIP1	US	11/198,070				Unknown	
AQUA.P117	AQUA.P117CIP2	US	11/738,864				Unknown	
AQUA.P118	AQUA.P118	US	11/006,329				Unknown	
AQUA.P120	AQUA.P120	US	11/474,158				Unknown	
AQUA.P124	AQUA.P124	US	11/501,324				Unknown	
AQUA.P125	AQUA.P125	US	11/508,593				Unknown	
AQUA.P127	AQUA.P127	US	11/607,416				Unknown	
AQUA.P128	AQUA.P128	US	11/405,346				Unknown	
AQUA.P129	AQUA.P129	US	11/442,485				Unknown	
AQUA.P130	AQUA.P130	US	11/451,220				Unknown	
AQUA.P131	AQUA.P131	US	11/405,646				Unknown	
AQUA.P132	AQUA.P132	US	11/676,946				Unknown	
AQUA.P133	AQUA.P133	US	11/678,054				Unknown	
AQUA.P136	AQUA.P136C1	US	12/247,833				Unknown	
AQUA.P137	AQUA.P137C1	US	12/118,650				Unknown	
AQUA.P141	AQUA.P141	US	12/139,447				Unknown	
AQUA.P146	AQUA.P146	US	12/329,514				Unknown	

PATENT

Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
AQUA.P146	AQUA.P146CIP1	US	13/190,288				Unknown	
AQUA.P147	AQUA.P147	US	12/369,702				Unknown	
AQUA.P148	AQUA.P148C1	US	12/563,927				Unknown	
AQUA.P149	AQUA.P149	US	13/111,785				Unknown	
AQUA.P149	AQUA.P149CIP	US	13/790,333				Unknown	
AQUA.P150	AQUA.P150	US	13/171,380				Unknown	
AQUA.P151	AQUA.P151	US	13/107,659				Unknown	
AQUA.P161	AQUA.P161C1	US	13/350,624				Unknown	
AQUA.P163	AQUA.P163	US	13/625,820				Unknown	
AQUA.P166	AQUA.P166C1	US	15/228,161				Pending	
AQUA.P168	AQUA.P168	US	13/346,534				Unknown	
AQUA.P169	AQUA.P169	US	13/413,029				Unknown	
AQUA.P169	AQUA.P169C1	US	14/843,394				Unknown	
AQUA.P174	AQUA.P174	US	13/523,826				Unknown	
AQUA.P175	AQUA.P175	US	13/966,208				Unknown	
AQUA.P176	AQUA.P176	US	13/671,132				Unknown	
AQUA.P176	AQUA.P176C1	US	14/634,605				Unknown	
AQUA.P176	AQUA.P176CIPIC2	US	16/114,084				Pending	
AQUA.P177	AQUA.P177	US	10/699,761				Unknown	
AQUA.P178	AQUA.P178	US	10/695,166				Unknown	
AQUA.P178	AQUA.P178C1	US	11/702,348				Unknown	
AQUA.P178	AQUA.P178CIP1	US	10/816,409				Unknown	
AQUA.P178	AQUA.P178CIP1CN	CHINA	580018010.7	3/18/2005	580018010	5/26/2010	Issued	SUB-BLOCK DOMAIN TRANSFORMATION MULTIPLE SIGNAL PROCESS
AQUA.P178	AQUA.P178CIP1JP	JAPAN		3/18/2005	4796051	8/5/2011	Issued	SUB-BLOCK DOMAIN TRANSFORMATION MULTIPLE SIGNAL PROCESS
AQUA.P178	AQUA.P178CIP1TW	TAIWA	94108854	3/22/2005	1360905	3/11/2012	Issued	SUB-BLOCK DOMAIN TRANSFORMATION MULTIPLE SIGNAL PROCESS
AQUA.P178	AQUA.P178TW	TAIWA	93132397	10/26/2004	1377814	11/21/2012	Issued	A METHOD AND APPARATUS FOR DOMAIN TRANSFORMATION MULT
AQUA.P180	AQUA.P180	US	11/148,988				Unknown	
AQUA.P180	AQUA.P180TW	TAIWA	95113859	6/5/2006	1338464	3/1/2011	Issued	SINGLE AMPLIFIER PRESAMPLE PROCESSING CIRCUITRY
AQUA.P181	AQUA.P181	US	10/926,699				Unknown	
AQUA.P181	AQUA.P181TW	TAIWA	94128617	8/22/2005	1371929	9/1/2012	Issued	METHOD OF A TRANSCIVER DECODING AN ETHERNET SIGNAL AND
AQUA.P183	AQUA.P183	US	11/090,109				Unknown	
AQUA.P183	AQUA.P183CIP1	US	12/217,962				Unknown	
AQUA.P183	AQUA.P183CIP1D1	US	12/613,627				Unknown	
AQUA.P184	AQUA.P184	US	11/205,615				Unknown	

PATENT

REEL: 053179 FRAME: 0360

Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
AQUA.P184	AQUA.P184TW	TAIWA	95129735	8/14/2006	1323983	4/21/2010	Issued	RECEIVER ADC CLOCK DELAY BASED ON ECHO SIGNALS
AQUA.P185	AQUA.P185	US	11/247,805				Unknown	
AQUA.P185	AQUA.P185TW	TAIWA	95137283		1337030	2/1/2011	Issued	MULTIPLE MODULATION RATE 10GBASE-T TRANSMISSION
AQUA.P186	AQUA.P186	US	11/303,364				Unknown	
AQUA.P186	AQUA.P186TW	TAIWA			1326176	6/11/2010	Issued	TRANSCIVER POWER BACKOFF
AQUA.P189	AQUA.P189TW	TAIWA			1351139	10/21/2011	Issued	TRANSCIVER NON-LINEARITY CANCELLATION
AQUA.P190	AQUA.P190	US	11/438,177				Unknown	
AQUA.P190	AQUA.P190C1	US	12/941,039				Unknown	
AQUA.P190	AQUA.P190C2	US	13/974,551				Unknown	
AQUA.P191	AQUA.P191	US	11/510,934				Unknown	
AQUA.P191	AQUA.P191TW	TAIWA	TW2007012980	8/13/2007	1355150	12/21/2011	Issued	MULTIPLE TRANSMISSION PROTOCOL TRANSCIVER
AQUA.P192	AQUA.P192	US	11/644,598				Unknown	
AQUA.P193	AQUA.P193TW	TAIWA	97109661		1368392	7/11/2012	Issued	SELECTION OF FILTER COEFFICIENTS FOR TRANSCIVER NON-LINEARIT
AQUA.P195	AQUA.P195	US	12/280,181				Unknown	
AQUA.P195	AQUA.P195C1	US	12/960,481				Unknown	
AQUA.P205	AQUA.P205	US	13/398,748				Unknown	
AQUA.P210	AQUA.P210	US	14/459,260				Unknown	
AQUA.P211	AQUA.P211	US	14/180,082				Pending	
AQUA.P212	AQUA.P212	US	14/961,797				Unknown	
AQUA.P212	AQUA.P212C1	US	15/683,618				Unknown	
AQUA.P213	AQUA.P213	US	14/961,810				Pending	
AQUA.P214	AQUA.P214	US	14/995,141				Unknown	
AQUA.P215	AQUA.P215	US	15/011,384				Unknown	
AQUA.P216	AQUA.P216	US	15/065,753				Unknown	
AQUA.P216	AQUA.P216C1	US	15/853,504				Pending	
AQUA.P217	AQUA.P217	US	15/063,468				Unknown	
AQUA.P218	AQUA.P218	US	15/083,041				Pending	
AQUA.P219	AQUA.P219	US	14/858,918				Unknown	
AQUA.P219	AQUA.P219CP1	US	15/276,250				Unknown	
AQUA.P219	AQUA.P219CP2	US	16/057,609				Pending	
AQUA.P221	AQUA.P221	US	14/858,902				Unknown	
AQUA.P222	AQUA.P222C1	US	16/112,583				Pending	
AQUA.P222	AQUA.P222CP1	US	16/365,510				Pending	
AQUA.P224	AQUA.P224	US	14/961,802				Unknown	

PATENT

REEL: 053179 FRAME: 0361

Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
AQUA.P225	AQUA.P225	US	14/961,806				Pending	
AQUA.P226	AQUA.P226	US	15/222,742				Pending	
AQUA.P227	AQUA.P227	US	15/063,474				Unknown	
AQUA.P228	AQUA.P228	US	15/478,046				Unknown	
AQUA.P229	AQUA.P229	US	15/398,583				Unknown	
AQUA.P230	AQUA.P230	US	15/669,717				Pending	
AQUA.P231	AQUA.P231	US	15/451,920				Pending	
AQUA.P232	AQUA.P232	US	15/620,618				Pending	
AQUA.P233	AQUA.P233	US	15/478,051				Pending	
AQUA.P235	AQUA.P235	US	16/354,512				Pending	
AQUA.P236	AQUA.P236	US	16/354,525				Pending	
AQUA.P237	AQUA.P237	US	16/354,542				Pending	
AQUA.P238	AQUA.P238	US	16/419,585				Pending	
AQUA.P239	AQUA.P239	US	16/419,625				Pending	
AQUA.P240	AQUA.P240	US	16/537,008				Pending	
AQUA.P241	AQUA.P241	US	16/514,844				Pending	
AQUA.P242	AQUA.P242	US	16/171,298				Pending	
AQUA.P243	AQUA.P243	US	16/362,479				Pending	
AQUA.P244	AQUA.P244	US	16/362,494				Pending	
AQUA.P245	AQUA.P245	US	16/362,500				Pending	
AQUA.P246	AQUA.P246	US	16/143,254				Pending	
AQUA.P247	AQUA.P247	US	16/162,751				Pending	
AQUA.P248	AQUA.P248	US	16/419,643				Pending	
AQUA.P249	AQUA.P249	US	16/554,000				Pending	
AQUA.P250	AQUA.P250	US	16/554,023				Pending	
AQUA.P251	AQUA.P251	US	16/554,036				Pending	
AQUA.P252	AQUA.P252P	US	62/868778				Pending	
ARC920030015	ARC920030015EPI	Europe	4766141.8		1644834	12/15/2010	Not Provide Autonomic Parity Exchange	
ARC920030015	ARC920030015TW1	Taiwan	93120449		1308689	4/11/2009	Not Provide Method and System for Increasing Failure Tolerance of a Storage Syst	
ARC920030015	ARC920030015US1	US	10/619,649	7/14/2003	7281177	10/9/2007	Autonomic Parity Exchange	
ARC920030040	ARC920030040CN1	China	2,00480020396-		ZL2004800203	6/3/2009	Not Provide RAID 3+3	
ARC920030040	ARC920030040US1	US	10/619,648	7/14/2003	7254754	8/7/2007	RAID 3+3	
AUS919970289	AUS919970289US1	US	08/873,830	6/12/1997	5949272	9/7/1999		
AUS919990403	AUS919990403US1	US	09/422,367	10/21/1999	6552589	4/22/2003		

PATENT

REEL: 053179 FRAME: 0362

Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
AUS919990403	AUS919990403US2	US	10/116,813	4/5/2002	6701444	3/2/2004		Method And Apparatus For Process Independent Clock Signal Distrib
AUS920000013	AUS920000013US1	US	09/615,767	7/13/2000	6643772	11/4/2003		Universal Boot Code For A Computer Network
AUS920000159	AUS920000159US1	US	09/574,186	5/18/2000	6829677	12/7/2004		Method And Apparatus For Preserving The Contents Of Synchronous
AUS920010256	AUS920010256US1	US	09/870,623	5/31/2001	6933752	8/23/2005		Method And Apparatus For Interface Signaling Using Single-Ended An
AUS920010615	AUS920010615US1	US	09/974,986	10/11/2001	6529084	3/4/2003		Interleaved Feedforward Vcc And Pll
AUS920030695	AUS920030695US1	US	11/002,559	12/2/2004	7656237	2/2/2010		Method To Gate Off Plls In A Deep Power Saving State Without Sepa
AUS920030707	AUS920030707US1	US	10/670,823	9/25/2003	7523157	4/21/2009		Managing A Plurality Of Processors As Devices
AUS920040151	AUS920040151US1	US	10/840,561	5/6/2004	6986038	12/27/2005		Circuit For Compensating Charge Leakage In A Low Pass Filter Capac
AUS920040361	AUS920040361US1	US	10/896,504	7/22/2004	7129859	10/31/2006		Method And Apparatus For Minimizing Threshold Variation From Bo
AUS920040682	AUS920040682US1	US	11/082,973	3/17/2005	7350095	3/25/2008		Digital Circuit To Measure And/Or Correct Duty Cycles
AUS920040682	AUS920040682US2	US	12/014,501	1/15/2008	7917795	3/29/2011		Digital Circuit To Measure And/Or Correct Duty Cycles
AUS920050120	AUS920050120US1	US	11/168,692	6/28/2005	7336105	2/26/2008		Dual Gate Transistor Keeper Dynamic Logic
AUS920050296	AUS920050298US1	US	11/168,691	6/28/2005	7411425	8/12/2008		Method For Power Consumption Reduction In A Limited-Switch Dyna
AUS920050346	AUS920050346US1	US	11/250,019	10/13/2005	7376001	5/20/2008		Row Circuit Ring Oscillator Method For Evaluating Memory Cell Perfo
AUS920050383	AUS920050383US1	US	11/381,373	5/3/2006	8037893	10/18/2011		Optimizing Thermal Performance Using Thermal Flow Analysis
AUS920050391	AUS920050391US1	US	11/381,391	5/3/2006	7556430	9/29/2009		Selection Of Processor Cores For Optimal Thermal Performance
AUS920050410	AUS920050410US1	US	11/137,234	5/25/2005	7444525	10/28/2008		Methods And Apparatus For Reducing Leakage Current In A Disabled
AUS920050827	AUS920050827US1	US	11/360,907	2/23/2006	7340361	3/4/2008		Method And Apparatus For Analyzing Computer System Performance
AUS920060507	AUS920060507US2	US	12/347,947	12/31/2008	8214660	7/3/2012		Structure For An Apparatus For Monitoring And Controlling Heat Gen
AUS920060770	AUS920060770CN1	China	2,007,104,867,4e-		21,2007,101,867	5/4/2011	Not Provide	Programmable local Clock Buffer Capable Of Varying Initial Settings
AUS920060770	AUS920060770US1	US	11/609,403	12/12/2005	7613944	11/3/2009		Programmable local Clock Buffer Capable Of Varying Initial Settings
AUS920070137	AUS920070137US1	US	12/023,337	1/31/2008	8117579	2/14/2012		Used Compatibility For Gsd Unified Local Clock Buffers
AUS920070146	AUS920070148US1	US	11/877,898	10/24/2007	7414436	8/19/2008		Limited Switch Dynamic Logic Cell Based Register
AUS920070155	AUS920070155US1	US	11/759,426	6/7/2007	746164	12/16/2008		Method And Apparatus For A Configurable Low Power High Fan-In M
AUS920070250	AUS920070250US1	US	11/772,592	7/2/2007	7474574	1/6/2009		Shift Register Latch With Embedded Dynamic Random Access Memor
AUS920070440	AUS920070440US2	US	12/174,572	7/16/2008	8086989	12/27/2011		Structure For Glitchless Clock Multiplexer Optimized For Synchronou
AUS920080274	AUS920080274CN1	China	2,009,801,388,8e-		21,2009,801,388	2/20/2013	Not Provide	OPTIMIZING SRAM PERFORMANCE OVER EXTENDED VOLTAGE OR PR
AUS920080274	AUS920080274DE1	Germa	20090783647		2345036	5/30/2012	Not Provide	OPTIMIZING SRAM PERFORMANCE OVER EXTENDED VOLTAGE OR PR
AUS920080274	AUS920080274EP1	Europe	9783647.2		2345036	5/30/2012	Not Provide	OPTIMIZING SRAM PERFORMANCE OVER EXTENDED VOLTAGE OR PR
AUS920090052	AUS920090052US1	US	12/508,235	7/23/2009	8212537	7/3/2012		Integratable Efficient Switching Down Converter
AUS920090076	AUS920090076US1	US	12/569,421	9/29/2009	8336008	12/18/2012		Characterization Of Long Range Variability
AUS920100102	AUS920100102US1	US	12/828,238	6/30/2010	8183920	5/22/2012		Variable Gain Amplifier With Reduced Power Consumption
AUS920100248	AUS920100248US1	US	12/827,693	6/30/2010	8338948	12/25/2012		Ball Grid Array With Improved Single-Ended And Differential Signal Pe
AUS920100248	AUS920100248US3	US	13/687,218	11/28/2012	8742565	6/3/2014		Ball Grid Array With Improved Single-Ended And Differential Signal Pe

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
AUS920120203	AUS920120203US1	US	13/689,044	11/29/2012	9057766	6/16/2015		Isolating Failing Latches Using A Logic Built-In Self-Test
AUS920120226	AUS920120226CN1	China	2.0131059077e-		ZL2013105967	3/1/2017	Not Provide	CHANGING RESONANT CLOCK MODES
AUS920120226	AUS920120226US1	US	13/719,408	12/19/2012	8736342	5/27/2014		Changing Resonant Clock Modes
AUS920120246	AUS920120246CN1	China	2.0141004309e-		ZL2014100430	4/12/2017	Not Provide	WIDE BANDWIDTH RESONANT GLOBAL CLOCK DISTRIBUTION
AUS920140069	AUS920140069US1	US	14/294,219	6/3/2014	9455976	9/27/2016		Multi-Factor Secure Appliance Decommissioning
AUS920140151	AUS920140151US1	US	14/506,331	10/3/2014	9582388	2/28/2017		Dynamic Multi Purpose External Access Points Connected To Core In
AUS920140213	AUS920140213US1	US	14/449,666	8/1/2014	9575822	2/21/2017		Tracking A Relative Arrival Order Of Events Being Stored In Multiple
AUS920140214	AUS920140214US1	US	14/521,739	10/23/2014	9424173	8/23/2016		Performing Secure Address Relocation Within A Multi-Processor Sys
BUR820150009	BUR820150009-CN-	China	2.0161007629e-				Not Provide	Electrostatic Discharge Protection Structures for Efuses
BUR820150009	BUR820150009-TW	Taiwan	105100696				Not Provide	Electrostatic Discharge Protection Structures for Efuses
BUR820150009	BUR820150009-US-	US	14/971,644	12/16/2015	9940986	4/10/2018		Electrostatic Discharge Protection Structures For Efuses
BUR820150017	BUR820150017-US-	US	14/945,530	11/19/2015	9767240	9/19/2017		Temperature-Aware Integrated Circuit Design Methods And Systems
BUR820150019	BUR820150019-US-	US	15/002,550	1/21/2016	9852259	12/26/2017		Area And/Or Power Optimization Through Post-Layout Modification
BUR820150044	BUR820150044-CN-	China	2.0161097842e-				Not Provide	CONTENT-ADDRESSABLE MEMORY HAVING MULTIPLE REFERENCE
BUR820150044	BUR820150044-US-	US	14/990,125	1/7/2016	9704575	7/11/2017		Content-Addressable Memory Having Multiple Reference Matchlines
BUR820150046	BUR820150046-CN-	China	2.0161071082e-				Not Provide	VOLTAGE-AWARE ADAPTIVE STATIC RANDOM ACCESS MEMORY (SR
BUR820150046	BUR820150046-TW	Taiwan	105112406		I 630617	7/21/2018	Not Provide	VOLTAGE-AWARE ADAPTIVE STATIC RANDOM ACCESS MEMORY (SR
BUR820150046	BUR820150046-US-	US	15/009,132	1/28/2016	9584420	11/29/2016		Voltage-Aware Adaptive Static Random Access Memory (Sram) Write
BUR820150051	BUR820150051-US-	US	15/015,535	2/4/2016	9653330	5/16/2017		Threshold Voltage (Vt)-Type Transistor Sensitive And/Or Fan-Out Sen
BUR820150056	BUR820150056-US-	US	15/001,763	1/20/2016	9967825	5/8/2018		Environmentally Aware Mobile Computing Devices
BUR820150061	BUR820150061-US-	US	15/019,590	2/9/2016				Memory Built-In Self-Test (Mbis) Test Reduction
BUR820150093	BUR820150093-CN-	China	201710063508.1				Not Provide	GIMBAL ASSEMBLY TEST SYSTEM AND METHOD
BUR820150093	BUR820150093-TW	Taiwan	106103210		I 639007	10/21/2018	Not Provide	GIMBAL ASSEMBLY TEST SYSTEM AND METHOD
BUR820150093	BUR820150093-US-	US	15/933,443	3/23/2018				Gimbal Assembly Test System And Method
BUR820150093	BUR820150093-US-	US	15/014,479	2/3/2016	10041976	8/7/2018		Gimbal Assembly Test System And Method
BUR820150094	BUR820150094-US-	US	14/992,426	1/11/2016	9654086	5/16/2017		Operational Amplifier With Current-Controlled Up Or Down Hysteres
BUR820150098	BUR820150098-US-	US	15/083,692	3/29/2016	9855486	1/9/2018		Timing/Power Risk Optimized Selective Voltage Binning Using Non-Li
BUR820150115	BUR820150115-US-	US	15/142,511	4/29/2016	9956648	6/12/2018		Netlist Editing Of Graphical Data
BUR820150121	BUR820150121-US-	US	15/012,331	2/1/2016	9766673	9/12/2017		Application Specific Integrated Circuit (Asic) Test Screens And Selecti
BUR919960066	BUR919960066US1	US	08/705,871	8/28/1996	5768152	6/16/1998		
BUR919960079	BUR919960079JP1	Japan	19970294010		4008989	9/7/2007	Not Provide	SELF REGULATING TEMPERATURE/PERFORMANCE/VOLTAGE SCHEM
BUR919960079	BUR919960079US1	US	08/772,706	12/23/1996	5832284	11/3/1998		
BUR919960155	BUR919960155US1	US	08/853,963	5/9/1997	6118318	9/12/2000		
BUR919960177	BUR919960177US1	US	08/803,056	2/19/1997	5757693	5/26/1998		

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BUR919970028	BUR919970028US1	US	08/833,367	4/4/1997	6070262	5/30/2000		
BUR919970057	BUR919970057US1	US	08/947,762	10/9/1997	5898623	4/27/1999		
BUR919970107	BUR919970107US1	US	09/056,903	4/7/1998	5945707	8/31/1999		
BUR919970149	BUR919970149US1	US	09/105,739	6/26/1998	6767789	7/27/2004		
BUR919970164	BUR919970164DE1	Germa	99303394.3		957490	3/16/2005	Not Provide HIGH BANDWIDTH NARROW I/O MEMORY DEVICE WITH COMMAND	
BUR919970164	BUR919970164EP1	Europe	99303394.3		957490	3/16/2005	Not Provide HIGH BANDWIDTH NARROW I/O MEMORY DEVICE WITH COMMAND	
BUR919970164	BUR919970164US1	US	09/079,572	5/15/1998	6065093	5/16/2000		
BUR919970169	BUR919970169US1	US	09/081,639	5/20/1998	6118719	9/12/2000		
BUR919970202	BUR919970202US1	US	09/121,933	7/24/1998	6178517	1/23/2001		
BUR919970219	BUR919970219TW	Taiwan	88102478		NI-128941	7/13/2001	Not Provide DEVICE AND METHOD TO REDUCE POWER CONSUMPTION IN INTEG	
BUR919970219	BUR919970219US1	US	09/159,861	9/24/1998	6097243	8/1/2000		
BUR919970220	BUR919970220US1	US	09/120,211	7/21/1998	6011383	1/4/2000		
BUR919970220	BUR919970220US2	US	09/159,898	9/24/1998	6097241	8/1/2000		
BUR919970231	BUR919970231US1	US	09/055,002	4/3/1998	6097655	8/1/2000		
BUR919970232	BUR919970232US1	US	09/092,412	6/5/1998	6057719	5/2/2000		
BUR919980122	BUR919980122US1	US	09/283,960	4/1/1999	6177817	1/23/2001		
BUR919980123	BUR919980123US1	US	09/224,766	1/4/1999	6157530	12/5/2000		
BUR919980148	BUR919980148US1	US	09/302,902	4/30/1999	6101245	10/31/2000		
BUR919990140	BUR919990140US1	US	09/524,661	3/13/2000	6292144	8/28/2001	Multi-Ported Memory With Asynchronous And Synchronous Protocol	
BUR919990218	BUR919990218TW	Taiwan	89124582		NI-155053	8/27/2002	Not Provide MACRO DESIGN TECHNIQUES TO ACCOMMODATE CHIP LEVEL WIRIN	
BUR919990218	BUR919990218US1	US	09/526,198	3/15/2000	6543040	4/1/2003	Macro Design Techniques To Accommodate Chip Level Wiring And Ci	
BUR919990240	BUR919990240CN1	China	00138852.8		ZI00138852.8	4/27/2005	Not Provide WAFER INTEGRATED RIGID SUPPORT RING	
BUR919990240	BUR919990240TW	Taiwan	89121973		NI-164171	2/19/2003	Not Provide WAFER INTEGRATED RIGID SUPPORT RING	
BUR919990276	BUR919990276US1	US	09/588,202	6/6/2000	6496037	12/17/2002	Automatic Off-Chip Driver Adjustment Based On Load Characteristics	
BUR919990287	BUR919990287US1	US	09/669,117	9/25/2000	6658610	12/2/2003	Compliable Address Magnitude Comparator For Memory Array Self-T	
BUR919990296	BUR919990296US1	US	09/757,267	1/9/2001	6420925	7/16/2002	Programmable Latch Device With Integrated Programmable Element	
BUR920000036	BUR920000036US1	US	09/737,012	12/14/2000	6523154	2/18/2003	Method For Supply Voltage Drop Analysis During Placement Phase Of	
BUR920000102	BUR920000102US1	US	09/733,295	12/8/2000	6496432	12/17/2002	Method And Apparatus For Testing A Write Function Of A Dual-Port S	
BUR920000103	BUR920000103US1	US	09/768,122	1/23/2001	6708298	3/16/2004	Method For Guaranteeing A Minimum Data Strobe Valid Window An	
BUR920000106	BUR920000106US1	US	09/690,674	10/17/2000	6555891	4/29/2003	Soi Hybrid Structure With	
BUR920000109	BUR920000109US1	US	09/862,427	5/21/2001	6832361	12/14/2004	System And Method For Analyzing Power Distribution Using Static Ti	
BUR920000111	BUR920000111CN1	China	2001142599		ZI01142599.7	2/9/2005	Not Provide METHOD AND APPARATUS FOR INITIALIZING AN INTEGRATED CIRCUI	
BUR920000111	BUR920000111TW	Taiwan	90129655		NI-177947	9/9/2003	Not Provide METHOD AND APPARATUS FOR INITIALIZING AN INTEGRATED CIRCUI	
BUR920000111	BUR920000111US1	US	09/731,147	12/5/2000	6577156	6/10/2003	Method And Apparatus For Initializing An Integrated Circuit Using Co	

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
BUR920000126	BUR920000126US1	US	09/810,763	3/16/2001	6470476	10/22/2002		Substitution Of Non-Minimum Groundrule Cells For Non-Critical Mini
BUR920000152	BUR920000152US1	US	09/791,003	2/22/2001	6754864	6/22/2004		System And Method To Predetermine A Bitmap Of A Self-Tested Emb
BUR920010028	BUR920010028US1	US	09/683,808	2/19/2002	6621324	9/16/2003		Redundant Antifuse Segments For Improved Programming Efficiency
BUR920010115	BUR920010115US1	US	09/682,638	10/1/2001	6420072	8/6/2002		Embedded Cam Test Structure For Fully Testing All Matchlines
BUR920010121	BUR920010121US1	US	10/063,394	4/18/2002	6990644	1/24/2006		On Chip Timing Adjustment In Multi-Channel Fast Data Transfer
BUR920010125	BUR920010125US1	US	10/065,475	10/22/2002	6990506	8/16/2005		Terminating Resistor Driver For High Speed Data Communication
BUR920010182	BUR920010182US1	US	10/063,212	3/29/2002	6778419	8/17/2004		Complementary Two Transistor Rom Cell
BUR920010192	BUR920010192US1	US	10/063,427	4/23/2002	6823496	11/23/2004		Physical Design Characterization System
BUR920010202	BUR920010202US1	US	10/063,497	4/30/2002	7149941	12/12/2006		Optimized Ecc/Redundancy Fault Recovery
BUR920010204	BUR920010204US1	US	10/064,921	8/29/2002	6794901	9/21/2004		Apparatus For Reducing Soft Errors In Dynamic Circuits
BUR920010216	BUR920010216TW	Taiwan	92109727		1222078	10/11/2004	Not Provide	Testing Of Ecc Memories
BUR920010216	BUR920010216US1	US	10/063,495	4/30/2002	7368621	12/11/2007		Testing Of Ecc Memories
BUR920020022	BUR920020022US1	US	10/063,504	5/1/2002	6731154	5/4/2004		Global Voltage Buffer For Voltage Islands
BUR920020025	BUR920020025EP1- Germa	Germa	02795913.9		1573553	4/1/2016	Not Provide	PAGE - Dynamic Variable Line Width Cache
BUR920020025	BUR920020025US1	US	11/160,184	8/19/2005	7406579	7/29/2008		Selectively Changeable Line Width Memory
BUR920020046	BUR920020046US1	US	10/289,311	3/31/2003	6731134	5/4/2004		Tri-State Delay Boost
BUR920020065	BUR920020065US1	US	10/063,859	5/20/2002	7020857	3/28/2006		Method And Apparatus For Providing Noise Suppression In An Integr
BUR920020079	BUR920020079US1	US	10/289,684	4/30/2003	6825711	11/30/2004		Power Reduction By Stage In Integrated Circuit
BUR920020080	BUR920020080US1	US	10/265,591	10/7/2002	6768694	7/27/2004		Method Of Electrically Blowing Fuses Under Control Of An On-Chip T
BUR920020085	BUR920020085CN1	China	2.0041009122e-	11/17/2004	21.2004100912	9/14/2011	Not Provide	MEMORY DEVICE WITH PROGRAMMABLE RECEIVERS TO IMPROVE P
BUR920020085	BUR920020085US1	US	10/707,053	11/18/2003	7646649	1/12/2010		Memory Device With Programmable Receivers To Improve Performa
BUR920020086	BUR920020086US1	US	10/249,273	3/27/2003	6826113	11/30/2004		Synchronous Dynamic Random Access Memory Device Having Memo
BUR920020088	BUR920020088EP1	Europe	2797402.1		1620857	11/11/2009	Not Provide	Enabling Memory Redundancy During Testing
BUR920020088	BUR920020088US1	US	11/160,268	6/16/2005	7364901	12/4/2007		Enabling Memory Redundancy During Testing
BUR920020092	BUR920020092CN1	China	2.0048001924e-		21.2004800192	2/24/2010	Not Provide	Nested Voltage Island Architecture
BUR920020092	BUR920020092EP1	Europe	2004077780.1		1649493	3/16/2016	Not Provide	PAGE - Nested Voltage Island Architecture
BUR920020092	BUR920020092US1	US	10/604,277	7/8/2003	7131074	10/31/2006		Nested Voltage Island Architecture
BUR920020097	BUR920020097US1	US	10/707,963	1/28/2004	7061304	6/13/2006		Fuse Latch With Compensated Programmable Resistive Trip Point
BUR920020137	BUR920020137US1	US	10/248,302	1/7/2003	6986116	1/10/2006		Signal Balancing Between Voltage Domains
BUR920020148	BUR920020148US1	US	10/604,186	6/30/2003	6887674	5/24/2005		Adaptive Integrated Circuit Based On Transistor Current Measureme
BUR920030013	BUR920030013US1	US	10/604,095	6/26/2003	6882230	4/19/2005		System And Method For Control Parameter Re-Centering In A Control
BUR920030017	BUR920030017US1	US	10/249,331	4/1/2003	7480774	1/20/2009		Method For Performing A Command Cancel Function In A Dram
BUR920030099	BUR920030099US1	US	10/729,751	12/4/2003	7287177	10/23/2007		Digital Reliability Monitor Having Autonomous Repair And Notification
BUR920030103	BUR920030103US1	US	10/651,874	8/29/2003	7305600	12/4/2007		Partial Good Integrated Circuit And Method Of Testing Same

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
BUR920030134	BUR920030134US1	US	10/707,121	11/21/2003	6940324	9/6/2005		Variation Of Effective Filter Capacitance In Phase Lock Loop Circuit Lo
BUR920030147	BUR920030147CN1	China	2,005,100,548,3e-		ZL2005,100,548	7/2/2008	Not Provide	Phase Change Memory Cell on Silicon-On Insulator Substrate
BUR920030147	BUR920030147US1	US	10/708,667	3/18/2004	7005665	2/28/2006		Phase Change Memory Cell On Silicon-On Insulator Substrate
BUR920030171	BUR920030171US1	US	10/708,317	2/24/2004	7110732	10/10/2006		Content Addressable Memory Structure
BUR920030171	BUR920030171US2	US	11/533,125	9/19/2006	7337268	2/26/2008		Content Addressable Memory Structure
BUR920030171	BUR920030171US3	US	11/850,840	9/6/2007	7464217	12/9/2008		Design Structure For Content Addressable Memory
BUR920040001	BUR920040001US1	US	10/709,754	5/26/2004	7373567	5/13/2008		System And Method Of Providing Error Detection And Correction Ca
BUR920040011	BUR920040011CN1	China	2,005,101,156,2e-		ZL2005,101,156	7/23/2008	Not Provide	METHOD AND APPARATUS FOR CONVERTING GLOBALLY CLOCK-GAT
BUR920040013	BUR920040013CN1	China	2,005,800,293,6e-		ZL2005,800,293	2/29/2012	Not Provide	LOW VOLTAGE PROGRAMMABLE eFUSE WITH DIFFERENTIAL SENSIN
BUR920040013	BUR920040013DE1	Germa	20050794850		1800323	11/16/2011	Not Provide	LOW VOLTAGE PROGRAMMABLE eFUSE WITH DIFFERENTIAL SENSIN
BUR920040013	BUR920040013EP1	Europe	5794850.7		1800323	11/16/2011	Not Provide	LOW VOLTAGE PROGRAMMABLE eFUSE WITH DIFFERENTIAL SENSIN
BUR920040013	BUR920040013TW	Taiwan	94129638		1392076	4/1/2013	Not Provide	LOW VOLTAGE PROGRAMMABLE eFUSE WITH DIFFERENTIAL SENSIN
BUR920040013	BUR920040013US1	US	10/711,205	9/1/2004	7098721	8/29/2006		Low Voltage Programmable Fuse With Differential Sensing Scheme
BUR920040024	BUR920040024CN1	China	2,005,100,562,9e-		ZL2005,100,562	3/25/2009	Not Provide	Low Leakage Monotonic CMOS Logic
BUR920040024	BUR920040024TW	Taiwan	94123273		1348273	9/1/2011	Not Provide	Low Leakage Monotonic CMOS Logic
BUR920040024	BUR920040024US1	US	10/710,453	7/13/2004	7084667	8/1/2006		Low Leakage Monotonic CMOS Logic
BUR920040027	BUR920040027CN1	China	2,005,100,653,6e+		ZL2005,100,652	2/18/2009	Not Provide	Programmable Frequency Divider With Symmetrical Output
BUR920040027	BUR920040027US1	US	10/709,804	5/28/2004	6961403	11/1/2005		Programmable Frequency Divider With Symmetrical Output
BUR920040045	BUR920040045US1	US	10/710,681	7/28/2004	7106110	9/12/2006		Clock Dithering System And Method During Frequency Scaling
BUR920040074	BUR920040074US1	US	10/709,362	4/29/2004	7280939	10/9/2007		System And Method Of Analyzing Timing Effects Of Spatial Distributio
BUR920040077	BUR920040077US1	US	10/710,745	7/30/2004	7053712	5/30/2006		Method And Apparatus For Controlling Common-Mode Output Volta
BUR920040078	BUR920040078US1	US	10/906,508	2/23/2005	8595557	11/26/2013		Method And Apparatus For Verifying Memory Testing Software
BUR920040133	BUR920040133US1	US	10/904,309	11/3/2004	7401307	7/15/2008		Slack Sensitivity To Parameter Variation Based Timing Analysis
BUR920040183	BUR920040183US1	US	10/906,343	2/15/2005	7404114	7/22/2008		System And Method For Balancing Delay Of Signal Communication Pa
BUR920040195	BUR920040195US1	US	10/906,147	2/4/2005	7404125	7/22/2008		Compliable Memory Structure And Test Methodology For Both Asic A
BUR920040209	BUR920040209US1	US	10/906,407	2/18/2005	7240266	7/3/2007		Clock Control Circuit For Test That Facilitates An At Speed Structural
BUR920040268	BUR920040268US1	US	10/908,033	4/26/2005	7251756	7/31/2007		Method And Apparatus For Increasing Fuse Programming Yield Throu
BUR920040268	BUR920040268US2	US	11/775,531	7/10/2007	785028	2/22/2011		Structure For Increasing Fuse Programming Yield
BUR920040273	BUR920040273US1	US	11/161,742	8/15/2005	7352609	4/1/2008		Voltage Controlled Static Random Access Memory
BUR920040273	BUR920040273US2	US	11/763,687	6/15/2007	7466582	12/16/2008		Voltage Controlled Static Random Access Memory
BUR920050034	BUR920050034US1	US	10/908,084	4/27/2005	7348210	3/25/2008		Post Bump Passivation For Soft Error Protection
BUR920050037	BUR920050037CN1	China	2,006,800,355,9e-		ZL2006,800,355	6/16/2010	Not Provide	CIRCUIT DESIGN VERIFICATION USING CHECKPOINTING
BUR920050037	BUR920050037US1	US	11/162,846	9/26/2005	7308663	12/11/2007		Circuit Design Verification Using Checkpointing
BUR920050081	BUR920050081US1	US	11/163,800	10/31/2005	7286425	10/23/2007		System And Method For Capacitive Mis-Match Bit-Line Sensing

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
BUR920050082	BUR920050082US1	US	11/633,112	1/15/2007	7573300	8/11/2009		Current Control Mechanism For Dynamic Logic Keeper Circuits In An I
BUR920050087	BUR920050087US1	US	11/095,327	3/31/2005	7142991	11/28/2006		Voltage Dependent Parameter Analysis
BUR920050118	BUR920050118US1	US	11/162,997	9/30/2005	7282949	10/16/2007		Fpga Powerup To A Known Functional State
BUR920050120	BUR920050120US1	US	11/275,035	12/5/2005	7444258	10/28/2008		Automated Simulation Testbench Generation For Serializer / Deseriali
BUR920050127	BUR920050127US1	US	11/276,413	2/28/2006	7490280	2/10/2009		Microcontroller For Logic Built-In Self Test (lbist)
BUR920050149	BUR920050149US1	US	11/276,451	2/28/2006	7449942	11/11/2008		Intrinsic Rtc Power Distribution For Noise Filtering Of Analog Supplies
BUR920050173	BUR920050173US1	US	12/183,099	7/31/2008	8077534	12/13/2011		Adaptive Noise Suppression Using A Noise Look-Up Table
BUR920050180	BUR920050180US3	US	11/946,096	11/28/2007	8087337	10/11/2011		Structures Including Circuits For Noise Reduction In Digital Systems
BUR920050200	BUR920050200US1	US	11/424,961	6/19/2006	7459958	12/2/2008		Circuits To Reduce Threshold Voltage Tolerance And Skew In Multi-Bit
BUR920050206	BUR920050206US1	US	11/276,380	2/27/2006	7256503	8/14/2007		Chip Underfill In Flip-Chip Technologies
BUR920050214	BUR920050214US1	US	11/275,482	1/9/2006	7470959	12/30/2008		Integrated Circuit Structures For Preventing Charging Damage
BUR920050226	BUR920050226US1	US	11/277,315	3/23/2006	7483061	7/22/2008		Method Of Improving Fuse State Detection And Yield In Semiconductor
BUR920050234	BUR920050234US1	US	12/135,249	6/9/2008	7821294	10/26/2010		Integrated Circuit Containing Multi-State Restore Circuitry For Restori
BUR920050235	BUR920050235US1	US	11/277,385	3/24/2006	7464163	7/22/2008		Static Timing Slacks Analysis And Modification
BUR920050237	BUR920050237US2	US	11/851,138	9/6/2007	7839206	11/23/2010		Design Structure For Low Voltage Applications In An Integrated Circui
BUR920050246	BUR920050246US1	US	11/278,169	3/31/2006	7307457	12/11/2007		Apparatus For Implementing Dynamic Data Path With Interlocked Ke
BUR920050271	BUR920050271CN1	China	2.007101396e-		21.2007101399	12/9/2009	Not Provide	CONFIGURABLE SRAM SYSTEM AND METHOD
BUR920050271	BUR920050271US1	US	11/463,917	8/11/2006	7450413	11/11/2008		Configurable Sram System And Method
BUR920050271	BUR920050271US2	US	11/947,092	11/29/2007	7602635	10/13/2009		Structure For A Configurable Sram System And Method
BUR920050293	BUR920050293US2	US	11/869,216	10/9/2007	8086832	12/27/2011		Structure For Dynamically Adjusting Pipelined Data Paths For Improv
BUR920050304	BUR920050304CN1	China	2.0071008982e-		21.2007100898	7/15/2009	Not Provide	DUAL-SIDED CHIP ATTACHED MODULES
BUR920050304	BUR920050304US1	US	11/383,595	5/16/2006	7462509	12/9/2008		Dual-Sided Chip Attached Modules
BUR920060018	BUR920060018US1	US	11/457,477	7/14/2006	7724559	5/25/2010		Self-Referenced Match-Line Sense Amplifier For Content Addressable
BUR920060018	BUR920060018US2	US	11/763,669	6/15/2007	7751218	7/6/2010		Self-Referenced Match-Line Sense Amplifier For Content Addressable
BUR920060040	BUR920060040CN1	China	2.0098011554e-		21.2009801155	8/6/2014	Not Provide	Method And Apparatus For Implementing Self-Referencing Read Oper
BUR920060040	BUR920060040TW	Taiwan	98113933		1462098	11/21/2014	Not Provide	%MIPS% METHOD AND APPARATUS FOR IMPLEMENTING SELF-REFER
BUR920060040	BUR920060040US1	US	12/112,454	4/30/2008	7660152	2/9/2010		Method And Apparatus For Implementing Self-Referencing Read Ope
BUR920060052	BUR920060052US1	US	12/135,250	6/9/2008	8239791	8/7/2012		Method Of Designing Multi-State Restore Circuitry For Restoring Stat
BUR920060053	BUR920060053US1	US	11/279,639	4/13/2006	7397718	7/8/2008		Determining Relative Amount Of Usage Of Data Retaining Device Bas
BUR920060067	BUR920060067CN1	China	2.0071013902e-		21.2007101390	8/17/2011	Not Provide	(e-Docker)A SYSTEM FOR ACQUIRING DEVICE PARAMETERS
BUR920060067	BUR920060067US1	US	11/459,367	7/24/2006	7382149	6/3/2008		System For Acquiring Device Parameters
BUR920060072	BUR920060072CN1	China	2.0071016942e-		21.2007101694	6/23/2010	Not Provide	TUNABLE CAPACITOR
BUR920060097	BUR920060097US1	US	11/550,573	10/18/2006	7732949	6/8/2010		System For Method Of Predicting Power Events In An Intermittent Po
BUR920060127	BUR920060127US1	US	11/684,655	3/12/2007	7692954	4/6/2010		Apparatus And Method For Integrating Nonvolatile Memory Capabilit

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BUR920060129	BUR920060129US1	US	11/743,686	5/3/2007	7468800	8/5/2008		Apparatus And Method For Improved Sram Device Performance Thro
BUR920060136	BUR920060136US1	US	11/631,248	1/9/2007	7432730	10/7/2008		Time Based Driver Output Transition (Slew) Rate Compensation
BUR920060136	BUR920060136US2	US	12/053,946	3/24/2008	8115508	2/14/2012		Structure For Time Based Driver Output Transition (Slew) Rate Comp
BUR920060143	BUR920060143US1	US	11/631,175	1/9/2007	7545165	6/9/2009		System Architectures For And Methods Of Scheduling On-Chip And A
BUR920060144	BUR920060144US1	US	11/670,537	2/2/2007	7403039	7/22/2008		Flexible Multimode Logic Element For Use In A Configurable Mixed-L
BUR920060144	BUR920060144US2	US	11/848,470	8/31/2007	7429877	9/30/2008		Design Structure For A Flexible Multimode Logic Element For Use In A
BUR920060186	BUR920060186US1	US	11/613,516	12/20/2006	7362138	4/22/2008		Flexible Multimode Logic Element For Use In A Configurable Mixed-L
BUR920060186	BUR920060186US3	US	12/494,642	6/30/2009	8361037	10/30/2012		Skewed Double Differential Pair Circuit For Offset Cancellation
BUR920060190	BUR920060190US1	US	11/949,066	12/3/2007	8136088	6/5/2012		Method And Structure For Screening Nfet-Io-Pfet Device Performan
BUR920060211	BUR920060211US1	US	11/949,063	12/3/2007	7848128	12/7/2010		Apparatus And Method For Implementing Matrix-Based Search Capa
BUR920060211	BUR920060211US2	US	12/110,375	4/28/2008	7859878	12/28/2010		Design Structure For Implementing Matrix-Based Search Capability In
BUR920060214	BUR920060214US1	US	11/760,856	6/11/2007	7932641	4/26/2011		Low Voltage Head Room Detection For Reliable Start-Up Of Self-Biase
BUR920070010	BUR920070010US2	US	11/937,111	11/8/2007	8132136	3/6/2012		Dynamic Critical Path Detector For Digital Logic Circuit Paths
BUR920070029	BUR920070029US1	US	11/941,994	11/19/2007	7669569	10/27/2009		System And Method For Implementing Row Redundancy With Reduc
BUR920070045	BUR920070045US1	US	11/949,904	12/4/2007	8024513	9/20/2011		Method And System For Implementing Dynamic Refresh Protocols Fo
BUR920070045	BUR920070045US2	US	12/126,499	5/23/2008	8108609	1/31/2012		Structure For Implementing Dynamic Refresh Protocols For Dram Bas
BUR920070058	BUR920070058US1	US	11/779,432	7/18/2007	8055925	11/8/2011		Structure And Method To Optimize Computational Efficiency In Low-
BUR920070058	BUR920070058US2	US	11/870,575	10/11/2007	8122273	2/21/2012		Structure And Method To Optimize Computational Efficiency In Low-
BUR920070080	BUR920070080US1	US	12/032,798	2/18/2008	7643357	1/5/2010		System And Method For Integrating Dynamic Leakage Reduction Wit
BUR920070088	BUR920070088US1	US	11/869,179	10/9/2007	8291357	10/16/2012		On-Chip Identification Circuit Incorporating Pairs Of Conductors, Each
BUR920070109	BUR920070109US1	US	11/949,068	12/3/2007	7665648	1/12/2010		Apparatus And Method For Implementing Memory Array Device Wit
BUR920070109	BUR920070109US2	US	12/110,456	4/28/2008	8117567	2/14/2012		Structure For Implementing Memory Array Device With Built In Com
BUR920070114	BUR920070114US1	US	11/850,488	9/5/2007	7825469	11/2/2010		Threshold Voltage Compensation For Pixel Design Of Cmos Image Se
BUR920070137	BUR920070137US1	US	12/014,240	1/15/2008	8181148	5/15/2012		Method For Identifying And Implementing Flexible Logic Block Logic F
BUR920070137	BUR920070137US2	US	12/054,835	3/25/2008	8141028	3/20/2012		Structure For Identifying And Implementing Flexible Logic Block Logic
BUR920070157	BUR920070157US1	US	12/048,237	3/14/2008	8233342	7/31/2012		Apparatus And Method For Implementing Write Assist For Static Ran
BUR920070167	BUR920070167US1	US	12/030,903	2/14/2008	8106817	1/31/2012		Semiconductor Structure And Method Of Designing Semiconductor S
BUR920070202	BUR920070202US1	US	12/109,379	4/25/2008	7963493	3/8/2011		Design Structure For Estimating And/Or Predicting Power Cycle Lengt
BUR920070204	BUR920070204US1	US	12/057,686	3/28/2008	8035452	10/11/2011		Structure For Transforming An Input Voltage To Obtain Linearity Bet
BUR920070235	BUR920070235US1	US	12/055,336	3/26/2008	8133893	6/5/2012		Inductor Having Opening Enclosed Within Conductive Line And Relat
BUR920070241	BUR920070241US1	US	12/053,705	3/24/2008	8239811	8/7/2012		System And Method For Wireless And Dynamic Intra-Process Measur
BUR920070276	BUR920070276US1	US	12/342,228	12/23/2008	8039868	10/18/2011		Structure And Method For An Electrostatic Discharge (Esdi) Silicon Co
BUR920070276	BUR920070276US3	US	13/686,422	11/27/2012	8637900	1/28/2014		Electrostatic Discharge (Esdi) Silicon Controlled Rectifier (Scr) Structur
BUR920070276	BUR920070276US4	US	14/079,048	11/13/2013	9029206	5/12/2015		Electrostatic Discharge (Esdi) Silicon Controlled Rectifier (Scr) Structur

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BUR920080006	BUR920080006US1	US	12/147,670	6/27/2008	8214699	7/3/2012		Circuit Structure And Method For Digital Integrated Circuit Performan
BUR920080015	BUR920080015US1	US	12/135,229	6/9/2008	7859921	12/28/2010		Apparatus And Method For Low Power Sensing In A Multi-Port Stram
BUR920080016	BUR920080016US1	US	12/135,237	6/9/2008	7836727	11/9/2010		Apparatus And Method For Low Power, Single-Ended Sensing In A M
BUR920080031	BUR920080031US1	US	12/098,038	4/4/2008	7539962	5/5/2009		System For Expanding A Window Of Valid Data
BUR920080032	BUR920080032US1	US	12/034,161	2/20/2008	8103998	1/24/2012		Verifying Non-Deterministic Behavior Of A Design Under Test
BUR920080078	BUR920080078US1	US	12/192,537	8/15/2008	7781283	8/24/2010		Split-Gate Dram With Mugfet, Design Structure, And Method Of Man
BUR920080078	BUR920080078US2	US	12/192,554	8/15/2008	7979836	7/12/2011		Split-Gate Dram With Mugfet, Design Structure, And Method Of Man
BUR920080079	BUR920080079US3	US	12/538,196	8/10/2009	8184472	5/22/2012		Split-Gate Dram With Lateral Control-Gate Mugfet
BUR920080090	BUR920080090US1	US	12/046,608	3/12/2008	7958477	6/7/2011		Structure, Failure Analysis Tool And Method Of Determining White fir
BUR920080110	BUR920080110US1	US	12/144,686	6/24/2008	8239715	8/7/2012		Method And Apparatus For A Robust Embedded Interface
BUR920080111	BUR920080111US2	US	12/193,058	8/18/2008	8095902	1/10/2012		Design Structure For Couple Noise Characterization Using A Single Os
BUR920080199	BUR920080199US1	US	12/344,711	12/29/2008	8916464	12/23/2014		Structures And Methods For Improving Solder Bump Connections In S
BUR920080199	BUR920080199US2	US	14/552,664	10/24/2014	9087754	7/21/2015		Structures And Methods For Improving Solder Bump Connections In S
BUR920080208	BUR920080208US1	US	12/192,571	8/15/2008	8189723	5/29/2012		Method, Circuit, And Design Structure For Capturing Data Across A Ps
BUR920080208	BUR920080208US2	US	12/192,586	8/15/2008	8309752	10/30/2012		Method, Circuit, And Design Structure For Capturing Data Across A Ps
BUR920080231	BUR920080231US2	US	14/200,613	3/7/2014	9269683	2/23/2016		Integrated Circuit Chip With Pyramid Or Cone-Shaped Conductive Pa
BUR920080245	BUR920080245US2	US	12/632,015	12/7/2009	8363367	1/29/2013		Electrical Overstress Protection Circuit
BUR920080324	BUR920080324US1	US	12/639,454	12/16/2009	8027207	9/27/2011		Leakage Compensated Reference Voltage Generation System
BUR920080326	BUR920080326US1	US	12/882,425	9/15/2010	8188765	5/29/2012		Circuit And Method For Asynchronous Pipeline Processing With Varia
BUR920080358	BUR920080358US1	US	12/862,310	8/24/2010	8521500	8/27/2013		Method And Device For Measuring Integrated Circuit Power Supply N
BUR920080447	BUR920080447US1	US	12/779,608	5/13/2010	8279687	10/2/2012		Single Supply Sub Vdd Bit-Line Precharge Stram And Method For Level
BUR920090025	BUR920090025KR1	Korea	2010-0100230		1625232	5/23/2016	Not Provide	DYNAMICALLY RECONFIGURABLE SELF-MONITORING CIRCUIT
BUR920090025	BUR920090025US1	US	12/605,417	10/26/2009	8407633	3/26/2013		Dynamically Reconfigurable Self-Monitoring Circuit
BUR920090028	BUR920090028US1	US	12/579,089	10/14/2009	8218378	7/10/2012		Word-Line Level Shift Circuit
BUR920090029	BUR920090029CN1	China	201080047089.1		212010800470	8/3/2015	Not Provide	SRAM DELAY CIRCUIT THAT TRACKS BITCELL CHARACTERISTICS
BUR920090029	BUR920090029DE1	Germany	20101103722T		11201000372	2/19/2015	Not Provide	Stram - units with a delay circuit, in order to represent characteristics
BUR920090029	BUR920090029US1	US	12/581,440	10/19/2009	8233337	7/31/2012		Stram Delay Circuit That Tracks Bitcell Characteristics
BUR920090034	BUR920090034US1	US	12/576,275	10/9/2009	8130525	3/6/2012		Method And Apparatus For Configuring A Content-Addressable Mem
BUR920090039	BUR920090039US2	US	14/151,866	1/10/2014	9646125	5/9/2017		Method For Conversion Of Commercial Microprocessor To Radiation-
BUR920090040	BUR920090040US1	US	12/560,593	9/16/2009	7930003	4/5/2011		Delay Circuit With Delay Equal To Percentage Of Input Pulse Width
BUR920090041	BUR920090041US1	US	12/686,457	1/13/2010	8560990	10/15/2013		Method Of Managing Electro Migration In Logic Design And Design St
BUR920090042	BUR920090042US1	US	12/615,856	11/10/2009	8381052	2/19/2013		Circuit And Method For Efficient Memory Repair
BUR920090042	BUR920090042US2	US	13/750,497	1/25/2013	8612813	12/17/2013		Circuit And Method For Efficient Memory Repair
BUR920090043	BUR920090043US1	US	12/686,013	1/12/2010	8300489	10/30/2012		Charge Pump System And Method Utilizing Adjustable Output Charge

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BUR920090068	BUR920090068US1	US	12/782,359	5/18/2010	8302063	10/30/2012		Method And System To Optimize Semiconductor Products For Power
BUR920090069	BUR920090069US1	US	12/770,976	4/30/2010	8238168	8/7/2012		VDD Pre-Set Of Direct Sense Dram
BUR920090076	BUR920090076US1	US	12/894,286	9/30/2010	8768679	7/1/2014		System And Method For Efficient Modeling Of Npskew Effects On Sta
BUR920090080	BUR920090080US1	US	12/845,390	7/28/2010	8415999	4/9/2013		High Frequency Quadrature PLL Circuit And Method
BUR920090080	BUR920090080US2	US	13/761,358	2/7/2013	8581648	11/12/2013		High Frequency Quadrature PLL Circuit And Method
BUR920090083	BUR920090083US1	US	12/877,628	9/8/2010	8233345	7/31/2012		Phase Change Memory Cycle Timer And Method
BUR920090085	BUR920090085US1	US	13/835,358	3/15/2013	9257352	2/9/2016		Semiconductor Test Wafer And Methods For Use Thereof
BUR920100023	BUR920100023US1	US	12/862,844	8/25/2010	8586982	11/19/2013		Semiconductor Test Chip Device To Mimic Field Thermal Mini-Cycles
BUR920100047	BUR920100047US1	US	12/892,160	9/28/2010	8228713	7/24/2012		Scram Having Wordline Up-Level Voltage Adjustable To Assist Bitcell
BUR920100064	BUR920100064DE1	Germa	112012000256.1				Not Provide	CIRCUIT FOR DETECTING STRUCTURAL DEFECTS IN AN INTEGRATED C
BUR920100064	BUR920100064US2	US	14/713,626	5/15/2015	9599664	3/21/2017		Circuit For Detecting Structural Defects In An Integrated Circuit Chip,
BUR920100081	BUR920100081CN1	China	2.0118005749e-		ZL2011800574	7/8/2015	Not Provide	STATIC RANDOM ACCESS MEMORY (SRAM) WRITE ASSIST CIRCUIT W
BUR920100081	BUR920100081US1	US	12/959,893	12/3/2010	8363453	1/29/2013		Static Random Access Memory (SRAM) Write Assist Circuit With Leaka
BUR920100109	BUR920100109US1	US	12/882,191	9/28/2010	8582351	11/12/2013		Method And Systems For Adjusting Wordline Up-Level Voltage To I
BUR920100003	BUR920100003US1	US	13/080,055	4/5/2011	8239818	8/7/2012		Data Structure For Describing Mbit Architecture
BUR920100015	BUR92010015US1	US	13/076,781	3/31/2011	8415763	4/9/2013		Tunable Semiconductor Device
BUR920100025	BUR920100025US1	US	13/115,492	5/25/2011	8634174	1/21/2014		Gate Dielectric Breakdown Protection During Esd Events
BUR920100050	BUR920100050US1	US	13/198,894	8/5/2011	8467260	6/18/2013		Structure And Method For Storing Multiple Repair Pass Data Into A F
BUR920100059	BUR920100059US1	US	13/204,929	8/8/2011	8484543	7/9/2013		Fusebay Controller Structure, System, And Method
BUR920100060	BUR920100060US1	US	13/227,028	9/7/2011	8493250	7/23/2013		Comparator Offset Cancellation In A Successive Approximation Anal
BUR920100062	BUR920100062US1	US	13/223,949	9/1/2011	8537627	9/17/2013		Determining Fusebay Storage Element Usage
BUR920100063	BUR920100063US1	US	13/195,114	8/1/2011	8611164	12/17/2013		Device And Method For Detecting Resistive Defect
BUR920100066	BUR920100066US1	US	13/241,496	9/23/2011	8539404	9/17/2013		Functional Simulation Redundancy Reduction By State Comparison A
BUR920100086	BUR920100086US1	US	13/289,140	11/4/2011	8381161	2/19/2013		Method For Providing A Secure Gray Box" View Proprietary Ip"
BUR920100115	BUR920100115US1	US	13/288,269	11/3/2011	8421495	4/16/2013		Speed Binning For Dynamic And Adaptive Power Control
BUR920100139	BUR920100139CN1	China	2.0131000556e-		ZL 2013100055	3/9/2016	Not Provide	OFF-LINE GAIN CALIBRATION IN A TIME-INTERLEAVED ANALOG-TO-D
BUR920100139	BUR920100139US1	US	13/346,242	1/9/2012	8587464	11/19/2013		Off-Line Gain Calibration In A Time-Interleaved Analog-To-Digital Con
BUR920100141	BUR920100141US1	US	13/327,847	12/16/2011	8612815	12/17/2013		Asynchronous Circuit With An At-Speed Built-In Self-Test (Bist) Archt
BUR920100142	BUR920100142US1	US	13/403,252	2/23/2012	8654594	2/18/2014		Vdoff Max Limiter In Straps For Improved Yield And Power
BUR920100145	BUR920100145US1	US	13/311,832	12/6/2011	8560989	10/15/2013		Statistical Clock Cycle Computation
BUR920100148	BUR920100148US1	US	13/353,383	1/19/2012	8471595	6/25/2013		Selectable Dynamic/Static Latch With Embedded Logic
BUR920100157	BUR920100157US1	US	13/348,850	1/12/2012	8656325	2/18/2014		Integrated Circuit Design Method And System
BUR920100158	BUR920100158CN1	China	2.0121048345e-		ZL2012104834	1/27/2016	Not Provide	FINE GRANULARITY POWER GATING
BUR920100158	BUR920100158DE1	Germa	102012217578.4		102012217578	4/12/2016	Not Provide	FINE GRANULARITY POWER GATING

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BUR920110158	BUR920110158US1	US	13/315,604	12/9/2011	8611169	12/17/2013		Fine Granularity Power Gating
BUR920110159	BUR920110159DE1	Germa	1020122218061		10 2012 221 80	7/27/2018	Not Provide	DUAL POWER SUPPLY MEMORY ARRAY HAVING A CONTROL CIRCUIT
BUR920110159	BUR920110159US1	US	13/307,245	11/30/2011	8630139	1/14/2014		Dual Power Supply Memory Array Having A Control Circuit That Dyna
BUR920110186	BUR920110186US1	US	13/445,187	4/12/2012	8839054	9/16/2014		Read Only Memory (Rom) With Redundancy
BUR920110186	BUR920110186US2	US	14/453,779	8/7/2014	9460811	10/4/2016		Read Only Memory (Rom) With Redundancy
BUR920110187	BUR920110187US1	US	13/558,498	7/26/2012	8754696	6/17/2014		Ring Oscillator
BUR920110189	BUR920110189US1	US	13/426,776	3/22/2012	8575964	11/5/2013		Inactivity Triggered Self Clocking Logic Family
BUR920110195	BUR920110195US2	US	13/948,567	7/23/2013	9172371	10/27/2015		Majority Dominant Power Scheme For Repeated Structures And Sema
BUR920110198	BUR920110198US1	US	13/448,268	5/10/2012	8648634	2/11/2014		Input Jitter Filter For A Phase-Locked Loop (Pll)
BUR920110212	BUR920110212CN1	China	2.0131005352e-		ZL 2013100531	3/30/2016	Not Provide	REFERENCE GENERATOR WITH PROGRAMMABLE M AND B PARAMET
BUR920110212	BUR920110212US1	US	13/401,368	2/21/2012	8773920	7/8/2014		Reference Generator With Programmable M And B Parameters And
BUR920110228	BUR920110228US1	US	13/567,127	8/6/2012	8661399	2/25/2014		Built-In Self-Test (Bist) Organizational File Generation
BUR920120012	BUR920120012US1	US	13/464,131	5/4/2012	8643987	2/4/2014		Current Leakage In Rc Esd Clamps
BUR920120035	BUR920120035US1	US	13/567,314	8/6/2012	8638149	1/28/2014		Equalized Rise And Fall Slew Rates For A Buffer
BUR920120042	BUR920120042US1	US	13/614,564	9/13/2012	9157956	10/13/2015		Adaptive Power Control Using Timing Canonicals
BUR920120045	BUR920120045US1	US	13/553,986	7/20/2012	9557378	1/31/2017		Method And Structure For Multi-Core Chip Product Test And Selectiv
BUR920120049	BUR920120049US1	US	13/564,337	8/1/2012	9429619	8/30/2016		Reliability Test Screen Optimization
BUR920120067	BUR920120067US1	US	13/676,063	11/13/2012	9188643	11/17/2015		Flexible Performance Screen Ring Oscillator Within A Scan Chain
BUR920120073	BUR920120073CN1	China	2.0138002691e-		ZL 2013800261	7/21/2017	Not Provide	CONTENT ADDRESSABLE MEMORY EARLY-PREDICT LATE-CORRECT SI
BUR920120073	BUR920120073DE1	Germa	20131102065T		112013002061	6/1/2017	Not Provide	Content addressable memory (content addressable memory) with pr
BUR920120073	BUR920120073IN1	India	8323/CHENP/20				Not Provide	CONTENT ADDRESSABLE MEMORY EARLY-PREDICT LATE-CORRECT SI
BUR920120073	BUR920120073US1	US	13/482,166	5/29/2012	9384835	7/5/2016		Content Addressable Memory Early-Predict Late-Correct Single Ende
BUR920120075	BUR920120075US1	US	13/732,713	1/2/2013	9270268	2/23/2016		Compensated Impedance Calibration Circuit
BUR920120075	BUR920120075US2	US	14/519,630	10/21/2014	9236863	1/12/2016		Compensated Impedance Calibration Circuit
BUR920120093	BUR920120093US1	US	13/626,040	9/25/2012	9310426	4/12/2016		On-Going Reliability Monitoring Of Integrated Circuit Chips In The Fiel
BUR920120105	BUR920120105CN1	China	2.0131044968e-		ZL 2013104496	9/22/2017	Not Provide	FERROELECTRIC RANDOM ACCESS MEMORY WITH OPTIMIZED HARD
BUR920120105	BUR920120105US1	US	13/628,715	9/27/2012	8796044	8/5/2014		Ferroelectric Random Access Memory With Optimized Hardmask
BUR920120138	BUR920120138CN1	China	2014100029671		ZL 2014100021	6/1/2017	Not Provide	DESIGN STRUCTURE FOR AN INDUCTOR-CAPACITOR VOLTAGE-CONT
BUR920120138	BUR920120138US1	US	13/734,364	1/4/2013	8912854	12/16/2014		Structure For An Inductor-Capacitor Voltage-Controlled Oscillator
BUR920120138	BUR920120138US2	US	14/491,037	9/19/2014	9281779	3/8/2016		Structure For An Inductor-Capacitor Voltage-Controlled Oscillator
BUR920120177	BUR920120177CN1	China	2.0141003105e-		ZL 2014100310	1/5/2018	Not Provide	COMPLEMENTARY METAL-OXIDE-SEMICONDUCTOR (CMOS) DYNAM
BUR920120177	BUR920120177US1	US	13/747,529	1/23/2013	8934286	1/13/2015		Complementary Metal-Oxide-Semiconductor (Cmos) Dynamic Rando
BUR920120185	BUR920120185US1	US	13/888,108	5/6/2013	9177646	11/3/2015		Implementing Computational Memory From Content Addressable M
BUR920120191	BUR920120191US2	US	14/515,597	10/16/2014	9245854	1/26/2016		Organic Module Emi Shielding Structures And Methods

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BUR920130013	BUR920130013UN1	China	2 0141011759e-		ZL2014101175	10/5/2016	Not Provide	SIGNAL MARGIN CENTERING FOR SINGLE-ENDED eDRAM SENSE AMP
BUR920130013	BUR920130013US1	US	13/851,202	3/27/2013	9093175	7/28/2015		Signal Margin Centering For Single-Ended Edram Sense Amplifier
BUR920130041	BUR920130041US1	US	13/955,401	7/31/2013	9286181	3/15/2016		Apparatus For Capturing Results Of Memory Testing
BUR920130052	BUR920130052US1	US	13/887,508	5/6/2013	9117547	8/25/2015		Reduced Stress High Voltage Word Line Driver
BUR920130054	BUR920130054US1	US	13/972,149	8/21/2013	9542981	1/10/2017		Self-Timed, Single-Ended Sense Amplifier
BUR920130057	BUR920130057US1	US	13/987,491	8/15/2013	9255962	2/9/2016		Determining Intra-Die Variation Of An Integrated Circuit
BUR920130074	BUR920130074US1	US	13/963,438	8/9/2013	9372520	6/21/2016		Reverse Performance Binning
BUR920130078	BUR920130078US1	US	13/958,754	8/5/2013	8879295	11/4/2014		Electronic Circuit For Remapping Faulty Memory Arrays Of Variable
BUR920130079	BUR920130079CN1	China	2 0141045235e-		ZL 201410452	8/18/2017	Not Provide	VERIFYING PARTIAL GOOD VOLTAGE ISLAND STRUCTURES
BUR920130079	BUR920130079US1	US	14/019,957	9/6/2013	9172373	10/27/2015		Verifying Partial Good Voltage Island Structures
BUR920130081	BUR920130081US2	US	14/886,177	10/19/2015	9331037	5/3/2016		Preventing Misshaped Solder Balls
BUR920130098	BUR920130098US1	US	14/059,486	10/22/2013	9180318	11/17/2015		Method Of Forming An Integrated Crackstop
BUR920130098	BUR920130098US2	US	14/876,889	10/7/2015	9397054	7/19/2016		Semiconductor Structure With An Interconnect Level Having A Condu
BUR920130110	BUR920130110US1	US	14/190,723	2/26/2014	9171125	10/27/2015		Limiting Skew Between Different Device Types To Meet Performance
BUR920130130	BUR920130130US1	US	14/026,158	9/13/2013	9159696	10/13/2015		Plug Via Formation By Patterned Plating And Polishing
BUR920130138	BUR920130138US2	US	14/708,755	5/11/2015	9185807	11/10/2015		Integrated Circuit Structures Having Off-Axis In-Hole Capacitor
BUR920130141	BUR920130141US1	US	14/185,079	2/20/2014	9262569	2/16/2016		Balancing Sensitivities With Respect To Timing Closure For Integrated
BUR920130145	BUR920130145US1	US	14/152,345	1/10/2014	9286980	3/15/2016		Converting An Xy Team To A Value Team
BUR920130146	BUR920130146US1	US	14/146,793	1/3/2014	9281023	3/8/2016		Single-Ended Sensing Circuits For Signal Lines
BUR920130163	BUR920130163US1	US	14/178,955	2/12/2014	9472269	10/18/2016		Stress Balancing Of Circuits
BUR920130177	BUR920130177US1	US	14/202,590	3/10/2014	9224091	12/29/2015		Learning Artificial Neural Network Using Ternary Content Addressabl
BUR920130183	BUR920130183US1	US	14/295,794	6/4/2014	9252717	2/2/2016		Phase Noise Reduction In Lc-Vco
BUR920140012	BUR920140012US1	US	14/546,318	11/18/2014				Scoped Search Engine
BUR920140019	BUR920140019US1	US	14/468,999	8/26/2014	9514844	12/6/2016		Fast Auto Shift Of Failing Memory Diagnostics Data Using Pattern Det
BUR920140037	BUR920140037US1	US	14/577,113	12/19/2014	9251890	2/12/2016		Bias Temperature Instability State Detection And Correction
BUR920140101	BUR920140101US1	US	14/620,273	2/12/2015	9618566	4/11/2017		Systems And Methods To Prevent Incorporation Of A Used Integrate
BUR920140102	BUR920140102US1	US	14/624,907	2/18/2015	9625925	4/18/2017		System And Method For Identifying Operating Temperatures And Mo
BUR920140164	BUR920140164US1	US	14/614,470	2/5/2015	9269407	2/23/2016		System And Method For Managing Circuit Performance And Power C
BUR920150001	BUR920150001US1	US	14/695,091	4/24/2015	9552447	1/24/2017		Systems And Methods For Controlling Integrated Circuit Chip Temper
BUR920150014	BUR920150014US1	US	14/819,784	8/6/2015	9437282	9/6/2016		High Performance Sense Amplifier
BUR920150022	BUR920150022US1	US	14/700,402	4/30/2015	9791502	10/17/2017		On-Chip Usable Life Depletion Meter And Associated Method
BUR920150023	BUR920150023US1	US	14/695,112	4/24/2015	9799767	9/12/2017		Pre-Test Power-Optimized Bin Reassignment Following Selective Volt
BUR920150024	BUR920150024US1	US	14/734,411	6/9/2015	9501607	11/22/2016		Composite Views For Ip Blocks In Asic Designs
BUR920150028	BUR920150028US1	US	14/734,504	6/9/2015	9601200	3/21/2017		Team Structures With Reduced Power Supply Noise

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BUR920150032	BUR920150032US1	US	14/832,127	8/21/2015	9570156	2/14/2017		Data Aware Write Scheme For Sram
BUR920150057	BUR920150057US1	US	14/918,149	10/20/2015	9761329	9/12/2017		Built-In Self-Test (Bist) Circuit And Associated Bist Method For Embed
BUR920150062	BUR920150062US1	US	14/751,222	6/26/2015	9519747	12/13/2016		Dynamic And Adaptive Timing Sensitivity During Static Timing Analysi
BUR920150064	BUR920150064US1	US	14/922,323	10/26/2015	9330769	7/12/2016		Sense Amplifiers And Multiplexed Latches
BUR920150069	BUR920150069US1	US	14/742,801	6/18/2015	9639645	5/12/2017		Integrated Circuit Chip Reliability Using Reliability-Optimized Failure
BUR920150075	BUR920150075US1	US	14/932,256	10/26/2015	9740815	8/22/2017		Electromigration-Aware Integrated Circuit Design Methods And Syst
BUR920150079	BUR920150079US1	US	15/076,139	3/21/2016	10020047	7/10/2018		Static Random Access Memory (Sram) Write Assist Circuit With Imp
BUR920150080	BUR920150080US1	US	15/136,404	4/22/2016	10014074	7/3/2018		Failure Analysis And Repair Register Sharing For Memory Bist
BUR920150090	BUR920150090US1	US	14/862,652	9/23/2015	9619609	4/11/2017		Integrated Circuit Chip Design Methods And Systems Using Process
CH920030065U	CH920030065US1	US	10/720,974	11/24/2003	7285604	11/13/2007		Method For Determining Jitter Of A Signal In A Serial Link And High S
CH920040024U	CH920040024US1	US	11/214,161	8/29/2005	7522687	4/21/2009		Clock And Data Recovery System And Method For Clock And Data Re
CH920120050U	CH920120050US1	US	13/606,940	9/7/2012	8674737	3/18/2014		Clock Feathered Slew Rate Control System
CH9319990017	CH9319990017EP1	Europe	101146.9		1024599	4/5/2006	Not Provide	DELAY-LOCKED-LOOP (DLL) HAVING SYMMETRICAL RISING AND FALLI
CH9319990017	CH9319990017US1	US	09/239,487	1/28/1999	6127866	10/3/2000		
CN820150610-	CN820150610-CN-N	China	2.0161110145e-				Not Provide	Ethernet Physical Layer Device Having An Integrated Physical Coding
CN820150610-	CN820150610-US-N	US	15/336,974	10/28/2016	10411832	9/10/2019		Ethernet Physical Layer Device Having Integrated Physical Coding An
CN920090009C	CN920090009CN1	China	2.0091013697e-		ZL2009101369	7/25/2012	Not Provide	Method for detecting contradictory timing constraints
CN920090009U	CN920090009US1	US	12/767,068	4/26/2010	8302048	10/30/2012		Method For Detecting Contradictory Timing Constraint Conflicts
CN920100115C	CN920100115CN1	China	2.0101057702e-		ZL2010105770	6/11/2014	Not Provide	Method and Apparatus for Timing improvement based on Delay Vari
CN920100115U	CN920100115US1	US	13/296,555	11/15/2011	8769470	7/1/2014		Timing Closure In Chip Design
CN920100119C	CN920100119CN1	China	2.0101061233e-		ZL2010106123	7/9/2014	Not Provide	Delay Weighted Clock Tree Branching Optimization Method
CN920100119U	CN920100119US1	US	13/325,102	12/14/2011	8484604	7/9/2013		Constructing A Clock Tree For An Integrated Circuit Design
CN920130127C	CN920130127CN1	China	2.013105302e+*		ZL 201310530.	9/18/2018	Not Provide	METHOD AND APPARATUS FOR SIMULATING A DIGITAL CIRCUIT
CN920130127U	CN920130127US1	US	14/524,413	10/27/2014	10146895	12/4/2018		Method And Apparatus For Simulating A Digital Circuit
CN920130180U	CN920130180US1	US	14/633,477	2/27/2015	9727684	8/8/2017		Method And Apparatus For Physical-Aware Hold Violation Fixing
CN920150040U	CN920150040US1	US	14/755,621	6/30/2015	9820248	11/14/2017		Network Clock Synchronization
DE920080004U	DE920080004US2	US	12/236,551	9/24/2008	8090929	1/3/2012		Generating Clock Signals For CoupledASIC Chips In Processor Interfac
END919970039	END919970039US1	US	08/936,032	9/23/1997	6074895	6/13/2000		
END919970148	END919970148US1	US	09/018,698	2/4/1998	6326696	12/4/2001		
END919990128	END919990128US1	US	09/501,393	2/9/2000	6229754	5/8/2001		Write Through Function For A Memory
END920000025	END920000025US1	US	09/795,610	2/27/2001	6531410	3/11/2003		Intrinsic Dual Gate Oxide Mosfet Using A Damascene Gate Process
END920010026	END920010026CN1	China	02812341.7		ZL02812341.7	9/13/2006	Not Provide	EXTENSION OF FATIGUE LIFE FOR C4 SOLDER BALL TO CHIP CONNECT
END920010026	END920010026TW	Taiwan	9111.657		567595	12/21/2003	Not Provide	EXTENSION OF FATIGUE LIFE FOR C4 SOLDER BALL TO CHIP CONNECT
END920010026	END920010026US1	US	09/885,853	6/20/2001	7067916	6/27/2006		Extension Of Fatigue Life For C4 Solder Ball To Chip Connection

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END920030035	END920030035US1	US	10/661,050	9/11/2003	6917662	7/12/2005		Programmable Low-Power High-Frequency Divider
END920030035	END920030035US3	US	11/325,786	1/5/2006	7342429	3/11/2008		Programmable Low-Power High-Frequency Divider
FIS820140038	FIS820140038-US-N	US	15/048,066	2/19/2016	9754071	9/5/2017		Integrated Circuit (ic) Design Analysis And Feature Extraction
FIS820150197	FIS820150197-US-N	US	15/176,598	6/8/2016	9865977	10/31/2017		Integrated Circuit Structure Having Through-Silicon Via And Method
FIS919950073	FIS919950073US1	US	08/536,901	9/29/1995	5745734	4/28/1998		
FIS919950075	FIS919950075US1	US	08/570,850	12/12/1995	5734582	3/31/1998		
FIS919950077	FIS919950077CN1	China	96121505.4		ZL96121505.4	8/6/2003	Not Provide FIELD-PROGRAMMABLE MEMORY ARRAY	
FIS919950077	FIS919950077US1	US	08/575,312	12/20/1995	5914906	6/22/1999		
FIS919950077	FIS919950077US4	US	09/189,750	11/10/1998	6118707	9/12/2000		
FIS919990172	FIS919990172US1	US	09/458,877	12/10/1999	6217989	4/17/2001		Conductive Line Features For Enhanced Reliability Of Multi-Layer Cer
FIS920000200	FIS920000200US1	US	09/765,035	1/17/2001	6373771	4/16/2002		Integrated Fuse Latch And Shift Register For Efficient Programming A
FIS920000242	FIS920000242US1	US	09/805,027	3/12/2001	7087997	8/8/2006		Copper To Aluminum Interlayer Interconnect Using Stud And Via Line
FIS920000302	FIS920000302US1	US	09/757,107	1/8/2001	6466100	10/15/2002		Linear Voltage Controlled Oscillator Transconductor With Gain Comp
FIS920000363	FIS920000363TW1	Taiwan	91111143		N#-190151	3/5/2004	Not Provide	Single Bitline Direct Sensing Architecture For High Speed Memory De
FIS920000363	FIS920000363US1	US	09/870,755	5/31/2001	6552944	4/22/2003		Single Bitline Direct Sensing Architecture For High Speed Memory De
FIS920000364	FIS920000364TW1	Taiwan	91111303		556222	10/1/2003	Not Provide	Stabilized Direct Sensing Memory Architecture
FIS920000364	FIS920000364US1	US	09/870,559	5/31/2001	6438051	8/20/2002		Stabilized Direct Sensing Memory Architecture
FIS920000380	FIS920000380US1	US	09/907,387	7/17/2001	6834365	12/21/2004		Integrated Real Time Data Tracing With Low Pin Count Output
FIS920000399	FIS920000399US1	US	09/892,396	6/27/2001	6552920	4/22/2003		Saving Content Addressable Memory Power Through Conditional Co
FIS920010028	FIS920010028US1	US	09/887,792	6/22/2001	6785832	8/31/2004		Process Independent Source Synchronous Data Capture Apparatus A
FIS920010235	FIS920010235US1	US	10/199,788	7/19/2002	6990525	7/19/2005		Method And Apparatus Of Local Word-Line Redundancy In Cam
FIS920010285	FIS920010285JP1	Japan	2003-019203		3954974	5/11/2007	Not Provide	Unique Feature Design Enabling Structural Integrity For Advanced Lo
FIS920010285	FIS920010285US1	US	10/078,174	2/15/2002	6650010	11/18/2003		Unique Feature Design Enabling Structural Integrity For Advanced Lo
FIS920010287	FIS920010287US1	US	10/059,863	1/30/2002	6650561	11/18/2003		High Reliability Content-Addressable Memory Using Shadow Content
FIS920010292	FIS920010292US1	US	10/235,435	9/4/2002	6821026	11/23/2004		Redundant Configurable Voxel Laser Array Optical Light Source
FIS920010312	FIS920010312US1	US	11/160,307	6/17/2005	7325213	1/29/2008		Nested Design Approach
FIS920020148	FIS920020148US1	US	10/342,420	1/14/2003	6800530	10/5/2004		Triple Layer Hard Mask For Gate Patterning To Fabricate Scaled Cmos
FIS920030022	FIS920030022US1	US	10/299,347	4/2/2003	6747890	6/8/2004		Gain Cell Structure With Deep Trench Capacitor
FIS920030048	FIS920030048US1	US	10/250,043	5/30/2003	6937054	8/30/2005		Programmable Peeking Receiver And Method
FIS920030050	FIS920030050US1	US	10/249,545	4/17/2003	6881357	5/10/2005		Reference Current Generation System And Method
FIS920030090	FIS920030090CN1	China	2,00490014966-		ZL2004900149	12/16/2009	Not Provide	Low Power Manager for Standby Operation
FIS920030090	FIS920030090DE1	Germa	04741612.8		112011100116	1/7/2009	Not Provide	Low Power Manager for Standby Operation
FIS920030090	FIS920030090EP1	Europe	04741612.8		1659602	1/7/2009	Not Provide	Low Power Manager for Standby Operation
FIS920030090	FIS920030090US1	US	10/250,233	6/16/2003	7046572	5/16/2006		Low Power Manager For Standby Operation Of Memory System

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FIS920030111	FIS920030111US1	US	10/604,025	6/23/2003	7352815	4/1/2008		Data Transceiver And Method For Equalizing The Data Eye Of A Differ
FIS920030121	FIS920030121US1	US	10/604,799	8/18/2003	6897712	5/24/2005		Apparatus And Method For Detecting Loss Of High-Speed Signal
FIS920030154	FIS920030154US1	US	10/604,419	7/18/2003	7113749	9/26/2006		System And Method For Measuring A High Speed Signal
FIS920030314	FIS920030314US1	US	10/708,233	2/18/2004	6993981	9/27/2005		Dynamic Threshold For Vco Calibration
FIS920030409	FIS920030409CN1	China	200510002155.7		100485806	5/6/2009	Not Provide	Concurrent Refresh Mode with Distributed Row Address Counters In
FIS920030409	FIS920030409JP1	Japan	2005-007728		4524194	6/4/2010	Not Provide	Concurrent Refresh Mode with Distributed Row Address Counters In
FIS920030409	FIS920030409US1	US	10/757,846	1/15/2004	6967885	11/22/2005		Concurrent Refresh Mode With Distributed Row Address Counters In
FIS920040082	FIS920040082US1	US	10/711,713	9/30/2004	7403019	8/5/2008		High Speed Multi-Mode Receiver With Adaptive Receiver Equalization
FIS920040132	FIS920040132US1	US	10/875,699	6/24/2004	7278459	10/9/2007		Common Carrier
FIS920040304	FIS920040304US1	US	10/904,460	11/11/2004	7106096	9/12/2006		Circuit And Method Of Controlling Integrated Circuit Power Consump
FIS920040400	FIS920040400US1	US	10/908,959	6/2/2005	8040813	10/18/2011		Apparatus And Method For Reduced Loading Of Signal Transmission
FIS920050261	FIS920050261US1	US	11/308,167	3/9/2006	7381590	6/3/2008		Method And Device Including Rewritable Alpha Particle Barrier And
FIS920050269	FIS920050269CN1	China	2 0061000408e-		ZL2006100040	8/3/2011	Not Provide	Multi-Anode System For Uniform Plating Of Alloys
FIS920050269	FIS920050269US1	US	11/617,494	1/26/2007	8177945	5/15/2012		Multi-Anode System For Uniform Plating Of Alloys
FIS920050336	FIS920050336US1	US	11/307,785	2/22/2006	7254078	8/7/2007		System And Method For Increasing Reliability Of Electrical Fuse Progr
FIS920050379	FIS920050379CN1	China	2 0078001133e-		ZL2007800113	11/3/2010	Not Provide	Method And Structure For Eliminating Aluminum Terminal Pad Materi
FIS920050379	FIS920050379TW1	Taiwan	96111680		I438852	5/21/2014	Not Provide	METHOD FOR FAR BACK END OF LINE (FBEOL) SEMICONDUCTOR DEV
FIS920050379	FIS920050379US1	US	11/308,539	4/4/2006	7335021	5/20/2008		Method And Structure For Eliminating Aluminum Terminal Pad Materi
FIS920050387	FIS920050387US1	US	11/307,894	2/27/2006	7441213	10/21/2008		Method For Testing The Validity Of Initial-Condition Statements In Cr
FIS920060116	FIS920060116US1	US	11/683,068	3/7/2007	7674691	3/9/2010		Method Of Manufacturing An Electrical Antifuse
FIS920060118	FIS920060118US1	US	11/380,515	4/27/2006	7397261	7/8/2008		Monitoring System For Detecting And Characterizing Classes Of Leaka
FIS920060132	FIS920060132US1	US	11/559,130	11/13/2006	7573115	8/11/2009		Structure And Method For Enhancing Resistance To Fracture Of Bond
FIS920060140	FIS920060140US1	US	11/461,208	7/31/2006	7470985	12/30/2008		Solder Connector Structure And Method
FIS920060279	FIS920060279US1	US	11/548,482	10/11/2006	8445362	5/21/2013		Apparatus And Method For Programming An Electronically Program
FIS920070026	FIS920070026US1	US	11/759,396	6/7/2007	7974304	7/5/2011		Out Of Band Signaling Enhancement For High Speed Serial Driver
FIS920070026	FIS920070026US2	US	12/154,796	5/27/2008	8144726	3/27/2012		Structure For Out Of Band Signaling Enhancement For High Speed Ser
FIS920070030	FIS920070030US1	US	11/769,128	6/27/2007	8219040	7/10/2012		Transmitter Bandwidth Optimization Circuit
FIS920070030	FIS920070030US2	US	11/985,963	11/19/2007	8219041	7/10/2012		Design Structure For Transmitter Bandwidth Optimization Circuit
FIS920070092	FIS920070092US1	US	11/949,426	12/3/2007	7432755	10/7/2008		Programming Current Stabilized Electrical Fuse Programming Circuit
FIS920070097	FIS920070097US1	US	11/766,268	6/21/2007	7855563	12/21/2010		Robust Cable Connectivity Test Receiver For High-Speed Data Receive
FIS920070278	FIS920070278US1	US	11/940,531	11/15/2007	8492846	7/23/2013		Stress Generating Shallow Trench Isolation Structure Having Dual Co
FIS920070278	FIS920070278US3	US	13/947,677	7/22/2013	9013001	4/21/2015		Stress-Generating Shallow Trench Isolation Structure Having Dual Co
FIS920070281	FIS920070281US1	US	12/365,919	2/5/2009	8133949	5/22/2012		Compensation Of Vco Gain Curve Offsets Using Auto-Calibration
FIS920070308	FIS920070308US1	US	12/698,629	2/2/2010	8401135	3/19/2013		Post-Equalization Amplitude Latch-Based Channel Characterization M

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FIS920070383	FIS920070383US1	US	11/947,832	11/30/2007	8120175	2/21/2012		Soft Error Rate Mitigation By Novel Interconnect Structure
FIS920070483	FIS920070483US1	US	12/036,320	2/25/2008	7908534	3/15/2011		Diagnosable General Purpose Test Registers Scan Chain Design
FIS920080171	FIS920080171CN1	China	20101021537e		ZL2010102153	11/6/2013	Not Provide	Electrically Programmable Fuse Using Anisometric Contacts and Fabri
FIS920080171	FIS920080171US1	US	12/493,616	6/29/2009	8519507	8/27/2013		Electrically Programmable Fuse Using Anisometric Contacts And Fabri
FIS920080224	FIS920080224US1	US	12/365,921	2/5/2009	8183950	5/22/2012		Auto-Calibration For Ring Oscillator Vco
FIS920090065	FIS920090065US1	US	12/492,198	6/26/2009	8312404	11/13/2012		Multi-Segments Modeling Bond Wire Interconnects With 2d Simulati
FIS920100066	FIS920100066US1	US	12/835,764	7/14/2010	8350264	1/8/2013		Secure Anti-Fuse With Low-Voltage Programming Through Localized
FIS920100108	FIS920100108US1	US	12/878,579	9/9/2010	8378498	2/19/2013		Chip Assembly With A Coreless Substrate Employing A Patterned Ad
FIS920110074	FIS920110074US1	US	13/345,290	1/6/2012	8901706	12/2/2014		Thermally Stable High-K Tetragonal HfO2 Layer Within High Aspect Ra
FIS920110157	FIS920110157US1	US	13/342,453	1/3/2012	8410835	4/2/2013		Leakage Tolerant Phase Locked Loop Circuit Device
FIS920110180	FIS920110180US1	US	13/295,351	11/14/2011	8493117	7/23/2013		Leakage Tolerant Delay Locked Loop Circuit Device
FIS920110221	FIS920110221CN1	China	20128005732e		ZL2012800573	8/24/2016	Not Provide	Retention Based Intrinsic Fingerprint Identification Featuring a Fuzzy
FIS920110221	FIS920110221DE1	Germa	20121104439T		11 2012 004 4	11/24/2016	Not Provide	Memory supported intrinsic fingerprint - identification with a fuzzy -
FIS920110221	FIS920110221IN1	India	2203/CHEMR/20				Not Provide	Retention Based Intrinsic Fingerprint Identification Featuring a Fuzzy
FIS920110221	FIS920110221US1	US	13/302,314	11/22/2011	8599010	11/19/2013		Retention Based Intrinsic Fingerprint Identification Featuring A Fuzzy
FIS920120068	FIS920120068US1	US	13/465,159	5/7/2012	9041113	5/26/2015		Forming Cmos With Close Proximity Stressors
FIS920120128	FIS920120128US1	US	13/552,106	7/18/2012	8841208	9/23/2014		Method Of Forming Vertical Electronic Fuse Interconnect Structures I
FIS920120200	FIS920120200US1	US	13/661,062	10/26/2012	8754690	6/17/2014		Programmable Duty Cycle Setter Employing Time To Voltage Domain
FIS920120203	FIS920120203US1	US	13/648,292	10/10/2012	9202040	12/1/2015		Chip Authentication Using Multi-Domain Intrinsic Identifiers
FIS920120234	FIS920120234US1	US	13/688,595	11/29/2012	8667448	3/4/2014		Integrated Circuit Having Local Maximum Operating Voltage
FIS920120249	FIS920120249US1	US	13/757,110	1/9/2013	8652600	4/8/2014		Multi-Protocol Driver Slew Rate Calibration System For Calibrating Sle
FIS920130003	FIS920130003US1	US	13/903,199	5/28/2013	9184948	11/10/2015		Decision Feedback Equalizer (dfe) With A Plurality Of Independently
FIS920130088	FIS920130088PCT1	Patent	US2014/040462				Not Provide	Electronic Module Assembly With Patterned Adhesive Array
FIS920130088	FIS920130088US2	US	14/567,052	12/11/2014	9293439	3/22/2016		Electronic Module Assembly With Patterned Adhesive Array
FIS920130196	FIS920130196CN1	China	201410681506.1		ZL2014106815	7/27/2017	Not Provide	(FDC)Uncionable ID Based Chip-to-Chip Communication
FIS920130196	FIS920130196US1	US	14/102,607	12/11/2013	9219722	12/22/2015		Uncionable Id Based Chip-To-Chip Communication
FIS920130232	FIS920130232US1	US	14/172,550	2/4/2014	9374098	6/21/2016		Transmitter Serializer Latency Trim
FIS920130233	FIS920130233US1	US	14/172,618	2/4/2014	9473172	10/18/2016		Receiver Deserializer latency Trim
FIS920140166	FIS920140166US1	US	14/467,191	8/25/2014	9396991	7/19/2016		Multilayered Contact Structure Having Nickel, Copper, And Nickel-Iro
FIS920140185	FIS920140185US1	US	14/525,267	10/28/2014	9728440	8/8/2017		Non-Transparent Microelectronic Grade Glass As A Substrate, Tempo
FIS920140280	FIS920140280US1	US	14/669,775	3/26/2015	9401315	7/26/2016		Thermal Hot Spot Cooling For Semiconductor Devices
FIS920150135	FIS920150135US1	US	14/964,327	12/9/2015	9571111	2/14/2017		System And Method To Speed Up Pll Lock Time On Subsequent Calibr
FIS920150135	FIS920150135US1-C	China	20161113183e-				Not Provide	SYSTEM AND METHOD TO SPEED UP PLL LOCK TIME ON SUBSEQUEN
FIS920150135	FIS920150135US1-T	Taiwan	105138372		1620416	4/1/2018	Not Provide	SYSTEM AND METHOD TO SPEED UP PLL LOCK TIME ON SUBSEQUEN

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FIS920150139	FIS920150139US1	US	14/961,484	12/7/2015	9659604	5/23/2017		Dual-Bit 3-T High Density Mtprom Array
FIS920150139	FIS920150139US1-C	China	2.016111372e				Not Provide	DUAL-BIT 3-T HIGH DENSITY MTPROM ARRAY
FIS920150139	FIS920150139US1-	Germa	201610123654				Not Provide	DUAL-BIT 3-T HIGH DENSITY MTPROM ARRAY
FIS920150139	FIS920150139US1-T	Taiwan	105138376		1646538	1/1/2019	Not Provide	DUAL-BIT 3-T HIGH DENSITY MTPROM ARRAY
FIS920150139	FIS920150139US1-	US	15/478,820	4/4/2017	9786333	10/10/2017		Dual-Bit 3-T High Density Mtprom Array
GU083	GU083-US-NP	US	14/165,039	1/27/2014	9337143	5/10/2016		E-Fuse Structure With Methods Of Fusing The Same And Monitoring
HAUS001	HAUS001-CN-NP	China	201711274097.1				Not Provide	Digitally controlled varactor structure for high resolution DCO
HAUS001	HAUS001-TW-NP	Taiwan	106125710		165867	7/11/2019	Not Provide	Digitally controlled varactor structure for high resolution DCO
HAUS001	HAUS001-US-NP	US	15/370,004	12/6/2016	10322966	6/25/2019		Digitally Controlled Varactor Structure For High Resolution Dco
HAUS002	HAUS002-US-NP	US	15/277,344	9/27/2016	9774302	9/26/2017		Amplifier Circuit With Single-Ended Input And Differential Outputs
HAUS003	HAUS003-CN-NP	China	2.0171058867e-				Not Provide	SWITCHED CAPACITOR CIRCUIT STRUCTURE WITH METHOD OF CON
HAUS003	HAUS003-DE-NP	Germa	10.2017.212.101				Not Provide	SWITCHED CAPACITOR CIRCUIT STRUCTURE WITH METHOD OF CON
HAUS003	HAUS003-TW-NP	Taiwan	106118674				Not Provide	SWITCHED CAPACITOR CIRCUIT STRUCTURE WITH METHOD OF CON
HAUS003	HAUS003-US-NP	US	15/213,529	7/19/2016	10348243	7/9/2019		Switched Capacitor Circuit Structure With Method Of Controlling Sou
HAUS004	HAUS004-CN-NP	China	201711308359.1				Not Provide	DIGITAL FREQUENCY MULTIPLIER TO GENERATE A LOCAL OSCILLATO
HAUS004	HAUS004-TW-NP	Taiwan	106127461				Not Provide	DIGITAL FREQUENCY MULTIPLIER TO GENERATE A LOCAL OSCILLATO
HAUS004	HAUS004-US-NP	US	15/373,791	12/9/2016	9806701	10/31/2017		Digital Frequency Multiplier To Generate A Local Oscillator Signal In F
HBUR001	HBUR001-US-NP	US	15/140,016	4/27/2016	9870163	1/16/2018		Double Bandwidth Algorithmic Memory Array
HBUR007	HBUR007-CN-NP	China	2.0171007264e-				Not Provide	MATCHLINE PRECHARGE ARCHITECTURE FOR SELF-REFERENCE MATC
HBUR007	HBUR007-DE-NP	Germa	102016213092.1				Not Provide	MATCHLINE PRECHARGE ARCHITECTURE FOR SELF-REFERENCE MATC
HBUR007	HBUR007-US-NP	US	15/164,325	5/25/2016	9583192	2/28/2017		Matchline Precharge Architecture For Self-Reference Matchline Sensi
HBUR008	HBUR008-CN-NP	China	2.0171042222e-				Not Provide	Self Pre-Charging Memory Circuits
HBUR008	HBUR008-DE-NP	Germa	102016215292				Not Provide	Self Pre-Charging Memory Circuits
HBUR008	HBUR008-TW-NP	Taiwan	105122590		1654617	3/21/2019	Not Provide	Self Pre-Charging Memory Circuits
HBUR008	HBUR008-US-NP	US	15/175,466	6/7/2016	9886998	2/6/2018		Self Pre-Charging Memory Circuits
HBUR012	HBUR012-CN-NP	China	2.0171045793e-				Not Provide	ALGORITHMIC N SEARCH/M WRITE TERNARY CONTENT ADDRESSABL
HBUR012	HBUR012-DE-NP	Germa	10.2016.220.321				Not Provide	ALGORITHMIC N SEARCH/M WRITE TERNARY CONTENT ADDRESSABL
HBUR012	HBUR012-TW-NP	Taiwan	105116730		1636458	9/21/2018	Not Provide	ALGORITHMIC N SEARCH/M WRITE TERNARY CONTENT ADDRESSABL
HBUR012	HBUR012-US-NP	US	15/185,956	6/17/2016	9859006	1/2/2018		Algorithmic N Search/M Write Ternary Content Addressable Memory
HD066	HD066-US-DIV	US	15/428,509	2/9/2017				Memory Device Structure
HD066	HD066-US-NP	US	14/918,736	10/21/2015	9614003	4/4/2017		Method Of Forming A Memory Device Structure And Memory Device
HD066-C	HD066-CN-DIV	China	201910430187.1				Not Provide	Method For Forming Memory Device Structure And Memory Device
HD066-C	HD066-CN-NP	China	2.0161092047e-		ZL 2016 1 0920	6/14/2019	Not Provide	Method For Forming Memory Device Structure And Memory Device
HD066-T	HD066-TW-NP	Taiwan	105138538		1620277	4/1/2018	Not Provide	Method Of Forming a Memory Device Structure and Memory Device

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HFS008	HFS008-US-DIV	US	16/033,932	7/12/2018				IC Structure Including Tsv Having Metal Resistant To High Temperature
HFS008	HFS008-US-NP	US	15/292,721	10/13/2016	10049979	8/14/2018		IC Structure Including Tsv Having Metal Resistant To High Temperature
HFS010	HFS010-CN-NP	China	2.0171071041e-					Not Provide IC structure on two sides of substrate and method of forming
HFS010	HFS010-TW-NP	Taiwan	106121256		1657559	4/21/2019		Not Provide IC STRUCTURE ON TWO SIDES OF SUBSTRATE AND METHOD OF FORMING
HFS010	HFS010-US-NP	US	15/239,976	8/18/2016	10068899	9/4/2018		IC Structure On Two Sides Of Substrate And Method Of Forming
HIND007	HIND007-CN-NP	China	201610517254.1					Not Provide HIGH PERFORMANCE MULTIPLEXED LATCHES
HIND007	HIND007-TW-NP	Taiwan	105104246		1649758	2/1/2019		Not Provide High Performance Multiplexed Latches
HIND007	HIND007-US-NP	US	15/003,598	1/21/2016	9787292	10/10/2017		High Performance Multiplexed Latches
HIND008	HIND008-CN-NP	China	2.0171009598e-					Not Provide SENSE AMPLIFIER AND LATCHING SCHEME
HIND008	HIND008-TW-NP	Taiwan	105108787		1645413	12/21/2018		Not Provide Sense Amplifier And Latching Scheme
HIND008	HIND008-US-NP	US	15/054,553	2/26/2016	9761285	9/12/2017		Sense Amplifier Latching Scheme
HMLT052	HMLT052-CN-NP	China	2.0171006357e-		21.201710063	12/21/2018		Not Provide METHOD, APPARATUS AND SYSTEM FOR VOLTAGE COMPENSATION I
HMLT052	HMLT052-TW-NP	Taiwan	106103445		1637252	10/1/2018		Not Provide METHOD, APPARATUS AND SYSTEM FOR VOLTAGE COMPENSATION I
HMLT052	HMLT052-US-NP	US	15/013,956	2/2/2016	10069490	9/4/2018		Method, Apparatus And System For Voltage Compensation In A Semi
HSG098	HSG098-US-NP	US	15/227,979	8/4/2016	9843326	12/12/2017		Wide Range Level Shifter For Low Voltage Input Applications
HSG099	HSG099-US-NP	US	15/431,796	2/14/2017	10153279	12/11/2018		Compact And Reliable Changeable Negative Voltage Transmission Cir
HSG108	HSG108-US-NP	US	15/383,083	12/19/2016	10115453	10/30/2018		Integrated Circuits With Stram Devices Having Read Assist Circuits And
IBUR005	IBUR005-CN-NP	China	2.0171050643e-					Not Provide TAMPER DETECTION FOR A CHIP PACKAGE
IBUR005	IBUR005-TW-NP	Taiwan	106112180		1660184	5/21/2019		Not Provide TAMPER DETECTION FOR A CHIP PACKAGE
IBUR005	IBUR005-US-NP	US	15/195,029	6/28/2016				Tamper Detection For A Chip Package
IBUR008	IBUR008-US-NP	US	15/276,182	9/26/2016	9875956	1/23/2018		Integrated Interface Structure
IBUR038	IBUR038-US-NP	US	15/464,397	3/21/2017	9833765	2/13/2018		Transmission System Having Duplicate Transmission Systems For Indl
IBUR050	IBUR050-CN-NP	China	2.0171105725e-					Not Provide Ternary Content Addressable Memory (TCAM) for Multi Bit Miss Dete
IBUR050	IBUR050-TW-NP	Taiwan	106113438		1.634560	9/1/2018		Not Provide Ternary Content Addressable Memory (TCAM) for Multi Bit Miss Dete
IBUR050	IBUR050-US-NP	US	15/340,579	11/1/2016	9916896	3/13/2018		Ternary Content Addressable Memory (TCam) For Multi Bit Miss Dete
IBUR055	IBUR055-CN-NP	China	2.0171065503e-					Not Provide ZERO TEST TIME MEMORY USING BACKGROUND BUILT-IN SELF-TEST
IBUR055	IBUR055-TW-NP	Taiwan	106122580		1644321	12/11/2018		Not Provide ZERO TEST TIME MEMORY USING BACKGROUND BUILT-IN SELF-TEST
IBUR055	IBUR055-US-NP	US	15/478,666	4/4/2017				Zero Test Time Memory Using Background Built-In Self-Test
ICIS-CH	ICIS-CHRT-0017&a	Singapo	10201408366W					Not Provide Ip Protection
ICIS-CHRT-0017&2D	ICIS-CHRT-0017&2D	US	13/161,514	6/16/2011	9069923	6/30/2015		
ICIS-CHRT-0017&3	ICIS-CHRT-0017&3	US	14/751,170	6/26/2015	9947645	4/17/2018		
ICNA003	ICNA003-US-NP	US	15/406,350	1/13/2017	10298345	5/21/2019		Clock Synchronization Using Codeword Marker
ICNA004	ICNA004-CN-NP	China	2.0171105814e-					Not Provide NANOSSECOND ACCURACY UNDER PRECISION TIME PROTOCOL FOR E
ICNA004	ICNA004-TW-NP	Taiwan	106124111		1651983	2/21/2019		Not Provide NANOSSECOND ACCURACY UNDER PRECISION TIME PROTOCOL FOR E

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ICNA004	ICNA004-US-NP	US	15/397,028	1/3/2017	10104148	10/16/2018		Nanosecond Accuracy Under Precision Time Protocol For Ethernet By
IFIS045	IFIS045A-US-NP	US	15/467,610	3/23/2017	10033334	7/24/2018		Peaking Amplifier Frequency Tuning
IFIS045	IFIS045B-US-NP	US	15/467,617	3/23/2017	9966738	5/1/2018		Peaking Amplifier Frequency Tuning
IFIS045	IFIS045C-US-CNT	US	15/790,144	10/23/2017	10111790	2/19/2019		Peaking Amplifier Frequency Tuning
IFIS045	IFIS045C-US-NP	US	15/467,589	3/23/2017	9853612	12/26/2017		Peaking Amplifier Frequency Tuning
IFIS051	IFIS051-US-NP	US	15/460,914	3/16/2017	10014364	7/3/2018		On-Chip Resistors With A Tunable Temperature Coefficient Of Resist
IFIND002	IFIND002-US-NP	US	15/266,261	9/15/2016	9721628	8/1/2017		Address Based Memory Data Path Programming Scheme
IFIND009	IFIND009-CN-NP	China	2.0171084564e-					Not Provide Integrated Level Translator And Latch For Fence Architecture
IFIND009	IFIND009-TW-NP	Taiwan	105144158		1655846	4/1/2019		Not Provide Integrated Level Translator And Latch For Fence Architecture
IFIND009	IFIND009-US-NP	US	15/269,139	9/19/2016	10020809	7/10/2018		Integrated Level Translator And Latch For Fence Architecture
IFIND012	IFIND012-US-NP	US	15/363,056	11/29/2016	9721673	8/1/2017		Distributed Current Source/Sink Using Inactive Memory Elements
IFIND014	IFIND014-CN-NP	China	2.0171108861e-					Not Provide Bending Circuit For Static Random Access Memory (SRAM) Self-Timer
IFIND014	IFIND014-TW-NP	Taiwan	106110780		1637398	10/1/2018		Not Provide Bending Circuit For Static Random Access Memory (SRAM) Self-Timer
IFIND014	IFIND014-US-NP	US	15/345,544	11/8/2016	10217507	2/26/2019		Bending Circuit For Static Random Access Memory (SRAM) Self-Timer
IFIND015	IFIND015-US-NP	US	15/446,091	3/1/2017	9881669	1/30/2018		Wordline Driver With Integrated Voltage Level Shift Function
IF920140066	IF920140066US1	US	14/548,929	11/20/2014	9465683	10/11/2016		Forward Error Correction Synchronization
IF920110081	IF920110081US1	US	13/316,067	12/9/2011	8797096	8/5/2014		Crossstalk Compensation For High Speed, Reduced Swing Circuits
IF920110082	IF920110082US1	US	13/269,942	10/10/2011	8593861	11/26/2013		Asymmetric Memory Cells
IF920110193	IF920110193US1	US	13/296,724	11/15/2011	8427896	4/23/2013		Dynamic Wordline Assist Scheme To Improve Performance Tradeoff I
IF920120023	IF920120023US1	US	13/563,960	8/1/2012	9047980	6/2/2015		Sense Amplifier For Static Random Access Memory With A Pair Of Co
IF920130090	IF920130090US1	US	14/018,451	9/5/2013	9177709	11/3/2015		Structure And Method For Realizing High Performance Multi-Port Ind
IF920130092	IF920130092US1	US	14/018,452	9/5/2013	9054069	6/9/2015		Variable Capacitance Integrated Circuit
IF920140013	IF920140013US1	US	14/317,806	6/27/2014	9286969	3/15/2016		Low Power Sense Amplifier For Static Random Access Memory
IF920140041	IF920140041US1	US	14/331,276	7/15/2014	9542354	1/10/2017		Generating A Parallel Data Signal By Converting Serial Data Of A Serial
IF920140046	IF920140046US1	US	14/466,385	8/22/2014	9288859	12/8/2015		Low Power Static Random Access Memory (SRAM) Read Data Path
IF920140201	IF920140201US1	US	14/734,525	6/9/2015	9570155	2/14/2017		Circuit To Improve Stram Stability
IF920140204	IF920140204US1	US	14/669,043	3/26/2015	9236116	1/12/2016		Memory Cells With Read Access Schemes
IF920150072	IF920150072US1	US	14/886,739	10/19/2015				Auto Test Grouping/Clock Sequencing For At-Speed Test
IFUS001	IFUS001-US-NP	US	15/412,193	1/23/2017	9871531	1/16/2018		Non-Geometric Scaling Current Steering Digital To Analog Converter
IFUS012	IFUS012-US-NP	US	15/451,470	3/7/2017	9911474	3/6/2018		Feedback Circuit At Wordline Ends
IFUS013	IFUS013-CN-NP	China	201810642737.1					Not Provide PHASE ROTATOR APPARATUS
IFUS013	IFUS013-TW-NP	Taiwan	107100641		1647916	1/11/2019		Not Provide PHASE ROTATOR APPARATUS
IFUS013	IFUS013-US-NP	US	15/629,900	6/22/2017	10075174	9/11/2018		Phase Rotator Apparatus
IFUS016	IFUS016-US-NP	US	15/659,352	7/25/2017	10014828	7/3/2018		Transmitter System And Method Of Calibration

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JBUR004	JBUR004-US-NP	US	15/684,492	8/23/2017				Sense-Line Muxing Scheme
JBUR010	JBUR010-CN-NP	China	201810982872.1				Not Provide	CHARGE PUMP CIRCUIT WITH BUILT-IN RETRY
JBUR010	JBUR010-DE-NP	Germa	102018218550.1				Not Provide	CHARGE PUMP CIRCUIT WITH BUILT-IN RETRY
JBUR010	JBUR010-TW-NP	Taiwan	107121316				Not Provide	CHARGE PUMP CIRCUIT WITH BUILT-IN RETRY
JBUR010	JBUR010-US-NP	US	15/822,318	11/27/2017				Charge Pump Circuit With Built-In Retry
JBUR023	JBUR023-CN-NP	China	2018107968399.1				Not Provide	DIFFERENTIAL VOLTAGE GENERATOR
JBUR023	JBUR023-TW-NP	Taiwan	107114680				Not Provide	DIFFERENTIAL VOLTAGE GENERATOR
JBUR023	JBUR023-US-NP	US	15/788,289	10/19/2017	10192590	1/29/2019		Differential Voltage Generator
JBUR038	JBUR038-US-NP	US	15/866,999	1/10/2018				Electrostatic Discharge Clamp With Reduced Off-State Power Consum
JBUR045	JBUR045-CN-NP	China	201910063850.1				Not Provide	ON-CHIP RELIABILITY MONITOR AND METHOD
JBUR045	JBUR045-DE-NP	Germa	10 2019 200 216				Not Provide	ON-CHIP RELIABILITY MONITOR AND METHOD
JBUR045	JBUR045-TW-NP	Taiwan	108102551				Not Provide	ON-CHIP RELIABILITY MONITOR AND METHOD
JBUR045	JBUR045-US-NP	US	15/903,231	2/23/2018				On-Chip Reliability Monitor And Method
JBUR047	JBUR047-CN-NP	China	201910131291				Not Provide	DYNAMIC POWER ANALYSIS WITH PER-MEMORY INSTANCE ACTIVITY
JBUR047	JBUR047-DE-NP	Germa	10 2019 202 015				Not Provide	DYNAMIC POWER ANALYSIS WITH PER-MEMORY INSTANCE ACTIVITY
JBUR047	JBUR047-TW-NP	Taiwan	108105231				Not Provide	DYNAMIC POWER ANALYSIS WITH PER-MEMORY INSTANCE ACTIVITY
JBUR047	JBUR047-US-NP	US	15/928,587	3/22/2018				Dynamic Power Analysis With Per-Memory Instance Activity Customi
JCNA001	JCNA001-US-NP	US	15/636,793	6/29/2017	10365328	7/30/2019		Register Array Having Groups Of Latches With Single Test Latch Testa
JCNA005	JCNA005-CN-NP	China	201910061789.4				Not Provide	WRITE SCHEME FOR A STATIC RANDOM ACCESS MEMORY (SRAM)
JCNA005	JCNA005-DE-NP	Germa	10 2019 208 515				Not Provide	WRITE SCHEME FOR A STATIC RANDOM ACCESS MEMORY (SRAM)
JCNA005	JCNA005-TW-NP	Taiwan	108102267				Not Provide	WRITE SCHEME FOR A STATIC RANDOM ACCESS MEMORY (SRAM)
JCNA005	JCNA005-US-NP	US	15/903,826	2/23/2018				Write Scheme For A Static Random Access Memory (Sram)
JFIS006	JFIS006-CN-NP	China	201810454031.1				Not Provide	FLEXIBLE HEAT SPREADER LID
JFIS006	JFIS006-DE-NP	Germa	10 2018 207 345				Not Provide	FLEXIBLE HEAT SPREADER LID
JFIS006	JFIS006-TW-NP	Taiwan	107108259				Not Provide	FLEXIBLE HEAT SPREADER LID
JFIS006	JFIS006-US-NP	US	15/583,969	5/12/2017	10224262	3/5/2019		Flexible Heat Spreader Lid
JIND001	JIND001-US-NP	US	15/668,362	8/3/2017	10416226	9/17/2019		Test Response Compaction Scheme
JIND002	JIND002-CN-NP	China	201711440928.1				Not Provide	BIT LINE STRAPPING SCHEME FOR HIGH DENSITY SRAM
JIND002	JIND002-TW-NP	Taiwan	106138448				Not Provide	BIT LINE STRAPPING SCHEME FOR HIGH DENSITY SRAM
JIND002	JIND002-US-NP	US	15/693,637	9/1/2017	10199095	2/5/2019		Bit Line Strapping Scheme For High Density Sram
JIND003	JIND003-CN-NP	China	201810311333.4				Not Provide	Intracycle Bitline Restore In High Performance Memory
JIND003	JIND003-TW-NP	Taiwan	107107003				Not Provide	Intracycle Bitline Restore In High Performance Memory
JIND003	JIND003-US-NP	US	15/814,969	11/16/2017				Intracycle Bitline Restore In High Performance Memory
JIND004	JIND004-CN-NP	China	201810755848.1				Not Provide	HYBRID STACK WRITE DRIVER

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JIND004	JIND004-TW-NP	Taiwan	107113834				Not Provide	HYBRID STACK WRITE DRIVER
JIND004	JIND004-US-NP	US	15/750,850	10/12/2017	10186312	1/22/2019		Hybrid Stack Write Driver
JIND009	JIND009-CN-NP	China	201910078311.1				Not Provide	COMMON BOOSTED ASSIST
JIND009	JIND009-DE-NP	Germa	10 2019 203 676				Not Provide	COMMON BOOSTED ASSIST
JIND009	JIND009-TW-NP	Taiwan	108102940				Not Provide	COMMON BOOSTED ASSIST
JIND009	JIND009-US-NP	US	15/906,588	2/27/2018	10381054	8/13/2019		Common Boosted Assist
JIND016	JIND016-CN-NP	China	201910032455.4				Not Provide	SENSE AMPLIFIER LATCH CIRCUIT AND SENSE AMPLIFIER MULTIPLEX
JIND016	JIND016-DE-NP	Germa	10 2019 200 853				Not Provide	SENSE AMPLIFIER LATCH CIRCUIT AND SENSE AMPLIFIER MULTIPLEX
JIND016	JIND016-TW-NP	Taiwan	108101188				Not Provide	SENSE AMPLIFIER LATCH CIRCUIT AND SENSE AMPLIFIER MULTIPLEX
JIND016	JIND016-US-NP	US	15/895,001	2/13/2018	10170164	1/1/2019		Sense Amplifier Latch Circuit And Sense Amplifier Multiplexed Latch C
JIND018	JIND018-DE-NP	Germa	10 2019 201 830				Not Provide	INTEGRATED LEVEL TRANSLATOR
JIND018	JIND018-TW-NP	Taiwan	108105659				Not Provide	INTEGRATED LEVEL TRANSLATOR
JIND018	JIND018-US-NP	US	15/926,272	3/20/2018	10395700	8/27/2019		Integrated Level Translator
JMLT128	JMLT128-CN-NP	China	201711257865.1				Not Provide	INTERCONNECT STRUCTURE
JMLT128	JMLT128-TW-NP	Taiwan	106137196				Not Provide	INTERCONNECT STRUCTURE
JMLT128	JMLT128-US-NP	US	15/664,484	7/31/2017	10381304	8/13/2019		Interconnect Structure
JMLT289	JMLT289-US-NP	US	15/801,380	11/2/2017				Testing Monolithic Three Dimensional Integrated Circuits
JSC011	JSC011-CN-NP	China	201810311075.3				Not Provide	INJECTION LOCKED OSCILLATOR SYSTEM AND PROCESSES
JSC011	JSC011-DE-NP	Germa	102018203578.1				Not Provide	INJECTION LOCKED OSCILLATOR SYSTEM AND PROCESSES
JSC011	JSC011-TW-NP	Taiwan	107106797				Not Provide	INJECTION LOCKED OSCILLATOR SYSTEM AND PROCESSES
JSC011	JSC011-US-NP	US	15/801,735	11/2/2017				Injection Locked Oscillator System And Processes
JSC013	JSC013-US-NP	US	15/886,322	2/1/2018				Controlling Current Flow Between Nodes With Adjustable Back-Gate
JSC025	JSC025-US-NP	US	15/951,426	4/12/2018				Column Multiplexer Decoding
JSG125	JSG125-CN-NP	China	201811581716.3				Not Provide	OVERHANG MODEL FOR REDUCING PASSIVATION STRESS AND METH
JSG125	JSG125-DE-NP	Germa	10 2019 202 082				Not Provide	OVERHANG MODEL FOR REDUCING PASSIVATION STRESS AND METH
JSG125	JSG125-TW-NP	Taiwan	107113836				Not Provide	OVERHANG MODEL FOR REDUCING PASSIVATION STRESS AND METH
JSG125	JSG125-US-NP	US	15/909,684	3/1/2018				Overhang Model For Reducing Passivation Stress And Method For Pr
JUS005	JUS005-CN-NP	China	201810974380.1				Not Provide	MANAGING DATA FLOW IN VCSEL-BASED OPTICAL COMMUNICATIO
JUS005	JUS005-TW-NP	Taiwan	107125708				Not Provide	MANAGING DATA FLOW IN VCSEL-BASED OPTICAL COMMUNICATIO
JUS005	JUS005-US-NP	US	15/686,304	8/25/2017				Managing Data Flow In Vcsel-Based Optical Communications System
JUS018	JUS018-US-NP	US	15/982,459	5/17/2018	10250010	4/2/2019		Control Of Vcsel-Based Optical Communications System
KBUR029	KBUR029-TW-NP	Taiwan	108136679	10/9/2019			Not Provide	ULTRA-LOW VOLTAGE LEVEL SHIFTER
KBUR042	KBUR042-DE-NP	Germa	10 2019 216 387	10/24/2019			Not Provide	PARALLEL-PREFIX ADDER & METHOD
KBUR042	KBUR042-TW-NP	Taiwan	108138626	10/25/2019			Not Provide	PARALLEL-PREFIX ADDER & METHOD

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KIND003	KIND003-US-NP	US	16/216,248	12/11/2018	10432436	10/1/2019		Feed Forward Equalizer With Power-Optimized Distributed Arithmetic
KIND018	KIND018-US-NP	US	16/266,707	2/4/2019	10447510	10/15/2019		On-Demand Feed Forward Equalizer With Distributed Arithmetic Arc
KSC001	KSC001-CN-NP	China	201910312721.1					Not Provide MERGED WRITE DRIVER BASED ON LOCAL SOURCE LINE MRAM ARC
KSC001	KSC001-DE-NP	Germa	10 2019 205 647					Not Provide MERGED WRITE DRIVER BASED ON LOCAL SOURCE LINE MRAM ARC
KSC001	KSC001-TW-NP	Taiwan	108113231					Not Provide MERGED WRITE DRIVER BASED ON LOCAL SOURCE LINE MRAM ARC
KUS001	KUS001-CN-NP	China	201910852860	9/10/2019				Not Provide LINEAR FEEDBACK SHIFT REGISTER-BASED CLOCK SIGNAL GENERATOR
KUS001	KUS001-DE-NP	Germa	10 2019 215 451	10/9/2019				Not Provide LINEAR FEEDBACK SHIFT REGISTER-BASED CLOCK SIGNAL GENERATOR
KUS001	KUS001-TW-NP	Taiwan	108132584	9/10/2019				Not Provide LINEAR FEEDBACK SHIFT REGISTER-BASED CLOCK SIGNAL GENERATOR
KUS001	KUS001-US-NP	US	16/156,460	10/10/2018	10432209	10/1/2019		Linear Feedback Shift Register-Based Clock Signal Generator, Time Drift
KUS002	KUS002-US-NP	US	16/248,290	1/15/2019	10396811	8/27/2019		Temperature Compensation For Reference Voltages In An Analog-To-
LEIS003	LEIS003-US-NP	US	16/726,331	12/24/2019			Pending	Apparatus and Related Method for Synchronizing Operation of Serial
LIND008	LIND008-US-NP	US	16/724,554	12/23/2019			Pending	
MP0001	MP0001	US	08/453,799	6/22/1995	5576647	11/19/1996	In Force	CHARGE PUMP FOR PHASE LOCK LOOP
MP0001	MP0001D1	US	08/660,006	6/3/1996	5635879	6/3/1997	In Force	VOLTAGE CONTROLLED OSCILLATOR FORMED OF TWO DIFFERENTIAL
MP0001	MP0001D2	US	08/657,087	6/3/1996	5686867	11/11/1997	In Force	REGULATED SUPPLY FOR VOLTAGE CONTROLLED OSCILLATOR
MP0003	MP0003	US	08/827,030	3/25/1997	5865988	2/9/1999	In Force	High Speed Write Driver For Inductive Heads
MP0004	MP0004	US	08/847,912	4/28/1997	5861829	1/19/1999	In Force	HIGH-SPEED, LOW POWER, MEDIUM RESOLUTION ANALOG-TO-DIGI
MP0004	MP0004RE	US	09/760,705	1/17/2001	RE37716	5/28/2002	In Force	High-Speed, Low Power, Medium Resolution Analog-To-Digital Conve
MP0005	MP0005	US	08/848,549	4/28/1997	5805006	9/8/1998	In Force	CONTROLLABLE INTEGRATOR
MP0005	MP0005RE	US	09/609,007	6/22/2000	RE37739	6/11/2002	In Force	Controllable Integrator
MP0005	MP0005RECE1	US	09/950,086	12/9/2001	RE38,455	9/3/2004	In Force	CONTROLLABLE INTEGRATOR
MP0005	MP0005RECE2	US	10/614,084	7/8/2003	RE41792	10/5/2010	In Force	Controllable Integrator
MP0006	MP0006	US	08/847,933	4/28/1997	5764718	6/9/1998	In Force	RIPPLE CARRY LOGIC ASND METHOD
MP0006	MP0006RE	US	09/585,343	6/2/2000	RE37335	8/21/2001	In Force	Ripple Carry Logic And Method
MP0007	MP0007	US	08/848,525	4/28/1997	5831456	11/3/1998	In Force	APPARATUS AND METHOD FOR TRANSIENT SUPPRESSION IN SYNCHR
MP0008	MP0008	US	09/015,733	1/29/1998	6219195	4/17/2001	In Force	Low-Noise Magnetic-Resistive Amplifier Using CMOS Technology
MP0009	MP0009	US	09/049,830	3/27/1998	6125154	9/26/2000	In Force	DIGITAL SERVO CHANNEL FOR RECORDING APPARATUS
MP0009	MP0009C1	US	09/605,133	6/27/2000	6430238	8/6/2002	In Force	Digital Servo Channel For Recording Apparatus
MP0009	MP0009C2	US	10/114,578	4/2/2002	6606358	8/12/2003	In Force	Digital Servo Channel For Recording Apparatus
MP0009	MP0009C3	US	10/441,761	5/19/2003	6775338	8/10/2004	In Force	Digital Servo Channel For Recording Apparatus
MP0009	MP0009D1	US	10/441,605	5/19/2003	6985543	1/10/2006	In Force	Digital Servo Channel For Recording Apparatus
MP0010	MP0010	US	09/072,729	5/4/1998	6011437	1/4/2000	In Force	HIGH PRECISION, HIGH BANDWIDTH VARIABLE GAIN AMPLIFIER AND
MP0011	MP0011	US	09/082,424	5/20/1998	6133861	10/17/2000	In Force	Selectable Delay Circuit Having Immunity To Variations In Fabrication
MP0011	MP0011C1	US	09/528,659	3/20/2000	6243031	6/5/2001	In Force	Selectable Delay Method And Circuit

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MP0012	MP0012	US	09/208,211	12/9/1998	6507449	1/14/2003	In Force	Digital Servo Demodulator With Synchronous Dithering And Method
MP0013	MP0013	US	09/434,207	11/4/1999	6427220	7/30/2002	In Force	METHOD AND APPARATUS FOR PRML DETECTION INCORPORATING
MP0014	MP0014	US	09/465,228	12/17/1999	6934387	8/23/2005	In Force	Method And Apparatus For Digital Near-End Echo/Near-End Crosstalk
MP0015	MP0015	US	09/470,170	12/22/1999	6526530	2/25/2003	In Force	Method And Apparatus For Encoding Data Incorporating Check Bits A
MP0016	MP0016aaga	US	13/230,769	9/12/2011	8619571	12/31/2013	In Force	Chip-To-Chip Interface For 100GBASE-T Gigabit Physical Layer Device
MP0016	MP0016aac1	US	12/723,915	3/15/2010	8018962	9/13/2011	In Force	Chip-To-Chip Interface For 1000 Base T Gigabit Physical Layer Device
MP0016	MP0016C1	US	10/892,356	7/16/2004	7173942	2/6/2007	In Force	Chip-To-Chip Interface For 1000 Base T Gigabit Physical Layer Device
MP0016	MP0016C2	US	11/516,359	9/6/2006	7680146	3/16/2010	In Force	Chip-To-Chip Interface For 1000 Base T Gigabit Physical Layer Device
MP0016	MP0016Zaaa	US	14/138,569	12/23/2013	9407562	8/2/2016	In Force	Systems And Methods For Operating According To An Auto-Negotia
MP0016	MP0016Z2aa	US	15/224,902	8/1/2016	9614776	4/4/2017	In Force	Systems And Methods For Transmitting Data According To An Auto-N
MP0016	MP0016ZZ2a	US	15/477,767	4/3/2017	9961006	5/1/2018	In Force	Network Switch For Transmitting Data According To An Auto-Negotia
MP0017	MP0017	US	09/501,557	2/9/2000	6232834	5/15/2001	In Force	Calibrated compensation for an operational amplifier
MP0018	MP0018	US	09/659,772	10/30/2000	6750727	6/15/2004	In Force	Low Phase Noise MOS I.C Oscillator
MP0019	MP0019	US	09/746,687	12/22/2000	6386772	4/30/2002	In Force	SELF-LIMITING PAD DRIVER
MP0020	MP0020	US	09/737,743	12/18/2000	7194037	3/20/2007	In Force	Active Replica Transformer Hybrid
MP0020	MP0020C1	US	11/030,205	7/1/2005	7719429	1/6/2010	In Force	ACTIVE REPLICA TRANSFORMER HYBRID
MP0021	MP0021	US	09/607,904	6/30/2000	6456208	9/24/2002	In Force	NOVEL TECHNIQUE TO CONSTRUCT 32/33 AND OTHER PLL CODES
MP0022	MP0022	US	09/556,758	4/21/2000	6384713	5/7/2002	In Force	SERIAL COMPARATOR
MP0023	MP0023	US	09/572,155	5/17/2000	7167516	1/23/2007	In Force	Circuit And Method For Finding The Sampling Phase And Canceling Pr
MP0023	MP0023C1	US	11/656,729	1/23/2007	8767813	1/7/2014	In Force	Circuit and method for finding the sampling phase and canceling inte
MP0023	MP0023C1C1	US	14/313,196	6/24/2014	9264264	2/16/2016	In Force	Systems And Methods For Filtering A Received Signal To Remove Inte
MP0024	MP0024	US	09/536,120	3/27/2000	6856790	2/15/2005	In Force	Receiver With Dual D.C. Noise Cancellation Circuits
MP0024	MP0024C1C1	US	11/820,115	6/18/2007	7813702	10/12/2010	In Force	Receiver With Dual D.C. Noise Cancellation Circuits
MP0025	MP0025	US	09/546,039	4/10/2000	6429987	8/6/2002	In Force	HIGH SPEED INDUCTOR CURRENT DRIVER WITH MINIMUM OVERSH
MP0027	MP0027	US	09/717,240	11/22/2000	6804695	10/12/2004	In Force	Method And Apparatus For Constraining Tap Coefficients In An Adapt
MP0028	MP0028	US	09/589,417	6/7/2000	6246268	6/12/2001	In Force	CMOS integrated signal detection circuit with high efficiency and perf
MP0030	MP0030	US	09/587,991	6/6/2000	6411233	6/25/2002	In Force	Method and apparatus for direct RAM analog-to-digital converter cell
MP0030	MP0030C1	US	10/035,761	10/23/2001	6553446	5/13/2003	In Force	Method And Apparatus For Direct RAM Analog-To-Digital Converter C
MP0031	MP0031	US	09/599,907	6/23/2000	6359499	3/19/2002	In Force	Temperature And Process Independent CMOS Circuit
MP0032	MP0032	US	09/578,886	5/26/2000	6711225	3/23/2004	In Force	Method And Apparatus For Synchronization Of A Bit Stream For Long
MP0033	MP0033	US	09/713,199	11/16/2000	6606048	8/12/2003	In Force	METHOD AND APPARATUS FOR EQUALIZING THE DIGITAL PERFORM
MP0033	MP0033C1	US	10/460,120	6/12/2003	6762708	7/13/2004	In Force	Method And Apparatus For Equalizing The Digital Performance Of Mu
MP0034	MP0034	US	09/698,236	10/30/2000	6489827	12/3/2002	In Force	Reduction Of Offset Voltage In Current Mirror Circuit
MP0035	MP0035	US	09/737,474	12/18/2000	6462688	10/8/2002	In Force	DIRECT DRIVE PROGRAMMABLE HIGH SPEED POWER DIGITAL-TO-AN

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MP0035	MP0035RE	US	11/220,304	9/6/2005	RE40971	11/17/2009	In Force	Direct Drive Programmable High Speed Power Digital-To-Analog Conv
MP0036	MP0036	US	09/528,314	3/20/2000	6366168	4/2/2002	In Force	Efficient Ground Noise And Common-Mode Suppression Network
MP0037	MP0037	US	09/566,861	5/8/2000	6331803	12/18/2001	In Force	Constant Bandwidth, Variable Gain Amplifier, And Method
MP0038	MP0038	US	09/559,186	4/27/2000	6888897	5/3/2005	In Force	Multi-Mode Iterative Detector
MP0038	MP0038C1	US	10/927,325	8/27/2004	7340003	3/4/2008	In Force	Multi-Mode Iterative Detector
MP0038	MP0038C1C1	US	11/973,500	10/9/2007	8136005	3/13/2012	In Force	Multi-Mode Iterative Detector
MP0039	MP0039	US	09/629,092	7/31/2000	6775529	8/10/2004	In Force	Active Resistive Summer For A Transformer Hybrid
MP0039	MP0039AAC1	US	11/821,637	6/25/2007	7536162	5/19/2009	In Force	Active Resistive Summer For A Transformer Hybrid
MP0039	MP0039aBa	US	12/156,984	6/6/2008	8050645	11/1/2011	In Force	Active Resistive Summer For A Transformer Hybrid
MP0039	MP0039C1	US	10/786,010	2/26/2004	7466971	12/16/2008	In Force	Active Resistive Summer For A Transformer Hybrid
MP0039	MP0039C1D1	US	11/523,169	9/19/2006	7327995	2/5/2008	In Force	Active Resistance Summer For A Transformer Hybrid
MP0039	MP0039C1P	US	09/920,240	8/1/2001	7606547	10/20/2009	In Force	Active Resistance Summer For A Transformer Hybrid
MP0039	MP0039Faaa	US	13/958,915	8/5/2013	8889017	11/4/2014	In Force	Active Resistive Summer For A Transformer Hybrid
MP0039	MP0039I1aa	US	13/279,873	10/24/2011	8503961	8/6/2013	In Force	Active Resistive Summer For A Transformer Hybrid
MP0039	MP0039I1C1	US	12/581,415	10/19/2009	8045946	10/25/2011	In Force	Active Resistive Summer For A Transformer Hybrid
MP0040	MP0040	US	09/629,095	7/31/2000	6577114	6/10/2003	In Force	Calibration Circuit
MP0040	MP0040D1	US	10/379,116	3/4/2003	6737851	5/18/2004	In Force	Calibration Circuit
MP0040	MP0040D2	US	10/379,260	3/4/2003	6885181	4/26/2005	In Force	Calibration Circuit
MP0041	MP0041	US	09/564,711	5/3/2000	6515506	2/4/2003	In Force	Circuit For Reducing Pin Count Of A Semiconductor Chip And Method
MP0041	MP0041C1	US	10/287,527	11/5/2002	6831479	12/14/2004	In Force	Circuit For Reducing Pin Count Of A Semiconductor Chip And Method
MP0041	MP0041D1	US	10/287,528	11/5/2002	6741097	5/25/2004	In Force	Circuit For Reducing Pin Count Of A Semiconductor Chip And Method
MP0042	MP0042	US	09/678,728	10/4/2000	7127481	10/24/2006	In Force	Movable Tap Finite Impulse Response Filter
MP0042	MP0042C1	US	11/500,603	8/8/2006	7831647	11/9/2010	In Force	MOVABLE TAP FINITE IMPULSE RESPONSE FILTER
MP0042	MP0042C1P	US	09/761,190	1/18/2001	7120656	10/10/2006	In Force	Movable Tap Finite Impulse Response Filter
MP0042	MP0042D2aa	US	13/917,165	6/13/2013	9083983	7/28/2015	In Force	Movable Tap Finite Impulse Response Filter
MP0042	MP0042D2C1	US	13/012,700	1/24/2011	8468188	6/18/2013	In Force	Movable Tap Finite Impulse Response Filter
MP0042	MP0042I1C1	US	11/501,185	8/8/2006	7584236	9/1/2009	In Force	Movable Tap Finite Impulse Response Filter
MP0042	MP0042ID1	US	11/496,667	7/31/2006	7831646	11/9/2010	In Force	Movable Tap Finite Impulse Response Filter
MP0043	MP0043	US	09/661,912	9/14/2000	6871251	3/22/2005	In Force	High latency interface Between Hardware Components
MP0043	MP0043baaa	US	13/404,510	2/24/2012	9519598	12/13/2016	In Force	High latency interface Between Hardware Components
MP0043	MP0043C1	US	10/962,321	10/12/2004	7171507	1/30/2007	In Force	High latency interface Between Hardware Components
MP0043	MP0043C2	US	11/311,848	12/19/2005	7389374	6/17/2008	In Force	High latency interface Between Hardware Components
MP0043	MP0043C2aa	US	12/858,457	8/18/2010	8127067	2/28/2012	In Force	High latency interface Between Hardware Components
MP0044	MP0044	US	09/603,188	6/26/2000	6473010	10/29/2002	In Force	Method And Apparatus For Determining Error Correction Code Failure

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0045	MP0045	US	09/620,545	7/20/2000	6859399	2/22/2005	In Force	Memory Architecture And System And Multipoint Interface Protocol
MP0045	MP0045D1	US	10/778,592	2/17/2004	686470	3/15/2005	In Force	Interface Protocol For A Disk Drive, SRAM And DRAM
MP0045	MP0045D2	US	11/014,777	12/20/2004	7405980	7/29/2008	In Force	Shared Terminal Memory Interface
MP0045	MP0045D2C1	US	12/220,607	7/25/2008	8386735	2/26/2013	In Force	Memory Architecture And System, And Interface Protocol
MP0046	MP0046	US	09/699,513	10/31/2000	6504493	1/7/2003	In Force	METHOD AND APPARATUS FOR ENCODING/DECODING DATA
MP0048	MP0048	US	10/118,504	4/8/2002	6868517	3/15/2005	In Force	Method And Apparatus For Checking Read Errors With Two Cyclic Redundancy Checks
MP0048	MP0048C1	US	11/049,753	4/2/2005	7310765	12/18/2007	In Force	METHOD AND APPARATUS FOR CHECKING READ ERRORS WITH TWO Cyclic Redundancy Checks
MP0049	MP0049	US	09/615,062	7/12/2000	6634441	10/14/2003	In Force	Method And Apparatus For Measuring An Output Signal Of A Floating Gate Memory Cell
MP0050	MP0050	US	09/583,316	5/30/2000	6920132	7/19/2005	In Force	Reduced Pin Gigabit Media Independent Interface
MP0050	MP0050baa	US	12/837,152	7/15/2010	7983259	7/19/2011	In Force	Reduced Pin Gigabit Media Independent Interface
MP0050	MP0050baaa	US	13/185,133	7/18/2011	8144635	3/27/2012	In Force	Reduced Pin Gigabit Media Independent Interface
MP0050	MP0050C1	US	11/072,323	3/7/2005	7065075	6/20/2006	In Force	Reduced Pin Gigabit Media Independent Interface
MP0050	MP0050C2	US	11/447,351	6/6/2006	7415013	8/19/2008	In Force	Reduced Pin Gigabit Media Independent Interface
MP0050	MP0050C2C1	US	12/228,960	8/18/2008	7760725	7/20/2010	In Force	Reduced Pin Gigabit Media Independent Interface
MP0051	MP0051	US	09/615,063	7/12/2000	6721913	4/13/2004	In Force	Method And Apparatus For Testing An Interface Between Separate Hosts
MP0052	MP0052	US	09/874,949	6/5/2001	6721114	4/13/2004	In Force	Precompensation Circuit For Magnetic Recording
MP0052	MP0052C1	US	10/810,893	3/29/2004	6956708	10/18/2005	In Force	Precompensation Circuit For Magnetic Recording
MP0052	MP0052C2	US	11/230,373	10/17/2005	7184231	2/27/2007	In Force	Precompensation Circuit For Magnetic Recording
MP0052	MP0052C2C1	US	11/711,475	2/27/2007	7515368	4/7/2009	In Force	Precompensation Circuit For Magnetic Recording
MP0053	MP0053	US	09/725,818	11/30/2000	6732286	5/4/2004	In Force	High Latency Timing Circuit
MP0053	MP0053C1	US	10/831,100	4/26/2004	7049896	5/23/2006	In Force	High Latency Timing Circuit
MP0053	MP0053C2C1	US	11/507,916	8/22/2006	7304545	12/4/2007	In Force	High Latency Timing Circuit
MP0054	MP0054	US	09/697,714	10/27/2000	6633197	10/14/2003	In Force	Gate Capacitor Stress Reduction In CMOS/BICMOS Circuit
MP0055	MP0055	US	09/660,392	9/12/2000	6594098	7/15/2003	In Force	Acquisition Timing Loop For Read Channel
MP0055	MP0055C1	US	10/385,977	3/11/2003	6801380	10/5/2004	In Force	Acquisition Timing Loop For Read Channel
MP0056	MP0056	US	09/660,929	9/13/2000	636967	4/9/2002	In Force	RACE AVOIDANCE IN DISK HEAD READ CIRCUIT
MP0056	MP0056C1aa	US	13/043,236	3/8/2011	8184391	5/22/2012	In Force	Phase-Adjustment Of Divided Clock In Disk Head Read Circuit
MP0057	MP0057	US	09/643,819	8/22/2000	6441765	8/27/2002	In Force	Analog To Digital Converter With Enhanced Differential Non-linearity
MP0057	MP0057C1	US	10/176,409	6/19/2002	6545628	8/4/2003	In Force	ANALOG-TO-DIGITAL CONVERTER WITH ENHANCED DIFFERENTIAL NON-LINEARITY
MP0058	MP0058	US	09/648,770	8/28/2000	6400214	6/4/2002	In Force	Switched Capacitor Filter For Reference Voltages In Analog To Digital Converter
MP0059	MP0059	US	09/648,462	8/28/2000	6417725	7/9/2002	In Force	High Speed Reference Buffer
MP0060	MP0060	US	09/654,392	9/1/2000	6369554	4/9/2002	In Force	A LINEAR REGULATOR WHICH PROVIDES STABILIZED CURRENT FLOW
MP0060	MP0060CN	CN	11311037	8/30/2001	Z12001011311	5/18/2005	In Force	Frequency Compensation For A Linear Regulator
MP0060	MP0060JP	JP	2001-265940	9/3/2001	4680447	2/10/2011	In Force	Frequency Compensation for a Linear Regulator

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MP0061	MP0061	US	09/648,464	8/28/2000	6396334	5/28/2002	In Force	Charge Pump For Reference Voltages In Analog To Digital Converter
MP0062	MP006211	US	10/184,505	6/26/2002	7546172	6/9/2009	In Force	Apparatus, Method, And Computer Program Product For Recording A
MP0062	MP006211C1	US	11/981,728	10/31/2007	8145331	3/27/2012	In Force	Apparatus, Method, And Computer Program For Recording And Repr
MP0062	MP006211C2	US	12/157,909	6/13/2008	9141619	9/22/2015	In Force	Apparatus, Method, And Computer Program Product For Recording A
MP0062	MP006212	US	10/184,302	6/26/2002	7457676	11/25/2008	In Force	Vehicle For Recording And Reproducing Digital Data
MP0062	MP006212C1	US	11/981,601	10/31/2007	8374710	2/12/2013	In Force	Vehicle For Recording And Reproducing Digital Data
MP0062	MP006212C2	US	12/157,860	6/13/2008	8145332	3/27/2012	In Force	Vehicle For Recording And Reproducing Digital Data
MP0062	MP006213	US	10/184,299	6/26/2002	7315764	1/1/2008	In Force	Integrated Circuit, Method, And Computer Program Product For Rec
MP0062	MP006214	US	10/690,022	10/20/2003	7577247	8/18/2009	In Force	Apparatus And Method For Telephone, Intercom, And Clock
MP0062	MP006215	US	10/692,644	10/24/2003	7778736	8/17/2010	In Force	Apparatus, Method, And Computer Program For Sprinkler Control
MP0062	MP006215C2	US	12/855,142	8/12/2010	8019482	9/13/2011	In Force	Method And Apparatus For Controlling A Sprinkler System
MP0062	MP006216	US	10/703,034	11/5/2003	7298252	11/20/2007	In Force	Apparatus, Method, And Computer Program For An Alarm System
MP0062	MP006216C1	US	11/981,729	10/31/2007	7522039	4/21/2009	In Force	Apparatus, Method, And Computer Program For An Alarm System
MP0063	MP0063	US	09/730,597	12/7/2000	6965652	11/15/2005	In Force	Address Generator For LDPC Encoder And Decoder And Method Ther
MP0063	MP0063D1	US	11/217,409	2/9/2005	7586485	8/25/2009	In Force	ADDRESS GENERATOR FOR LDPC ENCODER AND DECODER AND MET
MP0063	MP0063D1D1	US	11/595,179	11/9/2006	7760822	7/20/2010	In Force	Address Generator For LDPC Encoder And Decoder And Method Ther
MP0064	MP0064	US	09/730,752	12/7/2000	7072417	7/4/2006	In Force	LDPC Encoder And Method Thereof
MP0064	MP0064C1	US	11/343,775	1/31/2006	7583751	9/1/2009	In Force	LDPC Encoder Method Thereof
MP0065	MP0065	US	09/730,603	12/7/2000	7184486	2/27/2007	In Force	LDPC Encoder And Decoder And Method Thereof
MP0065	MP0065C1	US	11/397,318	4/4/2006	7453960	11/18/2008	In Force	LDPC Encoder And Encoder And Method Thereof
MP0065	MP0065C1C1	US	12/273,100	11/18/2008	7751505	7/6/2010	In Force	LDPC Encoder And Encoder And Method Thereof
MP0066	MP0066	US	09/881,474	6/14/2001	6570781	5/27/2003	In Force	Logic Process DRAM
MP0066	MP0066D1a8	US	11/449,957	6/9/2006	7609538	10/27/2009	In Force	Logic Process DRAM
MP0066	MP0066D1C1	US	10/734,060	12/12/2003	6947324	9/20/2005	In Force	Logic Process Dram
MP0067	MP0067	US	09/710,256	11/10/2000	6404290	6/11/2002	In Force	Fast Change Charge Pump Having Switchable Boost Function
MP0067	MP0067D1	US	10/135,646	4/29/2002	6565846	5/20/2003	In Force	Cascode Regulator With Plural Outputs
MP0068	MP0068	US	09/710,255	11/10/2000	6320796	11/20/2001	In Force	Variable Slope Charge Pump Control
MP0069	MP0069	US	09/730,598	12/7/2000	7000177	2/14/2006	In Force	Parity Check Matrix And Method Of Forming Thereof
MP0071	MP0071	US	09/759,151	1/16/2001	7281065	10/9/2007	In Force	Long Latency Interface Protocol
MP0071	MP0071a2aa	US	14/059,522	10/22/2013	9576606	2/21/2017	In Force	Long Latency Interface Protocol
MP0071	MP0071C1	US	11/973,633	10/9/2007	7487268	2/3/2009	In Force	Long Latency Interface Protocol
MP0071	MP0071C1aa	US	12/876,715	9/7/2010	8180946	5/15/2012	In Force	Long Latency Interface Protocol
MP0071	MP0071C17a	US	13/470,930	5/14/2012	8566499	10/22/2013	In Force	Long Latency Interface Protocol
MP0072	MP0072C1	US	11/011,178	12/15/2004	7426236	9/16/2008	In Force	FEEDFORWARD EQUALIZER FOR DFE BASED DETECTOR

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MP0072	MP0072C1aa	US	13/252,098	10/3/2011	8315301	11/20/2012	In Force	Feedforward Equalizer For DFE Based Detector
MP0072	MP0072C1C1	US	12/283,664	9/15/2008	8031765	10/4/2011	In Force	Feedforward Equalizer For DFE Based Detector
MP0073	MP0073	US	09/738,715	12/15/2000	6388506	5/14/2002	In Force	Regulator With Leakage Compensation
MP0074	MP0074	US	09/765,200	1/18/2001	6615006	9/23/2003	In Force	Fringing Capacitor Structure
MP0074	MP0074D1	US	10/372,617	2/21/2003	6784050	8/31/2004	In Force	Fringing Capacitor Structure
MP0074	MP0074D2	US	10/836,389	4/30/2004	6974744	12/13/2005	In Force	Fringing Capacitor Structure
MP0077	MP0077	US	09/901,507	7/9/2001	7059411	8/29/2006	In Force	Soft-Output Decoding Method And Apparatus For Controlled Interx
MP0077	MP0077C1	US	11/217,408	9/2/2005	7319726	1/15/2008	In Force	Soft-Output Decoding Method And Apparatus For Controlled Interx
MP0078	MP0078	US	10/054,595	1/22/2002	7257125	8/14/2007	In Force	Quality Of Service Half-Duplex Media Access Controller
MP0078	MP0078C1	US	11/893,104	8/14/2007	8139600	3/20/2012	In Force	Quality Of Service Half-Duplex Media Access Controller
MP0078	MP0078C1C1	US	12/074,802	3/6/2008	7684429	3/23/2010	In Force	Quality Of Service Half-Duplex Media Access Controller
MP0078	MP0078C3	US	13/421,170	3/15/2012	8633492	4/8/2014	In Force	Quality Of Service Half-Duplex Media Access Controller
MP0079	MP0079C1	US	10/754,325	1/9/2004	7286311	10/23/2007	In Force	Thermal Asperity Compensation In Perpendicular Recording
MP0082	MP0082	US	09/981,384	11/21/2001	7570657	8/4/2009	In Force	Autonegotiation Between 10GBASE-X and 1000BASE-T
MP0083	MP0083	US	09/990,137	11/21/2001	6993667	1/31/2006	In Force	Apparatus For Automatic Energy Savings Mode For Ethernet Transcei
MP0083	MP0083D1	US	11/254,986	10/20/2005	7278039	10/2/2007	In Force	PHYSICAL LAYER ENERGY SAVING TECHNIQUES FOR TRANSCEIVERS
MP0084	MP0084	US	09/860,599	6/13/2001	6459246	10/1/2002	In Force	Voltage Regulator
MP0085	MP0085C1	US	10/695,531	10/28/2003	6917314	7/12/2005	In Force	Method And Apparatus For DC-level Constrained Coding
MP0085	MP0085C2	US	11/166,425	6/24/2005	7286065	10/23/2007	In Force	Method And Apparatus For DC-level Constrained Coding
MP0086	MP0086	US	09/991,043	11/21/2001	7624197	11/24/2009	In Force	Apparatus And Method For Automatic Speed Downshift For A Two Pa
MP0086	MP0086C1	US	12/624,522	11/24/2009	7979587	7/12/2011	In Force	Apparatus And Method For Automatic Speed Downshift For A Two Pa
MP0087	MP0087	US	10/184,334	6/26/2002	7073099	7/4/2006	In Force	Method And Apparatus For Improving Memory Operation And Yield
MP0087	MP0087BFaa	US	13/632,547	10/1/2012	8745450	6/3/2014	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087BFay	TW	96128368	8/2/2007	1453751	9/21/2014	In Force	FULLY-BUFFERED DUAL IN-LINE MEMORY MODULE WITH FAULT COR
MP0087	MP0087BFca	US	12/908,376	10/20/2010	8103921	1/24/2012	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087BFfy	TW	96128369	8/2/2007	1462112	11/21/2014	In Force	FULLY-BUFFERED DUAL IN-LINE MEMORY MODULE WITH FAULT COR
MP0087	MP0087BFTW	TW	96128366	8/2/2007	1432957	4/1/2014	In Force	MEMORY MODULE AND HOST DEVICE WITH FAULT CORRECTION
MP0087	MP0087BF2a	US	14/292,020	5/30/2014	9158620	10/13/2015	In Force	Systems And Methods For Testing Pages Of Data Stored In A Memory
MP0087	MP0087B1a	US	11/655,449	1/19/2007	7814382	10/12/2010	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087B1b	US	11/655,451	1/19/2007	7823030	10/26/2010	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087B1e	US	11/655,603	1/19/2007	7870331	1/11/2011	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087C1	US	11/327,933	1/9/2006	7809998	10/5/2010	In Force	Method And Apparatus For Improving Memory Operation And Yield
MP0087	MP0087D2	US	11/328,373	1/9/2006	7962809	6/14/2011	In Force	Method And Apparatus For Improving Memory Operation And Yield
MP0087	MP0087D2D1	US	11/589,368	10/30/2006	7818636	10/19/2010	In Force	Method And Apparatus For Improving Memory Operation And Yield

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MP0087	MP0087D2Fa	US	12/903,606	10/13/2010	8281191	10/2/2012	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087D21	US	11/584,946	10/23/2006	7818639	10/19/2010	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087D21WC	WO	PCT/US2007/01	7/24/2007			Pending	Fully Buffered Dual in-Line Memory Module With Fault Correction
MP0088	MP0088	US	09/920,241	8/1/2001	7433665	10/7/2008	In Force	Apparatus And Method For Converting Single-Ended Signals To A Diff
MP0088	MP0088C1	US	12/075,231	3/10/2008	7761076	7/20/2010	In Force	Apparatus And Method For Converting Single-Ended Signals To A Diff
MP0088	MP00881	US	10/191,924	7/8/2002	6844837	1/18/2005	In Force	Class B Driver
MP0088	MP00881C1	US	10/972,143	10/25/2004	7280060	10/9/2007	In Force	Communication Driver
MP0088	MP00881CA	US	11/432,886	5/12/2006	7312739	12/25/2007	In Force	Communication Driver
MP0088	MP00881CAa	US	12/004,200	12/20/2007	7649483	1/19/2010	In Force	Communication Driver
MP0088	MP00881Gaa	US	12/689,066	1/18/2010	8069073	8/30/2011	In Force	Method And Apparatus For Generating An Analog Signal Having A Pre
MP0088	MP00881RE	US	11/284,395	11/21/2005	RE41831	10/19/2010	In Force	Class B Driver
MP0090	MP0090C1	US	10/921,810	8/20/2004	6987634	1/17/2006	In Force	WRITE CHANNEL HAVING A PREAMPLIFIER AND A NON-UNIFORM TR
MP0091	MP0091	US	10/067,556	2/4/2002	7218691	5/15/2007	In Force	Method And Apparatus For Estimation Of Orthogonal Frequency Divi
MP0091	MP0091D1	US	11/301,032	5/8/2007	7532693	5/12/2009	In Force	Method And Apparatus For Acquisition And Tracking Of Orthogonal Fr
MP0091	MP0091D1AA	US	13/660,580	10/25/2012	8619922	12/31/2013	In Force	Method And Apparatus For Acquisition And Tracking Of Orthogonal F
MP0091	MP0091D1D1	US	12/393,547	2/26/2009	8306743	10/30/2012	In Force	Method And Apparatus For Acquisition And Tracking Of Orthogonal F
MP0094	MP0094	US	10/072,843	2/6/2002	6762644	7/13/2004	In Force	Apparatus And Method For A Nested Transimpedance Amplifier
MP0094	MP0094C1	US	10/838,040	5/3/2004	6836182	12/28/2004	In Force	Nested Transimpedance Amplifier
MP0094	MP0094faaa	US	12/857,949	8/17/2010	8159293	4/17/2012	In Force	Nested Transimpedance Amplifier
MP0094	MP0094faC1	US	12/627,432	11/30/2009	7808311	10/5/2010	In Force	Nested Transimpedance Amplifier
MP0094	MP0094faP	JP	2012037544	1/18/2007	5367857	9/20/2013	In Force	Nested Transimpedance Amplifier
MP0094	MP0094fSG	SG	2007002272	1/17/2007	134251	7/31/2009	In Force	Nested Transimpedance Amplifier
MP0094	MP0094fu6	EP	07000516.0	1/11/2007	1811652	3/27/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094fu6DE	DE	07000516.0	1/11/2007	602007057934	3/27/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094fu6FR	FR	07000516.0	1/11/2007	1811652	3/27/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094fu6GB	GB	07000516.0	1/11/2007	1811652	3/27/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094fu6	TW	96101880	1/18/2007	1420804	12/21/2013	In Force	NESTED TRANSMIMPEDANCE AMPLIFIER
MP0094	MP0094fuCN	CN	2007100024131	1/17/2007	212007100024	4/18/2012	In Force	NESTED TRANSMIMPEDANCE AMPLIFIER
MP0094	MP0094fuJP	JP	2007009334	1/18/2007	4856745	11/18/2011	In Force	Nested Transimpedance Amplifier
MP0094	MP0094fuCN	CN	2007100024141	1/17/2007	212007100024	4/18/2012	In Force	NESTED TRANSMIMPEDANCE AMPLIFIER
MP0094	MP0094fSG	SG	2007002280	1/17/2007	134252	8/31/2009	In Force	Nested Transimpedance Amplifier
MP0094	MP0094fu8	EP	EP07000517.8	1/11/2007			Pending	Nested Transimpedance Amplifier
MP0094	MP0094fu8	TW	96101882	1/18/2007	1433452	4/1/2014	In Force	NESTED TRANSMIMPEDANCE AMPLIFIER
MP0094	MP0094fSG4	SG	2007002264	1/17/2007	134250	8/31/2009	In Force	Nested Transimpedance Amplifier

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MP0094	MP0094I	US	10/459,731	6/11/2003	7276965	10/2/2007	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2	US	11/495,813	7/28/2006	7551024	6/23/2009	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2C1	US	11/643,089	12/21/2006	7616057	11/10/2009	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2C1HK	HK	81000803	1/17/2007			Pending	Nested Transimpedance Amplifier
MP0094	MP0094I2C2	US	11/643,432	12/21/2006	7405616	7/29/2008	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2C3	US	11/643,557	12/21/2006	7605649	10/20/2009	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2C3HK	HK	81000812	1/17/2007			Pending	Nested Transimpedance Amplifier
MP0094	MP0094I2C3JP	JP	2007009330	1/18/2007	5048020	7/27/2012	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2CN	CN	2007100624121	1/17/2007	212007100024	9/26/2012	In Force	NESTED TRANSMITTANCE AMPLIFIER
MP0094	MP0094I2D1	US	12/217,800	7/9/2008	7626453	12/1/2009	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2EP	EP	EP07000520.2	1/11/2007	1811657	3/13/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2EPDE	DE	EP07000520.2	1/11/2007	602007057812	3/13/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2EPFR	FR	EP07000520.2	1/11/2007	1811657	3/13/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2EPGB	GB	EP07000520.2	1/11/2007	1811657	3/13/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2HK	HK	81000885	1/17/2007			Pending	Nested Transimpedance Amplifier
MP0094	MP0094I2JP	JP	2007009337	1/18/2007	5010293	6/8/2012	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2TW	TW	96101878	1/18/2007	1416864	11/21/2013	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2I	US	11/455,362	6/19/2006	7173486	2/6/2007	In Force	Nested Transimpedance Amplifier With Capacitive Cancellation Of In
MP0094	MP0094I2I	US	11/106,248	4/14/2005	7304536	12/4/2007	In Force	NESTED TRANSMITTANCE AMPLIFIER
MP0095	MP0095	US	10/059,516	1/28/2002	6775091	8/10/2004	In Force	Repeatable Run-Out Compensation For Disk Drive
MP0096	MP0096	US	10/074,747	2/11/2002	7149955	12/12/2006	In Force	Encoding And Decoding Apparatus And Method With Hamming Weig
MP0096	MP0096D1	US	11/606,678	11/30/2006	7783937	8/24/2010	In Force	Encoding And Decoding Apparatus And Method With Hamming Weig
MP0096	MP0096D2	US	11/606,510	11/30/2006	8359498	1/22/2013	In Force	Encoding And Decoding Apparatus And Method With Hamming Weig
MP0096	MP0096D3	US	11/606,677	11/30/2006	7802151	9/21/2010	In Force	Encoding And Decoding Apparatus And Method With Hamming Weig
MP0113	MP0113	US	10/098,865	3/15/2002	7203851	4/10/2007	In Force	Method And Apparatus For Detecting And Supplying Power By A First
MP0113	MP0113C1	US	11/752,010	4/2/2007	7552348	6/23/2009	In Force	Method And Apparatus For Detecting And Supplying Power By A First
MP0115	MP0115	US	09/966,914	9/27/2001	6979894	12/27/2005	In Force	Integrated Chip Package Having Intermediate Substrate
MP0115	MP0115D1	US	10/346,428	1/16/2003	6985463	2/7/2006	In Force	Integrated Chip Package Having Intermediate Substrate And Multiple
MP0115	MP0115D2	US	10/346,771	1/16/2003	6911724	6/28/2005	In Force	IMPROVED INTEGRATED CHIP PACKAGE HAVING INTERMEDIATE SUB
MP0115	MP0115D2C1	US	11/133,358	5/20/2005	8525317	9/3/2013	In Force	Integrated Chip Package Having Intermediate Substrate With Capacit
MP0116	MP0116	US	10/051,965	1/16/2002	6770982	8/3/2004	In Force	SEMICONDUCTOR DEVICE POWER DISTRIBUTION SYSTEM AND MET
MP0116	MP0116AFAA	US	14/163,429	1/24/2014	9240372	1/19/2016	In Force	Semiconductor Die Having Lead Wires Formed Over A Circuit In A Shi
MP0116	MP0116D1FA	US	13/601,442	8/31/2012	8637975	1/28/2014	In Force	Semiconductor Device Having Lead Wires Connecting Bonding Pads F
MP0116	MP0116D1I1	US	11/024,309	12/28/2004	8258616	9/4/2012	In Force	SEMICONDUCTOR DICE HAVING A SHIELDED AREA CREATED UNDER

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MP0117	MP0117	US	10/071,417	2/6/2002	7035273	4/25/2006	In Force	Quality Of Service Queuing System For A Network Switch
MP0118	MP0118	US	10/086,084	2/26/2002	7054398	5/30/2006	In Force	Method For Selecting Sync Marks And Sync Mark Detectors
MP0120	MP0120	US	10/085,683	2/26/2002	7248568	7/24/2007	In Force	Apparatus For Detecting Hidden Nodes In A Wireless Network
MP0120	MP0120C1	US	11/880,772	7/24/2007	8159384	4/17/2012	In Force	Apparatus And Method For Detecting Hidden Nodes In A Wireless Ne
MP0120	MP0120C2	US	11/880,771	7/24/2007	7965669	6/21/2011	In Force	Apparatus And Method For Detecting Hidden Nodes In A Wireless Ne
MP0121	MP0121	US	09/991,046	11/21/2001	7054309	5/30/2006	In Force	Ethernet Automatic Fiber/Copper Media Selection Logic
MP0121	MP0121C1	US	11/400,457	4/7/2006	7639675	12/29/2009	In Force	Ethernet Automatic Media Selection Logic
MP0121	MP0121C1C1	US	12/644,483	12/22/2009	7967605	3/15/2011	In Force	Method And Apparatus For Selecting A Media For Communication in
MP0121	MP0121Faa	US	12/748,946	3/29/2010	8018923	9/13/2011	In Force	Method and Apparatus For Establishing A Link Over A Preferred Me
MP0121	MP0121I	US	10/435,301	5/9/2003	7324507	1/29/2008	In Force	Ethernet Automatic Fiber/Copper Media Selection Logic
MP0121	MP0121IC1	US	12/011,532	1/28/2008	7688812	3/30/2010	In Force	Ethernet Automatic Media Selection Logic With Preferred Medium Se
MP0122	MP0122	US	10/010,732	12/5/2001	7042893	5/9/2006	In Force	Serial Media Independent Interface With Double Data Rate
MP0122	MP0122C1	US	11/378,551	3/17/2006	7672326	3/2/2010	In Force	Serial Media Independent Interface With Double Data Rate
MP0122	MP0122C1C1	US	12/714,746	3/1/2010	8094668	1/10/2012	In Force	Physical Layer Device Including A Serial Media Independent Interface
MP0123	MP0123	US	10/106,720	3/26/2002	7292596	11/6/2007	In Force	Method And Apparatus For Automatic Crossover And Parallel Detect
MP0126	MP0126	US	09/987,824	11/16/2001	6888655	5/24/2005	In Force	Multiport High Speed Communications Integrated Circuit
MP0126	MP0126C1	US	11/072,325	3/7/2005	7059982	8/29/2006	In Force	Multi-Port Communications Integrated Circuit
MP0126	MP0126C1C1	US	11/506,555	8/18/2006	7308521	12/1/2007	In Force	Multi-Port Communications Integrated Circuit And Method For Facilit
MP0132	MP0132	US	10/096,211	3/8/2002	7272114	9/18/2007	In Force	Physical Layer And Physical Layer Diagnostic System With Reversed L
MP0132	MP0132C1	US	11/901,694	9/18/2007	7701861	4/20/2010	In Force	Physical Layer And Physical Layer Diagnostic System With Reversed L
MP0133	MP0133	US	10/080,454	2/21/2002	7239648	7/3/2007	In Force	Extension Mode For Wireless LANs Complying With Short Interframe
MP0133	MP0133aaga	US	14/032,694	9/20/2013	9231737	1/5/2016	In Force	Method And Apparatus For Remotely Increasing Available Processing
MP0133	MP0133C1	US	11/824,993	7/3/2007	7468990	12/23/2008	In Force	Extension Mode For Wireless LANs Complying With Short Interframe
MP0133	MP0133C1aa	US	13/180,260	7/11/2011	8542703	9/24/2013	In Force	Extension Mode For Wireless LANs Complying With Short Interframe
MP0133	MP0133C1C1	US	12/151,185	5/5/2008	7978675	7/12/2011	In Force	Extension Mode For Wireless LANs Complying With Short Interframe
MP0133	MP0133C2	US	11/824,781	7/3/2007	7468989	12/23/2008	In Force	EXTENSION MODE FOR WIRELESS LANs COMPLYING WITH SHORT INT
MP0135	MP0135	US	10/099,050	3/15/2002	7161911	1/9/2007	In Force	Method And Apparatus For Autonegotiation Between Network Devic
MP0135	MP0135C1	US	11/650,052	1/5/2007	7774692	5/25/2010	In Force	Method And Apparatus For Autonegotiation Between Network Devic
MP0136	MP0136C1	US	10/189,385	7/5/2002	7577085	8/18/2009	In Force	MULTICARRIER TRANSMIT DIVERSITY
MP0136	MP0136C1AA	US	14/320,515	6/30/2014	9614605	4/4/2017	In Force	MULTICARRIER TRANSMIT DIVERSITY
MP0136	MP0136C1C1	US	12/507,418	7/22/2009	8767523	7/1/2014	In Force	MULTICARRIER TRANSMIT DIVERSITY
MP0137	MP0137	US	10/077,042	2/14/2002	8244916	8/14/2012	In Force	Method And Apparatus For Enabling A Network Interface To Support
MP0137	MP0137C1	US	13/584,341	8/13/2012	8447887	5/21/2013	In Force	Method And Apparatus For Enabling A Network Interface To Support
MP0137	MP0137C1C1	US	13/894,876	5/15/2013	8886839	11/11/2014	In Force	Method And Apparatus For Enabling A Network Interface To Support

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MP0138	MP0138	US	10/253,183	9/23/2002	7286528	10/23/2007	In Force	Multiple Address Databases In A Switch Without The Need For Extra
MP0138	MP0138C1	US	11/975,981	10/23/2007	7985581	8/9/2011	In Force	Multiple Address Databases In A Switch Without The Need For Extra
MP0138	MP0138C1C1	US	13/205,105	8/8/2011	8588229	11/19/2013	In Force	Multiple Address Databases In A Switch Without The Need For Extra
MP0139	MP0139D1	US	11/523,252	9/19/2006	7389619	5/6/2008	In Force	Interconnect Communications Apparatus And Method
MP0140	MP0140	US	10/170,306	6/12/2002	7388931	6/17/2008	In Force	ADC Architecture For Wireless Applications
MP0140	MP0140C1C1	US	12/945,035	11/12/2010	8031810	10/4/2011	In Force	ADC Architecture For Wireless Applications
MP0144	MP0144	US	10/135,422	4/29/2002	6961197	11/1/2005	In Force	Correcting Errors In Disk Drive Read Back Signals By Iterating With T
MP0144	MP0144C1C1	US	11/908,957	9/25/2007	8074151	12/6/2011	In Force	Correcting Errors In Disk Drive Read Back Signals By Iterating With T
MP0144	MP0144C2	US	11/217,349	9/2/2005	7372652	5/13/2008	In Force	Correcting Errors In Disk Drive Read Back Signals By Iterating With T
MP01441	MP01441	US	10/313,651	12/6/2002	7751138	7/6/2010	In Force	Correcting Errors In Disk Drive Read Back Signals By Iterating With Th
MP0145	MP0145	US	10/208,312	7/29/2002	6931585	8/16/2005	In Force	Detection In The Presence Of Media Noise
MP0145	MP0145C1	US	11/642,084	12/20/2006	7917836	3/29/2011	In Force	Detection In The Presence Of Media Noise
MP0148	MP0148	US	10/155,777	5/24/2002	6917313	7/12/2005	In Force	DC-Free Codes
MP0148	MP0148C1	US	11/181,084	7/14/2005	7106223	9/12/2006	In Force	DC-Free Codes
MP0148	MP0148C2	US	11/181,401	7/14/2005	7199731	4/3/2007	In Force	DC-Free Codes
MP0148	MP0148C3	US	11/181,545	7/14/2005	7187308	3/6/2007	In Force	DC-Free Codes
MP0148	MP0148C3C1	US	11/713,996	3/5/2007	7623048	11/24/2009	In Force	DC-Free Codes
MP0148	MP01481	US	10/765,431	1/26/2004	7288294	11/20/2007	In Force	Filter Driven DC-Free Codes
MP0148	MP01481a	US	12/352,872	1/13/2009	8009068	8/30/2011	In Force	Filter Driven DC-Free Code
MP0148	MP01481C1	US	11/729,747	3/29/2007	7479904	1/20/2009	In Force	Filter Driven DC-Free Code
MP0151	MP0151	US	10/193,439	7/10/2002	7346135	3/18/2008	In Force	Compensation For Residual Frequency Offset, Phase Noise And Samp
MP0151	MP0151a	US	14/314,684	6/25/2014	9185013	11/10/2015	In Force	Systems And Methods For Compensating A Channel Estimate For Sa
MP0151	MP0151C1	US	12/670,375	2/15/2008	7616719	11/10/2009	In Force	Compensation For Residual Frequency Offset, Phase Noise And Samp
MP0151	MP0151C1AA	US	13/548,883	7/13/2012	8767879	7/1/2014	In Force	Compensation For Residual Frequency Offset, Phase Noise And Samp
MP0151	MP0151C1C1	US	12/615,605	11/10/2009	8223893	7/17/2012	In Force	Compensation For Residual Frequency Offset, Phase Noise And Samp
MP0151	MP0151Z3	US	14/927,982	10/30/2015	9432726	8/30/2016	In Force	Systems And Methods For Compensating A Channel Estimate For Pha
MP0152	MP0152	US	10/259,142	9/26/2002	7133473	11/7/2006	In Force	Divisionless Baseband Equalization In Symbol Modulated Communica
MP0152	MP0152a	US	12/645,644	12/23/2009	8130853	3/6/2012	In Force	Method And Apparatus For Performing Baseband Equalization In Sym
MP0152	MP0152a	US	13/404,656	2/24/2012	8437417	5/7/2013	In Force	Method And Apparatus For Performing Baseband Equalization In Sym
MP0152	MP0152a	US	13/888,473	5/7/2013	8675786	3/18/2014	In Force	Method And Apparatus For Performing Baseband Equalization In Sym
MP0152	MP0152C1	US	11/590,708	10/31/2006	7564913	7/21/2009	In Force	Divisionless Baseband Equalization In Symbol Modulated Communica
MP0152	MP0152C1C1	US	11/645,881	12/27/2006	7643586	1/5/2010	In Force	Divisionless Baseband Equalization In Symbol Modulated Communica
MP0152	MP0152Z2a	US	14/215,636	3/17/2014	9036743	5/19/2015	In Force	System And Method For Performing Maximum Ratio Combining On A
MP0153	MP0153	US	10/158,337	5/30/2002	7362771	4/22/2008	In Force	Reduced Latency FIFO

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MP0153	MP0153C1	US	12/070,334	2/15/2008	7949002	5/24/2011	In Force	Reduced Latency FIFO
MP0154	MP0154	US	10/153,554	5/22/2002	7085330	8/1/2006	In Force	Method And Apparatus For Amplifier Linearization Using Adaptive Pr
MP0154	MP0154C1	US	11/412,637	4/27/2006	7362821	4/22/2008	In Force	Method And Apparatus For Amplifier Linearization Using Adaptive Pr
MP0154	MP0154C1C1	US	12/148,765	4/22/2008	7535974	5/19/2009	In Force	METHOD AND APPARATUS FOR AMPIFIER LINEARIZATION USING AD
MP0155	MP0155	US	10/242,135	9/10/2002	7293289	11/6/2007	In Force	Apparatus, Method And Computer Program Product For Detection Of
MP0155	MP0155C1	US	11/818,104	6/13/2007	7877805	1/25/2011	In Force	Apparatus, Method And Computer Program Product For Detection Of
MP0155	MP0155C1C1	US	13/013,294	1/25/2011	8151351	4/3/2012	In Force	Apparatus, Method And Computer Program Product For Detection Of
MP0156	MP0156	US	10/246,870	9/19/2002	6856200	2/15/2005	In Force	Protection Circuit And Method For RF Power Amplifiers In WLAN Tra
MP0156	MP0156C1	US	10/839,679	5/4/2004	7477107	1/13/2009	In Force	Protection Circuit And Method For RF Power Amplifiers In WLAN Tra
MP0156	MP0156C1C1	US	12/351,040	1/9/2009	7855604	12/21/2010	In Force	Protection Circuit And Method For RF Power Amplifiers In WLAN Tra
MP0157	MP0157	US	10/253,364	9/24/2002	7215933	5/8/2007	In Force	Local Transmitter Output Power Control System For Wireless Applica
MP0158	MP0158	US	10/195,047	7/12/2002	6819187	11/16/2004	In Force	LIMIT SWING CHARGE PUMP AND METHOD THEREOF
MP0158	MP0158I	US	10/219,225	6/13/2002	6747520	6/8/2004	In Force	Limit Swing Charge Pump And Method Thereof
MP0158	MP0158IC2	US	11/387,391	3/23/2006	7315197	1/1/2008	In Force	Limit Swing Charge Pump And Method Thereof
MP0159	MP0159	US	10/238,475	9/10/2002	7006824	2/28/2006	In Force	Frame/Package-Based Calibration For Wireless Transceivers
MP0159	MP0159C1	US	11/322,019	12/29/2005	7817998	10/19/2010	In Force	Frame/Package-Based Calibration For Wireless Transceivers
MP0159	MP0159C1C1	US	12/907,837	10/19/2010	8605633	12/10/2013	In Force	Frame/Package-Based Calibration For Wireless Transceivers
MP0161	MP0161	US	10/150,147	5/17/2002	7110415	9/19/2006	In Force	Apparatus And Method For Dynamically Limiting Output Queue Size I
MP0161	MP0161C1	US	11/523,185	9/19/2006	7764703	7/27/2010	In Force	Apparatus And Method For Dynamically Limiting Output Queue Size I
MP0161	MP0161C1aa	US	13/220,545	8/29/2011	8718077	5/6/2014	In Force	Apparatus And Method For Dynamically Limiting Output Queue Size I
MP0161	MP0161C1C1	US	12/840,327	7/21/2010	8069689	8/30/2011	In Force	Apparatus And Method For Dynamically Limiting Output Queue Size I
MP0162	MP0162	US	10/226,549	8/23/2002	6727736	4/27/2004	In Force	VOLTAGE CONTROL OSCILLATOR NOISE REDUCTION TECHNIQUE AN
MP0163	MP0163baa	US	11/890,738	8/7/2007	7463095	12/9/2008	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0163	MP0163baaa	US	12/284,810	9/25/2008	7598814	10/6/2009	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0163	MP0163C1	US	11/049,211	2/2/2005	7088187	8/8/2006	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0163	MP0163C2	US	11/435,995	5/17/2006	7190230	3/13/2007	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0163	MP0163C2C1	US	11/650,681	1/8/2007	7253690	8/7/2007	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0163	MP0163D1	US	11/175,943	7/5/2005	7230491	6/12/2007	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0163	MP0163D2	US	11/175,560	7/5/2005	7230494	6/12/2007	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0167	MP0167C1	US	11/280,027	11/16/2005	7446742	10/21/2008	In Force	Mixer Gain Calibration Method And Apparatus
MP0168	MP0168	US	10/292,181	11/12/2002	7653071	1/26/2010	In Force	Router Having A Single CPU Mac
MP0168	MP0168D1	US	11/809,823	6/1/2007	7564857	7/21/2009	In Force	Router Having A Single CPU MAC
MP0168	MP0168D2	US	11/809,844	6/1/2007	7715415	5/11/2010	In Force	Router Having A Single CPU MAC
MP0168	MP0168D3	US	11/809,822	6/1/2007	7742488	6/22/2010	In Force	Router Having A Single CPU MAC

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MP0168	MP016811	US	10/990,231	11/16/2004	7668203	2/23/2010	In Force	Network Switch Using A Steering Header For Management Frames
MP0168	MP016811C1	US	12/709,715	2/22/2010	8111715	2/7/2012	In Force	Method And Apparatus For Transferring A Frame Of Data From A First
MP0168	MP016811C1C1	US	13/366,791	2/6/2012	8894738	8/12/2014	In Force	Method And Apparatus For Transferring A Frame Of Data From A First
MP0171	MP0171	US	10/301,349	11/20/2002	6784738	8/31/2004	In Force	Method And Apparatus For Gain Control In A CMOS Low Noise Amplifier
MP0171	MP0171C1	US	10/889,643	7/12/2004	6887729	5/24/2005	In Force	Self-Calibrating Gain Control Circuit For Low Noise Amplifier
MP0172	MP0172	US	10/353,451	1/28/2003	7120834	10/10/2006	In Force	Fast Port Failover In A Network Switch
MP0172	MP0172a	US	12/906,911	10/18/2010	8117503	2/14/2012	In Force	Fast Port Failover In A Network Switch
MP0172	MP0172a	US	13/371,918	2/13/2012	8566649	10/22/2013	In Force	Fast Port Failover In A Network Switch
MP0172	MP0172C1	US	11/525,326	9/22/2006	7368612	12/11/2007	In Force	Fast Port Failover In A Network Switch
MP0172	MP0172C1C1	US	12/001,292	12/11/2007	7818628	10/19/2010	In Force	Fast Port Failover In A Network Switch
MP0172	MP0172a	US	14/059,947	10/22/2013	9344328	5/17/2016	In Force	Fast Port Failover In A Network Switch
MP0173	MP0173	US	10/141,096	5/7/2002	7209440	4/24/2007	In Force	Method And Apparatus For Preventing Blocking In A Quality Of Service
MP0173	MP0173C1	US	11/789,315	4/24/2007	7321554	1/22/2008	In Force	Method And Apparatus For Preventing Blocking In A Quality Of Service
MP0174	MP0174	US	10/189,321	7/3/2002	7469057	8/5/2008	In Force	Nonlinear Echo Compensator For Class B Transmitter Line Driver
MP0174	MP0174C1C1	US	14/289,980	5/29/2014	8989251	3/24/2015	In Force	METHOD AND APPARATUS TO COMPENSATE FOR NONLINEAR ECHO
MP0176	MP0176	US	10/353,482	1/28/2003	8653374	1/21/2014	In Force	Automatic Media Converter
MP0176	MP0176C1	US	14/158,141	1/17/2014	10075330	9/11/2018	In Force	Systems And Methods For Statuses Of Communication Links
MP0177	MP0177	US	10/305,091	11/26/2002	7051267	5/23/2006	In Force	Efficient High-Speed Reed-Solomon Decoder
MP0177	MP0177C1	US	11/407,436	4/20/2006	7322004	1/22/2008	In Force	Efficient High-Speed Reed-Solomon Decoder
MP0178	MP0178	US	10/304,511	11/26/2002	7010739	3/7/2006	In Force	Error Evaluator For Inversionless Berlekamp-Massey Algorithm In Reed
MP0178	MP0178C1	US	11/243,283	10/4/2005	7249310	7/24/2007	In Force	Error Evaluator For Inversionless Berlekamp-Massey Algorithm In Reed
MP0179	MP0179	US	10/165,467	6/7/2002	6825672	11/30/2004	In Force	Cable Tester
MP0179	MP0179D1	US	10/957,285	10/1/2004	7190172	3/13/2007	In Force	Cable Tester
MP0179	MP0179fB	US	11/895,372	8/24/2007	7403018	7/22/2008	In Force	Cable Tester
MP0179	MP0179fB	US	12/082,964	4/15/2008	7884615	2/8/2011	In Force	Cable Tester
MP0179	MP0179fB	US	13/013,410	2/8/2011	8179144	5/15/2012	In Force	Cable Tester
MP0179	MP0179fBZ	US	13/470,893	5/14/2012	8829917	9/9/2014	In Force	Cable Tester
MP0179	MP0179g	US	12/148,161	4/17/2008	7683628	3/23/2010	In Force	Cable Tester
MP0179	MP0179HD	US	11/831,670	7/31/2007	7576548	8/18/2009	In Force	Cable Tester
MP0179	MP0179I	US	10/331,221	12/30/2002	6977507	12/20/2005	In Force	Cable Tester With Indicator
MP0179	MP0179I2	US	10/401,221	3/27/2003	7005861	2/28/2006	In Force	Cable Tester
MP0179	MP0179I2C	US	11/455,467	6/19/2006	7358745	4/15/2008	In Force	Cable Tester
MP0179	MP0179I2D1	US	10/979,302	11/2/2004	6982557	1/3/2006	In Force	Cable Tester
MP0179	MP0179I2D2	US	10/981,361	11/4/2004	7276913	10/2/2007	In Force	Cable Tester

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MP0179	MP017912D3	US	10/990,176	11/16/2004	6992491	1/31/2006	In Force	Cable Tester
MP0179	MP017913	US	10/490,864	3/27/2003	7002353	2/23/2006	In Force	Cable Tester
MP0179	MP017913Aa	US	11/254,384	10/20/2005	7075283	7/11/2006	In Force	Cable Tester
MP0179	MP017913Ca	US	11/494,465	7/11/2006	7375532	5/20/2008	In Force	Cable Tester
MP0179	MP017913D1	US	11/066,755	2/25/2005	7068043	6/27/2006	In Force	Cable Tester
MP0179	MP017913D2	US	11/066,739	2/25/2005	6995551	2/7/2006	In Force	Cable Tester With Insertion Loss Estimator
MP0179	MP017913D3	US	11/066,807	2/25/2005	7068044	6/27/2006	In Force	Cable Tester
MP0179	MP017914	US	10/400,367	3/27/2003	6980307	12/27/2005	In Force	CABLE TESTER
MP0179	MP017914D1	US	10/997,119	11/24/2004	7173431	2/6/2007	In Force	Cable Tester
MP0179	MP017914DA	US	11/129,203	5/13/2005	7019533	3/28/2006	In Force	Cable Tester
MP0179	MP017914DB	US	11/129,202	5/13/2005	7026825	4/11/2006	In Force	Cable Tester
MP0179	MP017914DC	US	11/455,359	6/19/2006	7250771	7/31/2007	In Force	Cable Tester
MP0179	MP01791D1	US	10/967,602	10/18/2004	7002354	2/21/2006	In Force	Cable Tester
MP0179	MP01791D2	US	11/093,101	3/29/2005	7012436	3/14/2006	In Force	Cable Tester
MP0180	MP0180	US	10/329,196	12/23/2002	7593431	9/22/2009	In Force	Method And System For Monitoring A MAC Extra IPG Count And Adj
MP0182	MP0182	US	10/348,091	1/21/2003	6972977	12/6/2005	In Force	Flash Memory Module
MP0182	MP0182Aaa	US	12/831,660	7/7/2010	8072788	12/6/2011	In Force	Flash Memory Module
MP0182	MP0182C1	US	11/070,623	3/2/2005	7460382	12/2/2008	In Force	Flash Memory Module
MP0182	MP0182C2	US	11/184,753	7/19/2005	7433218	10/7/2008	In Force	Flash Memory Module
MP0182	MP0182D1	US	11/070,622	3/2/2005	7483284	11/27/2009	In Force	Flash Memory Module
MP0182	MP0182D1C1	US	12/350,939	12/9/2008	7755919	7/13/2010	In Force	Flash Memory Module
MP0183	MP0183	US	10/364,602	2/11/2003	7242693	7/10/2007	In Force	Method and Apparatus for Fiber Autonegotiation
MP0183	MP0183C1	US	11/825,820	7/9/2007	7561592	7/14/2009	In Force	Method And Apparatus For Fiber Auto-Negotiation
MP0184	MP0184	US	10/358,713	2/4/2003	7529180	5/5/2009	In Force	Switch Failover For Aggregated Data Communication Links
MP0184	MP0184C1	US	12/434,132	5/1/2009	7881185	2/1/2011	In Force	Switch Failover For Aggregated Data Communication Links
MP0185	MP0185	US	10/460,825	6/12/2003	6977492	12/20/2005	In Force	Output Regulator
MP0185	MP0185CND1	CN	2004100889182	7/10/2003	212004100889	4/9/2008	In Force	Output Regulator System
MP0185	MP0185CND2	CN	200410088920X	7/10/2003	212004100889	6/4/2008	In Force	Voltage Slicer
MP0185	MP0185CND3	CN	2004100889229	7/10/2003	212004100889	7/30/2008	In Force	Adaptive Multi-Mode System
MP0185	MP0185CND4	CN	2004100889214	7/10/2003	212004100889	9/3/2008	In Force	Adaptive Duty Cycle Limiter
MP0185	MP0185CND5	CN	2004100889178	7/10/2003	212004100889	6/4/2008	In Force	Duty Cycle Estimator
MP0185	MP0185CND6	CN	2004100889163	7/10/2003	212004100889	12/3/2008	In Force	Adaptive Control Loop
MP0185	MP0185CND7	CN	2004100889248	7/10/2003	212004100889	12/3/2008	In Force	Single Zero Compensator
MP0185	MP0185CND8	CN	2004100889197	7/10/2003	212004100889	9/3/2008	In Force	Energy Saving Discontinuous Mode System

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MP0185	MP0185CND9	CN	2004100889233	7/10/2003	212004100889	12/28/2011	In Force	Power Array System And Method
MP0185	MP0185D1	US	10/712,290	11/12/2003	7023192	4/4/2006	In Force	Output Regulator System And Method
MP0185	MP0185D2	US	10/811,430	3/26/2004	7009372	3/7/2006	In Force	Adaptive Multi-Mode System And Method
MP0185	MP0185D3	US	10/818,508	4/5/2004	6884465	5/17/2005	In Force	Power Array System And Method
MP0185	MP0185D3D1	US	11/080,829	3/15/2005	7368898	5/6/2008	In Force	Power Array System And Method
MP0185	MP0185D4	US	10/827,645	4/19/2004	7053594	5/30/2006	In Force	Adaptive Duty Cycle Limiter And Method
MP0185	MP0185D5	US	10/827,633	4/19/2004	6979988	12/27/2005	In Force	Duty Cycle Estimation
MP0185	MP0185D6	US	10/827,634	4/19/2004	7358711	4/15/2008	In Force	Adaptive Control Loop
MP0185	MP0185D6C1	US	12/082,825	4/15/2008	7863880	1/4/2011	In Force	Adaptive Control Loop
MP0185	MP0185D6D1	US	11/187,761	10/11/2005	7411377	8/12/2008	In Force	Adaptive Control Loop
MP0185	MP0185D6D1C1	US	12/148,078	4/16/2008	7966949	3/15/2011	In Force	Adaptive Control Loop
MP0185	MP0185D7	US	10/827,625	4/19/2004	7042202	5/9/2006	In Force	Energy Saving Discontinuous Mode System And Method
MP0185	MP0185D8	US	10/827,644	4/19/2004	6933711	8/23/2005	In Force	Voltage Slicer And Method
MP0185	MP0185EAA	US	11/906,087	9/28/2007	7622904	11/24/2009	In Force	Power Array System And Method
MP0185	MP0185EAB	US	12/080,645	4/4/2008	7609043	10/27/2009	In Force	Power Array System And Method
MP0185	MP0185EAC1	US	11/906,084	9/28/2007	7573249	8/11/2009	In Force	Power Array System And Method
MP0185	MP0185EPD6	EP	04023234.0	7/10/2003	EP1524568	10/15/2008	In Force	ADAPTIVE DUTY CYCLE LIMITER
MP0185	MP0185EPD7	EP	EP04023247.2	7/10/2003	EP1524569	9/10/2008	In Force	DUTY CYCLE ESTIMATOR
MP0185	MP0185EPD9	EP	04023192.0	7/10/2003	1524567	9/10/2008	In Force	DUTY CYCLE ESTIMATOR
MP0185	MP0185Q04N	DE	04023257.1	7/10/2003	1524573	4/20/2011	In Force	CONTROL SYSTEM FOR AN OUTPUT REGULATOR
MP0185	MP0185Q04R	FR	04023257.1	7/10/2003	1524573	4/20/2011	In Force	CONTROL SYSTEM FOR AN OUTPUT REGULATOR
MP0185	MP0185Q04T	GB	04023257.1	7/10/2003	1524573	4/20/2011	In Force	CONTROL SYSTEM FOR AN OUTPUT REGULATOR
MP0185	MP0185QDE5	DE	40232563	7/10/2003	1524572	12/28/2011	In Force	Power Array System And Method
MP0185	MP0185QFR5	FR	04023256.3	7/10/2003	1524572	12/28/2011	In Force	Power Array System And Method
MP0185	MP0185QGB5	GB	04023256.3	7/10/2003	1524572	12/28/2011	In Force	Power Array System And Method
MP0185	MP0185TW	TW	92118863	7/10/2003	1287912	10/11/2007	In Force	OUTPUT REGULATOR
MP0185	MP0185UD6G	DE	04023234.0	7/10/2003	60324148.4	10/15/2008	In Force	Adaptive Duty Cycle Limiter
MP0185	MP0185UD6N	GB	04023234.0	7/10/2003	1524568	10/15/2008	In Force	Adaptive Duty Cycle Limiter
MP0185	MP0185UD6R	FR	04023234.0	7/10/2003	1524568	10/15/2008	In Force	Adaptive Duty Cycle Limiter
MP0185	MP0185UD9G	DE	04023192.0	7/10/2003	1524567	9/10/2008	In Force	Duty Cycle Estimator
MP0185	MP0185UD9N	GB	04023192.0	7/10/2003	1524567	9/10/2008	In Force	Duty Cycle Estimator
MP0185	MP0185UD9R	FR	04023192.0	7/10/2003	1524567	9/10/2008	In Force	DUTY CYCLE ESTIMATOR
MP0185	MP0185WEF2	EP	EP04023259.7	7/10/2003	EP1524575	4/20/2011	In Force	Output Regulator
MP0185	MP0185W110	JP	2008-057075	3/6/2008	5281302	5/31/2013	In Force	Output Regulator System

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Family	JP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0185	MP0185W/P2	JP	2008-05/067	3/6/2008	4875012	12/2/2011	In Force	Output Regulator System
MP0185	MP0185W/P3	JP	2008-05/068	7/10/2003	4922968	2/10/2012	In Force	Output Regulator System
MP0185	MP0185W/P4	JP	2008-05/069	3/6/2008	4922969	2/10/2012	In Force	Output Regulator System
MP0185	MP0185W/P5	JP	2008-05/070	3/6/2008	4922970	2/10/2012	In Force	Output Regulator System
MP0185	MP0185W/P6	JP	2008-05/071	3/6/2008	5025528	6/29/2012	In Force	Duty Cycle Limiter
MP0185	MP0185W/P7	JP	2008-05/072	3/6/2008	5022948	6/22/2012	In Force	Output Regulator System
MP0185	MP0185W/P8	JP	2008-05/073	3/6/2008	5025529	6/29/2012	In Force	Output Regulator System
MP0185	MP0185W/P9	JP	2008-05/074	3/6/2008	5022949	6/22/2012	In Force	Output Regulator System
MP0185	MP0185W/O	WO	PCT/US2003/02	7/10/2003			Pending	Output Regulator
MP0185	MP0185W/CN	CN	38054655	7/10/2003	ZL2004638054	6/4/2008	In Force	Output Regulator
MP0185	MP0185W/DE03	DE	04023258.9	7/10/2003	603341020	9/8/2010	In Force	Output Regulator
MP0185	MP0185W/DE7	DE	EP04023247.2	7/10/2003	1524569	9/10/2008	In Force	DUTY CYCLE ESTIMATOR
MP0185	MP0185W/EP01	EP	40232514	7/10/2003	1524571	4/20/2011	In Force	Control System For An Output Regulator
MP0185	MP0185W/EP01DE	DE	04023251.4	7/10/2003	1524571	4/20/2011	In Force	Control System For An Output Regulator
MP0185	MP0185W/EP01FR	FR	04023251.4	7/10/2003	1524571	4/20/2011	In Force	Control System For An Output Regulator
MP0185	MP0185W/EP01G	GB	04023251.4	7/10/2003	1524571	4/20/2011	In Force	Control System For An Output Regulator
MP0185	MP0185W/EP08	EP	04023249.8	7/10/2003	1524570	9/10/2008	In Force	Adaptive Control Loop
MP0185	MP0185W/EP10	EP	37634218	7/10/2003	1532501	5/4/2011	In Force	Digital Controller For An Output Regulator
MP0185	MP0185W/EP3	EP	EP04023258.9	7/10/2003	EP1524574	9/8/2010	In Force	Output Regulator
MP0185	MP0185W/EPDE8	DE	04023249.8	7/10/2003	1524570	9/10/2008	In Force	Adaptive Control Loop
MP0185	MP0185W/EPDE10	DE	03763431.8	7/10/2003	60337012.8	5/4/2011	In Force	Digital Controller For An Output Regulator
MP0185	MP0185W/EPFR08	FR	04023249.8	7/10/2003	1524570	9/10/2008	In Force	Adaptive Control Loop
MP0185	MP0185W/EPFR10	FR	03763431.8	7/10/2003	1532501	5/4/2011	In Force	Digital Controller For An Output Regulator
MP0185	MP0185W/EPGB0	GB	04023249.8	7/10/2003	1524570	9/10/2008	In Force	Adaptive Control Loop
MP0185	MP0185W/EPGB1	GB	03763431.8	7/10/2003	1532501	5/4/2011	In Force	Digital Controller For An Output Regulator
MP0185	MP0185W/EPFR03	FR	04023258.9	7/10/2003	1524574	9/8/2010	In Force	Output Regulator
MP0185	MP0185W/EPFR7	FR	04023247.2	7/10/2003	1524569	9/10/2008	In Force	Duty Cycle Estimator
MP0185	MP0185W/EPGB03	GB	04023258.9	7/10/2003	1524574	9/8/2010	In Force	Output Regulator
MP0185	MP0185W/EPGB7	GB	04023247.2	7/10/2003	1524569	9/10/2008	In Force	Duty Cycle Estimator
MP0185	MP0185W/OJP	JP	2005258595	9/6/2005			Pending	Power Array System And Method
MP0185	MP0185W/OJP02	JP	2005258592	9/6/2005			Pending	Output Regulator
MP0185	MP0185W/OJP03	JP	2005258593	9/6/2005			Pending	Voltage Slicer
MP0185	MP0185W/OJP04	JP	2005258594	9/6/2005			Pending	Adaptive Multi-Mode System
MP0185	MP0185W/OJP07	JP	2005258597	9/6/2005			Pending	Duty Cycle Estimator

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MP0185	MP0185WOJP08	JP	2005258598	9/6/2005			Pending	Adaptive Control Loop
MP0185	MP0185WOJP09	JP	2005258599	9/6/2005			Pending	Single Zero Compensator
MP0185	MP0185WOJP10	JP	2005258600	9/6/2005			Pending	Energy Saving Discontinuous Mode System
MP0185	MP0185WOJP1D1	JP	2005-258596	9/6/2005			Pending	Adaptive Duty Cycle Limiter
MP0185	MP0185WUJ04	EP	EP04023257.1	7/10/2003	EP1524573	4/20/2011	In Force	Control System for an Output Regulator
MP0185	MP0185WUJ05	EP	04023256.3	7/10/2003	1524572	12/28/2011	In Force	POWER ARRAY SYSTEM AND METHOD
MP0185	MP0185WUJ2N	DE	04023259.7	7/10/2003	1524575	4/20/2011	In Force	OUTPUT REGULATOR
MP0185	MP0185WUJ2R	FR	04023259.7	7/10/2003	1524575	4/20/2011	In Force	OUTPUT REGULATOR
MP0185	MP0185WUJ2T	GB	04023259.7	7/10/2003	1524575	4/20/2011	In Force	OUTPUT REGULATOR
MP0188	MP0188	US	10/313,369	12/6/2002	6839015	1/4/2005	In Force	Low Power Analog to Digital Converter
MP0188	MP0188D1	US	10/946,321	9/21/2004	6967610	11/22/2005	In Force	LOW POWER BIT AND ONE-HALF ANALOG TO DIGITAL CONVERTOR
MP0188	MP0188D1D1	US	11/242,691	10/4/2005	7071863	7/4/2006	In Force	LOW POWER ANALOG TO DIGITAL CONVERTER HAVING REDUCED BI
MP0192	MP0192	US	10/251,372	9/19/2002	6970794	11/29/2005	In Force	Semiconductor Having Reduced Configuration Pins And Method Ther
MP0192	MP0192AAAB	US	12/075,464	3/11/2008	7788053	8/31/2010	In Force	Configurable Voltage Regulator
MP0192	MP0192AAAZ	US	12/862,815	8/25/2010	7979224	7/12/2011	In Force	Configurable Voltage Regulator
MP0192	MP0192AAD1	US	11/602,922	11/21/2006	7343256	3/11/2008	In Force	Configurable Voltage Regulator
MP0192	MP0192AAD2	US	11/789,194	4/24/2007	7437252	10/14/2008	In Force	Configurable Voltage Regulator
MP0192	MP0192AAIA	US	11/546,831	10/12/2006	7480578	1/20/2009	In Force	Configurable Voltage Regulator
MP0192	MP0192AAIB	US	11/546,832	10/12/2006	7516027	4/7/2009	In Force	Configurable Voltage Regulator
MP0192	MP0192AAIWQ	WO	PCT/US2007/01	5/18/2007			Pending	Configurable Voltage Regulator
MP0192	MP0192AAIWQCN	CN	2007800257150	5/18/2007	ZL2007800257	4/25/2012	In Force	CONFIGURABLE VOLTAGE REGULATOR
MP0192	MP0192D1	US	11/220,255	9/6/2005	7062392	6/13/2006	In Force	Configurable Voltage Regulator
MP0192	MP0192D1D1	US	11/411,377	4/26/2006	7209845	4/24/2007	In Force	Configurable Voltage Regulator
MP0192	MP0192-D1I1	US	11/517,932	9/8/2006	7512504	3/31/2009	In Force	Testing System Using Configurable Integrated Circuit
MP0192	MP0192YFay	TW	96124295	7/4/2007	1416146	11/21/2013	In Force	TESTING SYSTEM COMPRISING CONFIGURABLE INTEGRATED CIRCUIT
MP0192	MP0192YFby	TW	96124297	7/4/2007	1418964	12/11/2013	In Force	PRODUCTION TESTING SYSTEM FOR TESTING AN INTEGRATED CIRCUIT
MP0192	MP0192YFTW	TW	96124298	7/4/2007	1412769	10/21/2013	In Force	Testing System Using Configurable Integrated Circuit
MP0192	MP0192YICN	DE	07793014.5	5/18/2007	2038720	1/11/2012	In Force	CONFIGURABLE VOLTAGE REGULATOR
MP0192	MP0192YICR	FR	07793014.5	5/18/2007	2038720	1/11/2012	In Force	CONFIGURABLE VOLTAGE REGULATOR
MP0192	MP0192YICJ	GB	07793014.5	5/18/2007	2038720	1/11/2012	In Force	CONFIGURABLE VOLTAGE REGULATOR
MP0192	MP0192YIWJ	JP	2009-518122	5/18/2007	5047282	7/27/2012	In Force	CONFIGURABLE VOLTAGE REGULATOR
MP0192	MP0192YIWU	EP	07793014.5	5/18/2007	2038720	1/11/2012	In Force	CONFIGURABLE VOLTAGE REGULATOR
MP0194	MP0194	US	10/307,845	12/2/2002	6978337	12/20/2005	In Force	Serial ATA Controller Having Failover Function
MP0195	MP0195	US	10/339,897	1/10/2003	7246192	7/17/2007	In Force	Serial/Parallel ATA Controller And Converter

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MP0195	MP0195C1C1	US	14/215,919	3/17/2014	9514080	12/6/2016	In Force	Method And Apparatus For Providing An Interface Between A Host D
MP0197	MP0197	US	10/389,825	3/17/2003	6972916	12/6/2005	In Force	Preamplifier Arranged in Proximity Of Disk Drive Head
MP0197	MP0197C1	US	11/242,371	10/3/2005	7835097	11/16/2010	In Force	Preamplifier Arranged in Proximity Of Disk Drive Head
MP0199	MP0199	US	10/272,247	10/15/2002	7042301	5/9/2006	In Force	Crystal Oscillator Emulator
MP0199	MP0199CN	CN	31563511	9/4/2003	ZL20036031563	12/3/2008	In Force	Crystal Oscillator Emulator
MP0199	MP0199EP	EP	EP03017477.5	8/1/2003	EP1411630	7/6/2016	In Force	Crystal oscillator emulator
MP0199	MP0199EP01	EP	40207797	9/1/2004			Pending	Integrated Circuit Including Processor And Crystal Oscillator Emulat
MP0199	MP0199EPDE	DE	03017477.5	8/1/2003	603491022	7/6/2016	In Force	Crystal Oscillator Emulator
MP0199	MP0199EPFR	FR	03017477.5	8/1/2003	1411630	7/6/2016	In Force	Crystal Oscillator Emulator
MP0199	MP0199EPGB	GB	03017477.5	8/1/2003	1411630	7/6/2016	In Force	Crystal Oscillator Emulator
MP0199	MP0199SG4	SG	2006659182	8/31/2006	131026	9/30/2009	In Force	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199HeTW	TW	96138627	10/16/2007	1433465	4/1/2014	In Force	Crystal Oscillator Emulator
MP0199	MP0199HGTW	TW	96138628	10/16/2007	1424680	1/21/2014	In Force	CRYSTAL OSCILLATOR EMULATOR
MP0199	MP0199HLTW	TW	96138629	10/16/2007	1429187	3/1/2014	In Force	CRYSTAL OSCILLATOR EMULATOR
MP0199	MP0199I1	US	10/892,709	7/16/2004	7148763	12/12/2006	In Force	Integrated Circuit Including Processor And Crystal Oscillator Emulator
MP0199	MP0199I1JP	JP	2004-264760	9/10/2004			Pending	INTEGRATED CIRCUIT INCLUDING PROCESSOR AND CRYSTAL OSCILLA
MP0199	MP0199I1TW	TW	93127282	9/9/2004	1340393	4/11/2011	In Force	INTEGRATED CIRCUIT INCLUDING PROCESSOR AND CRYSTAL OSCILLA
MP0199	MP0199I2C1	US	11/486,945	7/14/2006	7301408	11/27/2007	In Force	Integrated Circuit With Low Dielectric Loss Packaging Material
MP0199	MP0199I2C2	US	11/486,898	7/14/2006	7812683	10/12/2010	In Force	Integrated Circuit Package With Glass Layer And Oscillator
MP0199	MP0199I2C5	US	11/487,077	7/14/2006	7253495	8/7/2007	In Force	Integrated Circuit Package With Air Gap
MP0199	MP0199I2CN	CN	2006101269472	9/6/2006			Pending	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2CN03	CN	2006101269491	9/6/2006	ZL2006101269	6/16/2010	In Force	INTEGRATED CIRCUIT PACKAGE WITH GLASS LAYER AND OSCILLATOR
MP0199	MP0199I2CN04	CN	2006101269487	9/6/2006			Pending	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2EP	EP	60181047	8/30/2006			Pending	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2G3a	US	14/089,261	11/25/2013	9143083	9/22/2015	In Force	Crystal Oscillator Emulator With Externally Selectable Operating Conf
MP0199	MP0199I2Gb	US	14/852,747	9/14/2015	9350360	5/24/2016	In Force	Systems And Methods For Configuring A Semiconductor Device
MP0199	MP0199I2HK	HK	71089648	9/6/2006			Pending	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2HK03	HK	71089666	9/6/2006			Pending	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2HK04	HK	71089657	9/6/2006			Pending	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2SG	SG	2006659158	8/31/2006	131024	3/31/2009	In Force	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2SG03	SG	2006659166	8/31/2006	131025	6/30/2009	In Force	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2TW	TW	95132917	9/6/2006	1400777	7/1/2013	In Force	INTEGRATED CIRCUIT INCLUDING SILICON WAFER WITH ANNEALED
MP0199	MP0199I2TW03	TW	95132921	9/6/2006			Pending	INTEGRATED CIRCUIT INCLUDING SILICON WAFER WITH ANNEALED
MP0199	MP0199I2TW04	TW	95132919	9/6/2006			Pending	INTEGRATED CIRCUIT INCLUDING SILICON WAFER WITH ANNEALED

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0199	MP019914	US	11/649,433	1/4/2007	7768360	8/3/2010	In Force	Crystal Oscillator Emulator
MP0199	MP019914C1	US	11/732,418	4/3/2007	7768317	8/31/2010	In Force	Crystal Oscillator Emulator
MP0199	MP019914C1WO	WO	PCT/US2007/02	10/17/2007			Pending	Crystal Oscillator Emulator
MP0199	MP019914C2	US	11/732,409	4/3/2007	7768361	8/3/2010	In Force	Crystal Oscillator Emulator
MP0199	MP019914C3	US	11/732,435	4/3/2007	7768039	7/20/2010	In Force	Crystal Oscillator Emulator
MP0199	MP019914C4	US	11/732,465	4/3/2007	7768036	7/20/2010	In Force	Crystal Oscillator Emulator
MP0199	MP019914C5	US	11/732,304	4/3/2007	7791424	9/7/2010	In Force	Crystal Oscillator Emulator
MP0199	MP019914C5WO	WO	PCT/US2007/02	10/16/2007			Pending	Crystal Oscillator Emulator
MP0199	MP019914La	US	12/8/6,739	9/7/2010	8063711	11/22/2011	In Force	Crystal Oscillator Emulator
MP0199	MP019914WO	WO	PCT/US2007/02	10/17/2007			Pending	Crystal Oscillator Emulator
MP0199	MP01991CN	CN	2004100784015	9/10/2004			Pending	Integrated Circuit Including Processor and Crystal Oscillator Emulator
MP0199	MP0199JP	JP	2003-347358	10/6/2003			Pending	CRYSTAL OSCILLATOR EMULATOR
MP0199	MP0199TW	TW	92119741	7/18/2003	1279072	4/11/2007	In Force	CRYSTAL OSCILLATOR EMULATOR
MP0200	MP02000	US	10/382,242	3/18/2003	7417817	8/26/2008	In Force	Write Driver Circuit For Magnetic Data Storage Systems
MP0200	MP0200C1	US	12/229,393	8/22/2008	7880989	2/1/2011	In Force	Write Driver Circuit For Magnetic Data Storage Systems
MP0200	MP0200D1	US	11/091,225	3/28/2005	7280300	10/9/2007	In Force	Write Driver Circuit For Magnetic Data Storage Systems
MP0200	MP0200D2	US	11/091,608	3/28/2005	7277245	10/2/2007	In Force	Write Driver Circuit For Magnetic Data Storage Systems
MP0202	MP0202	US	10/259,088	9/26/2002	7567634	7/28/2009	In Force	Reduced Complexity Viterbi Decoding Method And Apparatus
MP0202	MP0202C1	US	12/150,836	5/1/2008	7630462	12/8/2009	In Force	Reduced Complexity Viterbi Decoding Method And Apparatus
MP0202	MP0202C1C1	US	12/632,877	12/8/2009	7949079	5/24/2011	In Force	Reduced Complexity Viterbi Decoding Method And Apparatus
MP0203	MP0203	US	10/259,024	9/26/2002	7065146	6/20/2006	In Force	Method And Apparatus For Equalization And Decoding In A Wireless
MP0203	MP0203aaa	US	12/686,111	1/12/2010	8126074	2/28/2012	In Force	Method And Apparatus For Equalization And Decoding In A Wireless
MP0203	MP0203aaaa	US	13/400,291	2/20/2012	8454068	7/23/2013	In Force	Method And Apparatus For Equalization And Decoding In A Wireless
MP0203	MP0203aaazA	US	13/947,562	7/22/2013	8731085	5/20/2014	In Force	Method And Apparatus For Equalization And Decoding In A Wireless
MP0203	MP0203C1	US	11/449,502	6/8/2006	7388924	6/17/2008	In Force	Method And Apparatus For Equalization And Decoding In A Wireless
MP0203	MP0203C1C1	US	12/214,266	6/16/2008	7646821	1/12/2010	In Force	Method And Apparatus For Equalization And Decoding In A Wireless
MP0203	MP0203ZZaa	US	14/278,514	5/15/2014	8971459	3/3/2015	In Force	Receiver And Method For Incorporating Channel State Information In
MP0203	MP0203ZZZa	US	14/653,659	2/27/2015	9166854	10/20/2015	In Force	Method And Apparatus For Decoding A Symbol Incorporating Channe
MP0204	MP0204	US	10/316,806	12/10/2002	7433298	10/7/2008	In Force	Compensation For Residual Frequency Offset, Phase Noise And I/Q I
MP0204	MP0204C1	US	12/287,199	10/7/2008	7643405	1/5/2010	In Force	Compensation For Residual Frequency Offset, Phase Noise And I/Q I
MP0204	MP0204C1aa	US	13/010,295	1/20/2011	8488442	7/16/2013	In Force	Compensation For Residual Frequency Offset, Phase Noise And I/Q I
MP0204	MP0204C1C1	US	12/645,678	12/23/2009	7881237	2/1/2011	In Force	Compensation For Residual Frequency Offset, Phase Noise And I/Q I
MP0204	MP0204C1ZA	US	13/941,787	7/15/2013	8792325	7/29/2014	In Force	Apparatuses And Method For Mapping Bits Of Orthogonal Frequency
MP0206	MP0206	US	10/387,299	3/12/2003	7415652	8/19/2008	In Force	Out Of Order Checksum Calculation For Fragmented Packets

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MP0206	MP0206C1	US	12/221,168	7/31/2008	8161364	4/17/2012	In Force	Out Of Order Checksum Calculation For Fragmented Packets
MP0208	MP0208	US	10/383,640	3/21/2003	7107514	9/12/2006	In Force	METHOD AND APPARATUS FOR DETECTING VITERBI DECODER ERROR
MP0208	MP0208C1	US	11/516,272	9/6/2006	7383488	6/3/2008	In Force	Method And Apparatus For Detecting Viterbi Decoder Errors Due To
MP0208	MP0208C1C1	US	12/156,040	5/29/2008	8086670	10/25/2011	In Force	Method And Apparatus For Detecting Viterbi Decoder Errors Due To
MP0208	MP0208C2	US	11/516,364	9/6/2006	7383489	6/3/2008	In Force	Method And Apparatus For Detecting Viterbi Decoder Errors Due To
MP0208	MP0208C2C1	US	12/156,036	5/29/2008	8082485	12/20/2011	In Force	Method And Apparatus For Detecting Viterbi Decoder Errors Due To
MP0209	MP0209	US	10/375,986	2/27/2003	7216276	5/8/2007	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209C1	US	11/800,614	5/7/2007	7562276	7/14/2009	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209C1C1	US	12/500,245	7/9/2009	8074135	12/6/2011	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209Fa	US	12/778,225	5/12/2010	7930604	4/19/2011	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209Fa	US	13/089,660	4/19/2011	8161336	4/17/2012	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209Fa	US	13/446,627	4/13/2012	8356223	1/15/2013	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209FVZa	US	14/262,204	4/25/2014	8977921	3/10/2015	In Force	System And Method For Providing A Test Result From An Integrated
MP0209	MP0209FVZz	US	14/643,083	3/10/2015	9285421	3/15/2016	In Force	Serializer/Deserializer And Method For Transferring Data Between An
MP0209	MP0209FVZz	US	15/051,714	2/24/2016	9739834	8/22/2017	In Force	System And Method For Transferring Serialized Test Result Data Fro
MP0209	MP0209FZZa	US	13/741,496	1/15/2013	8572448	10/29/2013	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209FZZZ	US	14/064,945	10/28/2013	8713391	4/29/2014	In Force	SYSTEM AND METHOD FOR TESTING AN INTEGRATED CIRCUIT EMBE
MP0209	MP0209I1	US	11/065,584	2/24/2005	7444571	10/28/2008	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209I1C1	US	11/130,995	5/17/2005	7590911	9/15/2009	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209I1C2	US	11/131,073	5/17/2005	7496812	2/24/2009	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209I2	US	11/178,807	7/11/2005	7496818	2/24/2009	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209I2D1	US	12/154,896	5/28/2008	7721167	5/18/2010	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0210	MP0210	US	10/443,972	5/22/2003	7433401	10/7/2006	In Force	Mixed-Mode Signal Processor Architecture And Device
MP0210	MP0210C1	US	12/287,077	10/6/2008	7564900	7/21/2009	In Force	Mixed-Mode Signal Processor Architecture And Device
MP0213	MP0213	US	10/322,130	12/17/2002	7016651	3/21/2006	In Force	Apparatus And Method For Measuring Signal Quality Of A Wireless C
MP0213	MP0213C1	US	11/346,503	2/2/2006	7596663	10/6/2009	In Force	Apparatus And Method For Measuring Signal Quality Of A Wireless C
MP0213	MP0213C1C1	US	12/574,262	10/6/2009	7985607	2/8/2011	In Force	Apparatus And Method For Measuring Signal Quality Of A Wireless C
MP0214	MP0214	US	10/449,218	5/30/2003	7113356	9/26/2006	In Force	Method For Checking The Quality Of Servo Gray Codes
MP0214	MP0214C1	US	11/492,428	7/25/2006	7315427	1/1/2008	In Force	Method For Checking The Quality Of Servo Gray Codes
MP0216	MP0216	US	10/426,745	4/30/2003	7454643	11/18/2008	In Force	Pre-Emptive Power Supply Control System And Method
MP0216	MP0216C1	US	12/150,570	4/29/2008	7711972	5/4/2010	In Force	Pre-Emptive Power Supply Control System And Method
MP0216	MP0216D1	US	11/403,517	4/13/2006	7472294	12/30/2008	In Force	Pre-Emptive Power Supply Control System And Method
MP0216	MP0216D2	US	11/403,717	4/13/2006	7472295	12/30/2008	In Force	Pre-Emptive Power Supply Control System And Method
MP0216	MP0216EP	EP	EP04007362.9	3/26/2004	EP1473607	3/9/2011	In Force	Predictive Power Supply Control

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MP0216	MP0216EPOE	DE	40073629	3/26/2004	602604031686	3/9/2011	In Force	Predictive Power Supply Control
MP0216	MP0216EPFR	FR	04007362.9	3/26/2004	1473607	3/9/2011	In Force	Predictive Power Supply Control
MP0216	MP0216EPGB	GB	04007362.9	3/26/2004	1473607	3/9/2011	In Force	Predictive Power Supply Control
MP0216	MP0216JP	JP	2004-093561	3/30/2004	4733543	6/3/2011	In Force	PRE-EMPTIVE POWER SUPPLY CONTROL SYSTEM AND METHOD
MP0216	MP0216TW	TW	93108086	3/25/2004	1336435	1/21/2011	In Force	PRE-EMPTIVE POWER SUPPLY CONTROL SYSTEM AND METHOD
MP0219	MP0219	US	10/413,927	4/15/2003	6844711	1/18/2005	In Force	Low Power And High Accuracy Band Gap Voltage Circuit
MP0219	MP0219baaa	US	13/245,489	9/26/2011	8531171	9/10/2013	In Force	Low Power and High Accuracy Band Gap Voltage Circuit
MP0219	MP0219C1	US	10/926,185	8/25/2004	7023194	4/4/2006	In Force	Low Power And High Accuracy Band Gap Voltage Reference Circuit
MP0219	MP0219C2	US	11/334,030	1/18/2006	7579822	8/25/2009	In Force	Low Power And High Accuracy Band Gap Voltage Reference Circuit
MP0219	MP0219C2C1	US	12/546,298	8/24/2009	7795857	9/14/2010	In Force	Low Power And High Accuracy Band Gap Voltage Reference Circuit
MP0222	MP0222	US	10/280,420	10/25/2002	6809594	10/26/2004	In Force	Ultra Broadband Low Noise Amplifier With Matched Reactive Input I
MP0223	MP0223	US	10/271,664	10/15/2002	6856163	2/15/2005	In Force	Power Supply Decoupling For Parallel Terminated Transmission Line
MP0223	MP0223CN	CN	31569900	9/17/2003	Z12003031569	5/12/2010	In Force	Power Supply Decoupling For Parallel Terminated Transmission Line
MP0223	MP0223DE	DE	03017479.1	8/1/2003	603011101	7/27/2005	In Force	Power Supply Decoupling For Parallel Terminated Transmission Line
MP0223	MP0223EP	EP	EP03017479.1	8/1/2003	EP1403752	7/27/2005	In Force	POWER SUPPLY DECOUPLING FOR PARALLEL TERMINATED TRANSMISSION
MP0223	MP0223FR	FR	03017479.1	8/1/2003	1403752	7/27/2005	In Force	Power Supply Decoupling For Parallel Terminated Transmission Line
MP0223	MP0223GB	GB	03017479.1	8/1/2003	1403752	7/27/2005	In Force	Power Supply Decoupling For Parallel Terminated Transmission Line
MP0223	MP0223JP	JP	2003-333968	9/25/2003	4397665	10/30/2009	In Force	POWER SUPPLY DECOUPLING FOR PARALLEL TERMINATED TRANSMISSION
MP0223	MP0223TW	TW	92119720	7/18/2003	1281783	5/21/2007	In Force	POWER SUPPLY DECOUPLING FOR PARALLEL TERMINATED TRANSMISSION
MP0224	MP0224	US	10/358,709	2/5/2003	7185225	2/27/2007	In Force	Self-Repairable Semiconductor And Method Thereof
MP0224	MP0224CN	CN	31348041	9/24/2003	Z12003031348	4/11/2007	In Force	Self-Repairable Semiconductor And Method Thereof
MP0224	MP0224D1	US	11/594,390	11/8/2006	7373547	5/13/2008	In Force	Self-Repairable Semiconductor And Method Thereof
MP0224	MP0224D2	US	11/594,537	11/8/2006	7657784	2/2/2010	In Force	Self-Repairable Semiconductor And Method Thereof
MP0224	MP0224D3	US	11/594,312	11/8/2006	7313723	12/25/2007	In Force	Self-Repairable Semiconductor And Method Thereof
MP0224	MP0224EP	EP	EP03026882.5	11/24/2003	EP1434134	9/23/2015	In Force	Self-Repairable Semiconductor and Method Thereof
MP0224	MP0224EPDE	DE	03026882.5	11/24/2003	60348077.2	9/23/2015	In Force	Self-Repairable Semiconductor And Method Thereof
MP0224	MP0224EPFR	FR	03026882.5	11/24/2003	1434134	9/23/2015	In Force	Self-Repairable Semiconductor And Method Thereof
MP0224	MP0224EPGB	GB	03026882.5	11/24/2003	1434134	9/23/2015	In Force	Self-Repairable Semiconductor And Method Thereof
MP0224	MP0224I	US	10/892,707	7/16/2004	7340644	3/4/2008	In Force	Self-Repairable Semiconductor And Method Thereof
MP0224	MP0224IC1	US	13/735,588	1/7/2013	8812905	8/19/2014	In Force	Self-Repairable Semiconductor And Method Thereof
MP0224	MP0224ICP	JP	2012-019133	1/31/2012	5571709	7/4/2014	In Force	SELF-REPAIRABLE SEMICONDUCTOR AND METHOD THEREOF
MP0224	MP0224IC1	US	12/074,557	3/4/2008	7730349	6/1/2010	In Force	Self-Repairable Semiconductor And Method Thereof
MP0224	MP0224ICN	CN	2004100737372	9/9/2004	Z12004100737	12/30/2009	In Force	Self-Repairable Semiconductor And System Thereof
MP0224	MP0224IDE	DE	04020775.5	9/1/2004	602004027592	6/9/2010	In Force	Self-Repairable Semiconductor

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0224	MP0224EP	EP	04020775.5	9/1/2004	1544740	6/9/2010	In Force	Self-Repairable Semiconductor
MP0224	MP0224FR	FR	04020775.5	9/1/2004	1544740	6/9/2010	In Force	Self-Repairable Semiconductor
MP0224	MP0224GB	GB	04020775.5	9/1/2004	1544740	6/9/2010	In Force	Self-Repairable Semiconductor
MP0224	MP0224JP	JP	2004-264756	9/10/2004	4963782	4/6/2012	In Force	SELF-REPAIRABLE SEMICONDUCTOR AND METHOD THEREOF
MP0224	MP0224TW	TW	93127283	9/9/2004	1359546	3/1/2012	In Force	SELF-REPAIRABLE SEMICONDUCTOR AND METHOD THEREOF
MP0224	MP0224TW	TW	92120125	7/23/2003	1281547	5/21/2007	In Force	Self-Repairable Semiconductor And Method Thereof
MP0226	MP0226	US	10/267,177	10/9/2002	7263153	8/28/2007	In Force	Clock Offset Compensator
MP0226	MP0226C1	US	11/895,974	8/28/2007	8681914	3/25/2014	In Force	Clock Offset Compensator
MP0227	MP0227	US	10/619,278	7/14/2003	7295644	11/13/2007	In Force	APPARATUS FOR CLOCK DATA RECOVERY
MP0228	MP0228	US	10/459,798	6/12/2003	8447900	5/21/2013	In Force	Adaptive Multiple FIFO Scheme
MP0228	MP0228D1	US	13/896,869	5/17/2013	8788729	7/22/2014	In Force	Method And Apparatus For Resolving Write And Read Conflicts Betw
MP0229	MP0229	US	10/657,672	9/4/2003	7265522	9/4/2007	In Force	Dynamic Multiphase Operation
MP0229	MP0229C1	US	11/895,673	8/27/2007	7479766	1/20/2009	In Force	Dynamic Multiphase Operation
MP0229	MP0229C1C1	US	12/355,877	1/19/2009	7696732	4/13/2010	In Force	Dynamic Multiphase Operation
MP0229	MP0229CN	CN	200410048724X	6/10/2004			Pending	Dynamic Multiphase Operation
MP0229	MP0229EP	EP	40117798	5/18/2004			Pending	Dynamic Multiphase Operation
MP0229	MP0229JP	JP	2004-196815	7/12/2004	4542839	7/12/2010	In Force	DYNAMIC MULTIPHASE OPERATION
MP0229	MP0229TW	TW	93117302	6/16/2004	1340366	4/11/2011	In Force	DYNAMIC MULTIPHASE OPERATION
MP0230	MP0230	US	10/277,449	10/22/2002	7319705	1/15/2008	In Force	Programmable Pre-Emphasis Circuit For Serial ATA
MP0230	MP0230C1	US	11/904,886	9/28/2007	7733920	6/8/2010	In Force	Programmable Pre-Emphasis Circuit For Serial ATA
MP0230	MP0230C1AA	US	13/675,577	11/13/2012	8605759	12/10/2013	In Force	Device With Pre-Emphasis Based Transmission
MP0230	MP0230C1C1	US	12/792,247	6/2/2010	8311064	11/13/2012	In Force	Programmable Pre-Emphasis Circuit For Serial ATA
MP0230	MP0230C1Za	US	14/101,502	12/10/2013	8937975	1/20/2015	In Force	Apparatus And Method For Providing Pre-Emphasis To A Signal
MP0231	MP0231	US	10/412,076	4/9/2003	6818955	11/16/2004	In Force	ELECTROSTATIC DISCHARGE PROTECTION
MP0235	MP0235	US	10/423,552	4/25/2003	7269778	9/11/2007	In Force	Data Coding For Enforcing Constraints On Ones And Zeros In A Comm
MP0235	MP0235C1	US	11/789,362	5/1/2007	7568144	7/28/2009	In Force	Data Coding For Enforcing Constraints On Ones And Zeros In A Comm
MP0235	MP0235C1C1	US	12/509,754	7/27/2009	7865802	1/4/2011	In Force	Data Coding For Enforcing Constraints On Ones And Zeros In A Comm
MP0236	MP0236	US	10/434,524	5/8/2003	7288845	10/30/2007	In Force	FABRICATION OF WIRE BOND PADS OVER UNDERLYING ACTIVE DEVI
MP0237	MP0237	US	10/621,058	7/15/2003	7161342	1/9/2007	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237C1	US	11/131,106	5/16/2005	7148673	12/12/2006	In Force	METHOD AND APPARATUS INCLUDING LOW LOSS DC/DC CONVERTE
MP0237	MP0237C1C1	US	11/505,104	8/15/2006	7271573	9/18/2007	In Force	LOW LOSS DC/DC CONVERTER INCLUDING A MULTI-LEVEL CONTROL
MP0237	MP0237CN	CN	200310101782.1	10/23/2003	21200310101	11/28/2007	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237CND1	CN	200710162718.4	10/23/2003	21200710162	6/20/2012	In Force	LOW LOSS DC/DC CONVERTER AND METHOD OF DC/DC POWER CON
MP0237	MP0237CND2	CN	2007-10462719	10/23/2003	21200710162	6/2/2010	In Force	LOW LOSS DC/DC CONVERTER AND METHOD OF DC/DC POWER CON

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0237	MP0237EP	EP	03024437.0	10/23/2003	EP1414143	4/26/2017	In Force	Low Loss DC/DC Converter
MP0237	MP0237EPDE	DE	03024437.0	10/23/2003	60350154.0	4/26/2017	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237EPFR	FR	03024437.0	10/23/2003	1414143	4/26/2017	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237EPGB	GB	03024437.0	10/23/2003	1414143	4/26/2017	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237HKD1	HK	8110495.1	10/23/2003			Pending	LOW LOSS DC/DC CONVERTER
MP0237	MP0237HKD2	HK	8110496	10/23/2003			Pending	LOW LOSS DC/DC CONVERTER
MP0237	MP0237JP	JP	2003-362674	10/23/2003	3887368	12/1/2006	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237JPD1	JP	2006-221415	8/15/2006	4332166	6/26/2009	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237TW	TW	92129617	10/24/2003	1233250	5/21/2005	In Force	LOW LOSS DC/DC CONVERTER
MP0238	MP0238	US	10/386,364	1/2/2003	7420800	9/2/2008	In Force	Faceplate Including Wireless LAN Communications
MP0239	MP0239	US	10/648,632	8/25/2003	7173489	2/6/2007	In Force	PROGRAMMABLE GAIN VOLTAGE BUFFER
MP0242	MP0242	US	10/392,436	3/17/2003	6903448	6/7/2005	In Force	HIGH PERFORMANCE LEADFRAME IN ELECTRONIC PACKAGE
MP0242	MP0242D1	US	11/016,443	12/16/2004	7528013	5/5/2009	In Force	METHOD FOR FABRICATING HIGH PERFORMANCE LEADFRAME IN EL
MP0243	MP0243	US	10/380,431	3/14/2003	7062582	6/13/2006	In Force	Method And Apparatus For Bus Arbitration Dynamic Priority Based O
MP0243	MP0243C1	US	11/390,627	3/28/2006	7698486	4/13/2010	In Force	Method And Apparatus For Bus Arbitration Dynamic Priority Based O
MP0243	MP0243C1aa	US	13/274,674	10/17/2011	8307139	11/6/2012	In Force	Method And Apparatus For Dynamically Granting Access Of A Shared
MP0243	MP0243C1C1	US	12/758,849	4/13/2010	8041870	10/18/2011	In Force	Method And Apparatus For Dynamically Granting Access Of A Shared
MP0246	MP0246	US	10/621,755	7/16/2003	7336750	2/26/2008	In Force	OPTIMAL ONE-SHOT PHASE AND FREQUENCY ESTIMATION FOR TIMI
MP0248	MP0248	US	10/458,049	6/9/2003	7167942	1/23/2007	In Force	Dynamic Random Access Memory Controller
MP0248	MP0248C1	US	11/653,731	1/15/2007	9430379	8/30/2016	In Force	Dynamic Random Access Memory Controller
MP0250	MP0250	US	10/639,796	8/12/2003	7218255	5/15/2007	In Force	Methods And Apparatus For Improving Minimum Hamming Weights
MP0250	MP0250C1	US	11/786,246	4/11/2007	7312727	12/25/2007	In Force	Methods And Apparatus For Improving Minimum Hamming Weights
MP0252	MP0252	US	10/319,391	12/12/2002	7414306	8/19/2008	In Force	PREAMPLIFIER INTEGRATED CIRCUIT ON FLEX CIRCUIT FOR MAGNETI
MP0252	MP0252C1	US	12/221,707	8/6/2008	8803309	8/12/2014	In Force	Preamplifier Integrated Circuit On Flex Circuit For Magnetic Media St
MP0253	MP0253C1	US	10/894,864	7/20/2004	7158058	1/2/2007	In Force	Method And Apparatus For Generating A Seed Set In A Data Depend
MP0255	MP0255	US	10/387,402	3/26/2003	6911739	6/28/2005	In Force	Methods And Apparatus For Improving High Frequency Input/Output
MP0255	MP0255D1	US	11/033,202	1/11/2005	6987326	1/17/2006	In Force	Methods And Apparatus For Improving High Frequency Input/Output
MP0255	MP0255D2	US	11/034,093	1/12/2005	6977444	12/20/2005	In Force	Methods And Apparatus For Improving High Frequency Input/Output
MP0255	MP0255D3	US	11/034,979	1/13/2005	7009308	3/7/2006	In Force	Methods And Apparatus For Improving High Frequency Input/Output
MP0256	MP0256	US	10/656,001	9/5/2003	7505788	3/17/2009	In Force	SPATIAL MULTIPLEXING WITH ANTENNA AND CONSTELLATION SELEC
MP0257	MP0257	US	10/368,940	3/14/2003	7209522	4/24/2007	In Force	Blind MIMO Signal Processing Method And Apparatus
MP0261	MP0261	US	10/619,858	7/15/2003	6977490	12/20/2005	In Force	Compensation For Low Drop Out Voltage Regulator
MP0266	MP0266	US	10/402,438	3/27/2003	7451413	11/11/2008	In Force	METHODS OF MINIMIZING LEAKAGE CURRENT BY ANALYZING POST L
MP0270	MP0270	US	10/446,341	5/27/2003	7080172	7/18/2006	In Force	MANAGEMENT OF MEMORY, HARDWARE AND ASSOCIATED DEVICE

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0273	MP0273	US	10/701,271	11/4/2003	7234097	6/19/2007	In Force	Methods Of Supporting Host CRC In Data Storage Systems Without R
MP0273	MP0273D1	US	11/810,221	6/5/2007	8029069	9/13/2011	In Force	Methods Of Supporting Host CRC In Data Storage Systems Without R
MP0274	MP0274	US	10/649,187	8/26/2003	7231505	6/12/2007	In Force	ALIGNING IP PAYLOADS ON MEMORY BOUNDARIES FOR IMPROVED
MP0274	MP0274C1	US	11/761,876	6/12/2007	7386699	6/10/2008	In Force	ALIGNING IP PAYLOADS ON MEMORY BOUNDARIES FOR IMPROVED
MP0274	MP0274C1C1	US	12/131,837	6/12/2008	7870361	1/11/2011	In Force	ALIGNING IP PAYLOADS ON MEMORY BOUNDARIES FOR IMPROVED
MP0275	MP0275	US	10/600,419	6/20/2003	8161361	4/17/2012	In Force	AVERAGING SIGNALS TO IMPROVE SIGNAL INTERPRETATION
MP0275	MP0275C1	US	13/445,712	4/12/2012	8359527	1/22/2013	In Force	AVERAGING SIGNALS TO IMPROVE SIGNAL INTERPRETATION
MP0275	MP0275C1C1	US	13/745,655	1/18/2013	8589774	11/19/2013	In Force	AVERAGING SIGNALS TO IMPROVE SIGNAL INTERPRETATION
MP0277	MP0277	US	10/672,086	9/26/2003	7237178	6/26/2007	In Force	MARKING UNRELIABLE SYMBOLS IN A HARD DISK DRIVE READ BACK
MP0277	MP0277C1	US	11/821,937	6/26/2007	7490285	2/10/2009	In Force	Marking Unreliable Symbols In A Hard Disk Drive Read Back Signal
MP0277	MP0277C1C1	US	12/364,577	2/3/2009	7827474	11/2/2010	In Force	Marking Unreliable Symbols In A Hard Disk Drive Read Back Signal
MP0286	MP0286	US	10/620,024	7/14/2003	8149810	4/3/2012	In Force	DATA RATE ADAPTATION IN MULTIPLE-IN-MULTIPLE-OUT SYSTEMS
MP0286	MP0286C1	US	13/425,350	3/20/2012	8532081	9/10/2013	In Force	Data Rate Adaptation In Multiple-In-Multiple-Out Systems
MP0286	MP0286C1C1	US	14/022,046	9/9/2013	8861499	10/14/2014	In Force	Data Rate Adaptation In Multiple-In-Multiple-Out Systems
MP0290	MP0290	US	10/646,601	8/21/2003	7343425	3/11/2008	In Force	Multi-Speed Serial Interface For Media Access Control And Physical L
MP0290	MP0290Aaaa	US	14/107,369	12/16/2013	9479554	10/25/2016	In Force	Multi-Speed Serial Interface For Media Access Control And Physical L
MP0290	MP0290D1	US	11/891,930	8/14/2007	7418514	8/26/2008	In Force	Multi-Speed Serial Interface For Media Access Control And Physical L
MP0290	MP0290D1aa	US	13/245,513	9/26/2011	8612629	12/17/2013	In Force	Multi-Speed Serial Interface For Media Access Control And Physical L
MP0290	MP0290D1C1	US	12/229,376	8/22/2008	8028095	9/27/2011	In Force	Multi-Speed Serial Interface For Media Access Control And Physical L
MP0290	MP0290D2	US	11/891,929	8/14/2007	7840709	11/23/2010	In Force	Multi-Speed Serial Interface For Media Access Control And Physical L
MP0291	MP0291	US	10/601,235	6/20/2003	7251699	7/31/2007	In Force	Low Overhead Coding With Arbitrary Control Symbol Placement
MP0291	MP0291C1	US	11/888,188	7/31/2007	7613838	11/3/2009	In Force	Low Overhead Coding With Arbitrary Control Code Placement
MP0293	MP0293	US	10/715,551	11/17/2003	7469049	12/23/2008	In Force	Data Dependent Scrambler With Reduced Overhead
MP0293	MP0293C1	US	12/342,263	12/23/2008	7697685	4/13/2010	In Force	Data Dependent Scrambler With Reduced Overhead
MP0294	MP0294	US	10/455,668	6/5/2003	6960686	5/31/2005	In Force	Analog Switching Circuit
MP0294	MP0294C1	US	11/073,806	3/7/2005	7012458	3/14/2006	In Force	Analog Switching Circuit
MP0294	MP0294C211	US	11/857,238	9/18/2007	7989752	2/15/2011	In Force	Dual Ported Network Physical Layer
MP0296	MP0296	US	10/679,030	10/3/2003	7308530	12/11/2007	In Force	Architecture For A Data Storage Device
MP0297	MP0297	US	10/658,666	9/9/2003	7706295	4/27/2010	In Force	Methods And Apparatus For Breaking And Resynchronizing A Data Lin
MP0297	MP0297C1	US	12/764,171	4/21/2010	8027270	9/27/2011	In Force	Methods And Apparatus For Breaking And Resynchronizing A Data Lin
MP0299	MP0299	US	10/634,218	8/4/2003	7486718	2/3/2009	In Force	ARCHITECTURES, CIRCUITS, SYSTEMS AND METHODS FOR REDUCING
MP0299	MP0299D1	US	12/330,218	12/8/2008	7835425	11/16/2010	In Force	ARCHITECTURES, CIRCUITS, SYSTEMS AND METHODS FOR REDUCING
MP0300	MP0300	US	10/702,744	11/5/2003	7571287	8/4/2009	In Force	Multiport Memory Architecture, Devices and Systems Including the S
MP0300	MP0300CN	CN	20041009497.1	3/12/2004	21.2004100456	1/28/2009	In Force	System structure with multiport memory, device, system and use me

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MP0300	MP0300D1	US	12/494,076	6/29/2009	8335878	12/18/2012	In Force	Multiport memory architecture, devices and systems including the sa
MP0300	MP0300D1aa	US	14/230,555	3/31/2014	9105319	8/11/2015	In Force	Multiport Memory Architecture
MP0300	MP0300D1C1	US	13/716,481	12/17/2012	868877	4/1/2014	In Force	Multiport memory architecture
MP0300	MP0300DE	DE	0406012.1	3/12/2004	1457993	9/8/2010	In Force	MULTIPORT MEMORY ARCHITECTURE, DEVICES AND SYSTEMS INCLU
MP0300	MP0300EP	EP	EP04006012.1	3/12/2004	EP1457993	9/8/2010	In Force	Multiport Memory Architecture, Devices and Systems Including the S
MP0300	MP0300FR	FR	0406012.1	3/12/2004	1457993	9/8/2010	In Force	MULTIPORT MEMORY ARCHITECTURE, DEVICES AND SYSTEMS INCLU
MP0300	MP0300GB	GB	0406012.1	3/12/2004	1457993	9/8/2010	In Force	MULTIPORT MEMORY ARCHITECTURE, DEVICES AND SYSTEMS INCLU
MP0300	MP0300JP	JP	2004-074574	3/12/2004			Pending	MULTIPORT MEMORY ARCHITECTURE DEVICES AND SYSTEMS INCLU
MP0300	MP0300JPD1	JP	2008-270813	10/21/2008	5167204	10/12/2012	In Force	MULTIPORT MEMORY ARCHITECTURE AND INTEGRATED CIRCUIT
MP0300	MP0300TW	TW	93106492	3/11/2004	1263228	10/1/2006	In Force	Multiport memory architecture, devices and systems including the sa
MP0301	MP0301	US	10/629,164	7/28/2003	7088534	8/8/2006	In Force	DISK SYNCHRONOUS WRITE
MP0302	MP0302	US	10/761,879	1/21/2004	7826452	11/2/2010	In Force	Efficient Host-Controller Address Learning In Ethernet Switches
MP0302	MP0302C1	US	12/917,405	11/1/2010	8472445	6/25/2013	In Force	Efficient Host-Controller Address Learning In Ethernet Switches
MP0302	MP0302C1C1	US	13/920,577	6/18/2013	9294397	3/22/2016	In Force	Apparatus And Method For Forwarding Packets Based On Approved
MP0303	MP0303	US	10/773,474	2/5/2004	7590114	9/15/2009	In Force	Efficient IP Multicast Bridging In Ethernet Switches
MP0303	MP0303C1	US	12/559,969	9/15/2009	7983262	7/19/2011	In Force	Efficient IP Multicast Bridging In Ethernet Switches
MP0303	MP0303C1aa	US	14/101,750	12/10/2013	9065662	6/23/2015	In Force	Method And Apparatus For Using A Bridge Table As A Routing Table T
MP0303	MP0303C1C1	US	13/185,231	7/18/2011	8665727	12/10/2013	In Force	Efficient IP Multicast Bridging In Ethernet Switches
MP0304	MP0304	US	10/790,689	3/3/2004	7563777	3/8/2011	In Force	System And Method For Reducing Electromagnetic Interference And
MP0304	MP0304C1	US	13/042,344	3/7/2011	8170167	5/1/2012	In Force	System And Method For Reducing Electromagnetic Interference And
MP0304	MP0304C1aa	US	14/149,308	1/7/2014	9031181	5/12/2015	In Force	Method And Apparatus For Controlling Cloaking Of Transceivers In A
MP0305	MP0305	US	10/464,278	6/17/2003	6850124	2/1/2005	In Force	CIRCUIT, ARCHITECTURE AND METHOD FOR TRACKING LOOP BAND
MP0306	MP0306I	US	11/070,481	3/2/2005	7454634	11/18/2008	In Force	Power Savings Apparatus And Method For Wireless Network Devices
MP0307	MP0307	US	10/623,031	7/17/2003	7535955	5/19/2009	In Force	METHODS, ALGORITHMS, SOFTWARE, ARCHITECTURES, SYSTEMS AN
MP0308	MP0308	US	10/786,998	2/27/2004	7505537	3/17/2009	In Force	System And Method For Controlling Gain And Timing Phase In A Pres
MP0308	MP0308aaaa	US	14/091,514	11/27/2013	9319024	4/19/2016	In Force	Apparatus And Method For Accounting For Gain And Timing Phase Er
MP0308	MP0308aaz2a	US	15/491,424	4/19/2017	10027515	7/17/2018	In Force	Apparatus And Method For Introducing Gain And Phase Offset Via A
MP0308	MP0308C1	US	12/372,832	2/18/2009	8081720	12/20/2011	In Force	System And Method For Controlling Gain And Timing Phase In A Pres
MP0308	MP0308C1C1	US	13/329,987	12/19/2011	8279984	10/2/2012	In Force	System And Method For Controlling Gain And Timing Phase In A Pres
MP0308	MP0308C1Z2	US	15/098,639	4/14/2016	9641359	5/2/2017	In Force	Apparatus And Method For Accounting For Gain And Phase Error Intr
MP0309	MP0309	US	10/671,030	9/24/2003	7199961	4/3/2007	In Force	Detecting Fly Height Of A Head Over A Storage Medium
MP0309	MP0309C1	US	11/656,734	1/22/2007	7359139	4/15/2008	In Force	Detecting Fly Height Of A Head Over A Storage Medium
MP0309	MP0309C1C1	US	12/082,423	4/11/2008	7518820	4/14/2009	In Force	Detecting Fly Height Of A Head Over A Storage Medium
MP0309	MP0309D1	US	11/103,748	4/12/2005	7136250	11/14/2006	In Force	Detecting Fly Height Of A Head Over A Storage Medium

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MP0310	MP0310D1	US	11/463,278	8/8/2006	7262928	8/28/2007	In Force	DC-OFFSET COMPENSATION LOOPS FOR MAGNETIC RECORDING SYS
MP0310	MP0310D1C1	US	11/836,050	8/8/2007	7525746	4/28/2009	In Force	DC-OFFSET COMPENSATION LOOPS FOR MAGNETIC RECORDING SYS
MP0310	MP0310D2	US	11/463,280	8/8/2006	7266312	10/23/2007	In Force	DC-OFFSET COMPENSATION LOOPS FOR MAGNETIC RECORDING SYS
MP0311	MP0311	US	10/664,166	8/20/2003	7002761	2/21/2006	In Force	Demodulation Compensation For Spiral Servo Tracks In Hard Disk Dr
MP0311	MP0311a-a	US	12/330,789	12/9/2008	7715134	5/11/2010	In Force	Demodulation Compensation For Spiral Servo Tracks In Hard Disk Dr
MP0311	MP0311C1	US	11/353,725	1/17/2006	7307806	12/11/2007	In Force	Demodulation Compensation For Spiral Servo Tracks In Hard Disk Dr
MP0311	MP0311C1C1	US	12/001,285	12/11/2007	7463437	12/9/2008	In Force	Demodulation Compensation For Spiral Servo Tracks In Hard Disk Dr
MP0317	MP0317	US	10/752,339	1/6/2004	7222485	6/5/2007	In Force	ERROR CORRECTION USING ERROR DETECTION CODES
MP0317	MP0317C1	US	11/828,169	7/25/2007	8069391	11/29/2011	In Force	Error Correction Using Error Detection Codes
MP0317	MP0317D1	US	11/785,538	4/18/2007	7380195	5/27/2008	In Force	ERROR CORRECTION USING ERROR DETECTION CODES
MP0317	MP0317D1C1	US	12/127,669	5/27/2008	8190963	5/29/2012	In Force	Error Correction Using Error Detection Codes
MP0319	MP0319	US	10/765,474	1/26/2004	7265448	9/4/2007	In Force	Interconnect Structure For Power Transistors
MP0319	MP0319AFb	US	12/156,196	5/30/2008	7989852	8/2/2011	In Force	Integrated Circuits And Interconnect Structure For Integrated Circuits
MP0319	MP0319AFcN	CN	200830008400	3/17/2008			Pending	INTEGRATED CIRCUITS AND INTERCONNECT STRUCTURE FOR INTEGR
MP0319	MP0319AFTW	TW	97109386	3/17/2008	1479634	4/1/2015	In Force	INTEGRATED CIRCUITS AND INTERCONNECT STRUCTURE FOR INTEGR
MP0319	MP0319AFWO	WO	PCT/US2008/00	3/17/2008			Pending	Integrated Circuits And Interconnect Structure For Integrated Circuits
MP0319	MP0319AFWOCN	CN	2008300084014	9/15/2009			Pending	Integrated Circuits And Interconnect Structure For Integrated Circuits
MP0319	MP0319C1HK	HK	61112711	10/13/2006	1090752	8/7/2009	In Force	Interconnect Structure With Wings
MP0319	MP0319C2HK	HK	61112720	1/12/2005			Pending	Interconnect Structure With Aluminum Core
MP0319	MP0319C2TW	TW	94115682	1/12/2005	1354368	12/11/2011	In Force	Interconnect Structure With Aluminum Core
MP0319	MP0319CN	CN	2005100020307	1/12/2005	212005100020	11/19/2008	In Force	Integrated Circuit With Planar Connections
MP0319	MP0319CND1	CN	2005100026500	1/12/2005	212005100026	12/3/2008	In Force	Interconnect Structure With Wings
MP0319	MP0319D1	US	11/386,276	3/22/2006	7459381	12/2/2008	In Force	Integrated Circuits And Interconnect Structure For Integrated Circuits
MP0319	MP0319D311	US	12/049,533	3/17/2008	7960833	6/14/2011	In Force	Integrated Circuits And Interconnect Structure For Integrated Circuits
MP0319	MP0319EP	EP	05000284.9	1/26/2005	1571708	6/19/2019	In Force	Integrated Circuits and Interconnect Structure for Integrated Circuits
MP0319	MP0319EPDE	DE	05000284.9	1/26/2005	602005055921	6/19/2019	In Force	Integrated Circuits and Interconnect Structure for Integrated Circuits
MP0319	MP0319EPFR	FR	05000284.9	1/26/2005	1571708	6/19/2019	In Force	Integrated Circuits and Interconnect Structure for Integrated Circuits
MP0319	MP0319EPGB	GB	05000284.9	1/26/2005	1571708	6/19/2019	In Force	Integrated Circuits and Interconnect Structure for Integrated Circuits
MP0319	MP031912CN	CN	2005100026483	1/12/2005			Pending	Interconnect Structure With Aluminum Core
MP0319	MP031912EP	EP	06011396.6	1/7/2005			Pending	Integrated circuits and interconnect structure for integrated circuits
MP0319	MP03191CTW	TW	94115698	1/12/2005	1354369	12/11/2011	In Force	Interconnect Structure With Wings
MP0319	MP03191EP	EP	EP06011395.6	1/7/2005			Pending	Integrated circuits and interconnect structure for integrated circuits
MP0319	MP0319JP	JP	2005-006181	1/13/2005	4586925	10/1/2010	In Force	INTEGRATED CIRCUITS AND INTERCONNECT STRUCTURE FOR INTEGR
MP0319	MP0319JP2	JP	2005-276911	9/22/2005	5502255	3/20/2014	In Force	INTERCONNECT STRUCTURE WITH WINGS

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MP0319	MP0319JP3	JP	2005-276912	9/22/2005			Pending	INTERCONNECT STRUCTURE WITH ALUMINUM CORE
MP0319	MP0319TW	TW	94100907	1/12/2005	1354367	12/11/2011	In Force	Integrated Circuits And Interconnect Structure For Integrated Circuits
MP0320	MP0320aFHy	TW	96116400	5/8/2007	1420665	12/21/2013	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320aFcy	TW	96116397	5/8/2007	1407566	9/1/2013	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320AFUN	DE	07809055.2	5/8/2007	2030237	2/9/2011	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320AFUR	FR	07809055.2	5/8/2007	2030237	2/9/2011	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320AFUT	GB	07809055.2	5/8/2007	2030237	2/9/2011	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320aFW1	JP	2009-508936	5/8/2007	5137947	11/22/2012	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320AFWS	SG	200808294-3	5/8/2007	147678	7/15/2011	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320aFWU	EP	EP07809055.2	5/8/2007	EP2030237	2/9/2011	In Force	Efficient Transistor Structure
MP0320	MP0320aFy	TW	96116398	5/8/2007	1429078	3/1/2014	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320a11a	US	11/586,467	10/25/2006	7528444	5/5/2009	In Force	Efficient Transistor Structure
MP0320	MP0320a11aTW	TW	96116401	5/8/2007			Pending	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320a11c	US	11/586,471	10/25/2006	7652338	1/26/2010	In Force	Efficient Transistor Structure
MP0320	MP0320C111	US	11/524,113	9/20/2006	7851872	12/14/2010	In Force	Efficient Transistor Structure
MP0320	MP0320C111WO	WO	PCT/US2007/01	5/8/2007			Pending	Efficient Transistor Structure
MP0320	MP0320C111WOCN	CN	200780025919.4	5/8/2007	212007800259	1/26/2011	In Force	Efficient Transistor Structure
MP0320	MP0320C111WOKR	KR	1020687028444	5/8/2007	101373792	3/6/2014	In Force	Efficient Transistor Structure
MP0320	MP0320C111WOSG	SG	2008082943	11/7/2008	147678	7/15/2011	In Force	Efficient Transistor Structure
MP0320	MP0320CN	CN	2004100865328	10/21/2004	212004100865	11/25/2009	In Force	Efficient Transistor Structure
MP0320	MP0320P	JP	2004-238034	8/18/2004	4667788	1/21/2011	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320TW	TW	93119017	6/29/2004	1349978	10/1/2011	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0329	MP0329	US	10/701,626	11/6/2003	7796952	9/14/2010	In Force	Transceiver System Including Dual Low-Noise Amplifiers
MP0329	MP0329C1	US	12/881,226	9/14/2010	8027644	9/27/2011	In Force	TRANSCIVER SYSTEM INCLUDING DUAL LOW-NOISE AMPLIFIERS.
MP0330	MP0330D1	US	11/487,040	7/14/2006	7877075	1/25/2011	In Force	Signal Mixer Having A Single-Ended Input And A Differential Output
MP0330	MP0330D1C1	US	13/012,624	1/24/2011	8577322	11/5/2013	In Force	Signal Mixer Having A Single-Ended Input And A Differential Output
MP0331	MP0331	US	10/753,101	1/6/2004	7184799	2/27/2007	In Force	METHOD AND APPARATUS FOR REDUCING WAKE UP TIME OF A PO
MP0331	MP0331C1	US	11/546,605	10/11/2006	8359074	1/22/2013	In Force	METHOD AND APPARATUS FOR REDUCING WAKE UP TIME OF A PO
MP0331	MP0331C1C1	US	13/745,727	1/18/2013	8781539	7/15/2014	In Force	METHOD AND APPARATUS FOR REDUCING WAKE UP TIME OF A PO
MP0332	MP0332	US	10/673,263	9/30/2003	7433658	10/7/2008	In Force	System And Method For Controlling True Output Power Of A Transmi
MP0332	MP0332C1	US	12/267,194	10/7/2008	8099066	1/17/2012	In Force	System And Method For Controlling True Output Power Of A Transmi
MP0336	MP0336	US	10/714,804	11/17/2003	7475331	1/6/2009	In Force	DATA DEPENDENT SCRAMBLER WITH IMPROVED GLOBAL CONSTRAI
MP0337	MP0337	US	10/693,566	10/23/2003	7606316	10/20/2009	In Force	NAIMO-OFDM PREAMBLE FOR CHANNEL ESTIMATION
MP0342	MP0342	US	10/670,022	9/23/2003	7561590	7/14/2009	In Force	Network Switch Having Virtual Input Queues For Flow Control

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MP0342	MP0342C1	US	12/502,046	7/13/2009	8005079	8/23/2011	In Force	Network Switch Having Virtual Input Queues For Flow Control
MP0342	MP0342C1aa	US	14/164,597	1/27/2014	8929363	1/6/2015	In Force	Apparatus And Method For Allocating Buffers Of A Memory Including
MP0342	MP0342C1C1	US	13/215,106	8/22/2011	8638784	1/28/2014	In Force	Network Switch Having Virtual Input Queues For Flow Control
MP0343	MP0343	US	10/669,783	9/23/2003	7688736	3/30/2010	In Force	NETWORK SWITCH WITH QUALITY OF SERVICE FLOW CONTROL
MP0344	MP0344	US	10/752,785	1/5/2004	8319524	11/27/2012	In Force	Deglitch Circuit Removing Glitches From Input Clock Signal
MP0347	MP0347	US	10/825,781	4/16/2004	7239199	7/3/2007	In Force	AMPLIFIER CALIBRATION
MP0348	MP0348C1	US	11/189,464	7/26/2005	7015756	3/21/2006	In Force	Push-Pull Buffer/Amplifier
MP0349	MP0349	US	10/747,520	12/29/2003	7365582	4/29/2008	In Force	Bootstrapped Charge Pump Driver In A Phase-Lock Loop
MP0350	MP0350C1	US	11/717,576	3/13/2007	7561907	3/10/2009	In Force	Self-Biased Active VCO Level Shifter
MP0351	MP0351	US	10/776,476	2/10/2004	7444124	10/28/2008	In Force	ADJUSTABLE SEGMENTED POWER AMPLIFIER
MP0351	MP0351C1	US	12/212,598	9/17/2008	7863976	1/4/2011	In Force	ADJUSTABLE SEGMENTED POWER AMPLIFIER
MP0351	MP0351C1C1	US	12/984,477	1/4/2011	8044716	10/25/2011	In Force	ADJUSTABLE SEGMENTED POWER AMPLIFIER
MP0352	MP0352	US	10/694,306	10/27/2003	6950300	9/27/2005	In Force	Ultra Low Inductance Multi-Layer Ceramic Capacitor
MP0352	MP0352C1	US	11/184,308	7/19/2005	7230816	6/12/2007	In Force	Ultra Low Inductance Multi-Layer Ceramic Capacitor
MP0352	MP0352C1C1	US	11/811,881	6/12/2007	7701695	4/20/2010	In Force	Ultra Low Inductance Multi-Layer Ceramic Capacitor
MP0352	MP0352CN	CN	2004100310216	4/7/2004	212004100310	5/12/2010	In Force	Ultra Low Inductance Multi-Layer Ceramic Capacitor
MP0352	MP0352EP	EP	EP04010108.1	4/28/2004	EP1480236	6/8/2016	In Force	Ultra Low Inductance Multi-Layer Ceramic Capacitor
MP0352	MP0352EPDE	DE	04010108.1	4/28/2004	6020040941E	6/8/2016	In Force	Ultra Low Inductance Multi-Layer Ceramic Capacitor
MP0352	MP0352EPFR	FR	04010108.1	4/28/2004	1480236	6/8/2016	In Force	Ultra Low Inductance Multi-Layer Ceramic Capacitor
MP0352	MP0352EPGB	GB	04010108.1	4/28/2004	1480236	6/8/2016	In Force	Ultra Low Inductance Multi-Layer Ceramic Capacitor
MP0352	MP0352JP	JP	2004.134183	4/28/2004	4045258	11/22/2007	In Force	ULTRA LOW INDUCTANCE MULTI LAYER CERAMIC CAPACITOR
MP0352	MP0352JP01	JP	2007-206337	8/8/2007	4498397	4/23/2010	In Force	ULTRA LOW INDUCTANCE MULTI LAYER CERAMIC CAPACITOR
MP0352	MP0352TW	TW	93110964	4/20/2004	1338903	3/11/2011	In Force	ULTRA LOW INDUCTANCE MULTI LAYER CERAMIC CAPACITOR
MP0354	MP0354	US	10/743,941	12/23/2003	7545867	6/9/2009	In Force	Adaptive Channel Bandwidth Selection For MIMO Wireless Systems
MP0354	MP0354C1	US	12/480,179	6/8/2009	7688898	3/30/2010	In Force	Adaptive Channel Bandwidth Selection For MIMO Wireless Systems
MP0354	MP0354C1aa	US	13/567,599	8/6/2012	8634483	1/21/2014	In Force	Operating Environment Based Wireless Communication Devices And
MP0354	MP0354C1C1	US	12/748,911	3/29/2010	8238453	8/7/2012	In Force	Adaptive Channel Bandwidth Selection For MIMO Wireless Systems
MP0354	MP0354C12a	US	14/159,541	1/21/2014	9042466	5/26/2015	In Force	Apparatus And Method For Adjusting Bandwidth Based On Operat
MP0355	MP0355	US	10/621,297	7/17/2003	7321593	1/22/2008	In Force	Fibre Channel Serdes With Elastic Transmitter FIFO
MP0356	MP0356	US	10/795,907	3/8/2004	6933712	9/13/2005	In Force	Voltage Built-In Real-Time Digital Non-Linearity Measurement Device
MP0356	MP0356C1	US	11/190,781	7/27/2005	6999013	2/14/2006	In Force	Built-In Real-Time Digital Non-Linearity Measurement Device And Me
MP0357	MP0357	US	10/627,269	7/25/2003	7437532	10/14/2008	In Force	Memory Mapped Register File
MP0357	MP0357C1C1	US	13/017,867	1/31/2011	8078828	12/13/2011	In Force	Memory Mapped Register File
MP0359	MP0359	US	10/728,368	12/15/2003	7181180	2/20/2007	In Force	SIGMA DELTA MODULATED PHASE LOCK LOOP WITH PHASE INTERP

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP03359	MP03359C1	US	11/636,829	1/24/2007	7899422	3/1/2011	In Force	SIGMA DELTA MODULATED PHASE LOCK LOOP WITH PHASE INTERP
MP03360	MP03360	US	10/747,522	12/29/2003	7196936	3/13/2007	In Force	Voltage Regulator For High Performance RF Systems
MP03360	MP03360C1	US	11/745,027	3/7/2007	7809339	10/5/2010	In Force	Voltage Regulator For High Performance RF Systems
MP03360	MP03360C1Aa	US	13/709,627	12/10/2012	8659201	1/28/2014	In Force	Voltage Regulator For High Performance RF Systems
MP03360	MP03360C1C1	US	12/893,604	9/29/2010	8331884	12/11/2012	In Force	Voltage Regulator For High Performance RF Systems
MP03361	MP03361	US	10/753,007	1/6/2004	6985033	1/10/2006	In Force	CIRCUITS, SOFTWARE AND METHODS FOR ADJUSTING POWER AMPL
MP03362	MP03362	US	10/767,045	1/28/2004	7268990	9/11/2007	In Force	POWER AMPLIFIER PROTECTION
MP03362	MP03362C1	US	11/845,660	8/27/2007	7666016	10/20/2009	In Force	POWER AMPLIFIER PROTECTION
MP03362	MP03362C1a8	US	12/965,223	12/10/2010	8179648	5/15/2012	In Force	POWER AMPLIFIER PROTECTION
MP03362	MP03362C1C1	US	12/560,397	9/15/2009	7864498	1/4/2011	In Force	POWER AMPLIFIER PROTECTION
MP03364	MP03364C1	US	11/699,648	1/30/2007	7459975	12/2/2008	In Force	Baseband Filter Start-Up Circuit
MP03364	MP03364D1	US	11/689,890	1/30/2007	7528657	5/5/2009	In Force	Baseband Filter Start-Up Circuit
MP03364	MP03364D1C1	US	12/430,305	4/27/2009	7809317	10/5/2010	In Force	Baseband Filter Start-Up Circuit
MP03365	MP03365C1D1	US	13/154,305	6/6/2011	8731122	5/20/2014	In Force	SPURIOUS COMPONENT REDUCTION
MP03366	MP03366	US	10/861,481	6/7/2004	7167045	1/23/2007	In Force	System And Method For Modifying Output Power Of An Information
MP03366	MP03366C1	US	11/656,576	1/23/2007	7808322	10/5/2010	In Force	System And Method For Modifying Output Power Of An Information
MP03367	MP03367	US	10/672,774	9/26/2003	7096345	8/22/2006	In Force	DATA PROCESSING SYSTEM WITH BYPASS REORDER BUFFER HAVING
MP03367	MP03367C1	US	11/497,805	8/2/2006	7730285	6/1/2010	In Force	DATA PROCESSING SYSTEM WITH PARTIAL BYPASS REORDER BUFFER
MP03368	MP03368	US	10/752,601	1/6/2004	7378810	5/27/2008	In Force	METHOD AND APPARATUS FOR SPINDLE MOTOR CONTROL
MP03368	MP03368C2	US	12/098,008	4/4/2008	7573214	8/11/2009	In Force	METHOD AND APPARATUS FOR SPINDLE MOTOR CONTROL
MP03368	MP03368C2C1	US	12/494,868	6/30/2009	8022646	9/20/2011	In Force	METHOD AND APPARATUS FOR SPINDLE MOTOR CONTROL
MP03369	MP03369	US	10/701,661	11/5/2003	6837792	5/24/2005	In Force	REDUCING NUMBER OF CONSECUTIVE ONES IN DATA-DEPENDENT S
MP03376	MP03376	US	10/734,779	12/12/2003	6940107	9/6/2005	In Force	FUSE STRUCTURES, METHODS OF MAKING AND USING THE SAME, A
MP03376	MP03376D1	US	11/136,925	5/24/2005	7344924	3/18/2008	In Force	FUSE STRUCTURES, METHODS OF MAKING AND USING THE SAME, A
MP03376	MP03376D1C1	US	12/012,723	2/4/2008	7704805	4/27/2010	In Force	FUSE STRUCTURES, METHODS OF MAKING AND USING THE SAME, A
MP03376	MP03376D1D1	US	12/012,724	2/4/2008	7820493	10/26/2010	In Force	METHODS OF MAKING AND USING FUSE STRUCTURES, AND INTEGRA
MP03377	MP03377	US	10/829,866	4/21/2004	7706363	4/27/2010	In Force	METHOD AND APPARATUS FOR MANAGING PACKETS IN A PACKET S
MP03377	MP03377C1	US	12/753,808	4/2/2010	8144706	3/27/2012	In Force	METHOD AND APPARATUS FOR MANAGING PACKETS IN A PACKET S
MP03377	MP03377C1C1	US	13/415,557	3/8/2012	8923297	12/30/2014	In Force	METHOD AND APPARATUS FOR MANAGING PACKETS IN A PACKET S
MP03378	MP03378	US	10/752,817	1/6/2004	7330320	2/12/2008	In Force	METHOD AND APPARATUS TO LIMIT DC-LEVEL IN CODED DATA
MP03378	MP03378D1	US	12/022,131	1/29/2008	8058447	1/17/2012	In Force	METHOD AND APPARATUS TO LIMIT DC-LEVEL IN CODED DATA
MP03378	MP03378D1a8	US	13/745,694	1/18/2013	8773786	7/8/2014	In Force	SYSTEM FOR CORRECTING DC CHARACTERISTICS OF A MAGNETIC RE
MP03378	MP03378D1C1	US	13/350,684	1/13/2012	8358479	1/22/2013	In Force	METHOD AND APPARATUS TO LIMIT DC-LEVEL IN CODED DATA
MP03378	MP03378D12a	US	14/321,641	7/1/2014	9165599	10/20/2015	In Force	METHOD FOR CORRECTING DC CHARACTERISTICS IN A MAGNETIC RE

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0382	MP0382	US	10/818,327	4/5/2004	6972921	12/6/2005	In Force	Circuit And Method For Protecting Emergency Head-Retract
MP0382	MP0382aaa	US	12/287,899	10/13/2008	7623316	11/24/2009	In Force	Circuit And Method For Protecting Emergency Head-Retract Capacito
MP0382	MP0382C2	US	11/465,597	7/12/2006	7436618	10/14/2008	In Force	Circuit And Method For Protecting Emergency Head-Retract Capacito
MP0386	MP0386	US	10/796,599	3/9/2004	7647507	1/12/2010	In Force	Secure Digital Content Distribution System And Secure Hard Drive
MP0386	MP0386C1	US	12/685,344	1/11/2010	8230236	7/24/2012	In Force	Secure Digital Content Distribution System And Secure Hard Drive
MP0386	MP0386C1aa	US	14/159,505	1/21/2014	9009497	4/14/2015	In Force	Secure Methods For Generating Content And Operating A Drive Base
MP0386	MP0386C1C1	US	13/555,432	7/23/2012	8635466	1/21/2014	In Force	Secure Digital Content Distribution System And Secure Hard Drive
MP0388	MP0388	US	10/621,128	7/16/2003	7023313	4/4/2006	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388C1	US	11/274,360	11/15/2005	8035471	10/11/2011	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388CN	CN	2004100065182	3/4/2004	ZL2004100065	10/28/2009	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388EP	EP	EP04010841.7	5/6/2004	EP1498914	9/7/2016	In Force	Power inductor with reduced DC current saturation
MP0388	MP0388EP	EP	04760500.1	3/24/2004	1625666	8/11/2018	In Force	Dual Antenna System Having One Phase Lock Loop
MP0388	MP0388EP3	EP	EP04020568.4	8/30/2004	EP1548763	8/16/2017	In Force	Power inductor with Reduced DC Current Saturation
MP0388	MP0388EPDE	DE	04010841.7	5/6/2004	602004049893	9/7/2016	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388EPFR	FR	04010841.7	5/6/2004	1498914	9/7/2016	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388EPGB	GB	04010841.7	5/6/2004	1498914	9/7/2016	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388EPD1	US	11/728,112	3/23/2007	7868725	1/11/2011	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388EPD2	US	11/728,064	3/23/2007	7987580	8/2/2011	In Force	A METHOD OF FABRICATING CONDUCTOR CROSSOVER STRUCTURE F
MP0388	MP0388EPDE	DE	04020571.8	8/30/2004	602004049820	8/24/2016	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388EPFR	FR	04020571.8	8/30/2004	1548764	8/24/2016	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388EPGB	GB	04020571.8	8/30/2004	1548764	8/24/2016	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388EPJ2	JP	2005-183998	9/1/2004	4732811	4/28/2011	In Force	POWER INDUCTOR WITH REDUCED DC CURRENT SATURATION
MP0388	MP0388EP	US	10/744,416	12/22/2003	7489219	2/10/2009	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388I2	US	10/875,903	6/24/2004	7307502	12/11/2007	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388I2CN	CN	2004100741664	9/1/2004	ZL2004100741	8/24/2011	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388I2D2	US	11/367,516	3/3/2006	7218197	5/15/2007	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388I2D3	US	11/367,176	3/3/2006	8028401	10/4/2011	In Force	A METHOD OF FABRICATING A CONDUCTING CROSSOVER STRUCTUR
MP0388	MP0388I2EP	EP	04020571.8	8/30/2004	EP1548764	8/24/2016	In Force	Power inductor with Reduced DC Current Saturation
MP0388	MP0388I2JP	JP	2004254991	9/1/2004			Pending	Power inductor With Reduced DC Current Saturation
MP0388	MP0388I2JP02	JP	2004-254991	11/1/2004			Pending	POWER INDUCTOR WITH REDUCED DC CURRENT SATURATION
MP0388	MP0388I2TW	TW	93127467	9/10/2004	1401710	7/11/2013	In Force	Power inductor With Reduced DC Current Saturation, DC/DC Convert
MP0388	MP0388I3CN	CN	2004100738002	9/1/2004			Pending	Power inductor With Reduced DC Current Saturation
MP0388	MP0388I3TW	TW	93127468	9/10/2004	1332720	11/11/2010	In Force	POWER INDUCTOR WITH REDUCED DC CURRENT SATURATION
MP0388	MP0388I3CN	CN	2004100381809	5/11/2004			Pending	Power inductor With Reduced DC Current Saturation

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0388	MP0388ID1	US	11/327,100	1/6/2006	8089123	1/17/2012	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388ID2	US	11/327,065	1/6/2006	7849586	12/14/2010	In Force	A METHOD OF MAKING A POWER INDUCTOR WITH REDUCED DC CU
MP0388	MP0388IEP	EP	04011558.6	5/14/2004	EP1498915	8/5/2015	In Force	Power inductor with reduced DC current saturation
MP0388	MP0388JP	JP	2004178924	6/16/2004			Pending	Power inductor With Reduced DC Current Saturation
MP0388	MP0388ITW	TW	93116550	6/9/2004	1401711	7/11/2013	In Force	Power inductor With Reduced DC Current Saturation And System Co
MP0388	MP0388IUE	DE	04011558.6	5/14/2004	602004097604	8/5/2015	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388IUF	FR	04011558.6	5/14/2004	1468915	8/5/2015	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388IUG	GB	04011558.6	5/14/2004	1468915	8/5/2015	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388JP	JP	2004.146964	5/17/2004	4473040	3/12/2010	In Force	POWER INDUCTOR WITH REDUCED DC CURRENT SATURATION
MP0388	MP0388TW	TW	93108084	3/25/2004	1333219	11/11/2010	In Force	POWER INDUCTOR WITH REDUCED DC CURRENT SATURATION
MP0388	MP0388U3DE	DE	04020568.4	8/30/2004	602004051655	8/16/2017	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388U3FR	FR	04020568.4	8/30/2004	1548763	8/16/2017	In Force	Power inductor With Reduced DC Current Saturation
MP0388	MP0388U3GB	GB	04020568.4	8/30/2004	1548763	8/16/2017	In Force	Power inductor With Reduced DC Current Saturation
MP0389	MP0389	US	10/734,440	12/11/2003	7864678	1/4/2011	In Force	RATE ADAPTATION IN WIRELESS SYSTEMS
MP0389	MP0389C1C1	US	14/244,750	4/3/2014	9271192	2/23/2016	In Force	RATE ADAPTATION IN WIRELESS SYSTEMS
MP0392	MP0392	US	10/701,491	11/5/2003	7161752	1/9/2007	In Force	ASYMMETRIC COMPENSATION CIRCUIT
MP0393	MP0393	US	10/666,892	9/17/2003	7568083	7/28/2009	In Force	Memory Mapped Register File And Method For Accessing The Same
MP0393	MP0393D1	US	11/339,049	1/25/2006	7558942	7/7/2009	In Force	Memory Mapped Register File And Method For Accessing The Same
MP0394	MP0394	US	10/744,982	12/12/2003	7737666	6/15/2010	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394CN	CN	200410042854.1	5/26/2004	Z1 2004100421	5/26/2010	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394CND1	CN	200910221130.1	5/26/2004	Z1 200910221.1	3/13/2013	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394DE	DE	04009304.9	4/20/2004	1505716	11/3/2010	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394EP	EP	EP04009304.9	4/20/2004	EP1505716	11/3/2010	In Force	Split Gate Drive Scheme to improve reliable voltage operation Rang
MP0394	MP0394FR	FR	04009304.9	4/20/2004	1505716	11/3/2010	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394GB	GB	04009304.9	4/20/2004	1505716	11/3/2010	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394JP	JP	2004.114712	4/8/2004	3774220	2/24/2006	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394TW	TW	93111803	4/28/2004	1282203	6/1/2007	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0395	MP0395	US	10/722,079	11/25/2003	7477467	1/13/2009	In Force	Preamplifier Circuit including A Loopback Mode For Data Storage
MP0395	MP0395AAaa	US	13/767,176	2/14/2013	8514508	8/20/2013	In Force	Preamplifier Circuit including A Loopback Mode For Data Storage Devices
MP0395	MP0395AAZZ	US	14/181,864	2/17/2014	8947808	2/3/2015	In Force	Preamplifier Circuit including A Loopback Mode For Data Storage Devices
MP0395	MP0395D1	US	12/341,021	12/12/2008	7852585	12/14/2010	In Force	Preamplifier Circuit including A Loopback Mode For Data Storage Devices
MP0395	MP0395D1Aa	US	13/323,764	12/12/2011	8379341	2/19/2013	In Force	Preamplifier Circuit including A Loopback Mode For Data Storage Devices
MP0395	MP0395D1D1	US	12/966,099	12/13/2010	8077415	12/13/2011	In Force	Preamplifier Circuit including A Loopback Mode For Data Storage Devices
MP0396	MP0396	US	10/767,067	1/28/2004	7623441	11/24/2009	In Force	SCALABLE SPACE-FREQUENCY CODING FOR MIMO SYSTEMS

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0396	MP0396C1	US	12/616,753	11/11/2009	8159930	4/17/2012	In Force	SCALABLE SPACE-FREQUENCY CODING FOR MIMO SYSTEMS
MP0399	MP0399C1	US	11/513,423	9/19/2006	7262665	8/28/2007	In Force	Active Bias Circuit for Low-Noise Amplifiers
MP0399	MP0399C1C1	US	11/895,973	8/28/2007	7515000	4/7/2009	In Force	Active Bias Circuit for Low-Noise Amplifiers
MP0400	MP0400	US	10/693,787	10/24/2003	7766525	7/20/2010	In Force	Voltage Regulator
MP0400	MP0400CN	CN	2004100794322	8/20/2004	Z12004100794	6/24/2009	In Force	Voltage Regulator
MP0400	MP0400CN2	CN	2004100794337	8/20/2004	Z12004100794	9/22/2010	In Force	Voltage Regulator
MP0400	MP0400EP	EP	EP20040019978	8/23/2004			Pending	Digit Low Dropout Regulator
MP0400	MP0400EP1	EP	EP20040019981	8/23/2004			Pending	Voltage Regulator
MP0400	MP0400EP2	EP	EP10174008.2	8/23/2004	2254226	4/4/2018	In Force	Voltage Regulator
MP0400	MP0400EP3	EP	07004458.1	8/23/2004			Pending	Voltage Regulator
MP0400	MP0400EP4	EP	70049606	3/9/2007	1868952	4/16/2014	In Force	Voltage Regulator
MP0400	MP0400EP5	EP	10177633.4	8/23/2004			Pending	Digit Low Dropout Regulator
MP0400	MP0400FUDF	DE	04019963.0	8/23/2004	60200403019.1	11/24/2010	In Force	Voltage Regulator
MP0400	MP0400FULFR	FR	04019963.0	8/23/2004	1508958	11/24/2010	In Force	Voltage Regulator
MP0400	MP0400FULGB	GB	04019963.0	8/23/2004	1508958	11/24/2010	In Force	Voltage Regulator
MP0400	MP0400I	US	10/754,187	1/8/2004	7872454	1/18/2011	In Force	Digital Low Dropout Regulator
MP0400	MP0400I1C1	US	12/838,679	7/19/2010	8299763	10/30/2012	In Force	Digital Low Dropout Regulator
MP0400	MP0400ICN	CN	2004100794981	8/20/2004	Z12004100794	12/23/2009	In Force	Digital Low Dropout Regulator
MP0400	MP0400IEP	EP	04019963.0	8/23/2004	EP1508958	11/24/2010	In Force	Voltage Regulator
MP0400	MP0400IIP	JP	2004-243036	8/23/2004	4435647	6/11/2010	In Force	VOLTAGE REGULATOR
MP0400	MP0400IIP1	JP	2004-242998	8/23/2004	4527470	8/18/2010	In Force	DIGITAL LOW DROPOUT REGULATOR
MP0400	MP0400ITW	TW	93125139	8/20/2004	1373697	10/1/2012	In Force	Digital Low Dropout Regulator
MP0400	MP0400JP	JP	2005-338676	11/24/2005	4435731	1/8/2010	In Force	VOLTAGE REGULATOR
MP0400	MP0400ITW	TW	93125134	8/20/2004	1363263	5/1/2012	In Force	Coupled Inductor Regulator for Amplifier System, Vehicle System, Dri
MP0400	MP0400TW2	TW	93125133	8/20/2004	1361342	4/1/2012	In Force	Voltage Regulator
MP0400	MP0400U2DE	DE	EP10174008.2	8/23/2004	6020040525564	4/4/2018	In Force	Voltage Regulator
MP0400	MP0400U2FR	FR	EP10174008.2	8/23/2004	2254226	4/4/2018	In Force	Voltage Regulator
MP0400	MP0400U2GB	GB	EP10174008.2	8/23/2004	2254226	4/4/2018	In Force	Voltage Regulator
MP0400	MP0400U4DE	DE	07004960.6	8/23/2004	1808952	4/16/2014	In Force	VOLTAGE REGULATOR
MP0400	MP0400U4FR	FR	07004960.6	8/23/2004	1808952	4/16/2014	In Force	VOLTAGE REGULATOR
MP0400	MP0400U4GB	GB	07004960.6	8/23/2004	1808952	4/16/2014	In Force	VOLTAGE REGULATOR
MP0401	MP0401C1	US	11/170,926	6/30/2005	7030685	4/18/2006	In Force	Frequency Boosting Circuit For High Swing Cascode Biasing Circuits
MP0402	MP0402	US	10/789,306	2/27/2004	7049894	5/23/2006	In Force	Abuja Compensation Circuit With Enhanced Bandwidth
MP0403	MP0403	US	10/804,660	3/19/2004	7266001	4/9/2007	In Force	METHOD AND APPARATUS FOR CONTROLLING POWER FACTOR COR

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0403	MP0403C11	US	12/100,345	4/9/2008	7990740	8/2/2011	In Force	Method And Apparatus For Controlling Power Factor Correction
MP0403	MP0403B1	US	11/839,158	8/31/2007	7733678	8/6/2010	In Force	POWER FACTOR CORRECTION BOOST CONVERTER WITH CONTINUOUS
MP0405	MP0405	US	10/665,252	9/19/2003	7457271	11/25/2008	In Force	WIRELESS LOCAL AREA NETWORK AD-HOC MODE FOR REDUCING POWER
MP0405	MP0405C1	US	12/269,413	11/12/2008	8265048	9/11/2012	In Force	Wireless Local Area Network Ad-Hoc Mode For Reducing Power Cons
MP0405	MP0405C1C1	US	13/608,496	9/10/2012	8902872	12/2/2014	In Force	Wireless Local Area Network Ad-Hoc Mode For Reducing Power Cons
MP0407	MP0407	US	10/795,023	3/5/2004	7088181	8/8/2006	In Force	METHOD AND APPARATUS FOR COMMON MODE CONTROL
MP0407	MP0407C1	US	11/444,240	5/30/2006	7345544	3/13/2008	In Force	METHOD AND APPARATUS FOR COMMON MODE CONTROL
MP0408	MP0408	US	10/792,619	3/3/2004	7276969	10/2/2007	In Force	Multi-Amplifier Circuit
MP0408	MP0408C1	US	11/904,765	9/28/2007	7501890	3/10/2009	In Force	Multi-Amplifier Circuit
MP0408	MP0408D1	US	11/904,365	9/27/2007	7471149	12/30/2008	In Force	Multi-Amplifier Circuit
MP0408	MP0408FAA	US	12/286,772	10/2/2008	7688141	3/30/2010	In Force	Multi-Amplifier Circuit
MP0408	MP0408B1	US	11/504,348	8/15/2006	7312659	12/25/2007	In Force	Multi-Amplifier Circuit
MP0408	MP0408B1D1	US	12/004,126	12/20/2007	7468633	12/23/2008	In Force	Multi-Amplifier Circuit
MP0409	MP0409C1	US	11/511,138	8/28/2006	7411755	8/12/2008	In Force	Write Driver System For Data Storage Systems
MP0410	MP0410	US	10/703,135	11/6/2003	7190224	3/13/2007	In Force	Class D Amplifier
MP0410	MP0410aaa	US	11/985,653	11/16/2007	7468631	12/23/2008	In Force	Class D Amplifier
MP0410	MP0410aaaa	US	12/342,768	12/23/2008	7646240	1/12/2010	In Force	Class D Amplifier
MP0410	MP0410C1	US	11/170,440	6/28/2005	7232248	7/10/2007	In Force	Class D Amplifier
MP0410	MP0410C1C1	US	11/702,728	2/6/2007	7298209	11/20/2007	In Force	Class D Amplifier
MP0410	MP0410CN	CN	2004100704773	8/3/2004	212004100704	7/1/2009	In Force	Class D Amplifier
MP0410	MP0410EP	EP	EP04011557.8	5/14/2004	EP1530286	1/10/2007	In Force	CLASS D AMPLIFIER
MP0410	MP0410EPDE	DE	04011557.8	5/14/2004	602004004230	1/10/2007	In Force	Class D Amplifier
MP0410	MP0410EPRR	FR	04011557.8	5/14/2004	1530286	1/10/2007	In Force	Class D Amplifier
MP0410	MP0410EPGB	GB	04011557.8	5/14/2004	1530286	1/10/2007	In Force	Class D Amplifier
MP0410	MP0410JP	JP	2004-231068	8/6/2004	4015648	9/21/2007	In Force	CLASS D AMPLIFIER
MP0410	MP0410TW	TW	93118369	6/24/2004	1337805	2/21/2011	In Force	CLASS D AMPLIFIER
MP0410	MP0410Zaaa	US	12/685,884	1/12/2010	7984670	2/8/2011	In Force	Class D Amplifier
MP0412	MP0412	US	10/818,395	4/2/2004	8193686	6/12/2012	In Force	Wireless Local Area Network Infrastructure Mode For Reducing Power
MP0412	MP0412D1	US	13/493,210	6/11/2012	8842591	9/23/2014	In Force	Wireless Local Area Network Infrastructure Mode For Reducing Power
MP0414	MP0414	US	10/794,733	3/5/2004	7203017	4/10/2007	In Force	TIMING RECOVERY FOR DATA STORAGE CHANNELS WITH BUFFERED
MP0415	MP0415	US	10/862,414	6/8/2004	7460326	12/2/2008	In Force	Propagation Self Servo Write System And Method For Storage Device
MP0415	MP0415C1C1	US	12/945,204	11/12/2010	8084404	1/10/2012	In Force	Propagation Self Servo Write System And Method For Storage Device
MP0416	MP0416	US	10/799,474	3/11/2004	7307807	12/11/2007	In Force	DISK SERVO PATTERN WRITING
MP0416	MP0416C1	US	11/953,811	12/10/2007	7580216	8/25/2009	In Force	DISK SERVO PATTERN WRITING

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0416	MP0416D1	US	11/953,794	12/10/2007	7750459	7/20/2010	In Force	FRAMING SELECTION AND WAVEFORM POLARITY DETERMINATION F
MP0416	MP0416D1C1	US	12/795,129	6/7/2010	7950643	8/2/2011	In Force	READ CHANNEL CONFIGURATION BASED ON POLARITY DETERMINED
MP0417	MP0417	US	10/762,153	1/20/2004	7471670	12/30/2008	In Force	METHOD AND APPARATUS FOR REDUCING ECHO AND CROSSTALK IN
MP0417	MP0417C1	US	12/345,440	12/29/2008	8270394	9/18/2012	In Force	METHOD AND APPARATUS FOR REDUCING AN INTERFERENCE SIGNA
MP0417	MP0417C1a	US	14/258,781	4/22/2014	9461703	10/4/2016	In Force	INTERFERENCE SIGNAL COMPENSATION
MP0417	MP0417C1C1	US	13/607,547	9/7/2012	8787861	7/22/2014	In Force	METHOD AND APPARATUS FOR REDUCING ECHO AND CROSSTALK IN
MP0418	MP0418	US	10/845,028	5/13/2004	7640446	12/29/2009	In Force	System-On-Chip Power Reduction Through Dynamic Clock Frequenc
MP0418	MP0418C1	US	12/645,840	12/23/2009	8281170	10/2/2012	In Force	System-On-Chip Power Reduction Through Dynamic Clock Frequency
MP0418	MP0418C1a	US	14/065,879	10/29/2013	9423821	8/23/2016	In Force	Method And Apparatus For Setting A Frequency Of A Clock Signal For
MP0418	MP0418C1C1	US	13/632,551	10/1/2012	8572419	10/29/2013	In Force	System-On-Chip Power Reduction Through Dynamic Clock Frequency
MP0422	MP0422	US	10/802,597	3/16/2004	7116144	10/3/2006	In Force	HIGH BANDWIDTH PHASE LOCKED LOOP (PLL)
MP0422	MP0422D1	US	11/542,961	10/3/2006	7427883	9/23/2008	In Force	HIGH BANDWIDTH PHASE LOCKED LOOP (PLL)
MP0422	MP0422D1C1	US	12/235,507	9/22/2008	7898306	3/1/2011	In Force	HIGH BANDWIDTH PHASE LOCKED LOOP (PLL) WITH FEEDBACK LOOP
MP0423	MP0423	US	10/797,354	3/10/2004	7372924	5/13/2008	In Force	METHOD AND SYSTEM TO IMPROVE DECISION-DRIVEN CONTROL LO
MP0424	MP0424	US	10/819,333	4/6/2004	7595948	9/29/2009	In Force	Thermal Asperity Detection For Perpendicular Magnetic Recording
MP0425	MP0425	US	10/788,844	2/27/2004	7286328	10/23/2007	In Force	Electrostatic Discharge Protection Circuit For Preamps Connected To
MP0425	MP0425C1	US	11/975,975	10/23/2007	7480121	1/20/2009	In Force	Electrostatic Discharge Protection Circuit For Preamps Connected To
MP0425	MP0425D1	US	11/975,979	10/23/2007	8424190	4/23/2013	In Force	Method Of Providing Electrostatic Discharge Protection For A Read El
MP0425	MP0425Faa	US	12/967,753	12/14/2010	8149531	4/3/2012	In Force	Electrostatic Discharge Protection Circuit For Magneto-Resistive Read
MP0425	MP0425Faaa	US	13/437,322	4/2/2012	8462457	6/11/2013	In Force	Electrostatic Discharge Protection Circuit For Magneto-Resistive Read
MP0425	MP0425Faaa	US	13/911,571	6/6/2013	8760791	6/24/2014	In Force	Magnetic Storage Systems With Transistors And Limiting Circuits For
MP0425	MP0425I	US	10/877,033	6/25/2004	7167331	1/23/2007	In Force	Electrostatic Discharge Protection Circuit For Magneto-Resistive Read
MP0425	MP0425C1	US	11/643,431	12/21/2006	7852591	12/14/2010	In Force	Electrostatic Discharge Protection Circuit For Magneto-Resistive Read
MP0426	MP0426	US	10/814,534	3/31/2004	7023271	4/4/2006	In Force	Variable-Gain Constant-Bandwidth Transimpedance Amplifier
MP0426	MP0426C1C1	US	11/500,241	8/7/2006	7299202	7/3/2007	In Force	Variable-Gain Constant-Bandwidth Transimpedance Amplifier
MP0427	MP0427	US	10/794,563	3/5/2004	7610022	10/27/2009	In Force	FILTER CIRCUIT WITH NEGATIVE TRANSCONDUCTANCE
MP0428	MP0428	US	10/793,359	3/4/2004	7652515	1/26/2010	In Force	CLOCK SIGNAL CONVERSION SYSTEM
MP0429	MP0429	US	10/832,960	4/26/2004	7667916	2/23/2010	In Force	SIGNAL CONVERSION SYSTEM AND METHOD
MP0429	MP0429C1	US	12/701,412	2/5/2010	7929241	4/19/2011	In Force	SIGNAL CONVERSION SYSTEM AND METHOD
MP0431	MP0431	US	10/792,194	3/3/2004	7433425	10/7/2008	In Force	ADAPTIVE SIGNAL TUNING FOR DIGITAL RADIO RECEIVERS OPERATING
MP0431	MP0431C1	US	12/150,621	4/30/2008	7555065	6/30/2009	In Force	Adaptive Signal Tuning For Digital Radio Receivers Operating In FM B
MP0432	MP0432	US	10/799,548	3/11/2004	7532563	5/12/2009	In Force	MECHANISM TO IMPROVE QUALITY OF CHANNEL ESTIMATES IN OFD
MP0434	MP0434	US	10/896,161	7/20/2004	7675979	3/9/2010	In Force	METHODS, ALGORITHMS, SOFTWARE, CIRCUITS, ARCHITECTURES, A
MP0440	MP0440	US	10/886,860	7/7/2004	7957394	6/7/2011	In Force	Automatic Network Switch Configuration To Support Quality Of Servi

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0441	MP0441	US	10/883,540	6/30/2004	7466553	12/16/2008	In Force	Quality Of Service For A Stackable Network Switch
MP0442	MP0442C1	US	10/926,946	8/27/2004	7675869	3/9/2010	In Force	APPARATUS AND METHOD FOR MASTER ELECTION AND TOPOLOGY
MP0443	MP0443	US	10/977,245	10/29/2004	7646717	1/12/2010	In Force	METHOD AND APPARATUS FOR PACKET SCHEDULING
MP0443	MP0443C1	US	12/666,307	1/12/2010	7884347	2/22/2011	In Force	METHOD AND APPARATUS FOR PACKET SCHEDULING
MP0445	MP0445	US	10/901,689	7/29/2004	7266680	9/4/2007	In Force	METHOD AND APPARATUS FOR LOADING CONFIGURATION DATA
MP0447	MP0447	US	10/039,585	1/4/2002	7216285	5/8/2007	In Force	System And Method For Generating Cyclic Redundancy Check
MP0447	MP0447C1	US	11/385,962	3/21/2006	7418648	8/26/2008	In Force	System And Method For Generating A Cyclic Redundancy Check
MP0447	MP0447C1C1	US	12/229,585	8/25/2008	8161359	4/17/2012	In Force	System And Method For Generating A Cyclic Redundancy Check
MP0450	MP0450	US	10/779,544	2/13/2004	7421602	9/2/2008	In Force	Computer With Low-Power Secondary Processor And Secondary Dis
MP0450	MP0450C1	US	12/229,034	8/19/2008	8151129	4/3/2012	In Force	Computer With Low-Power Secondary Processor And Secondary Dis
MP0450	MP0450C1C1	US	13/437,490	4/2/2012	8341447	12/25/2012	In Force	Computer With Low-Power Secondary Processor And Secondary Dis
MP0450	MP0450CN	CN	2005100075297	2/5/2005	ZL2005100075	12/23/2009	In Force	Computer With Low-Power Secondary Processor And Secondary Dis
MP0450	MP0450EP	EP	50020593	2/1/2005			Pending	Computer With Low-Power Secondary Processor And Secondary Dis
MP0450	MP0450JP	JP	2005-033647	2/9/2005	4961791	4/6/2012	In Force	Computer With Low-Power Secondary Processor and Secondary Dis
MP0450	MP0450TW	TW	94101598	1/19/2005	1368140	7/11/2012	In Force	Computer With Low-Power Secondary Processor And Secondary Dis
MP0451	MP0451	US	10/872,098	6/17/2004	7301482	11/27/2007	In Force	CIRCUITS, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS AND
MP0451	MP0451C1	US	11/978,788	10/29/2007	7425905	9/16/2008	In Force	CIRCUITS, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS AND
MP0451	MP0451C1C1	US	12/206,673	8/9/2008	7786902	8/31/2010	In Force	CIRCUITS, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS AND
MP0453	MP0453C1	US	12/791,727	6/1/2010	8392548	3/5/2013	In Force	METHOD AND APPARATUS FOR GENERATING DIAGNOSTIC INFORMA
MP0454	MP0454	US	10/819,649	4/6/2004	7406302	7/29/2008	In Force	DIGITAL FM STEREO RECEIVER ARCHITECTURE
MP0454	MP0454C1	US	12/170,224	7/9/2008	8406717	3/26/2013	In Force	DIGITAL FM STEREO RECEIVER ARCHITECTURE TO RECOVER CARRIER
MP0454	MP0454C1C1	US	13/800,215	3/13/2013	9337944	5/10/2016	In Force	DIGITAL FM STEREO RECEIVER ARCHITECTURE
MP0455	MP0455	US	10/843,285	5/11/2004	7217031	10/2/2007	In Force	100BASE-FX SERIALIZER/DESERIALIZER USING 1000BASE-X SERIALIZ
MP0455	MP0455D1	US	11/900,484	9/12/2007	7477172	1/13/2009	In Force	100BASE-FX SERIALIZER/DESERIALIZER USING 1000BASE-X SERIALIZE
MP0455	MP0455D1a3	US	12/874,343	9/2/2010	7994950	8/9/2011	In Force	100Base-FX Serializer/Deserializer Using 1000Base-X Serializer/Deseri
MP0455	MP0455D1C1	US	12/351,061	1/9/2009	7791512	9/7/2010	In Force	100Base-FX Serializer/Deserializer Using 1000Base-X Serializer/Deseri
MP0458	MP0458	US	10/819,454	4/6/2004	7366488	4/29/2008	In Force	DIGITAL CARRIER-RECOVERY SCHEME FOR FM STEREO DETECTION
MP0458	MP0458C1	US	12/106,989	4/21/2008	7826812	11/2/2010	In Force	DIGITAL CARRIER-RECOVERY SCHEME FOR FM STEREO DETECTION
MP0458	MP0458C1C1	US	12/916,335	10/29/2010	8064858	11/22/2011	In Force	DIGITAL CARRIER-RECOVERY SCHEME FOR FM STEREO DETECTION
MP0460	MP0460	US	10/861,802	6/4/2004	7649855	1/19/2010	In Force	Alternative 1000Base-T Scrambler
MP0461	MP0461	US	10/852,540	5/24/2004	7246018	7/17/2007	In Force	Interpolator Testing Circuit
MP0461	MP0461C1	US	11/800,816	5/8/2007	7532995	5/12/2009	In Force	Interpolator Testing Circuit
MP0465	MP0465C1	US	11/710,384	2/23/2007	7679347	3/16/2010	In Force	Closed-loop Digital Control System For A DC/DC Converter
MP0465	MP0465C1C1	US	12/723,867	3/15/2010	8183846	5/22/2012	In Force	Method And Apparatus For Controlling A DC/DC Converter

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0465	MP0465EP	EP	EP05012809.9	6/14/2005	1607548	2/9/2011	In Force	Closed-loop Digital Control System for a DC/DC Converter
MP0465	MP0465EPDE	DE	05012809.9	6/14/2005	602905026235	2/9/2011	In Force	Closed-loop Digital Control System For A DC/DC Converter
MP0465	MP0465EPRR	FR	05012809.9	6/14/2005	1617548	2/9/2011	In Force	Closed-loop Digital Control System For A DC/DC Converter
MP0465	MP0465EPPGB	GB	05012809.9	6/14/2005	1617548	2/9/2011	In Force	Closed-loop Digital Control System For A DC/DC Converter
MP0465	MP0465JP	JP	2005-179978	6/20/2005	4652901	12/24/2010	In Force	Closed-loop Digital Control System For A DC/DC Converter
MP0465	MP0465TW	TW	94120283	6/17/2005	1381666	1/1/2013	In Force	CLOSE-LOOP DIGITAL CONTROL SYSTEM FOR A DC/DC CONVERTER
MP0467	MP0467	US	10/810,452	3/26/2004	8324872	12/4/2012	In Force	Voltage Regulator With Coupled Inductors Having High Coefficient Q
MP0467	MP0467CN	CN	20051005133380	3/4/2005			Pending	Voltage Regulator
MP0467	MP0467EP	EP	50032002	2/15/2005			Pending	Voltage Regulator
MP0467	MP0467JP	JP	2005059456	3/3/2005			Pending	Voltage Regulator
MP0467	MP0467TW	TW	94104865	2/18/2005			Pending	Voltage Regulator
MP0468	MP0468	US	10/883,342	1/7/2004	7420333	2/9/2008	In Force	MIXED MODE CONTROL FOR DIMMABLE FLUORESCENT LAMP
MP0468	MP0468C1	US	12/202,017	8/29/2008	7923939	12/4/2011	In Force	MIXED MODE CONTROL FOR DIMMABLE FLUORESCENT LAMP
MP0469	MP0469I	US	11/076,726	3/9/2005	7525293	4/28/2009	In Force	POWER SUPPLY SWITCHING CIRCUIT FOR A HALOGEN LAMP
MP0469	MP04691C1	US	12/422,857	4/13/2009	8044643	10/25/2011	In Force	POWER SUPPLY SWITCHING CIRCUIT FOR A HALOGEN LAMP
MP0470	MP0470	US	11/029,194	1/3/2005	7199661	4/3/2007	In Force	VARIABLE GAIN AMPLIFICATION USING TAYLOR EXPANSION
MP0474	MP0474	US	10/899,630	7/26/2004	7489750	2/10/2009	In Force	DC-CONTROL FOR POST PROCESSOR
MP0474	MP0474D1	US	12/360,065	1/26/2009	8218258	7/10/2012	In Force	DC-CONTROL FOR POST PROCESSOR
MP0477	MP0477	US	10/896,313	7/20/2004	7995455	8/9/2011	In Force	Scalable MIMO-OFDM PHY For High Throughput WLANs
MP0477	MP0477D1	US	13/206,105	8/9/2011	8345534	1/1/2013	In Force	Scalable MIMO-OFDM PHY For High Throughput WLANs
MP0477	MP0477D1aa	US	14/991,089	11/3/2015	9935665	4/3/2018	In Force	Systems And Methods For Transmitting Data Frames Using Multiple A
MP0477	MP0477D1C1	US	13/727,882	12/27/2012	9191051	11/17/2015	In Force	Scalable MIMO-OFDM PHY For High Throughput WLANs
MP0478	MP0478	US	10/776,381	2/11/2004	7062409	2/21/2006	In Force	Compensation Circuit For Amplifiers Having Multiple Stages
MP0478	MP0478C1	US	11/293,556	12/2/2005	7463067	7/22/2008	In Force	Compensation Circuit For Amplifiers Having Multiple Stages
MP0478	MP0478C1C1	US	12/218,991	7/18/2008	7633338	12/15/2009	In Force	Compensation Circuit For Amplifiers Having Multiple Stages
MP0480	MP0480D1	US	12/355,687	1/16/2009	8429507	4/23/2013	In Force	Device And Method For Detecting And Correcting Errors In A Data Str
MP0481	MP0481	US	10/946,717	5/13/2004	7514911	4/7/2009	In Force	Voltage Regulator Feedback Protection Method And Apparatus
MP0481	MP0481AUDE	DE	05011612.8	4/18/2005	602905019946	3/17/2010	In Force	Voltage Regulator With Signal Generator Having High Output Impeda
MP0481	MP0481AUFR	FR	05011612.8	4/18/2005	1602997	3/17/2010	In Force	Voltage Regulator With Signal Generator Having High Output Impeda
MP0481	MP0481AUGB	GB	05011612.8	4/18/2005	1602997	3/17/2010	In Force	Voltage Regulator With Signal Generator Having High Output Impeda
MP0481	MP0481CIHK	HK	71012962	5/13/2005			Pending	Voltage Regulator With Multiple Feedback
MP0481	MP0481CN	CN	2005100679545	4/29/2005	212005100679	6/2/2010	In Force	Voltage Regulator With Separately-Derived Pull-Up Voltage
MP0481	MP0481D1	US	12/398,265	3/5/2009	7960958	6/14/2011	In Force	Voltage Regulator Feedback Protection Method And Apparatus
MP0481	MP0481EP	EP	EP05008414.4	4/18/2005	1596267	10/15/2008	In Force	VOLTAGE REGULATOR FEEDBACK PROTECTION METHOD AND APPAR

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0481	MP0481EP01	EP	50116128	5/30/2005	1662997	3/17/2010	In Force	Voltage Regulator With Signal Generator Having High Output Impeda
MP0481	MP0481EPDE	DE	05008414.4	4/18/2005	602005010321	10/15/2008	In Force	Voltage Regulator Feedback Protection Method And Apparatus
MP0481	MP0481EPFR	FR	05008414.4	4/18/2005	1556267	10/15/2008	In Force	Voltage Regulator Feedback Protection Method And Apparatus
MP0481	MP0481EPGB	GB	05008414.4	4/18/2005	1556267	10/15/2008	In Force	Voltage Regulator Feedback Protection Method And Apparatus
MP0481	MP04812TW	TW	94132733	9/21/2005	1370341	8/11/2012	In Force	Integrated Circuit With Signal Generator Having High Output Impeda
MP0481	MP0481IEP	EP	50116136	5/30/2005			Pending	Voltage Regulator With Multiple Feedback
MP0481	MP0481ITW	TW	94132734	4/20/2005			Pending	Voltage Regulator With Multiple Feedback
MP0481	MP0481JP	JP	2005-126973	4/25/2005	4712429	4/1/2011	In Force	Voltage Regulator Feedback Protection Method and Apparatus
MP0481	MP0481JP2	JP	2005-180009	6/20/2005	4712453	4/1/2011	In Force	Voltage Regulator with Signal Generator Having High Output Imped
MP0481	MP0481TW	TW	94112632	4/20/2005	1388963	3/11/2013	In Force	A Feedback Protection Integrated Circuit Of A Voltage Regulator
MP0481	MP0481TW2	TW	94132733	4/20/2005	1370341	8/11/2012	In Force	INTEGRATED CIRCUIT WITH SIGNAL GENERATOR HAVING HIGH OUTP
MP0483	MP0483	US	10/993,106	11/18/2004	7583459	9/1/2009	In Force	METHOD AND APPARATUS FOR WRITE PRECOMPENSATION IN A MA
MP0483	MP0483C1C1	US	13/012,422	1/24/2011	8358478	1/22/2013	In Force	METHOD AND APPARATUS FOR WRITE PRECOMPENSATION IN A MA
MP0484	MP0484	US	10/865,732	6/10/2004	7730335	6/1/2010	In Force	Low Power Computer With Main And Auxiliary Processors
MP0484	MP0484ACN1	CN	2005100732231	5/20/2005	212005100732	5/5/2010	In Force	SEPARATE OS STORED IN SEPARATE NV MEMORY AND EXECUTED DU
MP0484	MP0484C1	US	12/787,478	5/26/2010	8572416	10/29/2013	In Force	Low Power Computer With Main And Auxiliary Processors
MP0484	MP0484C1C1	US	14/064,633	10/28/2013	8874948	10/28/2014	In Force	Apparatuses For Operating, During Respective Power Modes, Transist
MP0484	MP0484C1CN01	CN	2005100732231	5/20/2005	212005100732	5/5/2010	In Force	Separate OS Stored In Separate NV Memory And Executed During HP
MP0484	MP0484CN	CN	2005100718484	5/20/2005	212005100718	5/5/2010	In Force	High Power And Low Power Processors
MP0484	MP0484CN02	CN	2005100732246	5/20/2005	212005100732	5/5/2010	In Force	Cache Hierarchy
MP0484	MP0484D1	US	12/151,526	5/7/2008	7827423	11/2/2010	In Force	Low Power Computer With Main And Auxiliary Processors
MP0484	MP0484D2	US	12/152,030	5/12/2008	7788514	8/31/2010	In Force	Low Power Computer With Main And Auxiliary Processors
MP0484	MP0484EP3	EP	EP05010993.3	5/20/2005	1665361	4/18/2018	In Force	Cache hierarchy
MP0484	MP0484HK2	HK	71007344	5/20/2005			Pending	Separate OS Stored In Separate NV Memory And Executed During HP
MP0484	MP0484HK3	HK	71007353	5/20/2005			Pending	Cache Hierarchy
MP0484	MP0484JP	JP	2005-144894	5/18/2005	4621540	11/5/2010	In Force	Low Power Computer With Main and Auxiliary Processors
MP0484	MP0484JP2	JP	2005-209263	5/18/2005	4740673	5/13/2011	In Force	Separate OS stored in separate NV memory and executed during HP
MP0484	MP0484JP3	JP	2005-209264	5/18/2005	4749786	5/27/2011	In Force	Cache Hierarchy
MP0484	MP0484TW3	TW	94133391	5/17/2005	1379235	12/11/2012	In Force	Cache Hierarchy
MP0484	MP0484U3DE	DE	EP05010993.3	5/20/2005	602005053831	4/18/2018	In Force	Cache hierarchy
MP0484	MP0484U3FR	FR	EP05010993.3	5/20/2005	1665361	4/18/2018	In Force	Cache hierarchy
MP0484	MP0484U3GB	GB	EP05010993.3	5/20/2005	1665361	4/18/2018	In Force	Cache hierarchy
MP0485	MP0485D1	US	11/498,958	8/2/2006	7312642	12/25/2007	In Force	CONTINUOUS, WIDE-RANGE FREQUENCY SYNTHESIS AND PHASE TRA
MP0488	MP0488	US	10/921,526	8/19/2004	7336572	2/26/2008	In Force	Detecting Sync Patterns For Optical Media

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0488	MP0488C1	US	12/070,365	2/15/2008	7545711	6/9/2009	In Force	Detecting Sync Patterns For Optical Media
MP0492	MP0492	US	10/876,216	6/24/2004	7558014	7/7/2009	In Force	Programmable High Pass Amplifier For Perpendicular Recording Systems
MP0492	MP0492C1	US	11/656,774	1/22/2007	7876520	1/25/2011	In Force	Programmable High Pass Amplifier For Perpendicular Recording Systems
MP0492	MP0492CN	CN	2005100775538	6/17/2005	ZL2005100775	6/24/2015	In Force	Closed-loop Digital Control System For A DC/DC Converter
MP0492	MP049212CN	CN	2005100693699	5/13/2005	ZL2005100693	4/14/2010	In Force	Voltage Regulator With Signal Generator Having High Output Impedance
MP0492	MP04921CN	CN	2005100754254	5/13/2005			Pending	Voltage Regulator with Multiple Feedback
MP0492	MP0492TW	TW	94120283	6/17/2005	1381606	1/1/2013	In Force	Closed-Loop Digital Control System For A DC/DC Converter
MP0493	MP0493	US	10/929,211	8/30/2004	7414804	8/19/2008	In Force	TMR/GMR Amplifier With Input Current Compensation
MP0493	MP0493D1	US	11/652,202	1/11/2007	7529053	5/5/2009	In Force	TMR/GMR Amplifier With Input Current Compensation
MP0493	MP0493D2	US	11/652,184	1/11/2007	7359136	4/15/2008	In Force	TMR/GMR Amplifier With Input Current Compensation
MP0493	MP0493D3	US	11/652,183	1/11/2007	7289286	10/30/2007	In Force	TMR/GMR Amplifier With Input Current Compensation
MP0493	MP0493D4	US	11/652,701	1/11/2007	7532427	5/12/2009	In Force	TMR/GMR Amplifier With Input Current Compensation
MP0494	MP0494	US	10/832,783	4/27/2004	8310927	11/13/2012	In Force	PRIORITY SCHEME FOR CONTROL TRAFFIC IN NETWORK SWITCHES
MP0495	MP0495	US	10/836,661	4/30/2004	7559320	9/22/2009	In Force	FAILOVER SCHEME FOR STACKABLE NETWORK SWITCHES
MP0497	MP0497	US	10/929,214	8/30/2004	7183832	2/27/2007	In Force	Level Shifter With Boost And Attenuation Programming
MP0497	MP0497C1	US	11/710,362	2/23/2007	7417484	8/26/2008	In Force	Level Shifter With Boost And Attenuation Programming
MP0503	MP0503	US	11/006,189	12/6/2004	8228110	7/24/2012	In Force	LOW POWER, LOW VOLTAGE PHASE INTERPOLATOR
MP0503	MP0503C1	US	13/550,514	7/16/2012	8536927	9/17/2013	In Force	LOW POWER, LOW VOLTAGE PHASE INTERPOLATOR
MP0505	MP0505	US	10/866,326	6/9/2004	8465853	6/18/2013	In Force	Glassy Metal Disk
MP0505	MP0505CN	CN	200510088155	2/23/2005	ZL200510088	8/24/2011	In Force	Glassy Metal Disk
MP0505	MP0505EP	EP	05004127.6	2/25/2005			Pending	Glassy Metal Disk
MP0505	MP05051EP	EP	EP07794750.5	5/10/2007	2016476	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive with Flash Interface
MP0505	MP05051EPDE	DE	EP07794750.5	5/10/2007	602007058795	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive with Flash Interface
MP0505	MP05051EPFR	FR	EP07794750.5	5/10/2007	2016476	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive with Flash Interface
MP0505	MP05051EPGB	GB	EP07794750.5	5/10/2007	2016476	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive with Flash Interface
MP0505	MP0505JP	JP	2005-052133	2/25/2005	4384069	10/2/2009	In Force	GLASSY METAL DISK
MP0505	MP0505TW	TW	94103780	2/4/2005	1397066	5/21/2013	In Force	Glassy Metal Disk
MP0506	MP0506	US	10/829,131	4/21/2004	7768959	8/3/2010	In Force	All-in-One Wireless Network Device
MP0506	MP0506C1	US	12/845,315	7/28/2010	8018892	9/13/2011	In Force	All-in-One Wireless Network Device
MP0506	MP0506C1aa	US	14/076,732	11/11/2013	9195485	11/24/2015	In Force	Method And Apparatus For Concurrently Maintaining Queues For MU
MP0506	MP0506C1C1	US	13/220,595	9/12/2011	8582507	11/12/2013	In Force	All-in-One Wireless Network Device
MP0509	MP0509	US	10/888,307	7/9/2004	7636338	12/22/2009	In Force	ACCESS POINT CONFIGURATION
MP0509	MP0509C1	US	12/613,211	11/5/2009	7960938	8/2/2011	In Force	ACCESS POINT CONFIGURATION
MP0513	MP0513	US	10/891,672	7/15/2004	7522899	4/21/2009	In Force	IMAGE REJECTION SCHEME FOR RECEIVERS

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MP0513	MP0513C1	US	12/273,051	11/18/2008	8175565	5/8/2012	In Force	IMAGE REJECTION SCHEME FOR RECEIVERS
MP0514	MP0514	US	10/942,761	9/16/2004	7688968	3/30/2010	In Force	Adaptive Analog Echo/Next Cancellation
MP0514	MP0514Aaa	US	13/476,418	5/21/2012	8712034	4/29/2014	In Force	Adaptive Analog Echo/Next Cancellation
MP0514	MP0514C1	US	12/748,987	3/29/2010	8077859	12/13/2011	In Force	Adaptive Analog Echo/Next Cancellation
MP0514	MP0514D1	US	12/215,956	7/1/2008	8184802	5/22/2012	In Force	Adaptive Analog Echo/Next Cancellation
MP0514	MP0514D1aa	US	14/237,561	4/21/2014	9407322	8/2/2016	In Force	Adaptive Analog Echo/Next Cancellation
MP0514	MP0514D2	US	12/215,967	7/1/2008	8203975	6/19/2012	In Force	Adaptive Analog Echo/NEXT Cancellation
MP0515	MP0515	US	10/875,362	6/24/2004	8244228	8/14/2012	In Force	Method And Apparatus For Providing A Mobile Wireless Local Area Network
MP0515	MP0515C1	US	13/584,959	8/14/2012	8606250	12/10/2013	In Force	Method And Apparatus For Providing A Mobile Wireless Local Area Network
MP0516	MP0516	US	11/086,381	12/7/2004	7328395	2/5/2008	In Force	ITERATIVE REED-SOLOMON ERROR-CORRECTION DECODING
MP0516	MP0516C1	US	11/956,861	12/14/2007	7689893	3/30/2010	In Force	ITERATIVE REED-SOLOMON ERROR-CORRECTION DECODING
MP0517	MP0517	US	10/850,239	5/19/2004	7616587	11/10/2009	In Force	METHODS AND APPARATUS FOR PERFORMING REVERSE AUTO-NEG
MP0517	MP0517D1	US	12/573,498	10/5/2009	8045483	10/25/2011	In Force	METHODS AND APPARATUS FOR PERFORMING REVERSE AUTO-NEG
MP0517	MP0517D1aa	US	14/171,132	2/3/2014	9531586	12/27/2016	In Force	METHODS AND APPARATUS FOR PERFORMING REVERSE AUTO-NEG
MP0517	MP0517D1C1	US	13/280,098	10/24/2011	8665901	3/4/2014	In Force	METHODS AND APPARATUS FOR PERFORMING REVERSE AUTO-NEG
MP0518	MP0518	US	10/976,110	10/27/2004	7288570	11/20/2007	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0518	MP0518aaaa	US	13/346,665	1/9/2012	8331054	12/11/2012	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0518	MP0518C1	US	11/868,932	10/8/2007	7548389	6/16/2009	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0518	MP0518C1aa	US	13/004,805	1/11/2011	8094400	1/10/2012	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0519	MP0519	US	10/923,066	8/20/2004	8107542	1/31/2012	In Force	SOFT DECODING OF CODED BIT STREAMS
MP0519	MP0519C1	US	13/355,349	1/20/2012	8774289	7/8/2014	In Force	SOFT DECODING OF CODED BIT STREAMS
MP0519	MP0519C1C1	US	14/321,442	7/1/2014	9065473	6/23/2015	In Force	SOFT DECODING OF CODED BIT STREAMS
MP0520	MP0520	US	10/949,533	9/24/2004	7499233	3/3/2009	In Force	MULTI-PHASE CONTROL INFORMATION DETECTION AND SIGNAL POL
MP0522	MP0522	US	10/995,659	11/22/2004	8294503	10/23/2012	In Force	METHOD AND APPARATUS FOR REDUCING JITTER IN A TRANSMITTE
MP0522	MP0522C1	US	13/651,205	10/12/2012	8698537	4/15/2014	In Force	METHOD AND APPARATUS FOR REDUCING JITTER IN A TRANSMITTE
MP0524	MP0524	US	10/870,579	6/16/2004	6980414	12/27/2005	In Force	CAPACITOR STRUCTURE IN A SEMICONDUCTOR DEVICE
MP0524	MP0524aaa	US	13/192,388	7/27/2011	8537524	9/17/2013	In Force	CAPACITOR STRUCTURE IN A SEMICONDUCTOR DEVICE
MP0524	MP0524C1	US	11/179,133	7/11/2005	7116544	10/3/2006	In Force	CAPACITOR STRUCTURE IN A SEMICONDUCTOR DEVICE
MP0524	MP0524C1D1	US	11/474,246	6/23/2006	7578858	8/25/2009	In Force	MAKING CAPACITOR STRUCTURE IN A SEMICONDUCTOR DEVICE
MP0525	MP0525	US	10/865,368	6/10/2004	7634615	12/15/2009	In Force	Adaptive Storage System
MP0525	MP0525C1	US	11/489,664	7/19/2006	7512734	3/31/2009	In Force	Adaptive Storage System
MP0525	MP0525CN	CN	2005100708131	5/17/2005	212005100709	9/16/2009	In Force	A Disk Drive System
MP0525	MP0525CN03	CN	200510077196.1	5/17/2005	212005100772	9/16/2009	In Force	RAID SYSTEM WITH HIGH POWER AND LOW POWER DISK DRIVES
MP0525	MP0525CND1	CN	2005100771950	5/17/2005	212005100771	9/10/2008	In Force	Hard Disk Drive Power Reduction Module

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0525	MP0525D1DE	DE	05010676.4	5/17/2005	6.02085E+12	3/26/2008	In Force	Hard Disk Drive Power Reduction Module
MP0525	MP0525D1EP	EP	05010671.5	5/17/2005	EP1605453	9/27/2017	In Force	Adaptive Storage System
MP0525	MP0525D1EPDE	DE	05010671.5	5/17/2005	602005052746	9/27/2017	In Force	Adaptive Storage System
MP0525	MP0525D1EPFR	FR	05010671.5	5/17/2005	EP1605453	9/27/2017	In Force	Adaptive Storage System
MP0525	MP0525D1EPGB	GB	05010671.5	5/17/2005	EP1605453	9/27/2017	In Force	Adaptive Storage System
MP0525	MP0525D1FR	FR	05010676.4	5/17/2005	1605456	3/26/2008	In Force	Hard Disk Drive Power Reduction Module
MP0525	MP0525D1GB	GB	05010676.4	5/17/2005	1605456	3/26/2008	In Force	Hard Disk Drive Power Reduction Module
MP0525	MP0525D1HK	HK	71007317	5/17/2005	1094259	6/19/2009	In Force	Hard Disk Drive Power Reduction Module
MP0525	MP0525D2CN	CN	2005100771946	6/14/2005			Pending	Multi-Disk Drive With High Power And Low Power Disk Drivers'
MP0525	MP0525D2HK	HK	71007326	5/17/2005			Pending	Multi-Disk Drive With High Power And Low Power Disk Drivers'
MP0525	MP0525D3CN	CN	2005100771965	5/17/2005	ZL2005100771	9/16/2009	In Force	RAID System With High Power And Low Power Disk Drives
MP0525	MP0525D3DE	DE	05010673.1	5/17/2005	602005013322	3/18/2009	In Force	RAID With High Power And Low Power Disk Drives
MP0525	MP0525D3EP	EP	EP05010673.1	5/17/2005	EP2684321	3/18/2009	In Force	RAID with High Power and Low Power Disk Drives
MP0525	MP0525D3FR	FR	05010673.1	5/17/2005	1605455	3/18/2009	In Force	RAID With High Power And Low Power Disk Drives
MP0525	MP0525D3GB	GB	05010673.1	5/17/2005	1605455	3/18/2009	In Force	RAID With High Power And Low Power Disk Drives
MP0525	MP0525D3HK	HK	71007335	5/17/2005			Pending	RAID With High Power And Low Power Disk Drives
MP0525	MP0525EPD1	EP	05010676.4	5/17/2005	EP1605456	3/26/2008	In Force	HARD DISK DRIVE POWER REDUCTION MODULE
MP0525	MP0525FAWJ	JP	2009-509863	5/10/2007			Pending	ADAPTIVE STORAGE SYSTEM INCLUDING HARD DISK DRIVE WITH FLA
MP0525	MP0525FWO	WO	PCT/US2008/00	2/20/2008			Pending	EXTERNALLY REMOVABLE NON-VOLATILE SEMICONDUCTOR MEMOR
MP0525	MP0525FWVY	TW	97105956	2/20/2008	1472914	2/11/2015	In Force	EXTERNALLY REMOVABLE N/A-VOLATILE SEMICONDUCTOR MEMOR
MP0525	MP0525FWKR	KR	1020087029319	5/10/2007	101379940	4/1/2014	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525I1	US	11/508,016	8/11/2006	7792848	4/20/2010	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525I1C1	US	11/796,248	4/27/2007	7617359	11/10/2009	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525I1C1WO	WO	PCT/US2007/01	5/10/2007			Pending	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525I1C2	US	11/796,645	4/27/2007	7636809	12/22/2009	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525I1CN	CN	200780016904	5/10/2007	ZL2007800169	2/24/2016	In Force	ADAPTIVE STORAGE SYSTEM INCLUDING HARD DISK DRIVE WITH FLA
MP0525	MP0525I1TW	TW	96116619	5/10/2007	1426444	2/11/2014	In Force	ADAPTIVE STORAGE SYSTEM INCLUDING HARD DISK DRIVE WITH FLA
MP0525	MP0525I1WO	WO	PCT/US2007/01	5/10/2007			Pending	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525I2C1CN	CN	2007100873332	3/9/2007			Pending	System With High Power And Low Power Processors And Thread Tra
MP0525	MP0525I2C1EP	EP	70033535	2/16/2007			Pending	System With High Power And Low Power Processors And Thread Tra
MP0525	MP0525I2C1HK	HK	81068642	3/9/2007			Pending	System With High Power And Low Power Processors And Thread Tra
MP0525	MP0525I2C1JP	JP	2007059011	3/8/2007			Pending	System With High Power And Low Power Processors And Thread Tra
MP0525	MP0525I2C1SG	SG	2007011893	2/22/2007			Pending	System With High Power And Low Power Processors And Thread Tra
MP0525	MP0525I2C1TW	TW	96108295	3/9/2007			Pending	System With High Power And Low Power Processors And Thread Tra

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Family	JP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0525	MP0525I2SG01	SG	2009070632	10/21/2009			Pending	System With High Power and Low Power Processors and Thread Tran
MP0525	MP0525IWEF	EP	07794752.1	5/10/2007	2049368	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525IWEFDE	DE	07794752.1	5/10/2007	60360705880X	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525IWEFER	FR	07794752.1	5/10/2007	2049368	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525IWEFGB	GB	07794752.1	5/10/2007	2049368	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525IWIJP	JP	2009-509862	5/10/2007			Pending	ADAPTIVE STORAGE SYSTEM INCLUDING HARD DISK DRIVE WITH FLA
MP0525	MP0525JP	JP	2005-142145	5/16/2005	JP5059298	8/10/2012	In Force	DISK DRIVE SYSTEM AND DATA STORAGE SYSTEM
MP0525	MP0525JP2	JP	2005-189048	5/16/2005	JP4969803	4/13/2012	In Force	Adaptive Storage System
MP0525	MP0525JP3	JP	2005-189049	5/16/2005	JP4969804	4/13/2012	In Force	Adaptive Storage System
MP0525	MP0525JP4	JP	2005-189050	5/16/2005	JP4969805	4/13/2012	In Force	Adaptive Storage System
MP0525	MP0525TW2	TW	94143636	5/6/2005	1363293	5/1/2012	In Force	Multi-Disk Drive With High Power And Low Power Disk Drivers'
MP0525	MP0525TW3	TW	94143634	5/6/2005	1417743	12/1/2013	In Force	Raid With High Power And Low Power Disk Drives
MP0526	MP0526	US	10/896,726	7/22/2004	7392464	6/24/2008	In Force	Universal Parity Encoder
MP0526	MP0526D1	US	12/156,649	6/3/2008	8230314	7/24/2012	In Force	Universal Parity Decoder
MP0526	MP0526D1C1	US	13/555,860	7/23/2012	8533577	9/10/2013	In Force	Universal Parity Encoder
MP0530	MP0530	US	10/940,855	9/13/2004	6956510	10/18/2005	In Force	METHODS, SOFTWARE, CIRCUITS AND SYSTEMS FOR CODING INFOR
MP0530	MP0530C1	US	11/157,233	6/20/2005	6995694	2/7/2006	In Force	METHODS, SOFTWARE, CIRCUITS AND SYSTEMS FOR CODING INFOR
MP0531	MP0531	US	10/958,077	10/4/2004	7666230	10/20/2009	In Force	Link Aggregation For Routed Ports
MP0531	MP0531C1	US	12/581,512	10/19/2009	8085778	12/27/2011	In Force	Link Aggregation For Routed Ports
MP0532	MP0532	US	10/944,506	9/17/2004	7403351	7/22/2008	In Force	SEQUENCE-PERMUTATION CONTROL INFORMATION DETECTION AN
MP0533	MP0533	US	10/960,551	10/6/2004	8238287	8/7/2012	In Force	METHOD AND APPARATUS FOR PROVIDING QUALITY OF SERVICE (Q
MP0533	MP0533C1	US	13/585,734	8/2/2012	8780755	7/15/2014	In Force	CONFIGURING TRANSMISSION PARAMETERS BASED ON A CONGESTI
MP0534	MP0534	US	10/912,829	8/5/2004	7408976	8/5/2008	In Force	MIMO-OFDM RECEIVER PROCESSING WITH FREQUENCY AND CHANN
MP0534	MP0534C1	US	12/179,830	7/25/2008	7796681	9/14/2010	In Force	MIMO-OFDM RECEIVER PROCESSING
MP0534	MP0534C1aa	US	13/669,167	11/5/2012	8634451	1/21/2014	In Force	MIMO-OFDM RECEIVER PROCESSING
MP0534	MP0534C1C1	US	12/876,034	9/3/2010	8306094	11/6/2012	In Force	MIMO-OFDM RECEIVER PROCESSING
MP0534	MP0534C12a	US	14/146,665	1/2/2014	9094116	7/28/2015	In Force	MIMO-OFDM RECEIVER PROCESSING
MP0535	MP0535C1	US	11/788,571	4/20/2007	7493803	10/21/2008	In Force	Fully Differential Current-Feedback CMOS/Bipolar Operational Amplif
MP0536	MP0536	US	10/997,236	11/24/2004	7227393	6/5/2007	In Force	METHOD AND APPARATUS FOR ADAPTIVE DELAY CANCELLATION IN
MP0537	MP0537C1	US	11/616,256	12/26/2006	7551897	6/23/2009	In Force	METHOD AND APPARATUS FOR PERFORMING TRANSMIT PRE-EMPH
MP0539	MP0539	US	10/990,004	8/30/2004	7707450	4/27/2010	In Force	TIME SHARED MEMORY ACCESS
MP0540	MP0540	US	10/926,486	8/19/2004	7958292	6/7/2011	In Force	Disk Drive System On Chip With Integrated Buffer Memory And Supp
MP0540	MP0540C1	US	13/154,356	6/6/2011	8332555	12/1/2012	In Force	Disk Drive System On Chip With Integrated Buffer Memory And Supp
MP0540	MP0540C1C1	US	13/710,814	12/11/2012	8521928	8/27/2013	In Force	CIRCUIT WITH MEMORY AND SUPPORT FOR HOST ACCESSSES OF STO

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0540	MP0540CN	CN	2005100730626	5/27/2005			Pending	Storage Controller With High Speed External Interface To Remote Buf
MP0540	MP0540D1CN	CN	2005100796731	5/27/2005			Pending	System With Host And Storage Controller
MP0540	MP0540D1EP	EP	50117522	5/31/2005			Pending	System With Host And Storage Controller
MP0540	MP0540D1HK	HK	71007362	5/27/2005			Pending	System With Host And Storage Controller
MP0540	MP0540D1JP	JP	2005221928	7/29/2005			Pending	System With Host And Storage Controller
MP0540	MP0540D1TW	TW	94142124	11/30/2005			Pending	System With Host And Storage Controller
MP0540	MP0540D2CN	CN	2005100796746	5/27/2005	ZL2005100796	3/9/2009	In Force	Bridge Circuit
MP0540	MP0540D2EP	EP	50117514	5/31/2005			Pending	Bridge Circuit
MP0540	MP0540D2HK	HK	71007371	5/27/2005			Pending	Bridge Circuit
MP0540	MP0540D2TW	TW	94142119	11/30/2005			Pending	Bridge Circuit
MP0540	MP0540D3CN	CN	2005100796750	5/27/2005			Pending	Storage Device Circuit With SATA Interface And Remote Buffering
MP0540	MP0540D3EP	EP	50117654	5/31/2005			Pending	Storage Device Circuit With SATA Interface And Remote Buffering
MP0540	MP0540D3HK	HK	71007380	5/27/2005			Pending	Storage Device Circuit With SATA Interface And Remote Buffering
MP0540	MP0540D3TW	TW	94142133	11/30/2005			Pending	Storage Device Circuit With SATA Interface And Remote Buffering
MP0540	MP0540EP	EP	50117506	5/31/2005			Pending	Storage Controller With High Speed External Interface To Remote Buf
MP0540	MP0540JP	JP	2005-158376	5/31/2005			Pending	Disk Drive System on Chip With Integrated Buffer Memory and Suppo
MP0540	MP0540JP02	JP	2005-221929	7/29/2005			Pending	Bridge Circuit
MP0540	MP0540JP03	JP	2005-221930	7/29/2005			Pending	Storage device circuit with SATA interface and remote buffering
MP0540	MP0540TW	TW	94117116	5/25/2005			Pending	Storage Controller With High Speed External Interface To Remote Buf
MP0541	MP0541	US	10/949,624	9/24/2004	7292013	11/6/2007	In Force	CIRCUITS, SYSTEMS, METHODS, AND SOFTWARE FOR POWER FACTO
MP0541	MP0541aFTW	TW	98109005	3/20/2009	1470915	1/21/2015	In Force	BOOST CONVERTER AND POWER FACTOR CONTROLLER
MP0541	MP0541aFWO	WO	PCT/US2008/06	5/22/2008			Pending	Power Factor Control Systems And Methods
MP0541	MP0541C1	US	11/917,869	10/26/2007	7511460	3/31/2009	In Force	CIRCUITS, SYSTEMS, METHODS, AND SOFTWARE FOR POWER FACTO
MP0541	MP0541C1Fa	US	12/902,440	10/12/2010	8111050	2/7/2012	In Force	Power Factor Control Systems And Methods
MP0541	MP0541C1FaB	US	13/366,951	2/6/2012	8686822	3/25/2014	In Force	Power Factor Control Systems And Methods
MP0541	MP0541C1Fz3	US	14/224,398	3/25/2014	9455644	9/27/2016	In Force	Method And Apparatus For Changing A Frequency Of A Switch Prior T
MP0541	MP0541C111	US	12/115,175	5/5/2008	7812576	10/12/2010	In Force	Power Factor Control Systems And Methods
MP0541	MP0541C1RE	US	12/813,470	6/10/2010	RE42947	11/22/2011	In Force	Circuits, Systems, Methods, And Software For Power Factor Correctio
MP0541	MP0541RE	US	12/813,467	6/10/2010	RE42946	11/22/2011	In Force	Circuits, Systems, Methods, And Software For Power Factor Correctio
MP0542	MP0542	US	10/960,378	10/6/2004	7558289	7/7/2009	In Force	METHOD AND APPARATUS FOR PROVIDING QUALITY OF SERVICE (Q
MP0542	MP0542D1	US	12/474,153	5/28/2009	8559943	10/15/2013	In Force	ACKNOWLEDGING RECEIPT OF REAL-TIME DATA
MP0542	MP0542D1C1	US	14/053,257	10/14/2013	9179365	11/3/2015	In Force	METHOD AND APPARATUS FOR PROVIDING QUALITY OF SERVICE (Q
MP0544	MP0544	US	10/994,526	9/7/2004	7477621	1/13/2009	In Force	System And Method For Estimating Bandwidth Requirements Of And
MP0544	MP0544C1	US	12/351,097	1/9/2009	7957347	6/7/2011	In Force	System And Method For Estimating Bandwidth Requirements Of And

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MP0544	MP0544C1aa	US	14/330,229	7/14/2014	9178673	11/3/2015	In Force	Dynamic Bandwidth Allocation
MP0544	MP0544C1C1	US	13/155,128	6/7/2011	8786831	7/15/2014	In Force	System And Method For Estimating Bandwidth Requirements Of And
MP0546	MP0546C1	US	11/788,560	4/19/2007	7315189	1/1/2008	In Force	RETIMING CIRCUITS FOR PHASE-LOCKED LOOPS
MP0547	MP0547	US	10/936,348	9/7/2004	7570612	8/4/2009	In Force	Multi-Band Communications For A Single Wireless Base Station
MP0547	MP0547D1	US	12/482,573	6/11/2009	7881254	2/1/2011	In Force	Multi-Band Communications For A Single Wireless Base Station
MP0547	MP0547D1aa	US	13/711,230	12/11/2012	8971306	3/3/2015	In Force	Multi-Band Communications For A Single Wireless Base Station
MP0547	MP0547D1C1	US	13/018,926	2/1/2011	8331345	12/11/2012	In Force	Multi-Band Communications For A Single Wireless Base Station
MP0548	MP0548	US	10/962,376	10/8/2004	7653408	1/26/2010	In Force	Self-Adaptive Transmit Power Control For Wireless Network
MP0549	MP0549	US	11/030,619	1/5/2005	7209312	4/24/2007	In Force	SELF-SERVO-WRITE USING RAMP-TRACKS
MP0549	MP0549C1	US	11/739,620	4/24/2007	7522370	4/21/2009	In Force	SELF-SERVO-WRITE USING RAMP-TRACKS
MP0549	MP0549C1C1	US	12/409,428	3/23/2009	7852598	12/14/2010	In Force	SELF-SERVO-WRITE USING SLOPED TRACKS WRITTEN USING A CALIB
MP0549	MP0549I1	US	11/726,747	3/21/2007	7751144	7/6/2010	In Force	SELF-SERVO-WRITE USING RAMP-TRACKS
MP0549	MP0549I1Aa	US	13/474,657	5/17/2012	8514510	8/20/2013	In Force	SELF-SERVO-WRITE USING RAMP-TRACKS
MP0549	MP0549I1D1	US	12/813,406	6/10/2010	8184393	5/22/2012	In Force	SELF-SERVO-WRITE USING RAMP-TRACKS
MP0550	MP0550	US	10/968,318	11/12/2004	7697449	4/13/2010	In Force	ADAPTIVELY DETERMINING A DATA RATE OF PACKETIZED INFORMATION
MP0550	MP0550C1	US	12/750,531	3/30/2010	8687510	4/1/2014	In Force	ADAPTIVELY DETERMINING A DATA RATE OF PACKETIZED INFORMATION
MP0550	MP0550C1C1	US	14/223,838	3/24/2014	9369914	6/14/2016	In Force	ADAPTIVELY DETERMINING A DATA RATE OF PACKETIZED INFORMATION
MP0551	MP0551	US	10/902,299	7/29/2004	7417952	8/26/2008	In Force	Adaptive Wireless Network Multiple Access Techniques Using Traffic
MP0551	MP0551C1	US	12/229,377	8/22/2008	8121074	2/21/2012	In Force	Adaptive Wireless Network Multiple Access Techniques Using Traffic
MP0551	MP0551C1C1	US	13/400,369	2/20/2012	8774233	7/8/2014	In Force	Adaptive Wireless Network Multiple Access Techniques Using Traffic
MP0551	MP0551D1	US	12/082,437	4/11/2008	8144675	3/27/2012	In Force	Adaptive Wireless Network Multiple Access Techniques Using Traffic
MP0552	MP0552C1	US	11/602,676	11/20/2006	7408492	8/5/2008	In Force	A/D CONVERTER FOR WIDEBAND DIGITAL COMMUNICATION
MP0553	MP0553C1C1	US	12/166,028	7/1/2008	7732246	6/8/2010	In Force	A/D CONVERTER FOR WIDEBAND DIGITAL COMMUNICATION
MP0554	MP0554	US	11/028,687	1/4/2005	7395454	7/1/2008	In Force	Integrated Circuit With Integrated Debugging Mechanism For Standar
MP0556	MP0556C1	US	12/612,556	11/4/2009	7957491	6/7/2011	In Force	DIGITAL TECHNIQUE OF COMPENSATING MISMATCHES BETWEEN IN
MP0557	MP0557	US	10/961,777	10/8/2004	8131209	3/6/2012	In Force	REPEATER CONFIGURATION AND MANAGEMENT
MP0557	MP0557D1	US	13/411,014	3/2/2012	8818261	8/26/2014	In Force	CONFIGURATION OF A NETWORK DEVICE
MP0558	MP0558	US	11/026,543	12/30/2004	7611013	9/2/2008	In Force	MAXIMUM LIKELIHOOD ESTIMATION OF TIME AND FREQUENCY OFF
MP0558	MP0558C1	US	12/194,445	8/19/2008	7639733	12/29/2009	In Force	MAXIMUM LIKELIHOOD ESTIMATION OF TIME AND FREQUENCY OFF
MP0559	MP0559	US	11/046,070	1/28/2005	7607073	10/20/2009	In Force	METHODS, ALGORITHMS, SOFTWARE, CIRCUITS, RECEIVERS AND SYS
MP0560	MP0560	US	11/051,789	2/4/2005	7478314	1/13/2009	In Force	METHODS, ALGORITHMS, SOFTWARE, CIRCUITS, RECEIVERS AND SYS
MP0560	MP0560D1	US	12/330,260	12/8/2008	8201065	6/12/2012	In Force	METHODS, ALGORITHMS, SOFTWARE, CIRCUITS, RECEIVERS AND SYS
MP0560	MP0560D1aa	US	13/871,520	4/26/2013	9209837	12/8/2015	In Force	METHODS, ALGORITHMS, SOFTWARE, CIRCUITS, RECEIVERS AND SYS
MP0560	MP0560D1C1	US	13/473,165	5/16/2012	8443272	5/14/2013	In Force	METHODS, ALGORITHMS, SOFTWARE, CIRCUITS, RECEIVERS AND SYS

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0561	MP0561	US	10/961,004	10/7/2004	7643583	1/5/2010	In Force	HIGH-PRECISION SIGNAL DETECTION FOR HIGH-SPEED RECEIVER
MP0561	MP0561C1	US	12/621,703	11/19/2009	7949078	5/24/2011	In Force	HIGH-PRECISION SIGNAL DETECTION FOR HIGH-SPEED RECEIVER
MP0561	MP0561C1C1	US	13/107,194	5/13/2011	8144817	3/27/2012	In Force	HIGH-PRECISION SIGNAL DETECTION FOR HIGH-SPEED RECEIVER
MP0562	MP0562	US	10/978,128	10/29/2004	7542528	6/2/2009	In Force	Far End Crosstalk Mitigation In Multiple-Input Multiple-Output (MIMO)
MP0563	MP0563	US	11/002,786	12/1/2004	7596182	9/29/2009	In Force	OPTIMUM SYMBOL TIMING ESTIMATION WITH VARIOUS PERFORMANCE
MP0565	MP0565	US	11/031,984	1/7/2005	8036123	10/11/2011	In Force	Integrated Circuit For Network Stress Testing
MP0565	MP0565C1	US	13/267,248	10/6/2011	8854961	10/7/2014	In Force	Integrated Circuit For Network Stress Testing
MP0566	MP0566	US	11/489,458	7/26/2005	7250784	7/31/2007	In Force	Integrated Systems Testing
MP0566	MP0566C1	US	11/243,661	10/5/2005	7253652	8/7/2007	In Force	Integrated Systems Testing
MP0566	MP0566C1D1	US	11/601,170	11/17/2006	7868262	10/5/2010	In Force	Integrated Systems Testing
MP0566	MP0566C2	US	11/243,697	10/5/2005	7250751	7/31/2007	In Force	Integrated Systems Testing
MP0566	MP0566C3	US	11/243,660	10/5/2005	7439729	10/21/2008	In Force	Integrated Systems Testing
MP0566	MP0566C4	US	11/688,214	7/31/2007	7873766	1/18/2011	In Force	Integrated Systems Testing
MP0566	MP0566C4aa	US	13/762,682	2/8/2013	8624613	1/7/2014	In Force	Integrated Systems Testing
MP0566	MP0566C4C1	US	13/006,958	1/14/2011	8373422	2/12/2013	In Force	Integrated Systems Testing
MP0566	MP0566C4C2a	US	14/147,206	1/3/2014	9013198	4/21/2015	In Force	Systems And Methods For Testing Components Of A Hard Disk Drive
MP0567	MP0567C1	US	12/728,583	3/22/2010	7949833	5/24/2011	In Force	Transparent Level 2 Cache Controller
MP0568	MP0568	US	10/974,388	10/27/2004	7657688	4/13/2010	In Force	Pipelined Packet Encapsulation And Decapsulation For Temporal Key
MP0568	MP0568C1	US	12/758,865	4/13/2010	8208632	6/26/2012	In Force	Pipelined Packet Encapsulation And Decapsulation For Temporal Key
MP0568	MP0568C1C1	US	13/533,567	6/26/2012	8577037	11/5/2013	In Force	Pipelined Packet Encapsulation And Decapsulation For Temporal Key
MP0569	MP0569	US	11/166,548	6/23/2005	7571372	8/4/2009	In Force	METHODS AND ALGORITHMS FOR JOINT CHANNEL-CODE DECODING
MP0569	MP0569D1	US	12/498,320	7/6/2009	8231290	10/16/2012	In Force	METHODS AND ALGORITHMS FOR JOINT CHANNEL-CODE DECODING
MP0569	MP0569D1C1	US	13/608,390	9/10/2012	8516332	8/20/2013	In Force	METHODS AND ALGORITHMS FOR JOINT CHANNEL-CODE DECODING
MP0573	MP0573	US	11/115,578	4/26/2005	7235061	11/13/2007	In Force	FULLY INTEGRATED PROGRAMMABLE GAIN CHOPPER AMPLIFIER WITH
MP0574	MP0574	US	11/030,002	1/4/2005	7545057	6/9/2009	In Force	RELAY CIRCUITRY AND SWITCHING CIRCUITRY FOR POWER-OVER-NE
MP0574	MP0574C1	US	12/465,523	5/13/2009	7915764	3/29/2011	In Force	RELAY CIRCUITRY AND SWITCHING CIRCUITRY FOR POWER-OVER-NE
MP0575	MP0575	US	11/078,717	3/10/2005	7664204	2/16/2010	In Force	ADAPTIVE TIMING USING CLOCK RECOVERY
MP0575	MP0575C1	US	12/680,062	1/19/2010	8077807	12/13/2011	In Force	ADAPTIVE TIMING USING CLOCK RECOVERY
MP0575	MP0575C1aa	US	13/604,383	9/5/2012	8477895	7/12/2013	In Force	ADAPTIVE TIMING USING CLOCK RECOVERY
MP0575	MP0575C1C1	US	13/323,480	12/12/2011	8265201	9/11/2012	In Force	ADAPTIVE TIMING USING CLOCK RECOVERY
MP0576	MP0576	US	11/037,733	1/18/2005	7518447	4/14/2009	In Force	Transimpedance Amplifier
MP0577	MP0577C1	US	11/077,394	3/10/2005	7657160	2/12/2010	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577C2HK	HK	61111419	8/30/2005			Pending	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577C3	US	11/077,396	3/10/2005	7702221	4/20/2010	In Force	Unified Control And Memory For A Combined DVD/HDD System

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MP0577	MP0577C3HK	HK	71032931	8/31/2005			Pending	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577CN	CN	2005100937513	8/29/2005	ZL2005100937	4/18/2012	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577CN2	CN	2005100938234	8/30/2005	ZL2005100938	5/16/2012	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577CN3	CN	200510093822X	8/30/2005	ZL2005100938	2/8/2012	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577CN4	CN	2005100979253	8/31/2005	ZL2005100979	6/22/2011	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577EP	EP	05018923.2	8/31/2005			Pending	Unified control and memory for a combined DVD/HDD system
MP0577	MP0577EP03	EP	EP05018615.4	8/26/2005			Pending	UNIFIED CONTROL AND MEMORY FOR A COMBINED DVD/HDD SYSTEM
MP0577	MP0577EP1	EP	05018815.0	8/30/2005			Pending	Unified control and memory for a combined DVD/HDD system
MP0577	MP0577EP2	EP	05018616.2	8/26/2005			Pending	UNIFIED CONTROL AND MEMORY FOR A COMBINED DVD/HDD SYSTEM
MP0577	MP0577HK	HK	61111393	8/29/2005			Pending	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577JP	JP	2005-252816	8/31/2005			Pending	UNIFIED CONTROL AND MEMORY FOR A COMBINED DVD/HDD SYSTEM
MP0577	MP0577JP2	JP	2005-252814	8/31/2005			Pending	UNIFIED CONTROL AND MEMORY FOR A COMBINED DVD/HDD SYSTEM
MP0577	MP0577JP3	JP	2005-252815	8/31/2005			Pending	UNIFIED CONTROL AND MEMORY FOR A COMBINED DVD/HDD SYSTEM
MP0577	MP0577TW	TW	94127628	8/12/2005	1394147	4/21/2013	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577TW2	TW	94127609	8/12/2005	1383380	1/21/2013	In Force	DVD Copying System And Virtual DVD Carousel Including The Same
MP0577	MP0577TW3	TW	94127614	8/12/2005	1394146	4/21/2013	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577TW4	TW	94127623	8/12/2005	1394145	4/21/2013	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0581	MP0581	US	10/974,458	10/27/2004	7702594	6/22/2010	In Force	Pipelined Packet Encryption And Decryption Using Counter Mode With
MP0581	MP0581D1	US	12/217,928	7/10/2008	8229110	7/24/2012	In Force	Pipelined Packet Encryption And Decryption Using Counter Mode With
MP0581	MP0581D1D1	US	13/555,825	7/23/2012	8631233	1/14/2014	In Force	Pipelined Packet Encryption And Decryption Using Counter Mode With
MP0581	MP0581D2	US	14/154,464	1/14/2014	9055039	6/9/2015	In Force	System And Method For Pipelined Encryption In Wireless Network De
MP0582	MP0582	US	11/142,176	5/31/2005	7208974	4/24/2007	In Force	RAIL-TO-RAIL SOURCE FOLLOWERS
MP0582	MP0582C1	US	11/737,085	4/18/2007	7463066	12/9/2008	In Force	RAIL-TO-RAIL SOURCE FOLLOWERS
MP0584	MP0584	US	11/094,980	3/31/2005	7626938	12/1/2009	In Force	Local Area Network Switch Using Control Plane Packet Mirroring To S
MP0585	MP0585	US	11/090,418	3/25/2005	7644147	1/5/2010	In Force	Remote Network Device Management
MP0585	MP0585C1	US	12/633,137	12/8/2009	8301745	10/30/2012	In Force	Remote Network Device Management
MP0586	MP0586	US	11/101,047	4/6/2005	7502358	3/10/2009	In Force	Diffusion Bucket Scheduler For Wireless Network Devices
MP0587	MP0587	US	11/103,755	4/12/2005	7531975	5/12/2009	In Force	Adjustable Frequency PWM Driver
MP0590	MP0590	US	11/095,039	3/30/2005	7446514	11/4/2008	In Force	LINEAR REGULATOR FOR USE WITH ELECTRONIC CIRCUITS
MP0590	MP0590C1	US	12/764,118	11/3/2008	7782041	8/24/2010	In Force	LINEAR REGULATOR FOR USE WITH ELECTRONIC CIRCUITS
MP0590	MP0590C1C1	US	12/858,735	8/18/2010	8217638	7/10/2012	In Force	LINEAR REGULATOR FOR USE WITH ELECTRONIC CIRCUITS
MP0591	MP0591	US	11/034,664	1/13/2005	7764792	7/27/2010	In Force	System And Method For Encoding Data Transmitted On A Bus
MP0591	MP0591RE	US	13/558,734	7/26/2012	RE44777	2/25/2014	In Force	SYSTEM AND METHOD FOR ENCODING DATA TRANSMITTED ON A B
MP0591	MP0591RED1	US	14/031,881	9/19/2013	RE45334	1/13/2015	In Force	SYSTEM AND METHOD FOR ENCODING DATA TRANSMITTED ON A B

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0592	MP0592	US	11/122,444	5/5/2005	7298173	11/20/2007	In Force	Slew Rate Control Circuit For Small Computer System Interface (SCSI)
MP0592	MP0592C1	US	11/985,706	11/16/2007	7579873	8/25/2009	In Force	Slew Rate Control Circuit For Small Computer System Interface (SCSI)
MP0592	MP0592C1C1	US	12/546,237	8/24/2009	7719314	5/18/2010	In Force	Slew Rate Control Circuit For Small Computer System Interface (SCSI)
MP0594	MP0594	US	11/092,095	3/28/2005	7511910	3/31/2009	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0594	MP0594AaA	US	13/357,406	1/24/2012	8477447	7/12/2013	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0594	MP0594D1	US	12/405,161	3/16/2009	7817368	10/19/2010	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0594	MP0594D1C1	US	12/901,087	10/8/2010	8120870	2/21/2012	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0595	MP0595	US	11/195,403	8/1/2005	7590923	9/15/2009	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR IDENTIFI
MP0595	MP0595C1	US	13/572,783	8/13/2012	8527850	9/3/2013	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR IDENTIFI
MP0595	MP0595D1	US	12/512,710	7/30/2009	8245118	8/14/2012	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR IDENTIFIC
MP0596	MP0596	US	11/195,087	8/1/2005	7444582	10/28/2008	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR-CORRECT
MP0596	MP0596C1	US	12/324,285	11/26/2008	8219894	7/10/2012	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR-CORRECT
MP0596	MP0596C1C1	US	13/554,830	7/20/2012	8788916	7/22/2014	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR-CORRECT
MP0596	MP0596D1	US	12/249,474	10/10/2008	8132082	3/6/2012	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR-CORRECT
MP0596	MP0596D1C1	US	13/364,802	2/2/2012	8296635	10/23/2012	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR-CORRECT
MP0597	MP0597	US	11/254,588	10/20/2005	7680053	3/16/2010	In Force	Inter-Device Flow Control
MP0597	MP0597C1	US	11/291,477	12/1/2005	7668104	2/23/2010	In Force	Inter-Device Flow Control
MP0597	MP0597C2	US	11/291,476	12/1/2005	7668103	2/23/2010	In Force	Inter-Device Flow Control
MP0597	MP0597C3	US	12/723,078	3/12/2010	7957285	6/7/2011	In Force	Inter-Device Flow Control
MP0597	MP0597C3aa	US	13/785,229	3/5/2013	8705355	4/22/2014	In Force	Network Switch And Method For Ascerting Flow Control Of Frames Tr
MP0597	MP0597C3C1	US	13/154,240	6/6/2011	8391144	3/5/2013	In Force	Inter-Device Flow Control
MP0598	MP0598	US	11/124,997	5/9/2005	7135996	11/14/2006	In Force	Multispeed Communications Device
MP0598	MP0598aaa	US	12/356,155	1/20/2009	7839309	11/23/2010	In Force	Multispeed Communications Device
MP0598	MP0598C1C1	US	11/801,316	5/9/2007	7479906	1/20/2009	In Force	Multispeed Communications Device
MP0599	MP0599	US	11/114,402	4/26/2005	7392412	6/24/2008	In Force	Low Power Mode For A Network Interface
MP0599	MP0599C1	US	12/157,239	6/9/2008	7937600	5/3/2011	In Force	Low Power Mode For A Network Interface
MP0599	MP0599C1C1	US	13/099,947	5/3/2011	8286017	10/9/2012	In Force	Low Power Mode For A Network Interface
MP0600	MP0600	US	11/170,960	6/29/2005	8688458	7/16/2013	In Force	Secure Unauthenticated Virtual Local Area Network
MP0600	MP0600C1	US	13/942,126	7/15/2013	9118555	8/25/2015	In Force	Secure Unauthenticated Virtual Local Area Network
MP0601	MP0601	US	11/098,129	4/4/2005	7594127	9/22/2009	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601aWDE	DE	05025065.3	11/16/2005	602005051172	1/18/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601aWFR	FR	05025065.3	11/16/2005	1662645	1/18/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601aWGB	GB	05025065.3	11/16/2005	1662645	1/18/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601bUDE	DE	05025066.1	11/16/2005	602005052835	10/4/2017	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY L

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MP0601	MP0601BUFR	FR	05025066.1	11/16/2005	1662646	10/4/2017	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY L
MP0601	MP0601BUGB	GB	05025066.1	11/16/2005	1662646	10/4/2017	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY L
MP0601	MP0601C1	US	11/179,904	7/12/2005	7788510	8/31/2010	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C1EP	EP	EP05025065.3	11/16/2005	EP1662645	1/18/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C1HK	HK	71007807	11/16/2005			Pending	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C1TW	TW	94140474	11/17/2005	1400866	7/1/2013	In Force	NETWORK DEVICE
MP0601	MP0601C2	US	11/179,106	7/12/2005	7762929	4/20/2010	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C2EP	EP	EP05025066.1	11/16/2005	EP1662646	10/4/2017	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY
MP0601	MP0601C2HK	HK	71007576	11/16/2005			Pending	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C3	US	11/179,100	7/12/2005	7788509	8/31/2010	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C3EP	EP	EP05024935.8	11/15/2005	EP1662643	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C3HK	HK	71007816	11/16/2005			Pending	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C1CN	CN	200510114984.1	11/16/2005	Z12005101149	7/18/2012	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY L
MP0601	MP0601CN2	CN	200510114983.1	11/16/2005	Z12005101149	4/13/2011	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY L
MP0601	MP0601CN3	CN	200510114988.1	11/16/2005	Z12005101149	4/13/2011	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY L
MP0601	MP0601CN4	CN	200510114989.1	11/16/2005	Z12005101149	4/13/2011	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY L
MP0601	MP0601EP	EP	EP05024946.5	11/15/2005	EP1662644	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601EPDE	DE	05024946.5	11/15/2005	602005051043	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601EPFR	FR	05024946.5	11/15/2005	1662644	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601EPGB	GB	05024946.5	11/15/2005	1662644	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601eUDE	DE	05024935.8	11/15/2005	602005051044	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601eUFR	FR	05024935.8	11/15/2005	1662643	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601eUGB	GB	05024935.8	11/15/2005	1662643	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601HK	HK	71007790	11/16/2005			Pending	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601JP	JP	2005-332225	11/16/2005	JP4777747	7/8/2011	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601JP02	JP	2005-332226	11/16/2005	JP5095099	9/28/2012	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601JP04	JP	2005-332228	11/16/2005	JP5036171	7/13/2012	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601JP3	JP	2005-332227	11/16/2005	JP4690177	2/25/2011	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601TW	TW	94140259	11/16/2005	1416856	11/21/2013	In Force	A PROCESS SYSTEM FOR PERFORMING LOAD BALANCING AND THRO
MP0601	MP0601TW3	TW	94140475	11/17/2005	1400867	7/1/2013	In Force	A PROCESSING SYSTEM AND METHOD FOR REDUCING A DIFFERENCE
MP0602	MP0602	US	11/056,753	2/11/2005	7864816	1/4/2011	In Force	Integrated Circuit For Network Delay And Jitter Testing
MP0604	MP0604	US	11/169,957	6/28/2005	7233182	6/19/2007	In Force	CIRCUITRY FOR ELIMINATING FALSE LOCK IN DELAY-LOCKED LOOPS
MP0605	MP0605	US	11/109,372	4/19/2005	8035446	10/11/2011	In Force	Natural Sampling For A Digital Class-D Power Amplifier
MP0606	MP0606	US	11/109,988	4/20/2005	7539456	10/6/2009	In Force	Input/Output Data Rate Synchronization Using First in First Out Data

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MP0606	MP0606C1	US	12/574,133	10/6/2009	7924960	4/12/2011	In Force	Input/Output Data Rate Synchronization Using First In First Out Data
MP0607	MP0607AUDE	DE	06002269.6	2/3/2006	602906049098	5/18/2016	In Force	Hard Disk Drive System For Distributing Protected Content
MP0607	MP0607AUFR	FR	06002269.6	2/3/2006	1705593	5/18/2016	In Force	Hard Disk Drive System For Distributing Protected Content
MP0607	MP0607AUGB	GB	06002269.6	2/3/2006	1705593	5/18/2016	In Force	Hard Disk Drive System For Distributing Protected Content
MP0607	MP0607C1CA	CN	201310090941	3/20/2006			Pending	DVD SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607C1CN	CN	2006100648927	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607C1EP	EP	EP06002269.6	2/3/2006	EP1705593	5/18/2016	In Force	Hard Disk Drive System for Distributing Protected Content
MP0607	MP0607C1JP	JP	2006-077577	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607C1SG	SG	2006008262	2/8/2006	126031	11/28/2008	In Force	Hard Disk Drive System For Distributing Protected Content
MP0607	MP0607C1TW	TW	95109456	3/20/2006			Pending	HARD DISK DRIVE SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607C2CN	CN	200610064893.1	3/20/2006			Pending	DVD SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607C2DE	DE	06002265.4	2/3/2006	60 2006 015 51	7/21/2010	In Force	DVD System For Distributing Protected Content
MP0607	MP0607C2EP	EP	EP06002265.4	2/3/2006	EP1708112	7/21/2010	In Force	DVD System for Distributing Protected Content
MP0607	MP0607C2FR	FR	06002265.4	2/3/2006	1708112	7/21/2010	In Force	DVD System For Distributing Protected Content
MP0607	MP0607C2GB	GB	06002265.4	2/3/2006	1708112	7/21/2010	In Force	DVD System For Distributing Protected Content
MP0607	MP0607C2JP	JP	2006-077578	3/20/2006	JP4691462	2/25/2011	In Force	DVD System For Distributing Protected Content
MP0607	MP0607C2SG	SG	2006008247	2/8/2006	126030	10/31/2008	In Force	DVD System For Distributing Protected Content
MP0607	MP0607C2TW	TW	95109457	3/20/2006			Pending	DVD SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607CN	CN	2006100648912	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607EP	EP	60022464	2/3/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607Ffa	US	11/725,387	3/19/2007	9197434	11/24/2015	In Force	Network System For Distributing Protected Content
MP0607	MP0607FTW	TW	96109594	3/20/2007	1479847	4/1/2015	In Force	Network System For Distributing Protected Content
MP0607	MP0607FFWE	EP	07755328.4	3/3/2007	EP1997296	8/14/2013	In Force	Network System for Distributing Protected Content
MP0607	MP0607FWCJP	JP	2008503151	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607HK	HK	71029133	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607HK1	HK	71029106	3/20/2006			Pending	Hard Disk Drive System For Distributing Protected Content
MP0607	MP0607HK2	HK	71029115	3/20/2006			Pending	DVD System For Distributing Protected Content
MP0607	MP0607HK4	HK	81046658	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607I1	US	11/384,571	3/20/2006	7991887	8/2/2011	In Force	Network System For Distributing Protected Content
MP0607	MP0607I1I1WO	WO	PCT/US2007/00	3/3/2007			Pending	Network System For Distributing Protected Content
MP0607	MP0607I1TW	TW	96109593	3/20/2007	1462549	11/21/2014	In Force	Network System For Distributing Protected Content
MP0607	MP0607I1WO	WO	PCT/US2006/01	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607IODE	DE	07755328.4	3/3/2007	1997296	8/14/2013	In Force	NETWORK SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607IDFR	FR	07755328.4	3/3/2007	1997296	8/14/2013	In Force	NETWORK SYSTEM FOR DISTRIBUTING PROTECTED CONTENT

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MP0607	MP0607IGGB	GB	07753528.4	3/3/2007	1997296	8/14/2013	In Force	NETWORK SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607JP	JP	2006-077576	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607SG	SG	2006008270	2/8/2006	126632	1/30/2009	In Force	Network System For Distributing Protected Content
MP0607	MP0607TW	TW	95109459	3/20/2006			Pending	NETWORK SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607WCN4	CN	200680001352	3/20/2006	212006800013	5/19/2010	In Force	NETWORK SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607WOJP	JP	2009-501509	3/3/2007	JP4778088	7/8/2011	In Force	NETWORK SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0608	MP0608	US	11/110.623	4/19/2005	7711060	5/4/2010	In Force	DATA STREAM INTERLEAVING WITH NON-MONOTONICALLY ARRANG
MP0608	MP0608C1	US	12/759.643	4/13/2010	8080045	1/3/2012	In Force	DATA STREAM INTERLEAVING WITH NON-MONOTONICALLY ARRANG
MP0608	MP0608C1aa	US	14/148.353	1/6/2014	8837623	9/16/2014	In Force	DATA STREAM INTERLEAVING WITH NON-MONOTONICALLY ARRANG
MP0608	MP0608C1C1	US	13/338.137	12/27/2011	8625699	1/7/2014	In Force	DATA STREAM INTERLEAVING WITH NON-MONOTONICALLY ARRANG
MP0608	MP0608C1Za	US	14/486.609	9/15/2014	8989297	3/24/2015	In Force	DATA STREAM INTERLEAVING WITH NON-MONOTONICALLY ARRANG
MP0608	MP0608ZZa	US	14/664.601	3/20/2015	9215049	12/15/2015	In Force	DATA STREAM INTERLEAVING WITH DIFFERENT ROTATION APPLIED
MP0608	MP0608ZZaa	US	14/968.489	12/14/2015	9882686	1/30/2018	In Force	Data Stream interleaving with Different Rotations Applied to Change
MP0609	MP0609C1C1	US	11/680.513	3/23/2007	7365510	4/29/2008	In Force	MOTOR SPINDLE CONTROL SYSTEM AND METHOD
MP0612	MP0612	US	11/114.842	4/26/2005	7599391	10/6/2009	In Force	Media And Speed Independent Interface
MP0612	MP0612Baa	US	12/836.226	7/14/2010	8330400	11/27/2012	In Force	Media And Speed Independent Interface
MP0612	MP0612BaaA	US	13/685.257	11/26/2012	8565261	10/22/2013	In Force	Media And Speed Independent Interface
MP0612	MP0612bAza	US	14/660.011	10/22/2013	8953637	2/10/2015	In Force	Media And Speed Independent Interface
MP0612	MP0612C1	US	11/155.394	6/17/2005	7710998	5/4/2010	In Force	Media And Speed Independent Interface
MP0612	MP0612C2	US	11/156.059	6/17/2005	7593416	9/22/2009	In Force	Media And Speed Independent Interface
MP0612	MP0612C2D1	US	12/152.577	5/15/2008	7760750	7/20/2010	In Force	Media And Speed Independent Interface
MP0613	MP0613	US	11/116.988	4/27/2005	7639687	12/29/2009	In Force	ENCODING SCHEME WITH ARBITRARY CONTROL SYMBOL PLACEMENT
MP0615	MP0615	US	11/034.846	1/13/2005	8347034	1/1/2013	In Force	A TRANSPARENT LEVEL 2 CACHE THAT USES INDEPENDENT TAG AND
MP0616	MP0616	US	11/176.998	7/7/2005	7375559	5/20/2008	In Force	DIFFERENTIAL COMPARATOR WITH A REPLICA INPUT STAGE TO SET I
MP0617	MP0617	US	11/274.854	11/16/2005	8139628	3/20/2012	In Force	METHOD AND DEVICE TO COMPENSATE FOR BASELINE WANDER
MP0617	MP0617C1	US	13/418.013	3/12/2012	846351	3/26/2013	In Force	METHOD AND DEVICE TO COMPENSATE FOR BASELINE WANDER
MP0618	MP0618C1	US	11/797.196	1/5/2007	7327106	5/2/2008	In Force	SYSTEM AND PROCESS FOR UTILIZING BACK ELECTROMOTIVE FORCE
MP0618	MP0618C1AA	US	12/493.944	6/29/2009	7839102	11/23/2010	In Force	SYSTEM AND PROCESS FOR UTILIZING BACK ELECTROMOTIVE FORCE
MP0618	MP0618C1C1	US	12/026.274	5/2/2008	7560883	7/14/2009	In Force	SYSTEM AND PROCESS FOR UTILIZING BACK ELECTROMOTIVE FORCE
MP0621	MP0621	US	11/311.890	12/19/2005	7751374	7/6/2010	In Force	WLAN TDM Protocol
MP0621	MP0621bJP2	JP	2006-009289	1/17/2006	JP4841959	10/14/2011	In Force	New WLAN TDM Protocol
MP0621	MP0621bUDE	DE	06000610.3	1/12/2006	6.02066E+12	10/24/2012	In Force	New WLAN TDM Protocol
MP0621	MP0621buFR	FR	06000610.3	1/12/2006	1681806	10/24/2012	In Force	New WLAN TDM Protocol
MP0621	MP0621buGB	GB	06000610.3	1/12/2006	1681806	10/24/2012	In Force	New WLAN TDM Protocol

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MP0621	MP0621C1	US	11/321,914	12/29/2005	7920530	4/5/2011	In Force	WLAN TDM IPS Time Selection Protocol
MP0621	MP0621C1/HK	HK	71023479	1/18/2006			Pending	New WLAN TDM Protocol
MP0621	MP0621C1aa	US	13/934,267	7/3/2013	9301247	3/29/2016	In Force	Wireless Local Area Network (WLAN) Time Division Multiplexed (TDM)
MP0621	MP0621C1CN	CN	2006100015331	1/18/2006	ZL2006100015	6/15/2011	In Force	NEW WLAN TDM PROTOCOL
MP0621	MP0621C1DE	DE	0600609.5	1/12/2006	60200602631E	12/14/2011	In Force	New WLAN TDM Protocol
MP0621	MP0621C1EP	EP	0600609.5	1/12/2006	EP1681805	12/14/2011	In Force	New WLAN TDM protocol
MP0621	MP0621C1FR	FR	0600609.5	1/12/2006	1681805	12/14/2011	In Force	New WLAN TDM Protocol
MP0621	MP0621C1GB	GB	0600609.5	1/12/2006	1681805	12/14/2011	In Force	New WLAN TDM Protocol
MP0621	MP0621C1JP	JP	2006-009288	1/17/2006	JP4863718	11/18/2011	In Force	New WLAN TDM Protocol
MP0621	MP0621C1SG	SG	2006005151	1/17/2006	124398	7/31/2008	In Force	New WLAN TDM Protocol
MP0621	MP0621C1TW	TW	95101900	1/18/2006	1385973	2/11/2013	In Force	NEW WLAN TDM PROTOCOL
MP0621	MP0621C2	US	11/321,915	12/29/2005	8483190	7/9/2013	In Force	Wireless Local Area Network (WLAN) Time Division Multiplexed (TDM)
MP0621	MP0621C2CN	CN	2006100015331	1/18/2006	ZL2006100015	4/22/2015	In Force	A WIRELESS NETWORK DEVICE AND A METHOD FOR TRANSMITTING
MP0621	MP0621C2EP	EP	0600610.3	1/12/2006	EP1681806	10/24/2012	In Force	New WLAN TDM Protocol
MP0621	MP0621C2HK	HK	71010349	1/18/2006			Pending	New WLAN TDM Protocol
MP0621	MP0621C2SG	SG	2006005193	1/17/2006	124399	1/30/2009	In Force	New WLAN TDM Protocol
MP0621	MP0621C2TW	TW	95101901	1/18/2006	138597	3/11/2013	In Force	NEW WLAN TDM PROTOCOL
MP0621	MP0621CN	CN	2006100015341	1/18/2006	ZL2006100015	6/15/2011	In Force	NEW WLAN TDM PROTOCOL
MP0621	MP0621DE	DE	0600608.7	1/12/2006	60200600573E	3/18/2009	In Force	New WLAN TDM Protocol
MP0621	MP0621EP	EP	0600608.7	1/12/2006	EP1722511	3/18/2009	In Force	New WLAN TDM protocol
MP0621	MP0621FR	FR	0600608.7	1/12/2006	1722511	3/18/2009	In Force	New WLAN TDM Protocol
MP0621	MP0621GB	GB	0600608.7	1/12/2006	1722511	3/18/2009	In Force	New WLAN TDM Protocol
MP0621	MP0621JP	JP	2006-009287	1/17/2006	JP4829620	9/22/2011	In Force	New WLAN TDM Protocol
MP0621	MP0621SG	SG	2006005144	1/17/2006	124397	7/31/2008	In Force	New WLAN TDM Protocol
MP0621	MP0621TW	TW	95101898	1/18/2006	1424714	1/21/2014	In Force	WIRELESS NETWORK DEVICE WIRELESS NETWORK COMPRISING A PL
MP0624	MP0624C1	US	12/950,722	11/19/2010	8452001	5/28/2013	In Force	Class A-B Line Driver For Gigabit Ethernet
MP0625	MP0625C1CN	CN	2006100651431	3/21/2006	ZL2006100651	9/5/2012	In Force	WIRELESS PHONE SYSTEM
MP0625	MP0625C1EP	EP	60057643	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625C1JP	JP	2006-046004	2/22/2006	JP5208370	3/1/2013	In Force	WIRELESS PHONE SYSTEM
MP0625	MP0625C1SG	SG	2006014328	3/7/2006	126825	10/31/2008	In Force	Wireless Phone System With Wireless Modes For Short Range And Lo
MP0625	MP0625C1TW	TW	95109636	3/21/2006	1391019	3/21/2013	In Force	WIRELESS PHONE SYSTEM
MP0625	MP0625C2CN	CN	2006100651440	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625C2EP	EP	60057627	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625C2JP	JP	200646005	2/22/2006			Pending	Wireless Phone System

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MP0625	MP0625C2IP02	JP	2006-046005	2/22/2006			Pending	WIRELESS PHONE SYSTEM
MP0625	MP0625C2SG	SG	2006013593	3/7/2006	126824	12/31/2008	In Force	Wireless Phone System With Wireless Modes For Short Range And Lo
MP0625	MP0625C2TW	TW	95109633	3/21/2006			Pending	WIRELESS PHONE SYSTEM
MP0625	MP0625EP	EP	60057601	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625HK	HK	71029124	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625HK1	HK	71029142	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625HK2	HK	71029151	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625JP	JP	2006-046003	2/22/2006	JP4963845	4/6/2012	In Force	WIRELESS PHONE SYSTEM
MP0626	MP0626	US	11/135,557	5/23/2005	7610013	10/27/2009	In Force	Wireless Audio For Entertainment Systems
MP0626	MP0626/HK	HK	71039907	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626/HK2	HK	71039890	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626/HK3	HK	71039881	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626aaaJP	JP	2012139821	6/21/2012			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626CN	CN	200610057074	3/17/2006	ZL2006100570	11/21/2012	In Force	WIRELESS AUDIO FOR ENTERTAINMENT SYSTEMS
MP0626	MP0626CN01	CN	2006100570759	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626CN02	CN	2006100570693	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626EP	EP	EP06005402	3/16/2006	EP1717995	5/6/2015	In Force	Wireless Audio and Video For Entertainment Systems
MP0626	MP0626EP01	EP	60053840	3/16/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626EP02	EP	60054038	3/16/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626EPDE	DE	06005402.0	3/16/2006	602006045335	5/6/2015	In Force	Wireless Audio For Entertainment Systems
MP0626	MP0626EPFR	FR	06005402.0	3/16/2006	1717995	5/6/2015	In Force	Wireless Audio For Entertainment Systems
MP0626	MP0626EPGB	GB	06005402.0	3/16/2006	1717995	5/6/2015	In Force	Wireless Audio For Entertainment Systems
MP0626	MP0626JP	JP	2006075365	3/17/2006	JP5089062	9/21/2012	In Force	Wireless Audio For Entertainment Systems
MP0626	MP0626JP01	JP	200675372	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626JP02	JP	200675476	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626SG	SG	2006016802	3/17/2006	126834	10/31/2008	In Force	Wireless Audio And Video For Entertainment Systems
MP0626	MP0626SG01	SG	2006016794	3/17/2006	126833	10/31/2008	In Force	Wireless Audio For Entertainment Systems
MP0626	MP0626SG4	SG	2008077943	3/17/2006			Pending	Wireless Audio And Video For Entertainment Systems
MP0626	MP0626TW	TW	95108996	3/16/2006	1422237	1/1/2014	In Force	WIRELESS AUDIO FOR ENTERTAINMENT SYSTEMS
MP0626	MP0626TW01	TW	95108997	3/16/2006			Pending	WIRELESS AUDIO FOR ENTERTAINMENT SYSTEMS
MP0626	MP0626TW02	TW	95108999	3/16/2006			Pending	WIRELESS AUDIO FOR ENTERTAINMENT SYSTEMS
MP0627	MP0627	US	11/112,808	4/22/2005	7560866	7/14/2009	In Force	Control System For Fluorescent Light Fixture
MP0627	MP0627/HK	HK	71039872	3/29/2006			Pending	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627/HK/01	HK	71039863	3/29/2006			Pending	Improved Control System For Fluorescent Light Fixture

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MP0627	MP0627C1	US	12/502,570	7/14/2009	8120286	2/21/2012	In Force	Control System For Fluorescent Light Fixture
MP0627	MP0627CN	CN	2006100668303	3/29/2006	ZL2006100668	5/18/2011	In Force	IMPROVED CONTROL SYSTEM FOR FLUORESCENT LIGHT FIXTURE
MP0627	MP0627CN01	CN	2006100584761	3/28/2006	ZL2006100584	6/29/2011	In Force	IMPROVED CONTROL SYSTEM FOR FLUORESCENT LIGHT FIXTURE
MP0627	MP0627DE	DE	60063070	3/27/2006	6.02006e+12	10/8/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627EP	EP	60063070	3/27/2006	1718133	10/8/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627EP01	EP	60063088	3/27/2006	1720382	9/24/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627FR	FR	60063070	3/27/2006	1718133	10/8/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627GB	GB	60063070	3/27/2006	1718133	10/8/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627IL	US	11/190,025	7/26/2005	7414369	8/19/2008	In Force	Control System For Fluorescent Light Fixture
MP0627	MP0627ILDE	DE	60063088	3/27/2006	6.02006e+12	9/24/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627ILFR	FR	60063088	3/27/2006	1720382	9/24/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627ILGB	GB	60063088	3/27/2006	1720382	9/24/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627JP	JP	2006099049	3/31/2006	JP5204379	2/22/2013	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627JP01	JP	2006098164	3/31/2006	JP4830083	8/12/2011	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627JPD1	JP	2012040573	2/27/2012	JP5379875	10/4/2013	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627SG	SG	2006018683	3/22/2006	126839	10/31/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627SG01	SG	2006018675	3/22/2006	126838	10/31/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627TW	TW	95111645	3/31/2006	1405503	8/11/2013	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627TW2	TW	95111643	3/31/2006	1426827	2/11/2014	In Force	Improved Control System For Fluorescent Light Fixture
MP0630	MP0630	US	11/170,904	6/29/2005	8028160	9/27/2011	In Force	Data Link Layer Switch With Protection Against Internet Protocol Spo
MP0630	MP0630C1	US	13/245,501	9/26/2011	8661241	2/25/2014	In Force	Data Link Layer Switch With Protection Against Internet Protocol Spo
MP0630	MP0630C1C1	US	14/187,662	2/24/2014	9241005	1/19/2016	In Force	METHOD AND APPARATUS FOR UPDATING TRAFFIC PATTERNS OF PA
MP0631	MP0631	US	11/180,652	7/14/2005	8081715	12/20/2011	In Force	DEVICE AND METHOD FOR SAMPLING BASED ON MATCHED FILTERIN
MP0633	MP0633	US	11/084,364	3/18/2005	8195922	6/5/2012	In Force	SYSTEM FOR DYNAMICALLY ALLOCATING PROCESSING TIME TO MUL
MP0633	MP0633C1	US	13/485,642	5/31/2012	8468324	6/18/2013	In Force	DUAL THREAD PROCESSOR
MP0633	MP0633CN	CN	2006100653313	3/17/2006	ZL2006100653	12/3/2008	In Force	DUAL THREAD PROCESSOR
MP0633	MP0633EP	EP	6005551.4	3/17/2006			Pending	DUAL THREAD PROCESSOR
MP0633	MP0633FMDE	DE	06065552.2	3/17/2006	1703375	8/26/2015	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES
MP0633	MP0633FWFR	FR	06065552.2	3/17/2006	1703375	8/26/2015	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES
MP0633	MP0633FWGB	GB	06065552.2	3/17/2006	1703375	8/26/2015	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES
MP0633	MP0633HK	HK	7100730.8	3/17/2006			Pending	DUAL THREAD PROCESSOR
MP0633	MP0633ILCN	CN	2006100655283	3/20/2006	ZL2006100655	12/22/2010	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES
MP0633	MP0633IIEP	EP	EP06005552.2	3/17/2006	EP1703375	8/26/2015	In Force	Real-time control apparatus having a multi-thread processor
MP0633	MP0633ILHK	HK	7100729.1	3/17/2006			Pending	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES

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MP0633	MP063311JP	JP	2006-072084	3/16/2006	JP4410768	11/20/2009	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES
MP0633	MP063311SG	SG	200601676-0	3/17/2006	126673	8/31/2009	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES
MP0633	MP063311TW	TW	95108584	3/14/2006	1326428	6/21/2010	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES
MP0633	MP0633JP	JP	2006-071828	3/15/2006			Pending	DUAL THREAD PROCESSOR
MP0633	MP0633JPD1	JP	2007-242577	3/15/2006	JP4610593	10/22/2010	In Force	DUAL THREAD PROCESSOR
MP0633	MP0633SG	SG	200601675-2	3/17/2006	126672	3/31/2009	In Force	DUAL THREAD PROCESSOR
MP0633	MP0633TW	TW	95108580	3/14/2006	1315848	10/11/2009	In Force	DUAL THREAD PROCESSOR
MP0634	MP0634	US	11/070,780	3/1/2005	8074056	12/6/2011	In Force	VARIABLE LENGTH PIPELINE PROCESSOR ARCHITECTURE
MP0634	MP0634C1	US	13/311,184	12/5/2011	8969903	12/9/2014	In Force	PROVIDING DATA TO REGISTERS BETWEEN EXECUTION STAGES
MP0635	MP0635	US	11/068,441	2/28/2005	7385959	6/10/2008	In Force	Power-Efficient Channel Condition Feedback For OFDM Channels
MP0635	MP0635C1	US	12/154,031	5/20/2008	8045535	10/25/2011	In Force	Power-Efficient Channel Condition Feedback For OFDM Channels
MP0635	MP0635C1aa	US	14/138,644	12/23/2013	9143216	9/22/2015	In Force	Apparatus And Method For Transitioning A Circuit For Determining A
MP0635	MP0635C1C1	US	13/279,697	10/24/2011	8619656	12/31/2013	In Force	Power-Efficient Channel Condition Feedback For OFDM Channels
MP0636	MP0636	US	11/218,147	9/1/2005	7539694	5/26/2009	In Force	Concurrently Searching And Manipulating Binary Trees
MP0638	MP0638	US	11/206,384	8/17/2005	7449958	11/11/2008	In Force	OPEN LOOP DC CONTROL FOR A TRANSMITTANCE FEEDBACK AMPL
MP0638	MP0638C1	US	12/174,882	7/17/2008	7545219	6/9/2009	In Force	OPEN LOOP DC CONTROL FOR A TRANSMITTANCE FEEDBACK AMPL
MP0639	MP0639	US	11/204,069	8/15/2005	7636297	12/22/2009	In Force	Transmit Diversity Technique Based On Channel Randomization For
MP0639	MP0639C1	US	12/663,169	12/21/2009	8045450	10/25/2011	In Force	Transmit Diversity Technique Based On Channel Randomization For
MP0639	MP0639C1aa	US	14/281,368	5/19/2014	9001640	4/7/2015	In Force	Transmit Diversity Technique Based On Channel Randomization For
MP0639	MP0639C1C1	US	13/278,893	10/21/2011	8730789	5/20/2014	In Force	Transmit Diversity Technique Based On Channel Randomization For
MP0639	MP0639Zaa	US	14/679,630	4/6/2015	9246621	1/26/2016	In Force	Transmit Diversity Technique Based On Channel Randomization For
MP0640	MP0640	US	11/194,278	8/1/2005	7533240	5/12/2009	In Force	Device With Mapping Between Non-Programmable And Programmab
MP0641	MP0641C1	US	11/806,394	5/31/2007	7385364	6/10/2008	In Force	SYSTEM AND METHOD FOR DETECTING BACK ELECTROMOTIVE FORC
MP0642	MP0642	US	11/199,840	8/9/2005	7778336	8/17/2010	In Force	TIMING AND FREQUENCY SYNCHRONIZATION OF OFDM SIGNALS FO
MP0643	MP0643	US	11/218,809	9/2/2005	7529179	5/5/2009	In Force	Joint Maximum Likelihood Estimation Of Integer Carrier Frequency O
MP0645	MP0645	US	11/143,049	6/2/2005	7529178	5/5/2009	In Force	Automatic Gain Control For OFDM Receivers For Transmission Of Bur
MP0645	MP0645C1	US	12/434,924	5/4/2009	7889634	2/15/2011	In Force	Method And Apparatus For Transmitting An OFDM Signal
MP0646	MP0646	US	11/185,563	7/20/2005	7440209	10/21/2008	In Force	Communications Channel With Programmable Coupling
MP0650	MP0650	US	11/200,659	8/10/2005	7817661	10/19/2010	In Force	Dual-Media Network Interface That Automatically Disables Inactive
MP0650	MP0650C1	US	12/306,934	10/18/2010	8472470	6/25/2013	In Force	Method And Apparatus For Automatically Disabling An Interface To
MP0650	MP0650C1C1	US	13/921,987	6/19/2013	8982906	3/17/2015	In Force	Dual-Media Network Interface That Automatically Disables Inactive
MP0654	MP0654	US	11/204,484	8/16/2005	7916640	3/29/2011	In Force	Buffer Overflow Prevention For Network Devices
MP0654	MP0654C1	US	13/074,340	3/29/2011	8385204	2/26/2013	In Force	Buffer Overflow Prevention For Network Devices
MP0654	MP0654C1aa	US	14/077,819	11/12/2013	8857359	10/21/2014	In Force	Preventing Buffer Overflow By Adjusting Transmit Window Size

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0654	MP0654C1C1	US	13/774,379	2/22/2013	8582439	11/12/2013	In Force	Buffer Overflow Prevention For Network Devices
MP0655	MP0655	US	11/195,183	8/1/2005	7454690	11/18/2008	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON LIST DECODING
MP0655	MP0655D1	US	12/256,652	10/23/2008	7596924	9/15/2009	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON LIST DECODING
MP0655	MP0655D1D1	US	12/541,720	8/14/2009	8112083	3/6/2012	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON LIST DECODING
MP0656	MP0656	US	11/188,661	7/26/2005	7560309	7/14/2009	In Force	Drop-In Heat Sink And Exposed Die-Back For Molded Flip Die Package
MP0657	MP0657	US	11/171,563	6/30/2005	9047765	6/2/2015	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657C3	US	11/229,915	9/30/2005	7260472	8/21/2007	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657CN	CN	200610083623.1	5/29/2006	212006100836	1/12/2011	In Force	GPS-BASED TRAFFIC MONITORING SYSTEM
MP0657	MP0657EP	EP	06011022.8	5/29/2006	EP1742191	7/6/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657EPDE	DE	06011022.8	5/29/2006	602006022864	7/6/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657EPFR	FR	06011022.8	5/29/2006	1742191	7/6/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657EPGB	GB	06011022.8	5/29/2006	1742191	7/6/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657FATW	TW	96130864	8/21/2007	1442352	6/21/2014	In Force	GPS-BASED TRAFFIC MONITORING SYSTEM
MP0657	MP0657FF3	US	11/787,384	4/16/2007	8064931	11/22/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657FFAWO	WO	PCT/US2007/02	9/21/2007			Pending	GPS-Based Traffic Monitoring System
MP0657	MP0657FFB	US	11/787,281	4/16/2007	7885759	2/8/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657FFE	US	11/787,296	4/16/2007	7885760	2/8/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657FFEWO	WO	PCT/US2007/02	10/9/2007			Pending	GPS-Based Traffic Monitoring System
MP0657	MP0657FFH	US	11/787,350	4/16/2007	7983839	7/19/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657FFFTW	TW	96130861	8/21/2007	1434238	4/11/2014	In Force	GPS-BASED TRAFFIC MONITORING SYSTEM
MP0657	MP0657H11	US	11/637,173	12/11/2005	7885758	2/8/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657H11WO	WO	PCT/US2007/01	8/23/2007			Pending	GPS-Based Traffic Monitoring System
MP0657	MP0657HETW	TW	96130868	8/21/2007	1451366	9/1/2014	In Force	GPS-Based Traffic Monitoring System, And Vehicle And Cellular Phon
MP0657	MP0657JP	JP	2006149908	5/30/2006	JP5025993	6/29/2012	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657SG	SG	2006034532	5/30/2006	128558	2/27/2009	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657TW	TW	95119042	5/29/2006	1407382	9/1/2013	In Force	GPS-Based Traffic Monitoring System
MP0658	MP0658	US	11/372,865	3/9/2006	7586199	9/8/2009	In Force	STRUCTURES, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS A
MP0658	MP0658D1	US	12/542,619	8/17/2009	7956474	6/7/2011	In Force	STRUCTURES, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS A
MP0658	MP0658D1a	US	13/905,896	5/30/2013	8963342	2/24/2015	In Force	Structures, Architectures, Systems, Methods, Algorithms and Softwar
MP0658	MP0658D1C1	US	13/154,180	6/6/2011	8455347	6/4/2013	In Force	Structures, Architectures, Systems, Methods, Algorithms and Softwar
MP0658	MP0658H1	US	11/867,615	10/4/2007	8405220	3/26/2013	In Force	STRUCTURES, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS A
MP0658	MP0658H1C1	US	13/850,144	3/25/2013	9524927	12/20/2016	In Force	STRUCTURES, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS A
MP0659	MP0659	US	11/223,087	9/9/2005	7788570	8/31/2010	In Force	Optimized Reed-Solomon Decoder
MP0659	MP0659C1	US	11/327,934	1/9/2006	7721185	5/18/2010	In Force	Optimized Reed-Solomon Decoder

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0661	MP0661	US	11/196,993	8/4/2005	7548563	6/16/2009	In Force	Data Transmission Using Address Encoding
MP0661	MP0661C1	US	12/480,123	6/8/2009	8077742	12/13/2011	In Force	Data Transmission Using Address Encoding
MP0662	MP0662	US	11/196,961	8/4/2005	7826447	11/2/2010	In Force	Preventing Denial-Of-Service Attacks Employing Broadcast Packets
MP0662	MP0662C1	US	12/917,417	11/1/2010	8830997	9/9/2014	In Force	Preventing Denial-Of-Service Attacks Employing Broadcast Packets
MP0663	MP0663	US	11/388,343	3/23/2006	8022522	9/20/2011	In Force	SEMICONDUCTOR PACKAGE
MP0663	MP0663D1	US	13/224,390	9/16/2011	8482112	7/9/2013	In Force	SEMICONDUCTOR PACKAGE
MP0663	MP0663D1C1	US	13/937,051	7/8/2013	9224677	12/29/2015	In Force	SEMICONDUCTOR PACKAGE
MP0664	MP0664C1	US	12/619,761	11/17/2009	7885192	2/8/2011	In Force	Generalized Auto Media Selector
MP0664	MP0664C1C1	US	13/023,389	2/8/2011	8582434	11/12/2013	In Force	Generalized Auto Media Selector
MP0668	MP0668	US	11/247,941	10/10/2005	8265255	9/11/2012	In Force	End-To-End Session Without Signaling Protocol
MP0668	MP0668C1	US	13/608,710	9/10/2012	8817967	8/26/2014	In Force	End-To-End Session Without Signaling Protocol
MP0672	MP0672aaba	US	13/942,172	7/15/2013	9586025	3/14/2017	In Force	Method For Providing Internet Access To Network Devices Via A Mob
MP0672	MP0672C1	US	12/711,577	2/24/2010	8036721	10/11/2011	In Force	Wearable Access Point
MP0672	MP0672C1aa	US	13/532,357	6/25/2012	8489059	7/16/2013	In Force	Wearable Access Point
MP0674	MP0674	US	11/256,465	10/21/2005	7646718	1/12/2010	In Force	Flexible Port Rate Limiting
MP0674	MP0674C1	US	12/685,310	1/11/2010	8553969	11/26/2013	In Force	Method And Apparatus For Rate-Limiting Traffic Flow Of Packets In A
MP0675	MP0675CN	CN	200610083503.1	5/30/2006			Pending	WIRELESS LOCAL AREA NETWORK COMMUNICATIONS MODULE AND
MP0675	MP0675CN03	CN	200610083505.4	5/30/2006	212006100835	1/23/2013	In Force	WIRELESS LOCAL AREA NETWORK COMMUNICATIONS MODULE AND
MP0675	MP0675CN2	CN	200610083502.4	5/30/2006	212006100835	8/28/2013	In Force	WIRELESS LOCAL AREA NETWORK COMMUNICATIONS MODULE AND
MP0675	MP0675D1EP	EP	6010761.2	5/24/2006	1737069	1/11/2017	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675D1EPDE	DE	06010761.2	5/24/2006	602006051484	1/11/2017	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675D1EPFR	FR	06010761.2	5/24/2006	1737069	1/11/2017	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675D1EPGB	GB	06010761.2	5/24/2006	1737069	1/11/2017	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675D2EP	EP	EP06010742.2	5/24/2006	EP1737067	12/28/2016	In Force	Wireless local area network communications module and integrated
MP0675	MP0675D2EPDE	DE	06010742.2	5/24/2006	602006051341	12/28/2016	In Force	Wireless local Area Network Communications Module And Integrate
MP0675	MP0675D2EPFR	FR	06010742.2	5/24/2006	1737067	12/28/2016	In Force	Wireless local Area Network Communications Module And Integrate
MP0675	MP0675D2EPGB	GB	06010742.2	5/24/2006	1737067	12/28/2016	In Force	Wireless local Area Network Communications Module And Integrate
MP0675	MP0675D3EP	EP	EP06010755.4	5/24/2006	1737068	3/21/2018	In Force	Wireless local Area Network Communications Module and Integrate
MP0675	MP0675EP	EP	EP06010553.3	5/22/2006	EP1737065	11/1/2017	In Force	Wireless local Area Network Communications Module and Integrate
MP0675	MP0675EPDE	DE	EP06010553.3	5/22/2006	602006053975	11/1/2017	In Force	Wireless local Area Network Communications Module and Integrate
MP0675	MP0675EPFR	FR	EP06010553.3	5/22/2006	1737065	11/1/2017	In Force	Wireless local Area Network Communications Module and Integrate
MP0675	MP0675EPGB	GB	EP06010553.3	5/22/2006	1737065	11/1/2017	In Force	Wireless local Area Network Communications Module and Integrate
MP0675	MP0675EUDE	DE	EP06010755.4	5/24/2006	602006054941	3/21/2018	In Force	Wireless local Area Network Communications Module and Integrate

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0675	MP0675EUF8	FR	EP06010755.4	5/24/2006	1737068	3/21/2018	In Force	Wireless Local Area Network Communications Module and Integrate
MP0675	MP0675EUGB	GB	EP06010755.4	5/24/2006	1737068	3/21/2018	In Force	Wireless Local Area Network Communications Module and Integrate
MP0675	MP0675HK	HK	71054088	5/30/2006			Pending	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675HK01	HK	71054113	5/30/2006			Pending	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675HK02	HK	71054024	5/30/2006			Pending	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675HK03	HK	71054079	5/30/2006			Pending	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675JP	JP	2006158710	6/7/2006	JP4728172	4/22/2011	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675JP02	JP	2006158725	6/7/2006	JP4660428	1/7/2011	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675JP03	JP	2006158732	6/7/2006	JP4732959	4/28/2011	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675SG	SG	2006036842	6/5/2006	128568	4/30/2009	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675SG01	SG	2006036768	6/5/2006	128566	5/29/2009	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675SG02	SG	2006036750	6/5/2006	128565	1/30/2009	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675SG03	SG	2006036867	6/5/2006	128569	6/30/2009	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675TW	TW	95120024	6/6/2006	1446517	7/21/2014	In Force	WIRELESS LOCAL AREA NETWORK COMMUNICATIONS MODULE AND
MP0675	MP0675TW01	TW	95120026	6/6/2006	1495278	8/1/2015	In Force	WIRELESS LOCAL AREA NETWORK COMMUNICATIONS MODULE AND
MP0675	MP0675TW02	TW	95120028	6/6/2006	1438873	5/21/2014	In Force	WIRELESS LOCAL AREA NETWORK COMMUNICATIONS MODULE AND
MP0676	MP0676	US	11/214,933	8/31/2005	7312662	12/25/2007	In Force	Cascade Gain Boosting System And Method For A Transmitter
MP0676	MP0676C1	US	12/004,261	12/20/2007	7737788	6/15/2010	In Force	Cascade Gain Boosting System And Method For A Transmitter
MP0677	MP0677	US	11/227,387	9/15/2005	7782039	8/24/2010	In Force	Mixed Mode Digital Control For Switching Regulator
MP0677	MP0677C1	US	12/858,549	8/18/2010	7973524	7/5/2011	In Force	Mixed Mode Digital Control For Switching Regulator
MP0678	MP0678	US	11/351,936	2/9/2005	8149863	4/3/2012	In Force	Detection Of Strong High Frequency Sinusoidal Interference In Wirele
MP0678	MP0678C1	US	13/437,396	4/2/2012	8958437	2/17/2015	In Force	Method And Apparatus For Preventing A First Station From Transmitt
MP0679	MP0679	US	11/293,319	12/1/2005	7548047	6/16/2009	In Force	PULSE WIDTH MODULATED BUCK VOLTAGE REGULATOR WITH STAB
MP0680	MP0680	US	11/359,944	2/21/2006	8126145	2/28/2012	In Force	ENHANCED ASSOCIATION FOR ACCESS POINTS
MP0680	MP0680C1	US	13/397,536	2/15/2012	8787572	7/22/2014	In Force	ENHANCED ASSOCIATION FOR ACCESS POINTS
MP0681	MP0681	US	11/332,447	12/29/2005	7788427	8/31/2010	In Force	Flash Memory Interface For Disk Drive
MP0681	MP0681C1	US	12/361,325	8/23/2010	7979614	7/12/2011	In Force	Flash Memory/Disk Drive Interface And Method For Same
MP0686	MP0686	US	11/282,342	11/18/2005	7362192	4/22/2008	In Force	Low Noise Voltage-Controlled Oscillator
MP0687	MP0687	US	11/349,460	2/7/2006	7359261	4/15/2008	In Force	Memory Repair System And Method
MP0687	MP0687aaaa	US	13/544,482	7/9/2012	8462569	6/11/2013	In Force	Memory Repair System And Method
MP0687	MP0687aaaz	US	13/913,676	6/10/2013	8767491	7/1/2014	In Force	Apparatus And Method For Remapping Addresses Of Defective Mem
MP0687	MP0687aac1	US	13/114,866	5/24/2011	8218383	7/10/2012	In Force	Memory Repair System And Method
MP0687	MP0687C1	US	12/082,760	4/14/2008	7751264	7/6/2010	In Force	Memory Repair System And Method
MP0687	MP0687C1C1	US	12/827,446	6/30/2010	7948818	5/24/2011	In Force	Memory Repair System And Method

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MP0689	MP0689	US	11/291,584	11/30/2005	7646833	1/12/2010	In Force	CHANNEL EQUALIZATION IN RECEIVERS
MP0689	MP0689C1C1	US	12/916,167	10/29/2010	8059773	11/15/2011	In Force	CHANNEL EQUALIZATION IN RECEIVERS
MP0691	MP0691	US	11/269,272	11/8/2005	8909304	12/9/2014	In Force	Wireless Network With Power Save Mode
MP0692	MP0692	US	11/331,232	2/9/2006	7881755	2/1/2011	In Force	Wireless LAN Power Savings
MP0692	MP0692C1	US	13/019,110	2/1/2011	8559361	10/15/2013	In Force	Wireless LAN Power Savings
MP0692	MP0692C1C1	US	14/053,133	10/14/2013	9331247	5/24/2016	In Force	Wireless LAN Power Savings
MP0693	MP0693C1	US	12/723,122	3/12/2010	7960976	6/14/2011	In Force	Advanced Time Domain Reflection Cable Testing
MP0694	MP0694	US	11/216,843	8/31/2005	7610498	10/27/2009	In Force	Very Low Voltage Power Distribution For Mobile Devices
MP0694	MP0694C1C1P	JP	2012172879	8/3/2012			Pending	Very Low Voltage Power Distribution For Mobile Devices
MP0694	MP0694C1JP	JP	2006144471	5/24/2006	5080026	9/7/2012	In Force	Medical Device
MP0694	MP0694CN	CN	200610078231	5/16/2006	ZL2006100782	5/30/2012	In Force	VERY LOW VOLTAGE POWER DISTRIBUTION FOR MOBILE DEVICES
MP0694	MP0694EP	EP	EP06009374.7	5/5/2006	EP1729203	7/6/2016	In Force	Power distribution system for mobile computing devices
MP0694	MP0694EPDE	DE	06009374.7	5/5/2006	602006049501	7/6/2016	In Force	Power Distribution System For Mobile Computing Devices
MP0694	MP0694EPFR	FR	06009374.7	5/5/2006	1729203	7/6/2016	In Force	Power Distribution System For Mobile Computing Devices
MP0694	MP0694EPGB	GB	06009374.7	5/5/2006	1729203	7/6/2016	In Force	Power Distribution System For Mobile Computing Devices
MP0694	MP0694HK	HK	71054033	5/16/2006			Pending	Very Low Voltage Power Distribution For Mobile Devices
MP0694	MP0694I	US	11/324,450	1/3/2006	7725182	5/25/2010	In Force	Power Distribution System For A Medical Device
MP0694	MP0694IIEP	EP	EP06009378.8	5/5/2006	EP1728533	3/18/2009	In Force	Medical Device
MP0694	MP0694ICN	CN	200610078232	5/16/2006	ZL2006100782	11/7/2012	In Force	MEDICAL DEVICE
MP0694	MP0694ISG	SG	2006031595	5/16/2006	127816	1/30/2009	In Force	Battery Power Distribution System For A Medical Device
MP0694	MP0694ITW	TW	95117464	5/17/2005	1389716	3/21/2013	In Force	MEDICAL DEVICE
MP0694	MP0694JP	JP	2006144510	5/24/2006			Pending	Very Low Voltage Power Distribution For Mobile Devices
MP0694	MP0694SG	SG	2006031579	5/16/2006	127815	12/31/2008	In Force	Very Low Voltage Power Distribution For Mobile Devices
MP0694	MP0694ITW	TW	95117465	5/17/2006	1448881	8/11/2014	In Force	Very Low Voltage Power Distribution For Mobile Devices
MP0695	MP0695	US	11/328,532	1/10/2006	8045491	10/25/2011	In Force	Signal Handling For Wireless Clients
MP0695	MP0695D1	US	11/448,929	6/7/2006	8094608	1/10/2012	In Force	Method And Apparatus For Generating And Transmitting Packets On
MP0695	MP0695D2	US	11/449,415	6/8/2006	8050276	11/1/2011	In Force	Signal Handling For Wireless Clients
MP0698	MP0698	US	11/438,918	5/23/2006	7788981	8/3/2010	In Force	Bluetooth Coexistence Timing Synchronization
MP0698	MP0698C1	US	12/844,859	7/28/2010	8218516	7/10/2012	In Force	Bluetooth Coexistence Timing Synchronization
MP0698	MP0698C1aa	US	14/143,305	12/30/2013	9999011	6/12/2018	In Force	Systems And Methods For Bluetooth And WLAN Traffic Synchronizati
MP0698	MP0698C1C1	US	13/544,438	7/9/2012	8619739	12/31/2013	In Force	Bluetooth Coexistence Timing Synchronization
MP0699	MP0699	US	11/352,115	2/9/2006	7332883	2/19/2008	In Force	Method And Apparatus For Initializing Operation Of A Disk Drive
MP0699	MP0699C1	US	12/069,806	2/12/2008	7468590	12/23/2008	In Force	Method And Apparatus For Initializing Operation Of A Disk Drive
MP0699	MP0699C1C1	US	12/340,944	12/22/2008	8044616	10/25/2011	In Force	Method And Apparatus For Initializing Operation Of A Disk Drive

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MP0700	MP0700	US	11/265,074	11/1/2005	8831552	9/9/2014	In Force	RF band-pass filter with feedback control
MP0701	MP0701	US	11/326,039	1/5/2006	7423483	9/9/2008	In Force	Increasing Amplifier Bandwidth By Positive Capacitive Feedback
MP0702	MP0702	US	11/285,903	11/23/2005	7289381	10/30/2007	In Force	PROGRAMMABLE BOOSTING AND CHARGE NEUTRALIZATION
MP0703	MP0703C1	US	13/027,501	2/15/2011	8154816	4/10/2012	In Force	MAGNETO RESISTIVE VOLTAGE BIASING SCHEME WITH FAST RECOVER
MP0705	MP0705	US	11/394,586	3/31/2006	7330064	2/12/2008	In Force	GEOMETRIC LADDER CIRCUIT WITH LINEAR-IN-DB TRANSFER FUNCTI
MP0705	MP0705C1	US	12/030,068	2/12/2008	7557636	7/7/2009	In Force	GEOMETRIC LADDER CIRCUIT WITH LINEAR-IN-DB TRANSFER FUNCTI
MP0706	MP0706	US	11/394,561	3/31/2006	7339434	3/4/2008	In Force	LINEAR-IN-DB VARIABLE GAIN AMPLIFIER USING GEOMETRIC LADDE
MP0707	MP0707	US	11/433,128	5/12/2006	7394278	7/1/2008	In Force	METHODS AND APPARATUS FOR INTEGRATED CIRCUIT LOOPBACK T
MP0707	MP0707C1	US	12/027,895	2/7/2008	7868639	1/11/2011	In Force	METHODS AND APPARATUS FOR INTEGRATED CIRCUIT LOOPBACK T
MP0709	MP0709	US	11/305,875	12/16/2005	7583649	9/1/2009	In Force	Rate Adaptation
MP0709	MP0709aaa	US	13/657,968	10/23/2012	8787405	7/22/2014	In Force	Device And Method For Adapting Transmission Parameters Based On
MP0709	MP0709C1	US	12/550,841	8/31/2009	7881275	2/1/2011	In Force	Rate Adaptation
MP0709	MP0709C1C1	US	13/019,127	2/1/2011	8295259	10/23/2012	In Force	Rate Adaptation
MP0710	MP0710	US	11/355,491	2/16/2006	7654038	1/26/2010	In Force	Dual MAC Arbitration
MP0710	MP0710C1	US	12/693,184	1/25/2010	7773573	8/10/2010	In Force	Dual MAC Arbitration
MP0710	MP0710WAKR	KR	1020137011360	2/13/2007	101309754	9/11/2013	In Force	Dual MAC Arbitration
MP0710	MP0710WO	WO	PCT/US2007/00	2/13/2007			Pending	Dual MAC Arbitration
MP0710	MP0710WOCN	CN	200780012726.1	2/13/2007	212007800127	5/30/2012	In Force	DUAL MAC ARBITRATION
MP0710	MP0710WCEP	EP	EP0750547.7	2/13/2007	EP1994720	10/24/2012	In Force	Dual MAC Arbitration
MP0710	MP0710WQJP	JP	2008-555302	2/13/2007	JP4859935	11/11/2011	In Force	DUAL MAC ARBITRATION
MP0710	MP0710WCKR	KR	1020087022646	2/13/2007	101297819	8/12/2013	In Force	Dual MAC Arbitration
MP0710	MP0710WUDE	DE	07750547.7	2/13/2007	1994720	10/24/2012	In Force	DUAL MAC ARBITRATION
MP0710	MP0710WUFR	FR	07750547.7	2/13/2007	1994720	10/24/2012	In Force	DUAL MAC ARBITRATION
MP0710	MP0710WUGB	GB	07750547.7	2/13/2007	1994720	10/24/2012	In Force	DUAL MAC ARBITRATION
MP0711	MP0711	US	11/389,293	3/24/2006	8249064	8/21/2012	In Force	Remote Switching
MP0711	MP0711C1	US	13/580,455	8/21/2012	8848706	9/30/2014	In Force	Console With Network Device Based Frame Routing According To Re
MP0712	MP0712	US	11/437,560	5/19/2006	7738904	6/15/2010	In Force	SYSTEMS AND METHODS FOR CALIBRATING POWER REGULATED CO
MP0712	MP0712C1	US	12/775,694	5/7/2010	7830132	2/15/2011	In Force	SYSTEMS AND METHODS FOR CALIBRATING POWER REGULATED CO
MP0714	MP0714	US	11/355,459	2/15/2006	7522369	4/21/2009	In Force	CIRCUITS, ARCHITECTURES, APPARATUS, SYSTEMS, AND METHODS F
MP0716	MP0716	US	11/361,930	2/24/2006	7644340	1/5/2010	In Force	General Convolutional Interleaver And Deinterleaver
MP0716	MP0716C1	US	12/645,593	12/23/2009	8132076	3/6/2012	In Force	Method And Apparatus For Interleaving Portions Of A Data Block In A
MP0717	MP0717	US	11/345,885	2/12/2006	9225584	12/29/2015	In Force	Alternative Network Address Port Translation
MP0718	MP0718C1	US	11/449,190	6/8/2006	8223601	7/17/2012	In Force	Two Way Remote Control
MP0718	MP0718C1C1	US	13/550,271	7/16/2012	8638197	1/28/2014	In Force	Two Way Remote Control

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MP0718	MP0718C1DJP	JP	2011209527	7/5/2006	JP5254414	4/26/2013	In Force	Two Way Remote Control
MP0718	MP0718C1JP	JP	2006185860	7/5/2006			Pending	Two Way Remote Control
MP0718	MP0718CN	CN	2006100901801	7/3/2006	ZL2006100901	9/29/2010	In Force	TWO WAY REMOTE CONTROL
MP0718	MP0718CN1	CN	2006100901793	7/3/2006	ZL2006100901	6/15/2011	In Force	TWO WAY REMOTE CONTROL
MP0718	MP0718EP	EP	EP06013123.2	6/26/2006	EP1746555	8/10/2011	In Force	Two Way Remote Control
MP0718	MP0718EP01	EP	EP06013122.4	6/26/2006	EP1746554	8/24/2011	In Force	Two Way Remote Control
MP0718	MP0718EP01DE	DE	06013122.4	6/26/2006	602006023936	8/24/2011	In Force	Two Way Remote Control
MP0718	MP0718EP01FR	FR	06013122.4	6/26/2006	1746554	8/24/2011	In Force	Two Way Remote Control
MP0718	MP0718EP01GB	GB	06013122.4	6/26/2006	1746554	8/24/2011	In Force	Two Way Remote Control
MP0718	MP0718EPDE	DE	06013123.2	6/26/2006	602006023607	8/10/2011	In Force	Two Way Remote Control
MP0718	MP0718EPFR	FR	06013123.2	6/26/2006	1746555	8/10/2011	In Force	Two Way Remote Control
MP0718	MP0718EPGB	GB	06013123.2	6/26/2006	1746555	8/10/2011	In Force	Two Way Remote Control
MP0718	MP0718JP	JP	2006185881	7/5/2006			Pending	Two Way Remote Control
MP0718	MP0718JP01	JP	2011209599	7/5/2006	JP5389881	10/18/2013	In Force	Two Way Remote Control
MP0718	MP0718SG	SG	2006043038	6/28/2006	129359	2/27/2009	In Force	Two Way Remote Control
MP0718	MP0718SG01	SG	2006043020	6/28/2006	129358	2/27/2009	In Force	Two Way Remote Control
MP0718	MP0718TW	TW	95124327	7/4/2006	1392380	4/1/2013	In Force	Two Way Remote Control
MP0719	MP0719	US	11/325,760	1/4/2006	7256957	8/14/2007	In Force	CIRCUITS, SYSTEMS, AND METHODS FOR CAPACITIVE FLY HEIGHT ME
MP0719	MP0719C1	US	11/827,010	7/9/2007	7394611	7/1/2008	In Force	CIRCUITS, SYSTEMS, AND METHODS FOR CAPACITIVE FLY HEIGHT ME
MP0719	MP0719C1C1	US	12/124,123	5/20/2008	7623315	11/24/2009	In Force	CIRCUITS, SYSTEMS, AND METHODS FOR CAPACITIVE FLY HEIGHT ME
MP0720	MP0720	US	11/342,481	1/30/2006	7787390	8/31/2010	In Force	CUSTOM AUTOMATIC REMOTE MONITORING FOR NETWORK DEVICE
MP0720	MP0720C1	US	12/861,294	8/23/2010	8305923	11/6/2012	In Force	Custom Automatic Remote Monitoring For Network Devices
MP0722	MP0722	US	11/324,597	1/3/2006	7636356	12/22/2009	In Force	Processor Traffic Segregation For Network Switching And Routing
MP0722	MP0722C1	US	12/644,428	12/22/2009	8537823	9/17/2013	In Force	PROCESSOR TRAFFIC SEGREGATION FOR NETWORK SWITCHING AND
MP0743	MP0743	US	11/326,727	1/5/2006	7714748	5/11/2010	In Force	SYSTEMS AND METHODS FOR CONSTRUCTING HIGH-RATE CONSTRAI
MP0743	MP0743C1	US	12/772,891	5/3/2010	8049648	11/1/2011	In Force	SYSTEMS AND METHODS FOR CONSTRUCTING HIGH-RATE CONSTRAI
MP0744	MP0744C1CN	CN	2006100988331	7/13/2006	ZL2006100988	10/21/2009	In Force	PACKAGING FOR HIGH SPEED INTEGRATED CIRCUITS
MP0744	MP0744C1EP	EP	60140613	7/6/2006			Pending	Packaging For High Speed Integrated Circuits
MP0744	MP0744C1HK	HK	71098344	7/13/2006			Pending	Packaging For High Speed Integrated Circuits
MP0744	MP0744C1JP	JP	2006194500	7/14/2006	JP4764277	6/17/2011	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744C1SG	SG	2006046882	7/13/2006	129384	2/27/2009	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744C1TW	TW	095125822	7/14/2006			Pending	PACKAGING FOR HIGH SPEED INTEGRATED CIRCUITS
MP0744	MP0744C2CN	CN	2006100988321	7/13/2006	ZL2006100988	10/14/2009	In Force	PACKAGING FOR HIGH SPEED INTEGRATED CIRCUITS
MP0744	MP0744C2EP	EP	60142593	7/10/2006			Pending	Packaging For High Speed Integrated Circuits

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MP0744	MP0744C2HK	HK	71098273	7/13/2006			Pending	Packaging For High Speed Integrated Circuits
MP0744	MP0744C2JP	JP	2006194503	7/14/2006	JP5110814	10/19/2012	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744C2SG	SG	2006046858	7/13/2006	129383	2/27/2009	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744C3	US	11/472,904	6/22/2006	7884451	2/8/2011	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744C6	US	11/473,702	6/23/2006	7638870	12/29/2009	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744CN	CN	2006100988341	7/13/2006	Z12006100988	12/2/2009	In Force	PACKAGING FOR HIGH SPEED INTEGRATED CIRCUITS
MP0744	MP0744EP	EP	60142601	7/10/2006			Pending	Packaging For High Speed Integrated Circuits
MP0744	MP0744HK	HK	71098371	7/13/2006			Pending	Packaging For High Speed Integrated Circuits
MP0744	MP0744JP	JP	2006194499	7/14/2006	JP5149493	12/7/2012	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744SG	SG	2006046890	7/13/2006	129385	1/30/2009	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744TW	TW	9512825	7/14/2006	1418012	12/1/2013	In Force	PACKAGING FOR HIGH SPEED INTEGRATED CIRCUITS
MP0745	MP0745	US	11/327,684	1/6/2006	7567491	7/28/2009	In Force	SLICER BIAS LOOP
MP0746	MP0746	US	11/317,537	12/23/2005	7417474	8/26/2008	In Force	CLOCK FREQUENCY DIVISION METHODS AND CIRCUITS
MP0746	MP0746C1C1	US	12/613,248	11/5/2009	8089304	1/3/2012	In Force	CLOCK FREQUENCY DIVISION METHODS AND CIRCUITS
MP0747	MP0747CN	CN	2006101032831	7/24/2006	Z12006101032	10/13/2010	In Force	ON-DIE HEATING CIRCUIT AND CONTROL LOOP FOR RAPID HEATING
MP0747	MP0747EP	EP	06015255.0	7/21/2006			Pending	On-die heating circuit and control loop for rapid heating of the die
MP0747	MP0747HK	HK	HK07113236.8	7/24/2006			Pending	ON-DIE HEATING CIRCUIT AND CONTROL LOOP FOR RAPID HEATING
MP0747	MP0747JP	JP	2006-205994	7/28/2006	JP4756843	6/10/2011	In Force	ON-DIE HEATING CIRCUIT AND CONTROL LOOP FOR RAPID HEATING
MP0747	MP0747SG	SG	SG2006046056	7/12/2006	SG129370	3/31/2009	In Force	ON-DIE HEATING CIRCUIT AND CONTROL LOOP FOR RAPID HEATING
MP0747	MP0747WO	WO	PCT/US2006/03	8/1/2006			Pending	ON-DIE HEATING CIRCUIT AND CONTROL LOOP FOR RAPID HEATING
MP0748	MP0748	US	11/242,230	10/3/2005	7375597	5/20/2008	In Force	LOW-NOISE FINE-FREQUENCY TUNING
MP0748	MP0748C1	US	12/114,479	5/2/2008	7893785	2/22/2011	In Force	LOW-NOISE FINE-FREQUENCY TUNING
MP0748	MP0748CN	CN	2006100996001	8/1/2006	Z12006100996	8/10/2011	In Force	LOW-NOISE FINE-FREQUENCY TUNING
MP0748	MP0748EP	EP	EP06016030.6	8/1/2006			Pending	Low-noise frequency fine-tuning
MP0748	MP0748HK	HK	HK07107785.7	8/1/2006			Pending	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0748	MP074811TW	TW	98102050	1/20/2009			Pending	Variable Capacitance With Delay Lock Loop
MP0748	MP074811WO	WO	PCT/US2008/08	11/26/2008			Pending	Variable Capacitance With Delay Lock Loop
MP0748	MP0748JP	JP	2006-208096	7/31/2006	JP4316591	5/29/2009	In Force	LOW-NOISE FINE-FREQUENCY TUNING
MP0748	MP0748SG	SG	SG2006050801	8/1/2006	SG129424	2/27/2009	In Force	LOW-NOISE FINE-FREQUENCY TUNING
MP0748	MP0748TW	TW	95128166	8/1/2006	1337864	2/21/2011	In Force	LOW-NOISE FINE-FREQUENCY TUNING
MP0748	MP0748WO	WO	PCT/US2006/03	8/1/2006			Pending	LOW-NOISE FINE-FREQUENCY TUNING
MP0749	MP0749	US	11/242,621	10/3/2005	7292114	11/6/2007	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749C1	US	11/995,271	11/5/2007	7839228	11/23/2010	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749CN	CN	2006100995994	8/1/2006	Z12006100995	5/2/2012	In Force	CRYSTAL OSCILLATOR INTEGRATED CIRCUIT AND METHOD FOR GENE

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MP0749	MP0749CND1	CN	201210041799.3	8/1/2006	ZL2012100417	3/2/2016	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749EP	EP	EP06016018.1	8/1/2006	EP1753126	10/5/2011	In Force	Low-Noise High-Stability Crystal Oscillator
MP0749	MP0749EPDE	DE	06016018.1	8/1/2006	1753126	10/5/2011	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749EPFR	FR	06016018.1	8/1/2006	1753126	10/5/2011	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749EPGB	GB	06016018.1	8/1/2006	1753126	10/5/2011	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749HK	HK	HK07107784.8	8/1/2006			Pending	LOW-NOISE FINE-FREQUENCY TUNING
MP0749	MP0749JP	JP	2006-208095	7/31/2006	JP4392007	10/16/2009	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749JPD1	JP	2009-151923	6/26/2009			Pending	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749SG	SG	SG200650793	8/1/2006	SG129423	2/27/2009	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749TW	TW	TW095128168	8/1/2006	TW1328339	8/1/2010	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749WO	WO	PCT/US2006/03	8/1/2006			Pending	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0750	MP0750C1	US	11/803,100	5/11/2007	8080010	1/3/2012	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750C1C1	US	13/334,948	12/22/2011	8731021	5/20/2014	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750CN	CN	2006100935983	8/1/2006	ZL2006100935	9/15/2010	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750EP	EP	06015918.3	7/31/2006	EP1750381	3/8/2017	In Force	All Digital Implementation of Clock Spectrum Spreading (Dither) for L
MP0750	MP0750EPDE	DE	06015918.3	7/31/2006	DE1750381	3/8/2017	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750EPFR	FR	06015918.3	7/31/2006	FR1750381	3/8/2017	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750EPGB	GB	06015918.3	7/31/2006	GB1750381	3/8/2017	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750HK	HK	HK07107783.9	8/1/2006			Pending	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750JP	JP	2006-207502	7/31/2006	JP4234157	12/19/2008	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750SG	SG	SG2006046072	7/12/2006	SG129372	2/27/2009	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750TW	TW	95128165	8/1/2006	1318514	12/1/2009	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750WO	WO	PCT/US2006/03	8/1/2006			Pending	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0751	MP0751	US	11/324,933	1/4/2006	7676532	3/9/2010	In Force	Processing System And Method For Transform
MP0751	MP0751C1	US	12/719,340	3/8/2010	8484273	7/9/2013	In Force	Processing System And Method For Transform
MP0752	MP0752C1	US	12/573,361	10/5/2009	8041317	10/18/2011	In Force	Radar Detection Algorithm
MP0752	MP0752CN	CN	2006100988351	7/13/2006	ZL2006100988	2/1/2012	In Force	RADAR DETECTION APPARATUS AND METHOD THEREOF
MP0752	MP0752EP	EP	EP06014168.6	7/7/2006	EP1752787	6/13/2012	In Force	Radar Detection Apparatus and Method Thereof
MP0752	MP0752EPDE	DE	06014168.6	7/7/2006	602006030056	6/13/2012	In Force	Radar Detection Apparatus And Method Thereof
MP0752	MP0752EPFR	FR	06014168.6	7/7/2006	1752787	6/13/2012	In Force	Radar Detection Apparatus And Method Thereof
MP0752	MP0752EPGB	GB	06014168.6	7/7/2006	1752787	6/13/2012	In Force	Radar Detection Apparatus And Method Thereof
MP0752	MP0752JP	JP	2006194491	7/14/2006	JP5073370	8/31/2012	In Force	Radar Detection Apparatus And Method Thereof
MP0752	MP0752KR	KR	1020060066559	7/14/2006	101189817	10/4/2012	In Force	Radar Detection Apparatus And Method Thereof
MP0752	MP0752SG	SG	2006046841	7/13/2006	130093	12/31/2009	In Force	Radar Detection Apparatus And Method Thereof

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MP0753	MP0753	US	11/352,806	2/13/2006	7693226	4/6/2010	In Force	Aggregation Over Multiple 64-66 Lanes
MP0754	MP0754WO	WO	PCT/US2007/01	5/16/2007			Pending	Packet Tunneling For Wireless Clients Using Maximum Transmission
MP0754	MP0754WOCN	CN	200780027818	5/16/2007			Pending	PACKET TUNNELING FOR WIRELESS CLIENTS USING MAXIMUM TRAN
MP0754	MP0754WOEP	EP	77949071	5/16/2007			Pending	Packet Tunneling For Wireless Clients Using Maximum Transmission
MP0754	MP0754WOKR	KR	1020087030881	12/18/2008			Pending	Packet Tunneling For Wireless Clients Using Maximum Transmission
MP0756	MP0756	US	11/263,186	10/31/2005	7561361	7/14/2009	In Force	Dynamic Synchronization Of Timing Signals
MP0758	MP0758C1	US	11/677,961	2/22/2007	7362063	4/22/2008	In Force	MOTOR SPINDLE CONTROL SYSTEM AND METHOD
MP0758	MP0758C1C1	US	12/107,698	4/22/2008	7564789	3/17/2009	In Force	MOTOR SPINDLE CONTROL SYSTEM AND METHOD
MP0760	MP0760	US	11/264,921	11/12/2005	7262733	4/10/2007	In Force	Transconductance Amplifiers With Compensation
MP0760	MP076011EP	EP	EP06022343.5	10/25/2006	1783894	3/29/2019	In Force	Amplifiers with Compensation
MP0760	MP076011EPDE	DE	EP06022343.5	10/25/2006	60200605763	3/29/2019	In Force	Amplifiers with Compensation
MP0760	MP076011EPFR	FR	EP06022343.5	10/25/2006	1783894	3/29/2019	In Force	Amplifiers with Compensation
MP0760	MP076011EPGB	GB	EP06022343.5	10/25/2006	1783894	3/29/2019	In Force	Amplifiers with Compensation
MP0760	MP0760aTW3	TW	95140239	10/31/2006	1426704	2/11/2014	In Force	AMPLIFIERS WITH COMPENSATION
MP0760	MP0760C2	US	11/292,589	12/2/2005	7199656	4/3/2007	In Force	Amplifiers With Compensation
MP0760	MP0760C3	US	11/656,783	1/22/2007	7323930	1/29/2008	In Force	Amplifiers With Compensation
MP0760	MP0760CN	CN	200610150468.4	10/31/2006	ZL2006101504	6/20/2012	In Force	AMPLIFIERS WITH COMPENSATION
MP0760	MP0760EP	EP	06022508.3	10/27/2006	1783895	10/17/2018	In Force	Transconductance Amplifiers With Compensation
MP0760	MP0760EPDE	DE	06022508.3	10/27/2006	602006056585	10/17/2018	In Force	Transconductance Amplifiers With Compensation
MP0760	MP0760EPFR	FR	06022508.3	10/27/2006	1783895	10/17/2018	In Force	Transconductance Amplifiers With Compensation
MP0760	MP0760EPGB	GB	06022508.3	10/27/2006	1783895	10/17/2018	In Force	Transconductance Amplifiers With Compensation
MP0760	MP0760HK	HK	71117573	10/31/2006			Pending	Amplifiers With Compensation
MP0760	MP076011	US	11/503,436	8/11/2006	7459972	12/2/2008	In Force	Amplifiers With Compensation
MP0760	MP076011C1	US	12/326,197	12/2/2008	7737777	6/15/2010	In Force	Amplifiers With Compensation
MP0760	MP076011C3	CN	200610150469.4	10/31/2006	ZL2006101504	4/10/2013	In Force	AMPLIFIERS WITH COMPENSATION
MP0760	MP0760JP	JP	2006289307	11/2/2006			Pending	Amplifiers With Compensation
MP0760	MP07601HK	HK	71124565	10/31/2006			Pending	Amplifiers With Compensation
MP0760	MP07601JP	JP	2006289309	11/2/2006	JP5016239	6/8/2012	In Force	Amplifiers With Compensation
MP0760	MP0760SG	SG	2006073522	10/30/2006	131903	5/29/2009	In Force	Amplifiers With Compensation
MP0760	MP0760SG3	SG	2006073530	10/30/2006	131904	6/30/2009	In Force	Amplifiers With Compensation
MP0760	MP0760TW	TW	95140236	10/31/2006	1387199	2/21/2013	In Force	AMPLIFIERS WITH COMPENSATION
MP0761	MP0761	US	11/321,766	12/28/2005	7408313	8/5/2008	In Force	LOW POWER PREAMPLIFIER WRITER ARCHITECTURE
MP0761	MP0761C1	US	12/186,359	8/5/2008	7656111	2/2/2010	In Force	LOW POWER PREAMPLIFIER WRITER ARCHITECTURE
MP0763	MP0763	US	11/400,476	4/7/2006	7638619	4/13/2010	In Force	Error Forecasting And Error Correction Strategies

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MP0763	MP0763D1	US	12/758,881	4/13/2010	8046659	10/25/2011	In Force	Erasure Forecasting And Error-Correction Strategies
MP0764	MP0764	US	11/337,360	1/23/2006	7411758	8/12/2008	In Force	Disk Drive Servo Control Using Spirals
MP0764	MP0764C1	US	12/228,163	8/8/2008	7881005	2/1/2011	In Force	Disk Drive Servo Control Using Spirals
MP0765	MP0765	US	11/312,821	3/10/2006	7577892	8/18/2009	In Force	High Speed Iterative Decoder
MP0765	MP0765C1	US	12/538,953	8/11/2009	7853855	12/14/2010	In Force	High Speed Iterative Decoder
MP0767	MP0767	US	11/449,066	6/7/2006	7861131	12/28/2010	In Force	TENSOR PRODUCT CODES CONTAINING AN ITERATIVE CODE
MP0767	MP0767C1	US	12/946,112	11/15/2010	8086945	12/27/2011	In Force	TENSOR PRODUCT CODES CONTAINING AN ITERATIVE CODE
MP0769	MP0769	US	07/285,808	12/16/1988	4935825	6/19/1990	In Force	CYLINDER DEFECT MANAGEMENT SYSTEM FOR DATA STORAGE SYSTEMS
MP0770	MP0770	US	08/020,058	2/19/1993	5249271	9/28/1993	In Force	Buffer Memory Data Flow Controller
MP0771	MP0771	US	07/736,461	7/26/1991	5313474	5/17/1994	In Force	Method And Apparatus To Determine The Log Of An Element In GF(2
MP0772	MP0772	US	07/974,158	11/10/1992	5428627	6/27/1995	In Force	Method And Apparatus For Initializing An ECC Circuit
MP0773	MP0773	US	08/372,072	1/12/1995	5627695	5/6/1997	In Force	System And Method For Generating Unique Sector Identifiers For An
MP0775	MP0775CA	CA	2333386	11/24/2000			Pending	Circuit And Method For Rapid Checking Of Error Correction Codes Usi
MP0775	MP0775JP	JP	2000-551486	5/27/1999			Pending	Circuit And Method For Rapid Checking Of Error
MP0776	MP0776	US	09/096,709	6/12/1998	6082231	7/18/2000	In Force	Circuit And Method For Rapid Checking Of Error Correction Codes Usi
MP0777	MP0777	US	09/243,295	2/2/1999	6487631	11/26/2002	In Force	Circuit And Method For Monitoring Sector Transfers To And From Sto
MP0778	MP0778	US	09/275,629	3/24/1999	6530000	3/4/2003	In Force	Methods And Systems For Arbitrating Access To A Disk Controller Buf
MP0778	MP0778CA	CA	2364625	3/23/2000	2364625	8/7/2012	In Force	Arbitration Methods And Systems For Arbitrating Access To A Disk Co
MP0778	MP0778EPGB	GB	00918322.9	3/23/2000	1163573	8/1/2007	In Force	Methods And Systems For Arbitrating Access To A Disk Controller Buf
MP0778	MP0778JP	JP	2000607076	3/23/2000	JP4313516	5/22/2009	In Force	Arbitration Methods And Systems For Arbitrating Access To A Disk Co
MP0779	MP0779	US	09/326,851	6/7/1999	6470461	10/22/2002	In Force	Disk Drive Controller Circuit And Method For Skipping Defective And/
MP0779	MP0779CA	CA	2375672	6/1/2000	2375672	8/18/2009	In Force	Disk Drive Controller Circuit And Method For Skipping Defective And/
MP0779	MP0779JP	JP	2001-502120	6/1/2000	JP4652642	12/24/2010	In Force	Disk Drive Controller Circuit And Method For Skipping
MP0779	MP0779WCEP	EP	EP09041177.8	6/1/2000	EP1188163	8/1/2007	In Force	DISK DRIVE CONTROLLER CIRCUIT AND METHOD FOR SKIPPING DEFEC
MP0779	MP0779WCEPDE	DE	00941177.8	6/1/2000	608357805	8/1/2007	In Force	Disk Drive Controller Circuit And Method For Skipping Defective And/
MP0779	MP0779WCEPFR	FR	00941177.8	6/1/2000	1188163	8/1/2007	In Force	Disk Drive Controller Circuit And Method For Skipping Defective And/
MP0779	MP0779WCEPGB	GB	00941177.8	6/1/2000	1188163	8/1/2007	In Force	Disk Drive Controller Circuit And Method For Skipping Defective And/
MP0780	MP0780	US	09/643,636	8/22/2000	6826650	11/30/2004	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780aaaa	US	12/902,364	10/12/2010	8145835	3/27/2012	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780C122	US	13/430,063	3/26/2012	8769219	7/1/2014	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780C2	US	11/509,285	8/24/2006	7484037	1/27/2009	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780CA	CA	2418334	7/17/2001	2418334	1/24/2012	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780EPDE	DE	01958970.4	7/17/2001	1344213	9/17/2008	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780EPFR	FR	01958970.4	7/17/2001	1344213	9/17/2008	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri

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MP0780	MP0780EPGB	GB	01959970.4	7/17/2001	1344213	9/17/2008	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780WCEP	EP	01959970.4	7/17/2001	1344213	9/17/2008	In Force	DISK CONTROLLER CONFIGURED TO PERFORM OUT OF ORDER EXEC
MP0781	MP0781	US	10/199,911	7/19/2002	6961877	11/1/2005	In Force	System And Method For In-Line Error Correction For Storage Systems
MP0781	MP0781aaa	US	13/846,252	3/18/2013	9195538	11/24/2015	In Force	System And Method For In-Line Error Correction For Storage Systems
MP0781	MP0781C1	US	11/231,312	9/20/2005	8122299	2/21/2012	In Force	SYSTEM AND METHOD FOR IN-LINE ERROR CORRECTION FOR STORA
MP0781	MP0781C1C1	US	13/400,321	2/20/2012	8402323	3/19/2013	In Force	System And Method For In-Line Error Correction For Storage Systems
MP0782	MP0782	US	10/384,991	3/10/2003	7457903	11/25/2008	In Force	Interrupt Controller For Processing Fast And Regular Interrupts
MP0782	MP0782C1	US	12/271,261	11/14/2008	7870320	1/11/2011	In Force	Interrupt Controller For Prioritizing Interrupt Requests In An Embedd
MP0783	MP0783	US	10/385,022	3/10/2003	7080188	7/18/2006	In Force	Method And System For Embedded Disk Controllers
MP0785	MP0785	US	10/385,056	3/10/2003	7219182	5/15/2007	In Force	Method And System For Using An External Bus Controller In Embedd
MP0786	MP0786	US	09/547,567	4/12/2000	6330626	12/11/2001	In Force	Systems And Methods For A Disk Controller Memory Architecture
MP0786	MP0786CA	CA	2370596	5/5/2000	2370596	1/12/2010	In Force	Systems And Methods For A Disk Controller Memory Architecture
MP0786	MP0786JP	JP	2000-615882	5/5/2000	JP4741735	5/13/2011	In Force	Systems And Methods For A Disk Controller Memory
MP0786	MP0786WOKR	KR	1020017014078	11/5/2001	100638378	10/18/2006	In Force	Systems And Methods For A Disk Controller Memory Architecture
MP0788	MP0788C1	US	11/592,813	11/3/2006	7471485	12/30/2008	In Force	Method And System For Head Position Control In Embedded Disk Dri
MP0788	MP0788C1C1	US	12/338,071	12/18/2008	8116026	2/14/2012	In Force	Method And System For Head Position Control In Embedded Disk Dri
MP0789	MP0789	US	10/384,992	3/10/2003	7452545	2/17/2009	In Force	Method And System For Automatic Time Base Adjustment For Disk D
MP0789	MP0789C1	US	12/361,121	1/28/2009	8189285	5/29/2012	In Force	Method And System For Automatic Time Base Adjustment For Disk D
MP0790	MP0790C1	US	11/368,410	3/24/2006	7336435	2/26/2008	In Force	Method And System For Collecting Servo Field Data From Programma
MP0791	MP0791C1	US	11/384,657	3/20/2006	7975110	7/5/2011	In Force	Method And System For Supporting Multiple External Serial Port Devi
MP0792	MP0792	US	11/099,746	4/6/2005	7609468	10/27/2009	In Force	Method And System For Read Gate Timing Control For Storage Contr
MP0793	MP0793	US	11/056,320	2/11/2005	7543214	6/2/2009	In Force	Method And System For Performing CRC
MP0794	MP0794	US	10/429,495	5/5/2003	7111228	9/19/2006	In Force	System And Method For Performing Parity Checks In Disk Storage Sys
MP0794	MP0794C1	US	11/521,979	9/15/2006	7559009	7/17/2009	In Force	System And Method For Performing Parity Checks In Disk Storage Sys
MP0795	MP0795	US	10/761,786	1/21/2004	7287102	10/23/2007	In Force	System And Method For Concatenating Data
MP0796	MP0796	US	10/619,954	7/15/2003	7007114	2/28/2006	In Force	SYSTEM AND METHOD FOR PADDING DATA BLOCKS AND/OR REMOV
MP0796	MP0796C1	US	11/232,437	9/21/2005	8713224	4/29/2014	In Force	System And Method For Transferring Data In Storage Controllers
MP0797	MP0797	US	10/867,113	6/14/2004	7130084	10/10/2006	In Force	Integrated Memory Controller
MP0797	MP0797C1	US	11/542,862	10/4/2006	7286441	10/23/2007	In Force	Integrated Memory Controller
MP0797	MP0797C1C1	US	11/977,169	10/23/2007	7535791	5/19/2009	In Force	Integrated Memory Controller
MP0797	MP0797D1	US	11/542,726	10/4/2006	7556053	9/29/2009	In Force	Integrated Memory Controller
MP0798	MP0798	US	10/686,151	10/15/2003	7526691	4/28/2009	In Force	System And Method For Using Tap Controllers
MP0799	MP0799	US	10/878,803	6/28/2004	8166217	4/24/2012	In Force	System And Method For Reading And Writing Data Using Storage Con
MP0800	MP0800	US	10/894,143	7/19/2004	9201599	12/1/2015	In Force	System And Method For Transmitting Data In Storage Controllers

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0802	MP0802	US	10/894,208	7/19/2004	8032674	10/4/2011	In Force	System And Method For Controlling Buffer Memory Overflow And Un
MP0804	MP0804	US	10/965,468	10/13/2004	7386661	6/10/2008	In Force	Power Save Module For Storage Controllers
MP0804	MP0804C1	US	12/157,212	6/9/2008	8417900	4/9/2013	In Force	Power Save Module For Storage Controllers
MP0805	MP0805C1	US	12/886,806	9/21/2010	8370541	2/5/2013	In Force	Method And System For Processing Frames In Storage Controllers
MP0806	MP0806	US	10/983,944	11/8/2004	7240267	7/3/2007	In Force	System And Method For Conducting BIST Operations
MP0806	MP0806C1	US	11/820,226	6/19/2007	8015448	9/6/2011	In Force	System And Method For Conducting BIST Operations
MP0808	MP0808	US	09/548,330	4/12/2000	6401149	6/4/2002	In Force	Methods For Context Switching Within A Disk Controller
MP0809	MP0809	US	11/293,917	12/5/2005	7438896	10/21/2008	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809C1	US	11/481,477	7/6/2006	7379012	5/27/2008	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809C2	US	11/481,476	7/6/2006	7443329	10/28/2008	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809C2SG02	SG	2006050850	8/2/2006	131002	1/29/2010	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809CN	CN	200610104264.1	8/7/2006	ZL2006101042	5/4/2011	In Force	CAPACTIVE DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVERT
MP0809	MP0809CN01	HK	71089620	8/7/2006			Pending	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809CN02	CN	200610104265.1	8/7/2006	ZL2006101042	5/4/2011	In Force	CAPACTIVE DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVERT
MP0809	MP0809CN03	CN	200610104266.1	8/7/2006	ZL2006101042	4/1/2015	In Force	CAPACTIVE DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVERT
MP0809	MP0809EP	EP	EP06016268.2	8/3/2006	EP1763141	5/31/2017	In Force	Capacitive Digital To Analog and Analog To Digital Converters
MP0809	MP0809EP2	EP	06015726.0	7/27/2006	EP1770867	5/31/2017	In Force	Capacitive Digital To Analog and Analog To Digital Converters
MP0809	MP0809EP3	EP	06016259.1	8/3/2006	EP1763140	5/10/2017	In Force	Capacitive Digital To Analog and Analog To Digital Converters
MP0809	MP0809EPDE	DE	06016268.2	8/3/2006	602006052655	5/31/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809EPFR	FR	60162682	8/3/2006	1763141	5/31/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809EPGB	GB	06016268.2	8/3/2006	1763141	5/31/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809HK	HK	71089602	8/7/2006			Pending	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809HK02	HK	71089611	8/7/2006			Pending	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809JP	JP	2006215993	8/8/2006	JP5276782	5/24/2013	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809JP01	JP	2006215995	8/8/2006	JP4864594	11/18/2011	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809JP02	JP	2006215996	8/8/2006	JP4965185	4/6/2012	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809SG	SG	2006050884	8/2/2006	131004	3/31/2009	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809SG01	SG	2006050868	8/2/2006	131003	12/31/2009	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809TW	TW	95128858	8/7/2006	1407699	9/1/2013	In Force	CAPACTIVE DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVER
MP0809	MP0809TW2	TW	95128859	8/7/2006	1422160	1/1/2014	In Force	CAPACTIVE DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVER
MP0809	MP0809U2DE	DE	06015726.0	7/27/2006	602006052655	5/31/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809U2FR	FR	60157260	7/27/2006	1770867	5/31/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809U2GB	GB	06015726.0	7/27/2006	1770867	5/31/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809U3DE	DE	06016259.1	8/3/2006	602006052481	5/10/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters

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MP0809	MP0809U3FR	FR	60162591	8/3/2006	1763140	5/10/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809U3GB	GB	06016259.1	8/3/2006	1763140	5/10/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0810	MP0810	US	11/376,601	3/14/2006	7742556	6/22/2010	In Force	CIRCUITS, METHODS, APPARATUS, AND SYSTEMS FOR RECOVERY OF
MP0813	MP0813	US	11/407,417	4/20/2006	8536874	9/17/2013	In Force	Integrated Circuit Voltage Domain Detection System And Associated
MP0815	MP0815	US	11/491,021	7/20/2006	7576527	8/18/2009	In Force	LOW POWER DC-DC CONVERTER WITH IMPROVED LOAD REGULATION
MP0815	MP0815C1	US	12/507,322	7/22/2009	7990126	8/2/2011	In Force	LOW POWER DC-DC CONVERTER WITH IMPROVED LOAD REGULATION
MP0816	MP0816	US	11/453,219	6/13/2006	7715135	5/11/2010	In Force	METHODS, CIRCUITS, APPARATUS, AND SYSTEMS FOR READ CHANNEL
MP0817	MP0817CN	CN	200610111845.1	8/25/2006	212606101118	10/6/2010	In Force	AUTOMATIC WRITE STRATEGY CALIBRATION METHOD FOR OPTICAL
MP0817	MP0817EP	EP	EP06017068.5	8/16/2006	EP1768113	2/22/2012	In Force	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817EPDE	DE	06017068.5	8/16/2006	602006027716	2/22/2012	In Force	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817EPFR	FR	60170685	8/16/2006	1768113	2/22/2012	In Force	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817EPGB	GB	06017068.5	8/16/2006	1768113	2/22/2012	In Force	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817HK	HK	71098282	8/25/2006			Pending	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817JP	JP	2006231162	8/28/2006			Pending	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817JPD1	JP	2011184882	8/28/2006	5437332	12/20/2013	In Force	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817SG	SG	2006057855	8/28/2006	131018	6/30/2009	In Force	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817TW	TW	95131325	8/25/2006	1395210	5/1/2013	In Force	Write Strategy Calibration System And Optical Media Playback Device
MP0818	MP0818	US	11/364,539	2/27/2006	8484178	7/9/2013	In Force	File Sharing
MP0818	MP0818C1	US	13/934,769	7/3/2013	9141638	9/22/2015	In Force	File Sharing
MP0819	MP0819C1	US	12/619,198	11/16/2009	8063670	11/22/2011	In Force	High Voltage High Side Transistor Driver
MP0819	MP0819CN	CN	200610169921.1	8/24/2006	212006101099	4/18/2012	In Force	HIGH VOLTAGE HIGH SIDE TRANSISTOR DRIVER
MP0819	MP0819EP	EP	06016937.2	8/14/2006	EP1768257	6/18/2014	In Force	High Voltage High Side Transistor Driver
MP0819	MP0819EPDE	DE	06016937.2	8/14/2006	602006041936	6/18/2014	In Force	High Voltage High Side Transistor Driver
MP0819	MP0819EPFR	FR	60169372	8/14/2006	1768257	6/18/2014	In Force	High Voltage High Side Transistor Driver
MP0819	MP0819EPGB	GB	06016937.2	8/14/2006	1768257	6/18/2014	In Force	High Voltage High Side Transistor Driver
MP0819	MP0819HK	HK	71164060	8/24/2006			Pending	High Voltage High Side Transistor Driver
MP0819	MP0819JP	JP	2006229479	8/25/2006			Pending	High Voltage High Side Transistor Driver
MP0819	MP0819SG	SG	2006057830	8/29/2006	131017	4/30/2009	In Force	High Voltage High Side Transistor Driver
MP0819	MP0819TW	TW	95131328	8/25/2006	1394368	4/21/2013	In Force	HIGH VOLTAGE HIGH SIDE TRANSISTOR DRIVER
MP0820	MP0820	US	11/493,908	5/12/2006	8018888	9/13/2011	In Force	Mobile WLAN Repeater System
MP0820	MP0820D1	US	13/220,194	9/12/2011	8159991	4/17/2012	In Force	Mobile WLAN Repeater System
MP0823	MP0823	US	11/510,007	8/25/2006	7966973	3/15/2011	In Force	Cable Tester
MP0824	MP0824	US	11/518,020	9/8/2006	7765458	7/27/2010	In Force	ERROR PATTERN GENERATION FOR TRELLIS-BASED DETECTION AND/
MP0824	MP0824C1	US	12/824,517	6/28/2010	8762824	6/24/2014	In Force	ERROR PATTERN GENERATION FOR TRELLIS-BASED DETECTION AND/

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MP0825	MP0825	US	11/481,137	7/5/2006	8310965	11/13/2012	In Force	Buffered Audio System With Synchronizing Bus Controller
MP0829	MP0829	US	11/388,387	4/4/2006	7554360	6/30/2009	In Force	HIGH SPEED LEVEL SHIFTER CIRCUIT IN ADVANCED CMOS TECHNOLO
MP0830	MP0830	US	11/821,482	6/23/2007	8238889	8/7/2012	In Force	Server For Wireless Application Service System
MP0830	MP0830C1	US	13/568,617	8/7/2012	8515403	8/20/2013	In Force	Server For Wireless Application Service System
MP0830	MP0830C1C1	US	13/968,933	8/16/2013	9210531	12/8/2015	In Force	Server For Wireless Application Service System
MP0830	MP0830Za	US	14/955,619	12/1/2015	9854380	12/26/2017	In Force	System And Method For Providing One Or More Application Services
MP0831	MP0831	US	11/408,265	4/20/2006	7546103	6/9/2009	In Force	Antenna Diversity System And Method
MP0831	MP0831aasa	US	14/289,987	5/29/2014	9419697	8/16/2016	In Force	ANTENNA DIVERSITY SYSTEM AND METHOD
MP0831	MP0831C1	US	12/480,149	6/8/2009	8014744	9/6/2011	In Force	Antenna Diversity System And Method
MP0831	MP0831C1C1	US	13/225,078	9/2/2011	8351888	1/8/2013	In Force	Antenna Diversity System And Method
MP0832	MP0832aasa	US	13/730,462	12/28/2012	8509045	8/13/2013	In Force	Flexible Optical Write Strategy
MP0832	MP0832C1aa	US	13/405,818	2/27/2012	8345522	1/1/2013	In Force	Flexible Optical Write Strategy
MP0832	MP0832C1C1	US	13/019,011	2/1/2011	8125870	2/28/2012	In Force	Flexible Optical Write Strategy
MP0833	MP0833	US	11/417,414	5/3/2006	8482338	7/9/2013	In Force	SHOCK DETECTOR WITH DC OFFSET SUPPRESSION USING INTERNAL
MP0834	MP0834	US	11/488,437	7/17/2006	7587170	9/8/2009	In Force	DIGITAL RADIO DATA SYSTEM RECEIVER METHODS AND APPARATUS
MP0834	MP0834C1	US	12/544,567	8/20/2009	8089050	1/17/2012	In Force	DIGITAL RADIO DATA SYSTEM RECEIVER METHODS AND APPARATUS
MP0834	MP0834C1aa	US	13/681,200	11/19/2012	8594574	11/26/2013	In Force	DIGITAL RADIO DATA SYSTEM RECEIVER METHODS AND APPARATUS
MP0834	MP0834C1C1	US	13/345,650	1/6/2012	8315568	11/20/2012	In Force	DIGITAL RADIO DATA SYSTEM RECEIVER METHODS AND APPARATUS
MP0834	MP0834C1Za	US	14/076,129	11/8/2013	9065709	6/23/2015	In Force	DIGITAL RADIO DATA SYSTEM RECEIVER METHODS AND APPARATUS
MP0836	MP0836	US	11/390,704	3/28/2006	7996554	8/9/2011	In Force	System For Improving Quality Of Service For Wireless LANs
MP0836	MP0836C1	US	13/205,159	8/8/2011	8209431	6/26/2012	In Force	SYSTEM AND METHOD FOR IMPROVING QUALITY OF SERVICE FOR W
MP0836	MP0836C1C1	US	13/531,896	6/25/2012	8447858	5/21/2013	In Force	System For Improving Quality Of Service For Wireless LANs
MP0836	MP0836C1aa	US	13/898,855	5/21/2013	9380587	6/28/2016	In Force	System For Improving Quality Of Service For Wireless LANs
MP0837	MP0837	US	11/483,057	7/6/2006	8040982	10/18/2011	In Force	PHASE-ADJUSTED CHANNEL ESTIMATION FOR FREQUENCY DIVISION
MP0837	MP0837C1	US	13/236,036	9/19/2011	8331502	12/11/2012	In Force	PHASE-ADJUSTED CHANNEL ESTIMATION FOR FREQUENCY DIVISION
MP0837	MP0837C1C1	US	13/706,164	12/5/2012	8611476	12/17/2013	In Force	PHASE-ADJUSTED CHANNEL ESTIMATION FOR FREQUENCY DIVISION
MP0838	MP0838	US	11/447,762	6/5/2006	7872962	1/18/2011	In Force	SYSTEM AND METHOD FOR PRODUCING WEIGHTED SIGNALS IN A DI
MP0839	MP0839	US	11/410,688	4/24/2006	7801253	9/21/2010	In Force	NONLINEAR POST-PROCESSORS FOR CHANNELS WITH SIGNAL-DEPEN
MP0839	MP0839C1	US	12/862,669	8/24/2010	8259872	9/4/2012	In Force	NONLINEAR POST-PROCESSORS FOR CHANNELS WITH SIGNAL-DEPEN
MP0841	MP0841/WO	WO	PCT/US2007/00	3/6/2007			Pending	Personal Lifestyle Device
MP0841	MP0841aas	US	13/550,227	7/16/2012	8391919	3/5/2013	In Force	Personal Lifestyle Device
MP0841	MP0841C1	US	12/778,229	5/12/2010	7957764	6/7/2011	In Force	Personal Lifestyle Device
MP0841	MP0841C1C1	US	13/155,105	6/7/2011	8224377	7/17/2012	In Force	Personal Lifestyle Device
MP0841	MP0841C1Za	US	13/785,991	3/5/2013	8750920	6/10/2014	In Force	Personal Lifestyle Device

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MP0841	MP0841C1Z2	US	14/298,420	6/6/2014	9264853	2/16/2016	In Force	Personal Lifestyle Device
MP0841	MP0841TW	TW	96107922	3/7/2007	1436624	5/1/2014	In Force	Personal Lifestyle Device
MP0841	MP0841WOCN	CN	200780016481.1	3/6/2007	21,2007800164	3/20/2013	In Force	PERSONAL LIFESTYLE DEVICE
MP0841	MP0841WOP	JP	2008-5538339	3/6/2007			Pending	PERSONAL LIFESTYLE DEVICE
MP0841	MP0841WOKR	KR	1020087024554	3/6/2007	101373790	3/6/2014	In Force	Personal Lifestyle Device
MP0842	MP0842C1	US	12/698,427	2/2/2010	7940489	5/10/2011	In Force	Method And Apparatus For Repeatable Run-Out Compensation
MP0843	MP0843	US	11/493,269	7/25/2006	8160181	4/17/2012	In Force	NONLINEAR DETECTORS FOR CHANNELS WITH SIGNAL-DEPENDENT
MP0843	MP0843C1	US	13/415,465	3/8/2012	8462897	6/11/2013	In Force	NONLINEAR DETECTORS FOR CHANNELS WITH SIGNAL-DEPENDENT
MP0845	MP0845	US	11/339,020	1/25/2006	7388436	6/17/2008	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845C1	US	11/527,983	9/27/2006	7387306	7/8/2008	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845C2	US	11/528,034	9/27/2006	7417575	8/26/2008	In Force	HIGH-BANDWIDTH HIGH-GAIN AMPLIFIER
MP0845	MP0845C3	US	11/528,033	9/27/2006	7387310	8/7/2008	In Force	HIGH-BANDWIDTH HIGH-GAIN AMPLIFIER
MP0845	MP0845C3C1	US	12/217,753	8/7/2008	7760029	7/20/2010	In Force	HIGH-BANDWIDTH HIGH-GAIN AMPLIFIER
MP0845	MP0845CN	CN	200610137974.1	11/1/2006	21,2006101379	5/2/2012	In Force	HIGH-BANDWIDTH HIGH-GAIN AMPLIFIER
MP0845	MP0845CN01	CN	200610137973.1	11/1/2006	21,2006101379	4/6/2011	In Force	HIGH-BANDWIDTH HIGH-GAIN AMPLIFIER
MP0845	MP0845D1EPDE	DE	15188986.2	10/31/2006	602006056636	10/17/2018	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845D1EPFR	FR	15188986.2	10/31/2006	3002874	10/17/2018	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845D1EPGB	GB	15188986.2	10/31/2006	3002874	10/17/2018	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845SEP	EP	06022702.2	10/31/2006	EP1783898	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845EP01	EP	06022864	11/2/2006	EP1783899	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845EPD1	EP	15188986.2	10/31/2006	3002874	10/17/2018	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845EPDE	DE	06022702.2	10/31/2006	602006046904	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845EPFR	FR	06022702	10/31/2006	1783898	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845EPGB	GB	06022702.2	10/31/2006	1783898	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845HK	HK	71117582	11/1/2006			Pending	High-Bandwidth High-Gain Amplifier
MP0845	MP0845HK01	HK	71117546	11/1/2006			Pending	High-Bandwidth High-Gain Amplifier
MP0845	MP0845SG	SG	2006073472	10/27/2006	131900	6/30/2009	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845SG01	SG	2006073480	10/27/2006			Pending	High-Bandwidth High-Gain Amplifier
MP0845	MP0845TW	TW	95140412	11/1/2006	1342667	5/21/2011	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845TWO1	TW	95140411	11/1/2006	1350650	10/11/2011	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845UIDE	DE	06022864.0	11/2/2006	602006046905	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845UIFR	FR	06022864	11/2/2006	1783899	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845UIGB	GB	06022864.0	11/2/2006	1783899	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0846	MP0846	US	11/453,193	6/14/2006	7627023	12/1/2009	In Force	64/66 Encoder

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MP0847	MP0847	US	11/933,935	1/18/2006	7541962	6/2/2009	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847C1	US	12/475,947	6/1/2009	7886660	2/1/2011	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847CN	CN	200610168332.1	12/18/2006	212006101683	3/27/2013	In Force	PIPELINED ANALOG-TO-DIGITAL CONVERTERS
MP0847	MP0847EP	EP	EP06024141.1	11/21/2006	EP1814232	5/15/2013	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847EPDE	DE	06024141.1	11/21/2006	602006036246	5/15/2013	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847EPRR	FR	06024141.1	11/21/2006	1814232	5/15/2013	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847EPGB	GB	06024141.1	11/21/2006	1814232	5/15/2013	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847HK	HK	81000849	12/18/2006			Pending	Pipelined Analog-To-Digital Converters
MP0847	MP0847JP	JP	2006340437	12/18/2006	JP4939201	3/2/2012	In Force	Pipelined Analog-To-Digital Converter
MP0847	MP0847SG	SG	2006086094	12/18/2006	134212	7/31/2009	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847TW	TW	95147268	12/15/2006	1420827	12/21/2013	In Force	PIPELINED ANALOG-TO-DIGITAL CONVERTERS
MP0850	MP0850	US	11/446,480	6/2/2006	8199804	6/12/2012	In Force	EFFICIENT TAPPED DELAY LINE EQUALIZER METHODS AND APPARAT
MP0850	MP0850C1	US	13/478,853	5/23/2012	8454041	7/23/2013	In Force	EFFICIENT TAPPED DELAY LINE EQUALIZER METHODS AND APPARAT
MP0851	MP0851C1	US	12/712,021	2/24/2010	7978758	7/12/2011	In Force	EFFICIENT BLIND EQUALIZATION FOR QUADRATURE AMPLITUDE MO
MP0852	MP0852C1	US	12/829,750	7/12/2010	8054877	11/8/2011	In Force	CONSTELLATION RECOVERY FOR QUADRATURE AMPLITUDE MODUL
MP0854	MP0854C1	US	12/993,241	11/23/2010	8184511	5/22/2012	In Force	Method And Apparatus For Increasing The Storage Capacity Of An Op
MP0855	MP0855	US	11/469,835	7/19/2006	7528667	5/5/2009	In Force	T-NETWORK CAPBANK
MP0856	MP0856C1	US	12/694,922	1/27/2010	8319547	11/27/2012	In Force	SELF BIASED LOW NOISE HIGH PSRR CONSTANT GM FOR VCO
MP0858	MP0858	US	11/544,163	10/5/2006	7756486	7/13/2010	In Force	TRANSMITTER AND RECEIVER IMPEDANCE CONTROL USING SHUNT S
MP0858	MP0858C1	US	12/827,449	6/30/2010	8442451	5/14/2013	In Force	TRANSMITTER AND RECEIVER IMPEDENCE CONTROL USING SHUNT S
MP0861	MP0861	US	11/415,937	5/2/2006	7729389	6/1/2010	In Force	8/10 And 64/56 Aggregation
MP0861	MP0861C1	US	12/791,130	6/1/2010	8233507	7/31/2012	In Force	8/10 And 64/56 Aggregation
MP0861	MP0861C1aa	US	13/899,877	5/22/2013	8982915	3/17/2015	In Force	8/10 And 64/56 Aggregation
MP0861	MP0861C1C1	US	13/563,342	7/31/2012	8451869	5/28/2013	In Force	8/10 And 64/56 Aggregation
MP0862	MP0862C1	US	12/714,716	3/1/2010	8284747	10/9/2012	In Force	BSS Selection Using Path Loss
MP0862	MP0862C1aa	US	14/061,811	10/24/2013	8988559	12/9/2014	In Force	BSS Selection Using Path Loss
MP0862	MP0862C1C1	US	13/614,214	9/13/2012	8570997	10/29/2013	In Force	BSS Selection Using Path Loss
MP0863	MP0863	US	11/400,982	4/10/2006	7765681	7/20/2010	In Force	Transmit Power Adaptation Algorithm Using 802.11H
MP0863	MP0863aaaa	US	14/521,927	10/23/2014	9510299	11/29/2016	In Force	Method And Apparatus For Controlling A Power At Which A Signals
MP0863	MP0863C1	US	12/836,183	7/14/2010	8014802	9/6/2011	In Force	Transmit Power Adaptation Algorithm Using 802.11H
MP0863	MP0863C1aa	US	13/747,141	1/22/2013	8874154	10/28/2014	In Force	Transmit Power Adaptation Algorithm Using 802.11H
MP0863	MP0863C1C1	US	13/224,967	9/2/2011	8359052	1/22/2013	In Force	Transmit Power Adaptation Algorithm Using 802.11H
MP0864	MP0864	US	11/410,938	4/25/2006	7762044	4/20/2010	In Force	Radar Detection And Dynamic Frequency Selection
MP0864	MP0864C1	US	12/761,937	4/16/2010	9366750	6/14/2016	In Force	Radar Detection And Dynamic Frequency Selection

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MP0864	MP0864CN	CN	200610150782	10/26/2006	ZL2006101507	1/4/2012	In Force	RADAR DETECTION AND DYNAMIC FREQUENCY SELECTION
MP0864	MP0864EP	EP	EP06020170.4	9/26/2006	EP1793241	6/10/2009	In Force	System and Method for Radar Detection and Dynamic Frequency Sel
MP0864	MP0864EPDE	DE	06020170.4	9/26/2006	603006007203	6/10/2009	In Force	System And Method For Radar Detection And Dynamic Frequency Sel
MP0864	MP0864EPPR	FR	60201704	9/26/2006	1793241	6/10/2009	In Force	System And Method For Radar Detection And Dynamic Frequency Sel
MP0864	MP0864EPGB	GB	06020170.4	9/26/2006	1793241	6/10/2009	In Force	System And Method For Radar Detection And Dynamic Frequency Sel
MP0864	MP0864HK	HK	71132379	10/26/2006			Pending	Radar Detection And Dynamic Frequency Selection
MP0864	MP0864JP	JP	2006300741	11/16/2006	5214133	3/8/2013	In Force	Radar Detection And Dynamic Frequency Selection
MP0864	MP0864KR	KR	1020060122395	12/5/2006	101256217	4/12/2013	In Force	Radar Detection And Dynamic Frequency Selection
MP0864	MP0864SG	SG	2006069587	10/11/2006			Pending	Radar Detection And Dynamic Frequency Selection
MP0864	MP0864SG01	SG	2009038365	10/11/2006	153130	5/31/2011	In Force	Radar Detection And Dynamic Frequency Selection
MP0864	MP0864TW	TW	95139366	10/25/2006	1401462	7/11/2013	In Force	WIRELESS NETWORK DEVICE AND METHOD FOR DETECTING RADAR
MP0865	MP0865	US	11/455,476	6/19/2006	7466095	12/16/2008	In Force	Voice Coil Motor Control System And Method Using Pulse Width Mo
MP0865	MP0865C1	US	12/337,860	12/4/2008	7602131	10/13/2009	In Force	Voice Coil Motor Control System And Method Using Pulse Width Mo
MP0866	MP0866	US	11/590,031	10/31/2006	7872636	1/18/2011	In Force	Virtual Pointing Devices For Displays
MP0866	MP0866C1	US	13/008,179	1/18/2011	8531398	9/10/2013	In Force	Method And Apparatus For Using A Virtual Pointer To Select An Obje
MP0868	MP0868	US	11/368,308	3/3/2006	7835710	11/16/2010	In Force	VARIABLE POWER ADAPTIVE TRANSMITTER
MP0868	MP0868AUDE	DE	07000789.3	1/16/2007	602007021083	3/7/2012	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868AUFR	FR	07000789.3	1/16/2007	1814220	3/7/2012	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868AUGB	GB	07000789.3	1/16/2007	1814220	3/7/2012	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868C1	US	11/649,658	1/4/2007	7761066	7/20/2010	In Force	Variable Power Adaptive Transmitter
MP0868	MP0868C1CN	CN	200710002905	1/26/2007	ZL2007100029	2/13/2013	In Force	A VARIABLE POWER ADAPTIVE TRANSMITTER
MP0868	MP0868C1EP	EP	07000789.3	1/16/2007	EP1814220	3/7/2012	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868C1HK	HK	81029926	1/26/2007			Pending	A Variable Power Adaptive Transmitter
MP0868	MP0868C1JP	JP	2007016581	1/26/2007	5010296	6/8/2012	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868C1SG	SG	2007004997	1/26/2007			Pending	A Variable Power Adaptive Transmitter
MP0868	MP0868C1SG03	SG	2009050071	1/26/2007	154521	6/30/2011	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868C1TW	TW	96102999	1/26/2007	1458269	10/21/2014	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868CN	CN	200710002906	1/26/2007	ZL2007100029	8/8/2012	In Force	A VARIABLE POWER ADAPTIVE TRANSMITTER
MP0868	MP0868DE	DE	07000603.2	1/16/2007	6,020,07E+12	5/11/2011	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868EP	EP	EP07000603.2	1/16/2007	EP1814221	5/11/2011	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868FR	FR	70008032	1/16/2007	1814221	5/11/2011	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868GB	GB	07000803.2	1/16/2007	1814221	5/11/2011	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868HK	HK	81010963	1/26/2007			Pending	A Variable Power Adaptive Transmitter
MP0868	MP0868JP	JP	2007016577	1/26/2007	4949049	3/16/2012	In Force	A Variable Power Adaptive Transmitter

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MP0868	MP08685G	SG	2007005002	1/26/2007			Pending	A Variable Power Adaptive Transmitter
MP0868	MP08685G02	SG	2009050063	1/26/2007	154520	6/30/2011	In Force	A Variable Power Adaptive Transmitter
MP0869	MP0869	US	11/411,616	4/26/2006	7873048	1/18/2011	In Force	Flexible Port Rate Limiting
MP0869	MP0869C1	US	13/008,778	1/18/2011	8634335	1/21/2014	In Force	Flexible Port Rate Limiting
MP0870	MP0870	US	11/481,141	7/5/2006	7774675	8/10/2010	In Force	LDPC Codes And Expansion Method
MP0870	MP0870C1	US	12/852,817	8/9/2010	8281213	10/2/2012	In Force	LDPC Codes And Expansion Method
MP0870	MP0870C1a	US	13/942,183	7/15/2013	8762809	6/24/2014	In Force	LDPC Codes And Expansion Method
MP0870	MP0870C1C1	US	13/614,065	9/13/2012	8489960	7/16/2013	In Force	LDPC Codes And Expansion Method
MP0870	MP0870C1a	US	14/309,319	6/19/2014	9069560	4/14/2015	In Force	Apparatus For Encoding And Decoding Using Sparse Matrices
MP0873	MP0873	US	11/586,859	10/26/2006	8084631	1/10/2012	In Force	Coexistence System And Method For Wireless Network Devices
MP0873	MP0873C1	US	13/346,283	1/9/2012	8345652	1/1/2013	In Force	Coexistence System And Method For Wireless Network Devices
MP0873	MP0873C1a	US	14/101,656	12/10/2013	8923847	12/30/2014	In Force	Apparatus And Method For Communicating With An Access Point Via
MP0873	MP0873C1C1	US	13/730,144	12/28/2012	866263	12/10/2013	In Force	METHOD AND APPARATUS FOR RESPECTIVELY TRANSMITTING SIGNA
MP0873	MP0873PCT	WO	PCT/US2006/04	12/11/2006			Pending	Coexistence System And Method For Wireless Network Devices
MP0873	MP0873W1A	JP	2014-076691	4/3/2014	5778307	7/17/2015	In Force	COEXISTENCE SYSTEM AND METHOD FOR WIRELESS NETWORK DEVI
MP0873	MP0873W1D1	JP	2012-039936	12/11/2006	5548716	5/23/2014	In Force	COEXISTENCE SYSTEM AND METHOD FOR WIRELESS NETWORK DEVI
MP0873	MP0873W0CN	CN	200680052593	12/11/2006	ZL2006800525	3/14/2012	In Force	COEXISTENCE SYSTEM AND METHOD FOR WIRELESS NETWORK DEVI
MP0873	MP0873W0EP	EP	EP06839241.4	12/11/2006	1969772	2/20/2019	In Force	Coexistence System and Method for Wireless Network Devices
MP0873	MP0873W0EPDE	DE	EP06839241.4	12/11/2006	602006057455	2/20/2019	In Force	Coexistence System and Method for Wireless Network Devices
MP0873	MP0873W0EPPR	FR	EP06839241.4	12/11/2006	1969772	2/20/2019	In Force	Coexistence System and Method for Wireless Network Devices
MP0873	MP0873W0GB	GB	EP06839241.4	12/11/2006	1969772	2/20/2019	In Force	Coexistence System and Method for Wireless Network Devices
MP0873	MP0873W0IN	IN	2881CHENP2001	12/11/2006	284330	6/19/2017	In Force	Coexistence System And Method For Wireless Network Devices
MP0873	MP0873W0JP	JP	2008-544544	12/11/2006	4994388	5/18/2012	In Force	COEXISTENCE SYSTEM AND METHOD FOR WIRELESS NETWORK DEVI
MP0873	MP0873W0KR	KR	10-2008-701673	12/11/2006	10-1298244	8/13/2013	In Force	COEXISTENCE SYSTEM AND METHOD FOR WIRELESS NETWORK DEVI
MP0874	MP0874	US	11/607,116	11/30/2006	7853578	12/14/2010	In Force	High-Performance Pattern Matching
MP0874	MP0874C1	US	12/966,749	12/13/2010	8301604	10/30/2012	In Force	High Performance Pattern Matching
MP0876	MP0876C1	US	12/755,893	4/7/2010	7984246	7/19/2011	In Force	MULTICORE MEMORY MANAGEMENT SYSTEM
MP0877	MP0877	US	11/602,665	11/21/2006	7423833	9/9/2008	In Force	Servo Loop Employing Correction Vector From A Secondary Disturba
MP0877	MP0877C1	US	12/231,921	9/8/2008	7529057	5/5/2009	In Force	Servo Loop Employing Correction Vector From A Secondary Disturba
MP0879	MP0879	US	11/503,564	8/11/2006	7560961	7/14/2009	In Force	Voltage Driver Circuit
MP0879	MP0879C1	US	12/502,475	7/14/2009	7833733	2/22/2011	In Force	Voltage Driver Circuit
MP0881	MP0881	US	11/495,295	7/28/2006	7764462	7/27/2010	In Force	Thermal Solution For Drive Systems Such As Hard Disk Drives And Dig
MP0881	MP0881D1A	US	13/540,171	7/2/2012	8355221	1/15/2013	In Force	Thermal Solution For Drive Systems Such As Hard Disk Drives And Dig
MP0881	MP0881D1C1	US	13/154,339	6/6/2011	8218261	7/10/2012	In Force	Thermal Solution For Drive Systems Such As Hard Disk Drives And Dig

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0883	MP0883	US	11/450,693	6/9/2006	7660272	2/9/2010	In Force	Parallel Detection To Full Duplex
MP0883	MP0883C1	US	12/687,594	2/1/2010	7924750	4/12/2011	In Force	Method And Apparatus For Establishing A Communication Mode Bet
MP0884	MP0884	US	11/590,073	10/30/2006	7486154	2/3/2009	In Force	FLICKER NOISE DEGENERATION TECHNIQUE FOR VCO
MP0886	MP0886C1	US	12/574,110	10/6/2009	7761668	7/20/2010	In Force	Processor Architecture Having Multi-Ported Memory
MP0886	MP0886CN	CN	200610168331.1	12/18/2006	ZL2006101683	5/30/2012	In Force	PROCESSOR ARCHITECTURE
MP0886	MP0886EP	EP	EP06024608.9	11/28/2006	EP1811398	4/6/2011	In Force	Data Processing System with a Multiported Memory for Interprocess
MP0886	MP0886EPDE	DE	06024608.9	11/28/2006	602006021125	4/6/2011	In Force	Data Processing System With A Multiported Memory For Interproce
MP0886	MP0886EPFR	FR	06024608.9	11/28/2006	1811398	4/6/2011	In Force	Data Processing System With A Multiported Memory For Interproce
MP0886	MP0886EPGB	GB	06024608.9	11/28/2006	1811398	4/6/2011	In Force	Data Processing System With A Multiported Memory For Interproce
MP0886	MP0886HK	HK	8100858	12/18/2006			Pending	Processor Architecture
MP0886	MP0886JP	JP	2006340439	12/18/2006	5362507	6/28/2013	In Force	Processor Architecture
MP0886	MP0886SG	SG	2006086086	12/18/2006	134211	7/31/2009	In Force	Processor Architecture
MP0886	MP0886TW	TW	95147276	12/15/2006	514165	12/21/2015	In Force	DATA PROCESSING SYSTEM
MP0887	MP0887	US	11/384,855	3/20/2006	7212144	5/1/2007	In Force	Flash ADC
MP0887	MP0887C1	US	11/799,018	4/30/2007	7605739	10/20/2009	In Force	Differential Flash ADC With Dual Resistance Ladder Legs Receiving A
MP0887	MP0887C1C1	US	12/476,369	6/2/2009	7760127	7/20/2010	In Force	Flash ADC
MP0887	MP0887CN	CN	200610168330.1	12/18/2006	ZL2006101683	9/5/2012	In Force	IMPROVED FLASH ADC
MP0887	MP0887EP	EP	EP06024652.7	11/28/2006	EP1814233	10/21/2009	In Force	Improved Flash ADC
MP0887	MP0887EPDE	DE	06024652.7	11/28/2006	602006009894	10/21/2009	In Force	Improved Flash ADC
MP0887	MP0887EPFR	FR	06024652.7	11/28/2006	1814233	10/21/2009	In Force	Improved Flash ADC
MP0887	MP0887EPGB	GB	06024652.7	11/28/2006	1814233	10/21/2009	In Force	Improved Flash ADC
MP0887	MP0887HK	HK	8100867	12/18/2006			Pending	Improved Flash ADC
MP0887	MP0887JP	JP	2006340442	12/18/2006	JP4799391	8/12/2011	In Force	Improved Flash ADC
MP0887	MP0887SG	SG	2006084016	11/30/2006	134209	8/31/2009	In Force	Improved Flash ADC
MP0887	MP0887TW	TW	95147272	12/15/2006	1403093	7/21/2013	In Force	IMPROVED FLASH ADC
MP0888	MP0888	US	10/386,327	6/9/2004	7643582	1/5/2010	In Force	METHOD AND SYSTEM FOR DETERMINING SYMBOL BOUNDARY TIME
MP0888	MP0888C1	US	12/628,738	12/1/2009	8265215	9/11/2012	In Force	METHOD AND SYSTEM FOR DETERMINING SYMBOL BOUNDARY TIME
MP0888	MP0888C1C1	US	13/608,011	9/10/2012	8817919	8/26/2014	In Force	METHOD AND SYSTEM FOR DETERMINING SYMBOL BOUNDARY TIME
MP0888	MP0888WO	WO	PCT/US2005/02	6/6/2005			Pending	METHOD AND SYSTEM FOR DETERMINING SYMBOL BOUNDARY TIME
MP0889	MP0889	US	10/865,256	6/9/2004	7817730	10/19/2010	In Force	TRAINING SEQUENCE FOR SYMBOL BOUNDARY DETECTION IN A MUL
MP0889	MP0889WO	WO	PCT/US2005/02	6/6/2005			Pending	TRAINING SEQUENCE FOR SYMBOL BOUNDARY DETECTION IN A MUL
MP0890	MP0890	US	10/865,093	6/9/2004	7561626	7/14/2009	In Force	METHOD AND SYSTEM FOR CHANNEL ESTIMATION IN A DATA TRANS
MP0890	MP0890WO	WO	PCT/US2005/02	6/9/2005			Pending	METHOD AND SYSTEM FOR CHANNEL ESTIMATION IN A DATA TRANS
MP0891	MP0891	US	10/865,133	6/9/2004	7697619	4/13/2010	In Force	TRAINING SEQUENCE FOR CHANNEL ESTIMATION IN A DATA TRANS

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MP0891	MP0891WO	WO	PCT/US2005/01	6/6/2005		7/14/2009	Pending	TRAINING SEQUENCE FOR CHANNEL ESTIMATION IN A DATA TRANS
MP0892	MP0892	US	11/031,221	1/6/2005	7561627		In Force	METHOD AND SYSTEM FOR CHANNEL EQUALIZATION AND CROSSSTAL
MP0892	MP0892WO	WO	PCT/US2005/04	12/20/2005			Pending	METHOD AND SYSTEM FOR CHANNEL EQUALIZATION AND CROSSSTAL
MP0894	MP0894C1C1	US	14/157,977	1/17/2014	9490810	11/8/2016	In Force	LOW-NOISE, TEMPERATURE-INSENSITIVE, VOLTAGE OR CURRENT IN
MP0895	MP0895C1	US	12/860,440	8/20/2010	8026742	9/27/2011	In Force	PHASE DETECTOR
MP0898	MP0898	US	11/523,407	9/19/2006	8432920	4/30/2013	In Force	Direct Link Setup Mechanisms For Wireless LANs
MP0898	MP0898C1	US	13/871,490	4/26/2013	9198212	11/24/2015	In Force	Direct Link Setup Mechanisms For Wireless LANs
MP0898	MP0898WO	WO	PCT/US2007/02	9/19/2007			Pending	Direct Link Setup Mechanisms For Wireless LANs
MP0898	MP0898WOCN	CN	200780042934	9/19/2007	ZL2007800429	7/11/2012	In Force	DIRECT LINK SETUP MECHANISMS FOR WIRELESS LANs
MP0898	MP0898WCEP	EP	07838498.9	9/19/2007	EP2064837	11/22/2017	In Force	Direct Link Setup Mechanisms for Wireless LANs
MP0898	MP0898WOJP	JP	2009-529222	9/19/2007	4931558	2/10/2012	In Force	DIRECT LINK SETUP MECHANISMS FOR WIRELESS LANs
MP0898	MP0898WUDE	DE	07838498.9	9/19/2007	602007053146	11/22/2017	In Force	Direct Link Setup Mechanisms for Wireless LANs
MP0898	MP0898WUFR	FR	07838498.9	9/19/2007	2064837	11/22/2017	In Force	Direct Link Setup Mechanisms for Wireless LANs
MP0898	MP0898WUGE	GB	07838498.9	9/19/2007	2064837	11/22/2017	In Force	Direct Link Setup Mechanisms for Wireless LANs
MP0899	MP0899	US	11/528,176	9/26/2006	7388531	6/17/2008	In Force	CURRENT STEERING DAC USING THIN OXIDE DEVICES
MP0900	MP0900	US	11/835,952	8/8/2007	7576667	8/18/2009	In Force	HIERARCHIED CALIBRATION CIRCUIT
MP0901	MP0901	US	11/786,886	4/12/2007	7492219	2/17/2009	In Force	POWER EFFICIENT AMPLIFIER
MP0901	MP0901C1	US	12/350,735	1/8/2009	7737776	6/15/2010	In Force	POWER EFFICIENT AMPLIFIER
MP0901	MP0901C1C1	US	12/723,366	3/12/2010	7932778	4/26/2011	In Force	POWER EFFICIENT AMPLIFIER
MP0902	MP0902	US	11/504,523	8/15/2006	7822160	10/26/2010	In Force	Digitally-Assisted Power Reduction Technique For IQ Pipeline ADCs U
MP0902	MP0902C1	US	12/907,842	10/19/2010	8031821	10/4/2011	In Force	Digitally-Assisted Power Reduction Technique For IQ Pipeline ADCs U
MP0903	MP0903	US	11/486,906	7/14/2006	7397412	7/8/2008	In Force	Low Power Analog To Digital Converter
MP0904	MP0904	US	11/481,109	7/5/2006	7646555	1/12/2010	In Force	Testing Storage System Electronics Using Loopback
MP0904	MP0904C1	US	12/685,249	1/11/2010	8031424	10/4/2011	In Force	Method And Apparatus For Testing Electronics Of A Storage System
MP0904	MP0904CN	CN	200710000255.1	1/16/2007	ZL2007100002	7/6/2011	In Force	TESTING STORAGE SYSTEM ELECTRONICS USING LOOPBACK
MP0904	MP0904EP	EP	70004635	1/10/2007			Pending	Testing Storage System Electronic Using Loopback
MP0904	MP0904HK	HK	81006876	1/16/2007			Pending	Testing Storage System Electronics Using Loopback
MP0904	MP0904JP	JP	2007008412	1/17/2007			Pending	Testing Storage System Electronic Using Loopback
MP0904	MP0904SG	SG	2007002256	1/16/2007			Pending	Testing Storage System Electronic Using Loopback
MP0904	MP0904SG01	SG	2009048406	1/16/2007	154489	4/15/2011	In Force	Testing Storage System Electronics Using Loopback
MP0904	MP0904TW	TW	96101812	1/17/2007	1406272	8/21/2013	In Force	TESTING STORAGE SYSTEM ELECTRONICS USING LOOPBACK
MP0905	MP0905	US	11/471,057	6/19/2006	7675157	3/9/2010	In Force	THERMAL ENHANCED PACKAGE
MP0905	MP0905CN	CN	200780012013	1/29/2007	ZL2007800120	6/20/2012	In Force	THERMAL ENHANCED PACKAGE
MP0905	MP0905CND1	CN	201210124148.1	1/29/2007	ZL2012101241	2/17/2016	In Force	THERMAL ENHANCED PACKAGE

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MP0905	MP0905D1	US	12/496,379	7/1/2009	8357568	1/22/2013	In Force	THERMAL ENHANCED PACKAGE
MP0905	MP0905D1C1	US	13/613,886	9/13/2012	8906932	12/2/2014	In Force	Thermal enhanced package
MP0905	MP0905TW	TW	96103334	1/30/2007	1343102	6/1/2011	In Force	THERMAL ENHANCED PACKAGE
MP0905	MP0905WO	WO	PCT/US2007/00	1/29/2007			Pending	THERMAL ENHANCED PACKAGE
MP0907	MP0907	US	11/521,182	9/14/2006	7813421	10/12/2010	In Force	Order Recursive Computation For A MIMO Equalizer
MP0907	MP0907C1	US	12/902,394	10/12/2010	8340169	12/25/2012	In Force	Order Recursive Computation For A MIMO Equalizer
MP0907	MP0907C1aa	US	14/252,860	4/15/2014	9001873	4/7/2015	In Force	Method And Apparatus For Recursively Computing Equalizer Param
MP0907	MP0907C1C1	US	13/725,167	12/21/2012	8695556	4/15/2014	In Force	Method And Apparatus For Recursively Computing Equalizer Coeffic
MP0907	MP0907PCT	WO	PCT/US2007/00	1/16/2007			Pending	Order Recursive Computation For A MIMO Equalizer
MP0907	MP0907WP2	JP	2008-550466	1/17/2007	JP5160449	12/21/2012	In Force	RECURSIVE COMPUTATION OF A CHANNEL MATRIX FOR A MIMO EQ
MP0907	MP0907WO2IN	IN	3393CHENP2001	1/17/2007	283233	5/11/2017	In Force	Recursive Computation For A Channel Matrix For A MIMO Equalizer
MP0907	MP0907WO2KR	KR	1020087020091	1/17/2007	101373788	3/6/2014	In Force	Recursive Computation Of A Channel Matrix For A MIMO Equalizer
MP0907	MP0907WOCN	CN	200780002519.1	1/17/2007	ZL2007800025	7/18/2012	In Force	RECURSIVE COMPUTATION OF A CHANNEL MATRIX FOR A MIMO EQ
MP0907	MP0907WCEP	EP	EP07716730.2	1/17/2007	1974512	8/22/2018	In Force	Recursive Computation for a Channel Matrix For a MIMO Equalizer
MP0907	MP0907WCEPDE	DE	EP07716730.2	1/17/2007	602607055836	8/22/2018	In Force	Recursive Computation for a Channel Matrix For a MIMO Equalizer
MP0907	MP0907WCEPFR	FR	EP07716730.2	1/17/2007	1974512	8/22/2018	In Force	Recursive Computation for a Channel Matrix For a MIMO Equalizer
MP0907	MP0907WCEPGB	GB	EP07716730.2	1/17/2007	1974512	8/22/2018	In Force	Recursive Computation for a Channel Matrix For a MIMO Equalizer
MP0907	MP0907WCEFOB	WO	PCT/US2007/00	1/17/2007			Pending	Recursive Computation For A Channel Matrix For A MIMO Equalizer
MP0908	MP0908	US	11/652,907	1/11/2007	7791406	9/7/2010	In Force	LOW LEAKAGE POWER MANAGEMENT
MP0908	MP0908D1	US	12/873,997	9/1/2010	8004352	8/23/2011	In Force	LOW LEAKAGE POWER MANAGEMENT
MP0908	MP0908D1aa	US	13/548,808	7/13/2012	8436678	5/7/2013	In Force	LOW LEAKAGE POWER MANAGEMENT
MP0908	MP0908D1C1	US	13/213,695	8/19/2011	8248156	8/21/2012	In Force	LOW LEAKAGE POWER MANAGEMENT
MP0909	MP0909	US	11/598,117	11/8/2006	7844879	11/30/2010	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909C1	US	12/946,520	11/15/2010	8473812	6/25/2013	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909C1aa	US	14/217,675	3/18/2014	8856622	7/10/2014	In Force	APPARATUS AND METHOD FOR ENCODING DATA FOR STORAGE IN M
MP0909	MP0909C1C1	US	13/916,514	6/25/2013	8677215	3/18/2014	In Force	Method and System For Error Correction In Flash Memory
MP0909	MP0909C12a	US	14/505,820	10/3/2014	9053051	6/9/2015	In Force	Multi-Level Memory Controller With Probability-Distribution-Based E
MP0909	MP0909CN	CN	200780003987.1	1/19/2007	ZL2007800039	1/25/2012	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909EP	EP	07718293.9	1/19/2007	EP1987519	8/19/2015	In Force	Method and System for Error Correction in Flash Memory
MP0909	MP0909EPDE	DE	07718293.9	1/19/2007	1987519	8/19/2015	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909EPFR	FR	07718293.9	1/19/2007	1987519	8/19/2015	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909EPGB	GB	07718293.9	1/19/2007	1987519	8/19/2015	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909JP	JP	2008-551451	1/19/2007	5232013	3/29/2013	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION FLASH MEMORY
MP0909	MP0909KR	KR	1020087020392	1/19/2007	101410434	6/16/2014	In Force	Method And System For Error Correction In Flash Memory

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0909	MP0909TW	TW	96102213	1/19/2007	1367492	7/1/2012	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909TWD1	TW	99144839	1/19/2007	1455137	10/1/2014	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909WO	WO	PCT/US2007/00	1/19/2007			Pending	Method And System For Error Correction In Flash Memory
MP0910	MP0910	US	11/607,641	12/1/2006	7619457	11/17/2009	In Force	PROGRAMMABLE DELAY CIRCUIT
MP0910	MP0910C1	US	12/616,393	11/11/2009	8242828	8/14/2012	In Force	PROGRAMMABLE DELAY CIRCUIT
MP0911	MP0911	US	11/501,338	8/9/2006	7388529	6/17/2008	In Force	Interference Detection Using A Moving Window
MP0911	MP0911C1	US	12/157,255	6/9/2008	7864091	1/4/2011	In Force	Interference Detection Using A Moving Window
MP0911	MP0911C1C1	US	12/983,794	1/3/2011	7978106	7/12/2011	In Force	Interference Detection Using A Moving Window
MP0912	MP0912	US	11/501,341	8/9/2006	7623052	11/24/2009	In Force	Strong Short-Pulse Interference Detection Method
MP0912	MP0912C1	US	12/623,799	11/23/2009	7773014	8/10/2010	In Force	Strong Short-Pulse Interference Detection Method
MP0914	MP0914C1	US	12/724,250	3/15/2010	7974367	7/5/2011	In Force	SYMBOL TIMING METHOD AND DIGITAL RECEIVER APPARATUS
MP0916	MP0916	US	11/588,178	11/8/2006	8055979	11/8/2011	In Force	Flash Memory With Coding And Signal Processing
MP0916	MP0916CN	CN	200780010188.1	1/19/2007	ZL2007800101	1/4/2012	In Force	FLASH MEMORY WITH CODING AND SIGNAL PROCESSING
MP0916	MP0916EP	EP	07718319.2	1/19/2007	EP1984923	8/14/2013	In Force	Flash Memory with Coding and Signal Processing
MP0916	MP0916EPDE	DE	07718319.2	1/19/2007	1984923	8/14/2013	In Force	FLASH MEMORY WITH CODING AND SIGNAL PROCESSING
MP0916	MP0916EPFR	FR	07718319.2	1/19/2007	1984923	8/14/2013	In Force	FLASH MEMORY WITH CODING AND SIGNAL PROCESSING
MP0916	MP0916EPGB	GB	07718319.2	1/19/2007	1984923	8/14/2013	In Force	FLASH MEMORY WITH CODING AND SIGNAL PROCESSING
MP0916	MP0916JP	JP	2008-551453	1/19/2007	JP5232014	3/29/2013	In Force	FLASH MEMORY WITH CODING AND SIGNAL PROCESSING
MP0916	MP0916KR	KR	1020087020393	1/19/2007	101373789	3/6/2014	In Force	Flash Memory With Coding And Signal Processing
MP0916	MP0916TW	TW	96102390	1/22/2007	1352284	11/11/2011	In Force	FLASH MEMORY WITH CODING AND SIGNAL PROCESSING
MP0916	MP0916WO	WO	PCT/US2007/00	1/19/2007			Pending	Flash Memory With Coding And Signal Processing
MP0917	MP0917	US	11/504,093	8/14/2006	9363346	6/7/2016	In Force	Remote Control Of Network Appliances Using Voice Over Internet Pr
MP0917	MP0917JP	JP	2007102557	4/10/2007	JP5292717	6/21/2013	In Force	Remote Control Of Network Appliances Using Voice Over Internet Pr
MP0917	MP0917CN	CN	200710093723	4/5/2007	ZL2007100937	12/18/2013	In Force	REMOTE CONTROL OF NETWORK APPLIANCES USING VOICE OVER IN
MP0917	MP0917EPDE	DE	07065937.3	3/22/2007	60200705115.	3/7/2018	In Force	Remote Control of Network Appliances Using Voice Over Internet Pro
MP0917	MP0917EPFR	FR	07065937.3	3/22/2007	1855450	3/7/2018	In Force	Remote Control of Network Appliances Using Voice Over Internet Pro
MP0917	MP0917EPGB	GB	07065937.3	3/22/2007	1855450	3/7/2018	In Force	Remote Control of Network Appliances Using Voice Over Internet Pro
MP0917	MP0917HK	HK	81046649	4/5/2007			Pending	Remote Control Of Network Appliances Using Voice Over Internet Pr
MP0917	MP0917SG	SG	2007020654	3/22/2007			Pending	Remote Control Of Network Appliances Using Voice Over Internet Pr
MP0917	MP0917SG01	SG	2009070640	3/22/2007			Pending	Remote Control Of Network Appliances Using Voice Over Internet Pr
MP0917	MP0917TW	TW	96112272	4/9/2007	1435582	4/21/2014	In Force	Remote Control Of Network Appliances Using Voice Over Internet Pr
MP0922	MP0922	US	11/505,513	8/16/2006	7358876	4/15/2008	In Force	MIXED-MODE ANALOG OFFSET CANCELLATION FOR DATA CONVERSI
MP0923	MP0923	US	11/505,555	8/16/2006	7372329	5/13/2008	In Force	METHOD AND APPARATUS TO REMOVE THE DC COMPONENT OF A F
MP0923	MP0923C1	US	12/114,491	2/5/2008	7944292	5/17/2011	In Force	METHOD AND APPARATUS TO REMOVE THE DC COMPONENT OF A F

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0924	MP0924	US	11/505,782	8/16/2006	7808075	10/5/2010	In Force	INTEGRATED CIRCUIT DEVICES WITH ESD AND I/O PROTECTION
MP0924	MP0924C1	US	12/858,194	8/17/2010	7883947	2/8/2011	In Force	METHOD OF FABRICATING A DEVICE WITH ESD AND I/O PROTECTION
MP0925	MP0925	US	11/523,389	9/19/2006	8089907	1/3/2012	In Force	Duplex Mismatch Detection
MP0925	MP0925D1	US	13/315,800	9/12/2011	8705416	4/22/2014	In Force	DUPLEX MISMATCH DETECTION
MP0925	MP0925TW	TW	96106323	2/16/2007	146521	12/21/2014	In Force	DUPLEX MISMATCH DETECTION
MP0925	MP0925WO	WO	PCT/US2007/00	2/15/2007			Pending	Duplex Mismatch Detection
MP0925	MP0925WOCN	CN	200780019972	2/15/2007			Pending	DUPLEX MISMATCH DETECTION
MP0925	MP0925WOEP	EP	EP20070750897	2/15/2007	1993882	4/10/2019	In Force	Duplex Mismatch Detection
MP0925	MP0925WOEPDE	DE	EP20070750897	2/15/2007	602007058074	4/10/2019	In Force	Duplex Mismatch Detection
MP0925	MP0925WOEPFR	FR	EP20070750897	2/15/2007	1993882	4/10/2019	In Force	Duplex Mismatch Detection
MP0925	MP0925WOEPGB	GB	EP20070750897	2/15/2007	1993882	4/10/2019	In Force	Duplex Mismatch Detection
MP0925	MP0925WOJP	JP	2009-502781	2/15/2007	4972154	4/13/2012	In Force	DUPLEX MISMATCH DETECTION
MP0927	MP0927WO	WO	PCT/US2006/00	1/20/2006			Pending	Limiter Based Analog Demodulator
MP0927	MP0927WOCN	CN	200680027904	1/20/2006	ZL200680027	11/14/2012	In Force	LIMITER BASED ANALOG DEMODULATOR
MP0927	MP0927WOJP	JP	2007-552261	1/20/2006	4939437	3/2/2012	In Force	Limiter Based Analog Demodulator
MP0928	MP0928	US	11/417,778	5/4/2006	7720179	5/18/2010	In Force	Method For Timing Detection
MP0928	MP0928_PCT	WO	PCT/US2006/01	5/9/2006			Pending	Method For Timing Detection
MP0928	MP0928TW	TW	95118591	5/25/2006	1416912	11/21/2013	In Force	Method For Timing Detection
MP0929	MP0929CN	CN	200680023871	1/12/2006	ZL200680023	2/1/2012	In Force	METHOD AND APPARATUS FOR A TRANSMISSION SIGNAL UP-CONVE
MP0929	MP0929WO	WO	PCT/US2006/00	1/12/2006			Pending	Method And Apparatus For A Transmission Signal Up-Converting Filte
MP0929	MP0929WOHK	HK	81068580	6/20/2008			Pending	Method And Apparatus For A Transmission Signal Up-Converting Filte
MP0930	MP0930	US	11/546,671	10/12/2006	8121065	2/21/2012	In Force	TDMA Controller For A Wireless Communication System And Method
MP0930	MP0930C1	US	13/400,326	2/20/2012	8675541	3/18/2014	In Force	TDMA Controller For A Wireless Communication System And Method
MP0930	MP0930C1C1	US	14/215,831	3/17/2014	9161358	10/13/2015	In Force	Method And Apparatus For Synchronizing Timing Of A First Station T
MP0930	MP0930CN	CN	2007800039811	1/9/2007	ZL2007800039	5/25/2016	In Force	TDMA CONTROLLER FOR A WIRELESS COMMUNICATION SYSTEM AN
MP0930	MP0930PCT	WO	PCT/US2007/00	1/9/2007			Pending	TDMA Controller For A Wireless Communication System And Method
MP0930	MP0930TW	TW	96109257	1/29/2007	1451727	9/1/2014	In Force	TDMA CONTROLLER FOR A WIRELESS COMMUNICATION SYSTEM AN
MP0931	MP0931WO	WO	PCT/US2006/00	1/31/2006			Pending	Improved Precision Cordic Processor
MP0931	MP0931WOCN	CN	2006800037230	1/31/2006			Pending	Improved Precision Cordic Processor
MP0931	MP0931WOJP	JP	2007553372	1/31/2006			Pending	Improved Precision Cordic Processor
MP0932	MP0932C1CN	CN	2006800027421	1/19/2006	ZL2006800027	11/16/2011	In Force	STORAGE EFFICIENT SLIDING WINDOW SUM
MP0932	MP0932WO	WO	PCT/US2006/00	1/19/2006			Pending	Storage Efficient Sliding Window Sum
MP0932	MP0932WOJP	JP	2007552276	1/19/2006	4970283	4/13/2012	In Force	Storage Efficient Sliding Window Sum
MP0934	MP0934	US	11/442,838	5/30/2006	7839923	11/23/2010	In Force	Method And System For Equalizing Received Signals In Communicatio

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MP0934	MP0934PW0	WO	PCT/US2006/02	6/9/2006			Pending	Receiving System With Carrier Recovery, Timing Correction And Equalization
MP0934	MP0934TW	TW	95122284	6/21/2006	1403107	7/21/2013	In Force	METHOD AND SYSTEM FOR EQUALIZING RECEIVED SIGNAL IN COMMUNICATION
MP0935	MP0935	US	11/642,817	12/19/2006	8660617	2/25/2014	In Force	WIMAX ENHANCED SLEEP MODE
MP0935	MP0935C1	US	14/188,509	2/24/2014	9037198	5/19/2015	In Force	WIMAX ENHANCED SLEEP MODE
MP0935	MP0935WO	WO	PCT/US2007/00	2/9/2007			Pending	WIMAX ENHANCED SLEEP MODE
MP0936	MP0936	US	11/519,121	9/11/2006	7865784	1/4/2011	In Force	Write Validation
MP0937	MP0937	US	11/649,982	1/5/2007	7921243	4/5/2011	In Force	System And Method For A DDR SDRAM Controller
MP0941	MP0941	US	11/516,191	9/6/2006	8078687	12/13/2011	In Force	System And Method For Data Management
MP0943	MP0943C1	US	12/787,484	5/26/2010	7945812	5/17/2011	In Force	System And Method For Error Correction
MP0944	MP0944	US	11/519,979	9/12/2006	7423597	9/9/2008	In Force	Dual Band WLAN Antenna
MP0944	MP0944C1	US	11/581,502	10/16/2006	7394433	7/1/2008	In Force	Dual Band WLAN Antenna
MP0944	MP0944C1C1	US	12/214,165	6/17/2008	7890547	9/21/2010	In Force	Dual Band WLAN Antenna
MP0944	MP0944C2	US	11/581,540	10/16/2006	7423599	9/9/2008	In Force	Dual Band WLAN Antenna
MP0944	MP0944C3	US	11/581,386	10/16/2006	7495621	2/24/2009	In Force	Dual Band WLAN Antenna
MP0944	MP0944C4	US	11/581,717	10/16/2006	7403162	7/22/2008	In Force	Dual Band WLAN Antenna
MP0944	MP0944C4C1	US	12/215,819	6/30/2008	7825864	11/2/2010	In Force	Dual Band WLAN Antenna
MP0944	MP0944C5	US	12/231,804	9/5/2008	7872608	1/18/2011	In Force	Dual Band WLAN Antenna
MP0944	MP0944WO	WO	PCT/US2007/00	2/9/2007			Pending	Dual Band WLAN Antenna
MP0945	MP0945C1	US	12/917,426	11/1/2010	8090932	1/3/2012	In Force	Communication Bus With Hidden Pre-Fetch Registers
MP0946	MP0946	US	11/522,726	9/18/2006	7613887	11/3/2009	In Force	System And Method For Managing A Memory Storage Device
MP0946	MP0946C1	US	12/611,131	11/3/2009	7949838	5/24/2011	In Force	System And Method For Managing A Memory Storage Device
MP0947	MP0947	US	11/507,161	8/21/2006	7895652	9/28/2010	In Force	Methods For Generating And Implementing Quasi-Cyclic Irregular LDPC Codes
MP0947	MP0947C1	US	12/891,251	9/27/2010	8051357	11/1/2011	In Force	Methods For Generating And Implementing Quasi-Cyclic Irregular LDPC Codes
MP0950	MP0950	US	11/582,014	10/16/2006	7752523	7/6/2010	In Force	REDUCED-COMPLEXITY DECODING OF PARITY CHECK CODES
MP0952	MP0952	US	11/526,398	9/25/2006	7793063	9/7/2010	In Force	Method And System For Automatic Calibration Of A DQS Signal In A SDRAM
MP0952	MP0952C1	US	12/874,349	9/2/2010	8019957	9/13/2011	In Force	Method And System For Automatic Calibration Of A DQS Signal In A SDRAM
MP0954	MP0954	US	11/541,977	10/2/2006	7471482	12/30/2008	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954C1	US	12/336,771	12/17/2008	7777981	8/17/2010	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954TW	TW	96106324	2/16/2007	1396193	5/11/2013	In Force	System And Method For Improved Insitu Spiral Writing
MP0954	MP0954WO	WO	PCT/US2007/00	2/14/2007			Pending	Method For Improved Insitu Spiral Writing
MP0954	MP0954W0CN	CN	2007800060254	2/14/2007	212007800060	3/14/2012	In Force	METHOD FOR IMPROVED INSITU SPIRAL WRITING
MP0954	MP0954W0DE	DE	07750571.7	2/14/2007	1987514	12/23/2009	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954W0EP	EP	EP07750571.7	2/14/2007	EP1987514	12/23/2009	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954W0EPDE	DE	07750571.7	2/14/2007	602007003957	12/23/2009	In Force	Method For Improved Insitu Spiral Writing

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0954	MP0954WOEPFR	FR	07750571.7	2/14/2007	1987514	12/23/2009	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954WOEPGB	GB	07750571.7	2/14/2007	1987514	12/23/2009	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954WOCFR	FR	07750571.7	2/14/2007	1987514	12/23/2009	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954WOCGB	GB	07750571.7	2/14/2007	1987514	12/23/2009	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954WOCIN	IN	1921MUMNP20	2/14/2007	270743	1/14/2016	In Force	Self-Servo-Write (SSW) System And Method For A Hard Disk Drive
MP0954	MP0954WOCJP	JP	2008-555304	2/14/2007	4866919	11/18/2011	In Force	METHOD FOR IMPROVED INSITU SPIRAL WRITING
MP0954	MP0954WOCKR	KR	1020087023141	2/14/2007	101321559	10/17/2013	In Force	Method For Improved Insitu Spiral Writing
MP0957	MP0957	US	11/520,531	9/12/2006	7974366	7/5/2011	In Force	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND METHODS
MP0957	MP0957CN	CN	2007101606164	2/25/2007	212007101006	2/11/2015	In Force	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEM AND METHOD
MP0957	MP0957EP	EP	EP07003582.9	2/21/2007	EP1833052	4/10/2013	In Force	Low-Latency Baseline-Wander Compensation Systems and Methods
MP0957	MP0957EPDE	DE	07003582.9	2/21/2007	6,02007E+11	4/10/2013	In Force	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND METHODS
MP0957	MP0957EPRFR	FR	07003582.9	2/21/2007	1833052	4/10/2013	In Force	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND METHODS
MP0957	MP0957EPGB	GB	07003582.9	2/21/2007	1833052	4/10/2013	In Force	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND METHODS
MP0957	MP0957JP	JP	2007-037771	2/19/2007	5010938	6/8/2012	In Force	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND METHODS
MP0957	MP0957SG	SG	200701060-6	2/16/2007			Pending	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND METHODS
MP0957	MP0957TW	TW	96166322	2/16/2007	1441488	6/11/2014	In Force	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND METHODS
MP0958	MP0958	US	11/527,940	9/26/2006	7538616	5/26/2009	In Force	A WIDEBAND LOW DISTORTION/LOW POWER DRIVER
MP0959	MP0959D1	US	13/303,926	11/23/2011	9407875	8/2/2016	In Force	Secure Video Distribution
MP0959	MP0959CADE	DE	12003430.1	10/25/2007	2485454	8/15/2018	In Force	Secure Video Distribution
MP0959	MP0959QAFR	FR	12003430.1	10/25/2007	2485454	8/15/2018	In Force	Secure Video Distribution
MP0959	MP0959QAGB	GB	12003430.1	10/25/2007	2485454	8/15/2018	In Force	Secure Video Distribution
MP0959	MP0959WOC	WO	PCT/US2007/02	10/25/2007			Pending	Secure Video Distribution
MP0959	MP0959WOCJP	JP	07867276.3	10/25/2007	EP2074792	12/11/2013	In Force	Secure Video Distribution
MP0959	MP0959WOCJP	JP	2009-534643	10/25/2007	5245181	4/19/2013	In Force	SECURE VIDEO DISTRIBUTION
MP0959	MP0959WUD1	EP	12003430.1	10/25/2007	2485454	8/15/2018	In Force	Secure Video Distribution
MP0959	MP0959WUD2	EP	12003431.9	10/25/2007			Pending	Secure Video Distribution
MP0959	MP0959WUDE	DE	07867276.3	10/25/2007	602007034256	12/11/2013	In Force	SECURE VIDEO DISTRIBUTION
MP0959	MP0959WUFR	FR	07867276.3	10/25/2007	2074792	12/11/2013	In Force	SECURE VIDEO DISTRIBUTION
MP0959	MP0959WUGB	GB	07867276.3	10/25/2007	2074792	12/11/2013	In Force	SECURE VIDEO DISTRIBUTION
MP0960	MP0960CN	CN	200780006573.1	2/23/2007	212007800065	7/16/2014	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP0960	MP0960EP	EP	7789526.6	2/23/2007			Pending	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP0960	MP0960JP	JP	2008-555902	2/23/2007	4929293	2/17/2012	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP0960	MP0960JPD1	JP	2011-148140	2/23/2007	4929410	2/17/2012	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP0960	MP0960KR	KR	10-2008-702282	2/23/2007	10-1021633	3/4/2011	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A

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MP0960	MP0960TW	TW	96105647	2/15/2007	1437560	5/11/2014	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP0960	MP0960WO	WO	PCT/IB2007/002	2/23/2007			Pending	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP0961	MP0961	US	11/521,731	9/14/2006	8049249	11/1/2011	In Force	INTEGRATED CIRCUIT DEVICES WITH ESD PROTECTION IN SCRIBE LIN
MP0961	MP0961D1	US	13/285,928	10/31/2011	8372729	2/12/2013	In Force	INTEGRATED CIRCUIT DEVICES WITH ESD PROTECTION IN SCRIBE LIN
MP0961	MP0961D1C1	US	13/762,208	2/7/2013	8753960	6/17/2014	In Force	INTEGRATED CIRCUIT DEVICES WITH ELECTROSTATIC DISCHARGE (ES
MP0962	MP0962	US	11/541,994	10/2/2006	7400464	7/15/2008	In Force	Synchronous RRD Write
MP0962	MP0962C1	US	12/218,138	7/11/2008	7602568	10/13/2009	In Force	Synchronous RRD Write
MP0963	MP0963	US	11/504,524	8/15/2006	8457007	6/4/2013	In Force	Global Switch Resource Manager
MP0963	MP0963C1	US	13/905,776	5/30/2013	8787197	7/22/2014	In Force	Global Switch Resource Manager
MP0963	MP0963CN	CN	200710092305J	2/25/2007	212007100923	3/13/2013	In Force	GLOBAL SWITCH RESOURCE MANAGER
MP0963	MP0963DE	DE	07003496.2	2/20/2007	6.02007E+12	6/22/2011	In Force	Global Switch Resource Manager
MP0963	MP0963EP	EP	07003496.2	2/20/2007	EP1826962	6/22/2011	In Force	Global Switch Resource Manager
MP0963	MP0963FR	FR	07003496.2	2/20/2007	1826962	6/22/2011	In Force	Global Switch Resource Manager
MP0963	MP0963GB	GB	07003496.2	2/20/2007	1826962	6/22/2011	In Force	Global Switch Resource Manager
MP0963	MP0963HK	HK	81011007	2/25/2007			Pending	Global Switch Resource Manager
MP0963	MP0963SG	SG	2007011885	2/22/2007			Pending	Global Switch Resource Manager
MP0963	MP0963SG1	SG	2009057266	2/22/2007	155253	3/28/2013	In Force	Global Switch Resource Manager
MP0963	MP0963TW	TW	96106398	2/26/2007	1416968	11/21/2013	In Force	Global Switch Resource Manager And Management Method Thereof
MP0964	MP0964	US	11/526,485	9/25/2006	7425910	9/16/2008	In Force	Transmitter Digital-To-Analog Converter With Noise Shaping
MP0964	MP0964C1	US	11/636,283	12/8/2006	7471225	12/30/2008	In Force	Transmitter Digital-To-Analog Converter With Noise Shaping
MP0964	MP0964C2	US	11/636,069	12/8/2006	7436337	10/14/2008	In Force	Transmitter Digital-To-Analog Converter With Noise Shaping
MP0965	MP0965	US	09/675,194	9/29/2000	7268900	9/11/2007	In Force	PRINTER FORMATTER IN A CABLE
MP0965	MP0965C1	US	11/838,830	8/14/2007	7821664	10/26/2010	In Force	PRINTER FORMATTER IN A CABLE
MP0965	MP0965D1C1	US	12/715,022	3/1/2010	8339647	12/25/2012	In Force	PRINTER FORMATTER IN A CABLE
MP0966	MP0966	US	09/675,920	9/29/2000	7301655	11/27/2007	In Force	PRINTER FORMATTER IN A REMOVABLE CARD
MP0966	MP0966C1	US	11/846,495	8/28/2007	7944576	5/17/2011	In Force	PRINTER FORMATTER IN A REMOVABLE CARD
MP0966	MP0966C1C1	US	13/088,617	4/18/2011	9465567	10/11/2016	In Force	PRINTER FORMATTER IN A REMOVABLE CARD
MP0966	MP0966D1	US	11/846,492	8/28/2007	7907293	3/15/2011	In Force	PRINTER FORMATTER IN A REMOVABLE CARD
MP0967	MP0967	US	10/021,957	12/14/2001	6595633	7/22/2003	In Force	PRINTER PEN CARRIAGE SUPPORT
MP0967	MP0967DE	DE	02019017.9	8/26/2002	DE60206722.7	10/19/2005	In Force	PRINTER PEN CARRIAGE SUPPORT
MP0967	MP0967EP	EP	EP02019017.9	8/26/2002	EP1319515	10/19/2005	In Force	Printer pen carriage support
MP0967	MP0967FR	FR	02019017.9	8/26/2002	FR1319515	10/19/2005	In Force	PRINTER PEN CARRIAGE SUPPORT
MP0967	MP0967GB	GB	02019017.9	8/26/2002	GB1319515	10/19/2005	In Force	PRINTER PEN CARRIAGE SUPPORT
MP0967	MP0967JP	JP	2002-347163	11/29/2002	4365577	8/28/2009	In Force	PRINTER PEN CARRIAGE SUPPORT

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MP0968	MP0968	US	09/715,628	11/17/2000	6561618	5/13/2003	In Force	SERVICE STATION FOR PRINTERS HAVING FIRING NOZZLES PERPENDI
MP0968	MP0968DE	DE	01120813.9	8/29/2001	DE60122093.5	8/9/2006	In Force	A SERVICE STATION FOR PRINTERS HAVING FIRING NOZZLES PERPEN
MP0968	MP0968EP	EP	EP01120813.9	8/29/2001	EP1208393	8/9/2006	In Force	A service station for printers having firing nozzles perpendicular to dir
MP0968	MP0968FR	FR	01120813.9	8/29/2001	FR1208393	8/9/2006	In Force	A SERVICE STATION FOR PRINTERS HAVING FIRING NOZZLES PERPEN
MP0968	MP0968GB	GB	01120813.9	8/29/2001	GB1208993	8/9/2006	In Force	A SERVICE STATION FOR PRINTERS HAVING FIRING NOZZLES PERPEN
MP0968	MP0968JP	JP	2001-353776	11/19/2001	4236079	12/26/2008	In Force	SERVICE STATION FOR PRINTERS HAVING FIRING NOZZLES PERPENDI
MP0969	MP0969	US	09/832,583	4/11/2001	6533387	3/18/2003	In Force	INKJET PRINTING SYSTEM USING SINGLE MOTOR FOR PRINT MEDIA
MP0969	MP0969DE	DE	02002179.6	1/29/2002	DE60221599.4	8/8/2007	In Force	INKJET PRINTING SYSTEM USING SINGLE MOTOR FOR PRINT MEDIA
MP0969	MP0969EP	EP	EP02002179.6	1/29/2002	EP1249352	8/8/2007	In Force	inkjet printing system using single motor for print media advance ar
MP0969	MP0969FR	FR	02002179.6	1/29/2002	1249352	8/8/2007	In Force	INKJET PRINTING SYSTEM USING SINGLE MOTOR FOR PRINT MEDIA
MP0969	MP0969GB	GB	02002179.6	1/29/2002	GB1249352	8/8/2007	In Force	INKJET PRINTING SYSTEM USING SINGLE MOTOR FOR PRINT MEDIA
MP0969	MP0969JP	JP	2002-108859	4/11/2002	4363510	8/28/2009	In Force	INKJET PRINTING SYSTEM
MP0970	MP0970DE	DE	DE20125100	6/4/2002	DE60221473.4	8/1/2007	In Force	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A THREADMILL
MP0970	MP0970EP	EP	EP02012510	6/4/2002	EP1291182	8/1/2007	In Force	Pen Wiping Method and System That Employs a Treadmill Belt
MP0970	MP0970EPD1	EP	EP7013413.5	6/4/2002			Pending	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A THREADMILL
MP0970	MP0970EPD2	EP	EP7013412.7	6/4/2002			Pending	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A THREADMILL
MP0970	MP0970FR	FR	FR02012510	6/4/2002	FR1291182	8/1/2007	In Force	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A THREADMILL
MP0970	MP0970GB	GB	GB02012510	6/4/2002	GB1291182	8/1/2007	In Force	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A THREADMILL
MP0970	MP0970JP	JP	2002-259143	9/4/2002			Pending	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A TREADMILL BEL
MP0970	MP0970JPD1	JP	2010-053204	3/10/2010	4939620	3/2/2012	In Force	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A TREADMILL BE
MP0971	MP0971	US	09/832,607	4/11/2001	6550992	4/22/2003	In Force	CORED ROLLER FOR IMAGE FORMING DEVICE
MP0971	MP0971EP	EP	EP2001703.40	1/24/2002			Pending	CORED ROLLER FOR IMAGE FORMING DEVICE
MP0971	MP0971JP	JP	2002-108860	4/11/2002	4139987	6/20/2008	In Force	CORED ROLLER FOR IMAGE FORMING DEVICE
MP0972	MP0972DE	DE	02019118.5	8/29/2002	DE60235275.4	2/3/2010	In Force	IMPROVED POWER CONNECT FOR A PRINT CARRIAGE
MP0972	MP0972EP	EP	EP02019118.5	8/29/2002	EP1308305	2/3/2010	In Force	Improved power connect for a print carriage
MP0972	MP0972FR	FR	FR02019118.5	8/29/2002	FR1308305	2/3/2010	In Force	IMPROVED POWER CONNECT FOR A PRINT CARRIAGE
MP0972	MP0972GB	GB	GB02019118.5	8/29/2002	GB1308305	2/3/2010	In Force	IMPROVED POWER CONNECT FOR A PRINT CARRIAGE
MP0972	MP0972JP	JP	2002-321052	11/5/2002	4386406	10/9/2009	In Force	IMPROVED POWER CONNECT FOR A PRINT CARRIAGE
MP0975	MP0975	US	10/365,563	2/12/2003	7280246	10/9/2007	In Force	LASER PRINT APPARATUS THAT GENERATES PULSE WIDTH VALUE AN
MP0975	MP0975C1C1	US	12/564,646	9/22/2009	8045212	10/25/2011	In Force	LASER PRINT APPARATUS THAT GENERATES PULSE WITH VALUE AND
MP0975	MP0975DE	DE	DE10350863.5	10/31/2003	DE10350863	6/18/2009	In Force	A laser printing apparatus that generates a pulse width value and an
MP0975	MP0975GB	GB	GB402801.5	2/9/2004	GB2398416	2/15/2006	In Force	LASER PRINT APPARATUS THAT GENERATES PULSE WIDTH VALUE AN
MP0976	MP0976	US	10/650,160	8/27/2003	7483167	1/27/2009	In Force	IMAGE FORMING APPARATUS FOR IDENTIFYING UNDESIRABLE TONE
MP0976	MP0976C1C1	US	13/102,305	5/6/2011	8194282	6/5/2012	In Force	IMAGE FORMING APPARATUS FOR IDENTIFYING UNDESIRABLE TONE

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MP0977	MP0977	US	10/625,971	7/23/2003	7245779	7/17/2007	In Force	IMAGE ENHANCEMENT EMPLOYING PARTIAL TEMPLATE MATCHING
MP0977	MP0977C1	US	11/827,602	7/11/2007	7412108	8/12/2008	In Force	IMAGE ENHANCEMENT EMPLOYING PARTIAL TEMPLATE MATCHING
MP0977	MP0977JP	JP	2004-211599	7/20/2004	4436202	1/8/2010	In Force	IMAGE ENHANCEMENT EMPLOYING PARTIAL TEMPLATE MATCHING
MP0978	MP0978	US	10/655,418	9/3/2003	7535679	4/28/2009	In Force	EFFICIENT PRINTER CONTROL ELECTRONICS
MP0978	MP0978C1	US	12/419,815	4/7/2009	8059298	11/15/2011	In Force	EFFICIENT PRINTER CONTROL ELECTRONICS
MP0978	MP0978C1C1	US	13/188,235	7/21/2011	8174723	5/8/2012	In Force	EFFICIENT PRINTER CONTROL ELECTRONICS
MP0979	MP0979C1	US	11/401,525	4/10/2006	7284828	10/23/2007	In Force	METHOD AND APPARATUS FOR COLOR FORMATTING IN A COLOR PRINT
MP0980	MP0980	US	10/689,321	10/20/2003	7359082	4/15/2008	In Force	INDEPENDENT VIDEO HARDWARE BLOCKS TO SUPPORT LASER PRINT
MP0980	MP0980DE	DE	DE10206403173	6/30/2004	DE102064031	2/9/2017	In Force	INDEPENDENT VIDEO HARDWARE BLOCKS TO SUPPORT LASER PRINT
MP0980	MP0980JP	JP	2004-302437	10/18/2004	4883746	12/16/2011	In Force	INDEPENDENT VIDEO HARDWARE BLOCKS TO SUPPORT LASER PRINT
MP0980	MP0980JPD1	JP	2011-143352	6/28/2011	5639012	10/31/2014	In Force	INDEPENDENT VIDEO HARDWARE BLOCKS TO SUPPORT LASER PRINT
MP0980	MP0980JPD2	JP	2014-048557	3/12/2014	5878948	2/5/2016	In Force	INDEPENDENT VIDEO HARDWARE BLOCKS TO SUPPORT LASER PRINT
MP0981	MP0981	US	10/840,696	5/5/2004	7532345	5/12/2009	In Force	HOST-BASED PRINTED DRIVER
MP0981	MP0981C1	US	12/431,347	4/9/2009	8027053	9/27/2011	In Force	HOST-BASED PRINTER DRIVER
MP0983	MP0983DE	DE	DE10200500435	1/31/2005	DE102005004	9/10/2015	In Force	TWO-DIMENSIONAL CMOS SENSOR ARRAY TO IMAGE DOCUMENTS
MP0983	MP0983JPD1	JP	2009-191506	8/21/2009	5322297	7/26/2013	In Force	TWO-DIMENSIONAL CMOS SENSOR ARRAY TO IMAGE DOCUMENTS
MP0983	MP0983JPD2	JP	2013-251220	12/4/2013	5959014	7/1/2016	In Force	TWO-DIMENSIONAL CMOS SENSOR ARRAY TO IMAGE DOCUMENTS
MP0984	MP0984C1C1	US	13/097,220	4/29/2011	8155678	2/28/2012	In Force	HANDHELD PRINTER
MP0984	MP0984GB	GB	GB0610837.7	6/1/2006	GB2428025	1/7/2009	In Force	HANDHELD PRINTER
MP0984	MP0984JP	JP	2006-154187	6/2/2006	4958095	3/30/2012	In Force	HANDHELD PRINTER AND PRINTING METHOD OF USING THE SAME
MP0985	MP0985	US	10/826,738	4/16/2004	8406645	3/19/2013	In Force	PRINTER WITH SELECTABLE CAPABILITIES
MP0985	MP0985C1C1	US	14/326,331	7/8/2014	9753679	9/5/2017	In Force	PRINTER WITH SELECTABLE CAPABILITIES
MP0986	MP0986C1	US	14/200,859	3/7/2014	9237250	1/12/2016	In Force	IMAGE PROCESSING APPARATUS WITH SYMMETRIC PROCESSORS
MP0988	MP0988	US	10/897,945	7/23/2004	7302048	11/27/2007	In Force	PRINTER WITH SPEECH TRANSCRIPTION OF A RECORDED VOICE MESSAGE
MP0989	MP0989	US	10/940,863	9/13/2004	7298391	11/20/2007	In Force	LASER PRINTER FOR BRAILLE
MP0990	MP0990C1	US	11/856,478	9/17/2007	8085287	12/27/2011	In Force	LASER PRINTER WITH REDUCED BANDING ARTIFACTS
MP0990	MP0990GB	GB	GB0525467.7	12/14/2005	GB2421340	9/22/2010	In Force	LASER PRINTER WITH REDUCED BANDING ARTIFACTS
MP0990	MP0990JP	JP	2005-361270	12/15/2005	4326522	6/19/2009	In Force	LASER PRINTER WITH REDUCED BANDING ARTIFACTS
MP0991	MP0991GB	GB	GB0516071.8	8/4/2005	GB2416841	4/29/2009	In Force	METHOD AND APPARATUS FOR DETERMINING THE MECHANICAL SIN
MP0991	MP0991JP	JP	2005-226368	8/4/2005	4678677	2/10/2011	In Force	METHOD AND APPARATUS FOR DETERMINING THE MECHANICAL SIN
MP0992	MP0992	US	11/118,591	4/28/2005	7333121	2/19/2008	In Force	SYSTEM AND METHOD FOR SCALING DATA TO COMPENSATE FOR SL
MP0992	MP0992C1	US	12/018,559	1/23/2008	7701477	4/20/2010	In Force	SYSTEM AND METHOD FOR SCALING DATA TO COMPENSATE FOR SL
MP0992	MP0992GB	GB	GB0606218.6	3/28/2006	GB2425672	6/22/2011	In Force	SYSTEM AND METHOD FOR SCALING DATA TO COMPENSATE FOR SL
MP0992	MP0992JP	JP	2006-123357	4/27/2006	4384643	10/2/2009	In Force	SYSTEM AND METHOD FOR SCALING DATA TO COMPENSATE FOR SL

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MP0995	MP0995	US	11/138,128	5/26/2005	7447361	11/4/2008	In Force	SYSTEM AND METHOD FOR GENERATING A CUSTOM FONT
MP0997	MP0997	US	11/087,284	3/22/2005	7274883	9/25/2007	In Force	HYBRID PRINTER AND RELATED SYSTEM AND METHOD
MP0997	MP0997C1	US	11/903,651	9/24/2007	7576890	8/4/2009	In Force	HYBRID PRINTER AND RELATED SYSTEM AND METHOD
MP0999	MP0999GB	GB	GB0523740.9	11/22/2005	GB2420316	7/30/2008	In Force	SYSTEM AND METHOD FOR CREATING DOCUMENT FINISHES
MP0999	MP0999JP	JP	2005-336991	11/22/2005			Pending	SYSTEM AND METHOD FOR CREATING DOCUMENT FINISHES
MP1000	MP1000	US	10/990,022	11/16/2004	8050709	11/1/2011	In Force	WIRELESS PRINTER CONFIGURATION MODULE
MP1000	MP1000C1	US	13/285,810	10/31/2011	8249653	8/21/2012	In Force	WIRELESS PRINTER CONFIGURATION MODULE
MP1000	MP1000CN	CN	200510114985.1	11/16/2005	ZL2005101149	6/8/2011	In Force	WIRELESS PRINTER CONFIGURATION MODULE
MP1000	MP1000GB	GB	GB0521654.4	10/24/2005	GB2420473	5/6/2009	In Force	WIRELESS PRINTER CONFIGURATION MODULE
MP1000	MP1000JP	JP	2005-331518	11/16/2005	5131722	11/16/2012	In Force	WIRELESS PRINTER CONFIGURATION MODULE
MP1000	MP1000JP2	JP	2011-182705	11/16/2005	5175376	1/11/2013	In Force	WIRELESS PRINTER CONFIGURATION MODULE
MP1002	MP1002C1	US	12/878,806	9/9/2010	8244172	8/14/2012	In Force	REMOTE HOST-BASED MEDIA PRESENTATION
MP10026	MP10026	US	15/460,676	3/16/2017			Pending	Apparatuses and Methods for Measuring Position and Velocity
MP1004	MP1004C1	US	12/781,565	5/17/2010	8098405	1/17/2012	In Force	LASER PRINT APPARATUS WITH DUAL HALFTONES
MP1004	MP1004C1C1	US	13/350,448	1/13/2012	8405877	3/26/2013	In Force	LASER PRINT APPARATUS WITH DUAL HALF TONES
MP1004	MP1004GB	GB	GB0521853.2	10/26/2005	GB2419766	12/29/2010	In Force	LASER PRINT APPARATUS WITH DUAL HALFTONES
MP1004	MP1004JP	JP	2005-298437	10/13/2005	4605782	10/15/2010	In Force	LASER PRINT APPARATUS WITH DUAL HALFTONES
MP10049	MP10049	US	15/611,332	6/1/2017	10037290	7/31/2018	In Force	Dual-Port Memories And Input/Output Circuits For Preventing Failure
MP1005	MP1005CN	CN	200510093895.1	8/31/2005	ZL2005100938	9/16/2009	In Force	LASER PRINT APPARATUS WITH TONER EXPLOSION COMPENSATION
MP1005	MP1005GB	GB	GB0522057.9	10/28/2005	GB2419767	5/26/2010	In Force	LASER PRINT APPARATUS WITH TONER EXPLOSION COMPENSATION
MP1005	MP1005JP	JP	2005-316279	10/31/2005			Pending	LASER PRINT APPARATUS WITH TONER EXPLOSION COMPENSATION
MP10052	MP10052	US	15/791,517	10/24/2017	10412545	9/10/2019	In Force	Device-to-Device Radio Frequency Proximity Determination
MP10052	MP10052C1	US	16/530,317	8/2/2019			Pending	Device-to-Device Radio Frequency Proximity Determination
MP10056	MP10056	US	15/672,117	8/8/2017	10389414	8/20/2019	In Force	Methods and Apparatus for Matrix Compensation to Prevent Rank D
MP1006	MP1006	US	10/976,937	10/29/2004	7538905	5/26/2009	In Force	RESOLUTION DOUBLING FOR PRINTER APPARATUS
MP1006	MP1006C1	US	12/442,001	4/10/2009	8054493	11/8/2011	In Force	RESOLUTION DOUBLING FOR PRINTER APPARATUS
MP10062	MP10062	US	15/611,628	6/1/2017	10037400	7/31/2018	In Force	INTEGRATED CIRCUIT MANUFACTURING PROCESS FOR ALIGNING TH
MP10062	MP10062CN	CN	201710409483.1	6/2/2017			Pending	INTEGRATED CIRCUIT MANUFACTURING PROCESS FOR ALIGNING TH
MP10062	MP10062JP	JP	2017-110491	6/2/2017			Pending	INTEGRATED CIRCUIT MANUFACTURING PROCESS FOR ALIGNING TH
MP10062	MP10062KR	KR	10-2017-006895	6/2/2017			Pending	INTEGRATED CIRCUIT MANUFACTURING PROCESS FOR ALIGNING TH
MP10062	MP10062TW	TW	106118347	6/2/2017			Pending	INTEGRATED CIRCUIT MANUFACTURING PROCESS FOR ALIGNING TH
MP1007	MP1007CN	CN	CN20051011275.1	11/2/2005			Pending	MULTIPLE-FUNCTION INKJET PRINTING SYSTEM WITH SINGLE MOTO
MP1007	MP1007GB	GB	GB513323.60	6/29/2005			Pending	MULTIPLE-FUNCTION INKJET PRINTING SYSTEM WITH SINGLE MOTO
MP10081	MP10081	US	15/812,797	11/14/2017	10187082	1/22/2019	In Force	SYSTEMS AND METHODS FOR GRAY CODING BASED ERROR CORRECT

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MP10082	MP10082	US	15/812,780	11/14/2017	10218341	2/26/2019	In Force	SYSTEMS AND METHODS FOR PHASE SYNCHRONIZATION OF LOCAL
MP10082	MP10082C1	US	16/284,773	2/25/2019			Pending	SYSTEMS AND METHODS FOR PHASE SYNCHRONIZATION OF LOCAL
MP10082	MP10082WO	WO	PCT/US17/6160	11/14/2017			Pending	SYSTEMS AND METHODS FOR PHASE SYNCHRONIZATION OF LOCAL
MP10082	MP10082WO DE	DE	212017000244.1	11/14/2017	212007000244	6/18/2019	In Force	SYSTEMS AND METHODS FOR PHASE SYNCHRONIZATION OF LOCAL
MP10085	MP10085	US	15/688,606	8/28/2017	10469332	11/5/2019	In Force	METHOD AND APPARATUS OF REMOTE CONFIGURATION AND MANA
MP10085	MP10085CN	CN	201710752755.4	8/28/2017			Pending	METHOD AND APPARATUS OF REMOTE CONFIGURATION AND MANA
MP10087	MP10087	US	15/692,979	8/31/2017	10244536	3/26/2019	In Force	METHOD AND APPARATUS FOR DETERMINING WHETHER A SECOND
MP1009	MP1009C1	US	11/700,518	1/31/2007	7336049	2/26/2008	In Force	METHOD AND APPARATUS FOR ADAPTIVE MOTOR SPEED CONTROL
MP10091	MP10091	US	15/691,398	8/30/2017	10257328	4/9/2019	In Force	METHOD AND APPARATUS FOR CONTROL FRAME EXTENSION
MP10092	MP10092	US	15/622,713	6/14/2017	10008228	6/26/2018	In Force	METHOD AND APPARATUS FOR DETERMINING READ-HEAD DEVIATIO
MP10094	MP10094	US	15/697,230	9/6/2017	10431515	10/1/2019	In Force	Methods and Apparatus for Self-Alignment of Integrated Circuit Dies
MP10094	MP10094CN	CN	201710725792.1	8/22/2017			Pending	METHODS AND APPARATUS FOR SELF-ALIGNMENT OF INTEGRATED C
MP10094	MP10094TW	TW	106130044	9/1/2017			Pending	Methods and Apparatus for Self-Alignment of Integrated Circuit Dies
MP10096	MP10096	US	15/688,913	8/29/2017	10459783	10/29/2019	In force	Low-latency Decoder for Reed Solomon Codes
MP10096	MP10096CN	CN	201710765440.1	8/30/2017			Pending	LOW-LATENCY DECODER FOR REED SOLOMON CODES
MP10098	MP10098	US	15/691,268	8/30/2017			Pending	METHOD AND APPARATUS FOR RANGING
MP10099	MP10099	US	15/695,928	9/5/2017	10142960	11/27/2018	In Force	MULTI-USER RANGING WITH UNASSOCIATED STATIONS
MP10099	MP10099WO	WO	PCT/US2017/05	9/5/2017			Pending	MULTI-USER RANGING WITH UNASSOCIATED STATIONS
MP10099	MP10099WO CN	CN	201780067467.1	9/5/2017			Pending	MULTI-USER RANGING WITH UNASSOCIATED STATIONS
MP10099	MP10099WO DE	DE	112017004472.1	9/5/2017			Pending	MULTI-USER RANGING WITH UNASSOCIATED STATIONS
MP10101	MP10101CN	CN	201710805209.1	9/8/2017			Pending	MULTI-CPU DEVICE WITH TRACKING OF CACHE-LINE OWNER CPU
MP10102	MP10102	US	15/652,747	7/18/2017	10128981	11/13/2018	In Force	MESSAGE-PASSING DECODER WITH FAST CONVERGENCE AND REDU
MP10102	MP10102.C1	US	16/161,328	10/16/2018			Pending	MESSAGE-PASSING DECODER WITH FAST CONVERGENCE AND REDU
MP10104	MP10104	US	15/664,621	7/31/2017	1017810	1/8/2019	In Force	SYSTEMS AND METHODS FOR ECHO OR INTERFERENCE CANCELLATI
MP10104	MP10104C1	US	16/240,037	1/4/2019			Pending	SYSTEMS AND METHODS FOR ECHO OR INTERFERENCE CANCELLATI
MP10104	MP10104CN	CN	201710780452.1	9/1/2017			Pending	SYSTEMS AND METHODS FOR ECHO OR INTERFERENCE CANCELLATI
MP10106	MP10106	US	15/689,882	8/29/2017	10277440	4/30/2019	In Force	Determining Common Phase Error
MP1011	MP1011	US	11/546,600	10/11/2006	7653221	11/18/2008	In Force	SYSTEM FOR CONTROLLING ROTATION OF A DISK DRIVE
MP10111	MP10111	US	15/690,839	8/30/2017			Pending	Methods and Apparatus for Bi-Directional Fine Timing Measurement
MP10113	MP10113	US	15/694,390	9/1/2017	10349213	7/9/2019	In Force	METHOD AND APPARATUS FOR DETERMINING AN ANGLE OF ARRIVA
MP10113	MP10113WO	WO	PCT/US2017/04	9/1/2017			Pending	METHOD AND APPARATUS FOR DETERMINING AN ANGLE OF ARRIVA
MP10113	MP10113WO CN	CN	201780063881.1	9/1/2017			Pending	METHOD AND APPARATUS FOR DETERMINING AN ANGLE OF ARRIVA
MP10113	MP10113WO DE	DE	112017004430.1	9/1/2017			Pending	METHOD AND APPARATUS FOR DETERMINING AN ANGLE OF ARRIVA
MP10118	MP10118	US	15/697,464	9/7/2017	10305541	5/28/2019	In Force	Reduction of Far-End Crosstalk in High-Speed Single-Ended Receivers

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MP10118	MP10118KR	KR	10-2017-01512	9/8/2017			Pending	REDUCTION OF FAR-END CROSSTALK IN HIGH-SPEED SINGLE-ENDED
MP1012	MP1012	US	11/614,868	12/21/2006	8219886	7/10/2012	In Force	HIGH DENSITY MULTI-LEVEL MEMORY
MP1012	MP1012C1	US	13/542,353	7/5/2012	8583991	11/12/2013	In Force	HIGH DENSITY MULTI-LEVEL MEMORY
MP10123	MP10123	US	15/844,274	12/15/2017	10439701	10/8/2019	In Force	Low Complexity Beamforming with Compressed Feedback
MP10123	MP10123C1	US	16/594,856	10/7/2019			Pending	Low Complexity Beamforming with Compressed Feedback
MP10123	MP10123WO	WO	PCT/US2017/06	12/15/2017			Pending	LOW COMPLEXITY BEAMFORMING WITH COMPRESSED FEEDBACK
MP10123	MP10123WOCN	CN	201780086676.1	12/15/2017			Pending	LOW COMPLEXITY BEAMFORMING WITH COMPRESSED FEEDBACK
MP10125	MP10125	US	15/783,314	10/13/2017	10841053	7/2/2019	In Force	SYSTEMS AND METHODS FOR A LOG-LIKELIHOOD RATIO BASED DYN
MP10125	MP10125WO	WO	PCT/IB2017/001	10/13/2017			Pending	LOW COMPLEXITY BEAMFORMING WITH COMPRESSED FEEDBACK
MP10125	MP10125WOCN	CN	201780073513.1	5/28/2019			Pending	SYSTEMS AND METHODS FOR A LOG-LIKELIHOOD RATIO BASED DYN
MP10125	MP10125WODE	DE	112017005181.1	10/13/2017			Pending	SYSTEMS AND METHODS FOR A LOG-LIKELIHOOD RATIO BASED DYN
MP10127	MP10127	US	15/664,708	7/31/2017	10126769	11/13/2018	In Force	CLASS-D DRIVEN LOW-DROP-OUTPUT (LDO) REGULATOR
MP10127	MP10127EP	EP	EPI7187096.3	8/21/2017			Pending	A Class-D Driven Low-Drop-Output (LDO) Regulator
MP10128	MP10128	US	15/769,186	9/19/2017	10366963	8/27/2019	In Force	FRAME FORMATS FOR MULTI-USER ACKNOWLEDGMENT INFORMATION
MP10129	MP10129	US	15/710,640	9/20/2017	10375695	8/6/2019	In Force	Spatial Reuse Transmissions in Wireless Local Area Networks (WLANS
MP10129	MP10129C1	US	16/532,016	8/5/2019			Pending	Spatial Reuse Transmissions in Wireless Local Area Networks (WLANS
MP10129	MP10129WO	WO	PCT/US2017/05	9/20/2017			Pending	SPATIAL REUSE TRANSMISSIONS IN WLANS
MP10129	MP10129WOCN	CN	201780057521.1	9/20/2017			Pending	SPATIAL REUSE TRANSMISSIONS IN WLANS
MP10129	MP10129WODE	DE	112017004721.1	9/20/2017			Pending	SPATIAL REUSE TRANSMISSIONS IN WLANS
MP1013	MP1013	US	11/455,168	6/16/2006	8028109	9/27/2011	In Force	Hard Disk Drive Integrated Circuit With Integrated Gigabit Ethernet in
MP1013	MP1013C1	US	13/245,269	9/26/2011	8161215	4/17/2012	In Force	Hard Disk Drive Integrated Circuit With Integrated Gigabit Ethernet in
MP1013	MP1013C1aa	US	13/681,885	11/20/2012	8606978	12/10/2013	In Force	Hard Disk Drive With Integrated Ethernet Interface
MP1013	MP1013C1C1	US	13/447,486	4/16/2012	8316167	11/20/2012	In Force	Hard Disk Drive Integrated Circuit With Integrated Gigabit Ethernet in
MP1013	MP1013C12a	US	14/101,687	12/10/2013	9063670	6/23/2015	In Force	Hard Disk Drive With Integrated Ethernet Network Interface
MP10131	MP10131	US	15/703,940	9/13/2017	9966094	5/8/2018	In Force	QUADRATIC CURRENT SLEW CONTROL CIRCUIT
MP10132	MP10132	US	15/702,371	9/12/2017	10367758	7/30/2019	In Force	MERGING READ REQUESTS IN NETWORK DEVICE ARCHITECTURE
MP10132	MP10132C1	US	16/525,113	7/29/2019			Pending	MERGING READ REQUESTS IN NETWORK DEVICE ARCHITECTURE
MP10132	MP10132WO	WO	PCT/IB2017/055	9/12/2017			Pending	MERGING READ REQUESTS IN NETWORK DEVICE ARCHITECTURE
MP10132	MP10132WOCN	CN	201780063973.1	9/12/2017			Pending	MERGING READ REQUESTS IN NETWORK DEVICE ARCHITECTURE
MP10134	MP10134	US	15/710,707	9/20/2017			Pending	METHODS AND SYSTEMS FOR ENABLING COMMUNICATIONS FROM
MP10135	MP10135	US	15/706,519	9/15/2017			Pending	GROUP ADDRESSING FOR BEAMFORMING TRAINING
MP10136	MP10136	US	15/483,429	4/10/2017			Pending	METHOD AND APPARATUS FOR FULL-DUPLEX OPERATION IN A MULT
MP10136	MP10136WO	WO	PCT/US17/4882	8/28/2017			Pending	METHOD AND APPARATUS FOR FULL-DUPLEX OPERATION IN A MULT
MP10137	MP10137	US	15/487,004	4/13/2017	10135649	11/20/2018	In Force	SYSTEMS AND METHODS FOR PERFORMING MULTI-LEVEL CODING I

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MP10137	MP10137WO	WO	PCT/JP2017/001	8/28/2017			Pending	SYSTEMS AND METHODS FOR PERFORMING MULTI-LEVEL CODING IN
MP10137	MP10137WOCN	CN	201780062777.1	8/28/2017			Pending	SYSTEMS AND METHODS FOR PERFORMING MULTI-LEVEL CODING IN
MP10138	MP10138	US	15/463,241	4/10/2017	10079672	9/18/2018	In Force	METHODS AND APPARATUS FOR PERFORMING FULL-DUPLEX COMM
MP10138	MP10138WO	WO	PCT/JP2017/001	8/28/2017			Pending	METHODS AND APPARATUS FOR PERFORMING FULL-DUPLEX COMM
MP10138	MP10138WOCN	CN	201780062059.1	8/28/2017			Pending	METHODS AND APPARATUS FOR PERFORMING FULL-DUPLEX COMM
MP10137	MP10137W	TW	96108291	3/9/2007	1405180	8/11/2013	In Force	HARD DISK DRIVE INTEGRATED CIRCUIT WITH INTEGRATED GIGABIT
MP1013W	MP1013WO	WO	PCT/US2007/00	3/8/2007			Pending	Hard Disk Drive Integrated Circuit With Integrated Gigabit Ethernet
MP1013W	MP1013WCEP	EP	77524650	3/8/2007			Pending	Hard Disk Drive Integrated Circuit With Integrated Gigabit Ethernet
MP1013W	MP1013WGP	JP	2008-558368	3/8/2007	4995844	5/18/2012	In Force	HARD DISK DRIVE INTEGRATED CIRCUIT WITH INTEGRATED GIGABIT
MP1013W	MP1013WOKR	KR	1020087024316	3/8/2007	101304588	8/30/2013	In Force	Hard Disk Drive Integrated Circuit With Integrated Gigabit Ethernet
MP1014	MP1014	US	11/701,338	2/1/2007	7423901	9/9/2008	In Force	Calibration System For Writing And Reading Multiple States Into Phas
MP1014	MP1014C1	US	12/283,041	9/9/2008	7679953	3/16/2010	In Force	Calibration System For Writing And Reading Multiple States Into Phas
MP10140	MP10140	US	15/706,712	9/17/2017	9998137	6/12/2018	In Force	Power-Efficient Successive-Approximation Analog-To-Digital Convert
MP10141	MP10141	US	15/708,898	9/13/2017	10330783	6/25/2019	In Force	Location Service Offload for Bluetooth Positioning
MP10142	MP10142	US	15/798,281	10/30/2017	10292012	5/14/2019	In Force	Reverse Locationing with Bluetooth Angle of Arrival (AoA)
MP10143	MP10143	US	15/710,274	9/20/2017	10459868	10/29/2019	In force	MODULAR CHIP EXPANSION BRIDGE AND CORRESPONDING METHO
MP10145	MP10145	US	15/704,724	9/14/2017	10068589	9/4/2018	In Force	SELF-SERVO-WRITE SYSTEMS FOR DETECTING INTERFERENCE CAUSE
MP10146	MP10146	US	15/708,507	9/19/2017			Pending	SYSTEMS AND METHODS FOR TRANSMITTING A WAKE-UP RADIO SIG
MP10146	MP10146WO	WO	PCT/US2017/05	9/19/2017			Pending	SYSTEMS AND METHODS FOR TRANSMITTING A WAKE-UP RADIO SIG
MP10146	MP10146WOCN	CN	201780057509.1	9/19/2017			Pending	SYSTEMS AND METHODS FOR TRANSMITTING A WAKE-UP RADIO SIG
MP10146	MP10146WODE	DE	112017004723.4	9/19/2017			Pending	SYSTEMS AND METHODS FOR TRANSMITTING A WAKE-UP RADIO SIG
MP10147	MP10147	US	15/708,668	9/19/2017	10170142	1/1/2019	In Force	System And Method For Compensating For Radial Incoherence Associ
MP1014T	MP1014TW	TW	96107192	3/2/2007	144180	6/11/2014	In Force	CALIBRATION SYSTEM FOR WRITING AND READING MULTIPLE STATE
MP1014W	MP1014WO	WO	PCT/US2007/00	3/2/2007			Pending	Calibration System For Writing And Reading Multiple States Into Phas
MP1015	MP1015	US	11/699,138	1/26/2007	7443627	10/28/2008	In Force	LOWEST POWER MODE FOR A MOBILE DRIVE
MP1015	MP1015C1C1	US	14/076,872	11/11/2013	9329660	5/3/2016	In Force	LOWEST POWER MODE FOR A MOBILE DRIVE
MP1015	MP1015CN	CN	200780008171.1	3/5/2007	212007800081	8/26/2015	In Force	LOWEST POWER MODE FOR A MOBILE DRIVE IN USB APPLICATION
MP10150	MP10150TW	TW	106137982	11/2/2017			Pending	TECHNIQUES AND STRUCTURES FOR SELECTABLY PASSING TRACES T
MP10152	MP10152	US	15/799,776	10/31/2017	10319727	6/11/2019	In Force	Fabricating Memory Devices with Optimized Gate Oxide Thickness
MP10152	MP10152CN	CN	201711047712.1	10/31/2017			Pending	FABRICATING MEMORY DEVICES WITH OPTIMIZED GATE OXIDE THIC
MP10152	MP10152D1	US	16/397,943	4/29/2019			Pending	Fabricating Memory Devices with Optimized Gate Oxide Thickness
MP10152	MP10152JP	JP	2017-210692	10/31/2017			Pending	FABRICATING MEMORY DEVICES WITH OPTIMIZED OXIDE GATE THIC
MP10152	MP10152KR	KR	10-2017-014251	10/30/2017			Pending	Fabricating Memory Devices with Optimized Gate Oxide Thickness
MP10152	MP10152TW	TW	106137222	10/27/2017			Pending	FABRICATING MEMORY DEVICES WITH OPTIMIZED GATE OXIDE THIC

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MP10153	MP10153	US	15/794,916	10/26/2017			Pending	SYSTEMS AND METHODS FOR EFFICIENT PASSWORD BASED PUBLIC K
MP10156	MP10156	US	15/865,871	1/9/2018			Pending	SELECTION OF TRANSMISSION MODES IN DENSE WIRELESS NETWORK
MP10156	MP10156WO	WO	PCT/US2018/01	1/9/2018			Pending	HE MULTIRATE SELECTION AND 2G4 40MHZ HE DEVICES
MP10156	MP10156WOCN	CN	201880016736.4	1/9/2018			Pending	SELECTION OF TRANSMISSION MODES IN DENSE WIRELESS NETWORK
MP10156	MP10156WOEP	EP	18714086.8	1/9/2018			Pending	SELECTION OF TRANSMISSION MODES IN DENSE WIRELESS NETWORK
MP10156	MP10156WOJP	JP	2019-537219	1/9/2018			Pending	HE MULTIRATE SELECTION AND 2G4 40MHZ HE DEVICES
MP10156	MP10156WOKR	KR	10-2019-702301	1/9/2018			Pending	SELECTION OF TRANSMISSION MODES IN DENSE WIRELESS NETWORK
MP10157	MP10157	US	15/835,200	12/7/2017	10318449	6/11/2019	In Force	SYSTEM AND METHOD FOR MEMORY ACCESS TOKEN REASSIGNMENT
MP10157	MP10157WO	WO	PCT/IB2017/001	12/7/2017			Pending	SYSTEM AND METHOD FOR MEMORY ACCESS TOKEN REASSIGNMENT
MP10157	MP10157WOCN	CN	201780084274.1	12/7/2017			Pending	SYSTEM AND METHOD FOR MEMORY ACCESS TOKEN REASSIGNMENT
MP10159	MP10159	US	15/799,627	10/31/2017			Pending	METHODS AND APPARATUS FOR FORWARDING PACKETS IN A MESH
MP1015J	MP1015JP	JP	2008-558350	3/5/2007	4616395	10/29/2010	In Force	LOWEST POWER MODE FOR A MOBILE DRIVE IN USB APPLICATION
MP1015K	MP1015KR	KR	KR7024555/200	3/5/2007	KR10-1379493	3/24/2014	In Force	LOWEST POWER MODE FOR A MOBILE DRIVE IN USB APPLICATION
MP1015K	MP1015KRD1	KR	KR10-2013-7025	3/5/2007	KR10-1502107	3/6/2015	In Force	LOWEST POWER MODE FOR MOBILE DRIVES IN USB APPLICATION
MP1015T	MP1015TW	TW	TW096107934	3/7/2007	TW413113	10/21/2013	In Force	LOWEST POWER MODE FOR A MOBILE DRIVE IN USB APPLICATION
MP1015W	MP1015WO	WO	PCT/US2007/00	3/5/2007			Pending	LOWEST POWER MODE FOR A MOBILE DRIVE IN USB APPLICATION
MP1016	MP1016	US	11/820,391	6/19/2007	8086935	12/27/2011	In Force	SOFT ERROR CORRECTION FOR A DATA STORAGE MECHANISM
MP10160	MP10160	US	15/866,216	1/9/2018			Pending	Port Extender with Local Switching
MP10160	MP10160C1	US	15/878,961	1/24/2018	10469382	11/5/2019	In force	Port Extender with Local Switching
MP10160	MP10160C1C1	US	16/673,424	11/4/2019			Pending	Port Extender with Local Switching
MP10160	MP10160WO	WO	PCT/US2018/01	1/9/2018			Pending	PORT EXTENDER WITH LOCAL SWITCHING
MP10165	MP10165	US	15/818,510	11/20/2017			Pending	Methods and Apparatus for Fabricating IC Chips with Tilted Pattern
MP10165	MP10165CN	CN	201711168476.4	11/21/2017			Pending	METHOD OF TILTED PATTERNING
MP10165	MP10165JP	JP	2017-223260	11/21/2017			Pending	Methods and Apparatus for Fabricating IC Chips with Tilted Pattern
MP10165	MP10165KR	KR	10-2017-015576	11/21/2017			Pending	METHOD OF TILTED PATTERNING
MP10165	MP10165TW	TW	106140536	11/21/2017			Pending	Methods and Apparatus for Fabricating IC Chips with Tilted Pattern
MP10167	MP10167	US	15/867,501	1/10/2018			Pending	SELECTION OF TRANSMISSION PARAMETERS FOR ACKNOWLEDGMENT
MP10168	MP10168	US	15/975,288	5/9/2018	10391437	5/14/2019	In Force	SYSTEMS AND METHODS FOR MEASURING IMPULSE RESPONSES IN A
MP1017	MP1017	US	11/544,253	10/9/2006	7349171	3/25/2008	In Force	Method For Measuring Actuator Velocity During Self-Serve-Write
MP1017	MP1017C1	US	12/077,582	3/20/2008	7567404	7/28/2009	In Force	Method For Measuring Actuator Velocity During Self-Serve-Write
MP10172	MP10172	US	15/867,067	1/10/2018			Pending	SIGNALING OF TRAINING FIELD LENGTH AND GUARD INTERVAL DURA
MP10172	MP10172WO	WO	PCT/US2018/01	1/10/2018			Pending	SIGNALING OF TRAINING FIELD LENGTH AND GUARD INTERVAL DURA
MP10172	MP10172WOCN	CN	201880013489.1	1/10/2018			Pending	SIGNALING OF TRAINING FIELD LENGTH AND GUARD INTERVAL DURA
MP10172	MP10172WOEP	EP	18701996.3	1/10/2018			Pending	SIGNALING OF TRAINING FIELD LENGTH AND GUARD INTERVAL DURA

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MP10175	MP10175	US	15/947,306	4/6/2018			Pending	Ranging Measurements in Wireless Communication Systems
MP10175	MP10175WO	WO	PCT/US2018/02	4/6/2018			Pending	RANGING MEASUREMENTS IN WIRELESS COMMUNICATION SYSTEM
MP10176	MP10176	US	15/849,213	12/20/2017	10397933	8/27/2019	In Force	Interference Cancellation for Receivers with Multiple Antennas
MP10176	MP10176C1	US	16/523,659	7/26/2019			Pending	Interference Cancellation for Receivers with Multiple Antennas
MP10177	MP10177	US	15/849,247	12/20/2017	10276198	4/30/2019	In Force	Method and Apparatus for Multi-Channel Servo Demodulation
MP10177	MP10177W	TW	96107634	3/6/2007	1402826	7/21/2013	In Force	System And Method For Measuring Actuator Velocity During Self-Set
MP1017W	MP1017WO	WO	PCT/US2007/00	3/6/2007			Pending	Method For Measuring Actuator Velocity
MP1017W	MP1017WOJP	JP	2008-558338	3/6/2007	4879283	12/9/2011	In Force	METHOD FOR MEASURING ACTUATOR VELOCITY
MP1017W	MP1017WOKR	KR	1020087024317	3/6/2007	101247359	3/19/2013	In Force	Method For Measuring Actuator Velocity
MP1018	MP1018	US	11/704,885	2/9/2007	8457576	6/4/2013	In Force	Cellular Phone With Integrated FM Radio And Remote Low Noise Am
MP1018	MP1018C1	US	11/725,397	3/19/2007	8768274	7/11/2014	In Force	Cellular Phone With Integrated FM Radio And Remote Low Noise Am
MP1018	MP1018C2	US	11/725,426	3/19/2007	8457577	6/4/2013	In Force	Cellular Phone With Integrated FM Radio And Remote Low Noise Am
MP10180	MP10180	US	16/376,709	4/5/2019			Pending	METHOD AND APPARATUS FOR CLOCK RECOVERY
MP10181	MP10181	US	16/376,382	4/5/2019			Pending	PHASE AND FREQUENCY CONTROL FOR CLOCK DATA RECOVERY
MP10183	MP10183	US	15/880,395	1/25/2018	10079033	9/18/2018	In Force	WRITE STRATEGY IMPROVEMENTS FOR READ CHANNEL AND PRE-A
MP10184	MP10184	US	15/955,449	4/17/2018			Pending	OPERATING MODE NOTIFICATION FOR WIRELESS COMMUNICATION
MP10184	MP10184WO	WO	PCT/US2018/02	4/17/2018			Pending	OPERATING MODE NOTIFICATION FOR WIRELESS COMMUNICATION
MP10186	MP10186	US	15/835,179	12/7/2017			Pending	System and Method for Memory Deallocation
MP10188	MP10188	US	15/947,451	4/6/2018			Pending	NULL DATA PACKET (NDP) RANGING MEASUREMENT FEEDBACK
MP10188	MP10188WO	WO	PCT/US2018/02	4/6/2018			Pending	NULL DATA PACKET (NDP) RANGING MEASUREMENT FEEDBACK
MP1018T	MP1018TW	TW	96110120	3/23/2007	1426768	2/11/2014	In Force	CELLULAR PHONE WITH INTEGRATED FM RADIO AND REMOTE LOW
MP1018W	MP1018WJP4	JP	2009-502875	3/22/2007	4944945	3/9/2012	In Force	CELLULAR PHONE, SYSTEM AND REMOTE DEVICE
MP1018W	MP1018WO	WO	PCT/US2007/00	3/22/2007			Pending	Cellular Phone With Integrated FM Radio
MP1018W	MP1018WOKR	KR	PI0709125-7	3/22/2007			Pending	CELLULAR PHONE WITH INTEGRATED WITH INTEGRATED FM RADIO
MP1018W	MP1018WOCN	CN	200780019120	3/22/2007			Pending	CELLULAR PHONE WITH INTEGRATED FM RADIO AND REMOTE LOW
MP1018W	MP1018WCEP	EP	77537629	3/22/2007	2085604	3/16/2016	In Force	Cellular Phone with Integrated FM Radio
MP1018W	MP1018WCEPDE	DE	60 2007 045 304	3/22/2007	2005604	3/16/2016	In Force	CELLULAR PHONE WITH INTEGRATED WITH INTEGRATED FM RADIO
MP1018W	MP1018WCEPFR	FR	07753762.9	3/22/2007	2005604	3/16/2016	In Force	CELLULAR PHONE WITH INTEGRATED WITH INTEGRATED FM RADIO
MP1018W	MP1018WCEPGB	GB	07753762.9	3/22/2007	2005604	3/16/2016	In Force	CELLULAR PHONE WITH INTEGRATED WITH INTEGRATED FM RADIO
MP1018W	MP1018WOKR	KR	1020087026027	3/22/2007	101424242	7/22/2014	In Force	Cellular Phone with Integrated FM Radio
MP1019	MP1019CN	CN	2007800165843	3/6/2007	ZL2007800165	8/8/2012	In Force	IMPROVED WRITE SPICE FOR OPTICAL RECORDING CHANNELS
MP10193	MP10193	US	16/264,020	1/31/2019			Pending	OSCILLATOR CALIBRATION STRUCTURE AND METHOD
MP10193	MP10193CN	CN	2019101151514	2/14/2019			Pending	OSCILLATOR CALIBRATION STRUCTURE AND METHOD FOR CLOCK-DATA
MP10198	MP10198	US	15/868,526	1/11/2018	10368303	7/30/2019	In Force	MULTI-CHANNEL RF SYSTEM WITH OFF-CHANNEL SCANNING

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MP10198	MP10198C1	US	16/441,283	6/14/2019			Pending	MULTI-CHANNEL RF SYSTEM WITH OFF-CHANNEL SCANNING
MP1019E	MP1019EP	EP	7752548.3	3/6/2007			Pending	IMPROVED WRITE SPURCE FOR OPTICAL RECORDING CHANNELS
MP1019J	MP1019JP	JP	2008-558380	3/6/2007	4951630	3/16/2012	In Force	IMPROVED WRITE SPURCE FOR OPTICAL RECORDING CHANNELS
MP1019K	MP1019KR	KR	10-2008-702455	10/7/2008			Pending	IMPROVED WRITE SPURCE FOR OPTICAL RECORDING CHANNELS
MP1019T	MP1019TW	TW	96107927	3/7/2007	1402828	7/21/2013	In Force	IMPROVED WRITE SPURCE FOR OPTICAL RECORDING CHANNELS
MP1019W	MP1019WO	WO	PCT/US2007/00	3/6/2007			Pending	IMPROVED WRITE SPURCE FOR OPTICAL RECORDING CHANNELS
MP1020	MP1020	US	11/640,088	12/15/2006	8160529	4/17/2012	In Force	Adaptive FM/AM Radio Architecture
MP10202	MP10202	US	16/159,334	10/12/2018			Pending	ON-CHIP INDUCTORS
MP10207	MP10207	US	15/927,280	3/21/2018			Pending	Apparatus for Super-fine Pitch Integrated Circuit Testing and Method
MP10212	MP10212	US	15/453,547	3/8/2017	9786319	10/10/2017	In Force	Codes and Techniques for Magnetic Recording
MP10214	MP10214	US	15/902,821	2/22/2018	10668609	9/4/2018	In Force	Variable Frequency Write Pattern Generation
MP10215	MP10215	US	15/932,398	2/16/2018	10367487	7/30/2019	In Force	Quadrature clock divider with 25%/75% duty cycle
MP10217	MP10217	US	15/913,475	3/6/2018	10348511	7/9/2019	In Force	METHODS AND APPARATUS FOR ESTABLISHING ENERGY-EFFICIENT E
MP10218	MP10218	US	15/885,357	2/13/2018			Pending	PHASE LOCK LOOP CIRCUITS AND METHODS INCLUDING MULTIPLEXE
MP10220	MP10220	US	15/908,329	2/28/2018	10128856	11/13/2018	In Force	DIGITAL LOCKING LOOP CIRCUIT AND METHOD OF OPERATION
MP10220	MP10220.C1	US	16/164,014	10/18/2018	10340925	7/2/2019	In Force	DIGITAL LOCKING LOOP CIRCUIT AND METHOD OF OPERATION
MP10227	MP10227	US	15/885,551	1/31/2018	10062407	8/28/2018	In Force	MUX SELECT CONTROL OF TWO PHASE SHIFTED DATA IN WRITE PRE
MP10228	MP10228	US	15/900,002	2/20/2018	10340927	7/2/2019	In Force	Digital Phase Locked Loop System
MP10228	MP10228C1	US	16/458,672	7/1/2019			Pending	Digital Phase Locked Loop System
MP1023	MP1023	US	11/589,532	10/30/2006	7962827	6/14/2011	In Force	SYSTEMS AND METHODS FOR ACHIEVING HIGHER CODING RATE USI
MP1023	MP1023C1	US	13/112,915	5/20/2011	8225148	7/17/2012	In Force	SYSTEMS AND METHODS FOR ACHIEVING HIGHER CODING RATE USI
MP1023	MP1023C1C1	US	13/536,522	6/28/2012	8484527	7/9/2013	In Force	SYSTEMS AND METHODS FOR ACHIEVING HIGHER CODING RATE USI
MP10231	MP10231	US	15/903,876	2/23/2018	10263582	4/16/2019	In Force	Variable Gain Amplifier with Gain-Based Compensation
MP10235	MP10235	US	15/900,470	2/20/2018			Pending	Method and Apparatus for Updating Error Detection Information in P
MP10235	MP10235CN	CN	2018101692667	2/28/2018			Pending	METHOD FOR INCREMENTAL CHECKSUM UPDATE OF ADDED VALUES
MP10236	MP10236	US	15/977,643	5/11/2018			Pending	NULL DATA PACKET (NDP) ANNOUNCEMENT FRAME AND TRIGGER F
MP1023T	MP1023TW	TW	96108050	3/8/2007			Pending	SYSTEMS AND METHODS FOR ACHIEVING HIGHER CODING RATE USI
MP1023W	MP1023WO	WO	PCT/US2007/00	3/5/2007			Pending	SYSTEMS AND METHODS FOR ACHIEVING HIGHER CODING RATE USI
MP1024	MP1024C1	US	12/916,052	10/29/2010	8144420	3/27/2012	In Force	VCM PWM CONTROL ARCHITECTURE FOR POWER SAVING IN HARD
MP10242	MP10242	US	15/466,181	3/22/2017			Pending	Wafer Level Chip Scale Package With Photodefinable 5-Sided Protecti
MP10244	MP10244	US	15/468,827	3/24/2017	10128171	11/13/2018	In Force	Leadframe With Improved Half-Etch Layout To Reduce Defects Cause
MP10246	MP10246	US	15/797,489	10/30/2017	10409507	9/10/2019	In Force	Methods and Apparatus for Implementing Virtual Dynamic Library Lo
MP10257	MP10257	US	15/996,681	6/4/2018			Pending	MULTI-TEST TYPE PROBE CARD AND CORRESPONDING TESTING SYST
MP1026	MP1026	US	11/592,357	11/2/2006	8014477	9/6/2011	In Force	RECEIVER EMPLOYING SELECTABLE A/D SAMPLE CLOCK FREQUENCY

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MP1026	MP1026C1	US	13/213,846	8/19/2011	8462887	6/11/2013	In Force	RECEIVER EMPLOYING SELECTABLE A/D SAMPLE CLOCK FREQUENCY
MP1026	MP1026C1C1	US	13/914,111	6/10/2013	8842787	9/23/2014	In Force	RECEIVER EMPLOYING SELECTABLE A/D SAMPLE CLOCK FREQUENCY
MP10260	MP10260	US	15/977,490	5/11/2018			Pending	LOW POWER WAKE UP RADIO SYNCHRONIZATION WITH LOW POWER
MP10262	MP10262	US	15/938,995	3/26/2018			Pending	FLEXIBLE PROCESSOR OF A PORT EXTENDER DEVICE
MP10262	MP10262.D1	US	15/969,116	5/12/2018			Pending	FLEXIBLE PROCESSOR OF A PORT EXTENDER DEVICE
MP10262	MP10262.WO	WO	PCT/182018/052	3/26/2018			Pending	PASSIVE INTELLIGENT PORT EXTENDER
MP10262	MP10262.WO.CN	CN	201880014848.1	3/28/2018			Pending	TRIPLE FLEXIBLE PROCESSOR OF A PORT EXTENDER DEVICE
MP10263	MP10263	US	15/927,889	3/21/2018			Pending	Memory Chip Design for Manufacturing
MP10264	MP10264	US	16/177,176	10/31/2018			Pending	APPARATUS AND METHODS FOR ENHANCING SIGNALING BANDWIDTH
MP10265	MP10265	US	15/979,100	5/14/2018	10264544	4/16/2019	In Force	STATION SELECTION FOR NULL DATA PACKET (NDP) RANGING
MP10266	MP10266	US	15/986,073	5/22/2018			Pending	SYSTEMS AND METHODS FOR MANAGING BASIC SERVICE SETS THROUGH
MP1027	MP1027C1	US	12/620,902	11/18/2009	7924526	4/12/2011	In Force	METHOD OF RAMP STOP DETECTION FOR VCM VELOCITY-CONTROL
MP10270	MP10270	US	15/985,480	5/21/2018			Pending	WAKEUP RADIO (WUR) PREAMBLE DESIGN
MP10270	MP10270.WO	WO	PCT/US2018/03	5/21/2018			Pending	WAKEUP RADIO (WUR) PREAMBLE DESIGN
MP10274	MP10274	US	16/001,648	6/6/2018			Pending	PACKETS WITH MIDAMBLE HAVING COMPRESSED OFDM SYMBOLS
MP10274	MP10274.WO	WO	PCT/US2018/03	6/6/2018			Pending	MIMO-OFDM MIDAMBLE WITH COMPRESSED HE-LTF
MP10275	MP10275	US	16/005,437	6/11/2018			Pending	Methods and Apparatus for Secure Fine Timing Measurement with E
MP1028	MP1028	US	11/674,008	2/12/2007	8008137	8/30/2011	In Force	Method For Fabricating 1T-DRAM On Bulk Silicon
MP1028	MP1028.CN	CN	200710100618.1	3/15/2007	ZL2007101006	1/4/2012	In Force	METHOD FOR FABRICATING 1T-DRAM ON BULK SILICON
MP10283	MP10283	US	16/014,366	6/21/2018	10439695	10/8/2019	In Force	SYSTEMS AND METHODS FOR PERFORMING IMPLICIT SOUNDING IN
MP10283	MP10283C1	US	16/569,262	9/12/2019			Pending	SYSTEMS AND METHODS FOR PERFORMING IMPLICIT SOUNDING IN
MP10284	MP10284	US	16/264,411	1/31/2019			Pending	SYSTEMS AND METHODS FOR CALIBRATING IMPEDANCE OF A LOW P
MP10285	MP10285	US	16/261,116	1/29/2019			Pending	SYSTEMS AND METHODS FOR A SHORT NULL DATA PACKET TRANSMI
MP1028E	MP1028EP	EP	70054069	3/15/2007			Pending	Method For Fabricating 1T-DRAM On Bulk Silicon
MP1028H	MP1028HK	HK	81020792	3/15/2007			Pending	Method For Fabricating 1T-DRAM On Bulk Silicon
MP1028J	MP1028JP	JP	2007067107	3/15/2007			Pending	Method For Fabricating 1T-DRAM On Bulk Silicon
MP1028T	MP1028TW	TW	96108777	3/14/2007	1423422	1/11/2014	In Force	METHOD FOR FABRICATING 1T-DRAM ON BULK SILICON
MP1029	MP1029C1	US	11/709,631	2/22/2007	7622731	11/24/2009	In Force	Cross-Point Memory Array
MP10295	MP10295	US	16/019,997	6/27/2018			Pending	Modular Memory-Like Layout For FinFET Analog Designs
MP1029T	MP1029TW	TW	96108782	3/14/2007	1418069	12/1/2013	In Force	CROSS-POINT MEMORY ARRAY
MP1029W	MP1029WO	WO	PCT/US2007/00	3/14/2007			Pending	Cross-Point Memory Array
MP1030	MP1030C1	US	12/696,558	1/29/2010	8260238	9/4/2012	In Force	SYSTEMS AND METHODS FOR RECOVERY OF A SUB-CARRIER SIGNAL
MP1030	MP1030C1C1	US	13/599,621	8/30/2012	8472906	6/25/2013	In Force	SYSTEMS AND METHODS FOR RECOVERY OF A SUB-CARRIER SIGNAL
MP10300	MP10300	US	16/030,264	7/9/2018			Pending	Wakeup Packet Modulation and Demodulation

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MP10300	MP10300WO	WO	PCT/US2018/04	7/9/2018			Pending	WAKEUP PACKET MODULATION AND DEMODULATION
MP10305	MP10305	US	16/352,886	3/14/2019			Pending	SCALING OF LOG-LIKELIHOOD RATIOS (LLR) BASED ON LONG TRAINING
MP10305	MP10305DE	DE	102019203536.1	3/15/2019			Pending	SCALING OF LOG-LIKELIHOOD RATIOS (LLR) BASED ON LONG TRAINING
MP10307	MP10307	US	16/117,991	8/30/2018			Pending	SYSTEMS AND METHODS FOR HIGH-THROUGHPUT COMPUTATIONS I
MP1031	MP1031C1	US	13/288,403	11/3/2011	8792591	7/29/2014	In Force	SYSTEMS AND METHODS FOR I/Q IMBALANCE CORRECTION AND CAL
MP10313	MP10313	US	16/133,456	9/17/2018	10469070	11/5/2019	In force	PAM-4 VOLTAGE-MODE DRIVER WITH STABILIZED OUTPUT
MP10314	MP10314	US	16/150,081	10/2/2018			Pending	INTERFACE CIRCUITRY
MP10317	MP10317	US	16/131,789	9/14/2018			Pending	Clear Channel Assessment for Duplex Transmissions in Wireless Local
MP10317	MP10317CN	CN	201811084126.1	9/17/2018			Pending	CLEAR CHANNEL ASSESSMENT FOR DUPLEX MAC
MP10318	MP10318	US	16/139,778	9/24/2018			Pending	SYSTEMS AND METHODS FOR MULTI-USER RANGING USING A NULL
MP10321	MP10321	US	16/123,037	9/6/2018			Pending	SYSTEMS AND METHODS FOR CARRIER SENSING AND SYMBOL TIMING
MP10322	MP10322	US	16/149,831	10/2/2018			Pending	SYSTEMS AND METHODS FOR ERROR RECOVERY IN NAND MEMORY
MP10324	MP10324	US	16/149,825	10/2/2018			Pending	SYSTEMS AND METHODS FOR MULTI-USER OPERATION WITH DUPLE
MP10324	MP10324CN	CN	201811169181.1	10/8/2018			Pending	SYSTEMS AND METHODS FOR MULTI-USER OPERATION WITH DUPLE
MP10324	MP10324DE	DE	102018216944.1	10/2/2018			Pending	SYSTEMS AND METHODS FOR MULTI-USER OPERATION WITH DUPLE
MP10326	MP10326WO	WO	PCT/US2018/04	7/30/2018			Pending	WAKEUP RADIO (WUR) PACKET PREAMBLE DESIGN
MP10328	MP10328	US	16/046,108	7/26/2018			Pending	ANGLE-OF-ARRIVAL PROCESSING IN MULTI-ANTENNA MULTI-CHANN
MP10329	MP10329	US	16/046,114	7/26/2018	10076539	11/12/2019	In force	AUTOMATIC GAIN CONTROL ADJUSTMENT IN MULTI-ANTENNA SHO
MP10331	MP10331	US	16/106,205	8/21/2018			Pending	METHOD OF CREATING ALIGNED VIAS IN ULTRA-HIGH DENSITY INTE
MP10331	MP10331CN	CN	201811116294.1	9/25/2018			Pending	METHOD OF CREATING ALIGNED VIAS IN ULTRA-HIGH DENSITY INTE
MP10331	MP10331JP	JP	2018-160009	8/29/2018			Pending	METHOD OF CREATING ALIGNED VIAS IN ULTRA-HIGH DENSITY INTE
MP10331	MP10331TW	TW	107138634	9/25/2018			Pending	METHOD OF CREATING ALIGNED VIAS IN ULTRA-HIGH DENSITY INTE
MP1034	MP1034	US	11/649,899	1/4/2007	7450040	11/11/2008	In Force	Method And Apparatus For Generating Non-Binary Balanced Codes
MP1034	MP1034C1	US	11/709,385	2/22/2007	7629903	12/8/2009	In Force	Method And Apparatus For Generating Non-Binary Balanced Codes
MP1034	MP1034CN	CN	200780018546.1	3/20/2007	ZL2007800185	12/26/2012	In Force	METHOD AND APPARATUS FOR GENERATING NON-BINARY BALANCE
MP1034T	MP1034TW	TW	96109357	3/19/2007	1470940	1/21/2015	In Force	METHOD AND APPARATUS FOR GENERATING N/A-BINARY BALANCE
MP1034W	MP1034W/D1	JP	2011-223798	3/20/2007	5298174	6/21/2013	In Force	METHOD AND APPARATUS FOR GENERATING NON-BINARY BALANCE
MP1034W	MP1034WO	WO	PCT/US2007/00	3/20/2007			Pending	Method And Apparatus For Generating Non-Binary Balanced Codes
MP1034W	MP1034WOIL	IL	194154	3/20/2007	194154	2/1/2013	In Force	Method And Apparatus For Generating Non-Binary Balanced Codes
MP1034W	MP1034WOJP	JP	2009-501507	3/20/2007	4856215	1/6/2012	In Force	METHOD AND APPARATUS FOR GENERATING NON-BINARY BALANCE
MP1034W	MP1034WOKR	KR	1020087025654	3/20/2007	101321560	10/17/2013	In Force	Method And Apparatus For Generating Non-Binary Balanced Codes
MP1035	MP1035	US	11/649,486	1/4/2007	7425907	9/16/2008	In Force	Rate-7/8 Direct-Current Free And Runlength Limited Code
MP10353	MP10353	US	16/169,489	10/24/2018			Pending	Clock Tree Gating for Register Arrays
MP10354	MP10354	US	16/179,320	11/2/2018			Pending	Methods and Apparatus for Generation of Physical Layer Protocol Da

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP10354	MP10354WO	WO	PCT/US2018/058	11/11/2018			Pending	METHODS AND APPARATUS FOR GENERATION OF PHYSICAL LAYER P
MP10355	MP10355	US	16/162,113	10/16/2018			Pending	WIFI CHANNEL AGGREGATION
MP10355	MP10355WO	WO	PCT/US2018/05	10/16/2018			Pending	WIFI CHANNEL AGGREGATION
MP1036	MP1036	US	11/704,747	2/9/2007	7701660	4/20/2010	In Force	Shock Cancellation System
MP10364	MP10364	US	16/050,082	7/31/2018			Pending	Efficient Ethernet Multi-Mode Coding and Modulation for Twisted Pa
MP10369	MP10369	US	16/037,487	7/17/2018			Pending	Self-Aligned Gate-All-Around Transistors
MP1037	MP1037	US	11/710,759	2/26/2007	8054715	11/8/2011	In Force	Track Counting System And Method For Recordable Optical Media
MP1037	MP1037C1	US	13/290,685	11/7/2011	8335138	12/18/2012	In Force	Track Counting System And Method For Recordable Optical Media
MP10373	MP10373	US	16/359,968	3/20/2019			Pending	Correlation-based Detection of Encoded Address in Packet
MP10373	MP10373DE	DE	102019203940.1	3/22/2019			Pending	CORRELATION-BASED DETECTION OF ENCODED ADDRESS IN PACKET
MP10378	MP10378	US	16/176,104	10/31/2018			Pending	TRIGGER MPDU FOR CLIENT-SELECTABLE TRANSMIT POWER
MP10383	MP10383	US	16/136,329	9/20/2018			Pending	Ethernet Transceiver with PHY-Level Signal-Loss Detector
MP10383	MP10383DE	DE	102018218952.1	11/7/2018			Pending	ETHERNET TRANSCEIVER WITH PHY-LEVEL SIGNAL-LOSS DETECTOR
MP10383	MP10383WO	WO	PCT/IB2018/057	9/26/2018			Pending	ETHERNET TRANSCEIVER WITH PHY-LEVEL SIGNAL-LOSS DETECTOR
MP10383	MP10383WOCN	CN	0	9/26/2018			Not Filed	ETHERNET TRANSCEIVER WITH PHY-LEVEL SIGNAL-LOSS DETECTOR
MP10383	MP10383WOJP	JP	2019-562352	9/26/2018			Pending	ETHERNET TRANSCEIVER WITH PHY-LEVEL SIGNAL-LOSS DETECTOR
MP10388	MP10388	US	16/161,961	10/16/2018			Pending	WAKEUP RADIO PACKET WITH NEIGHBOR ACCESS POINT INFORMATION
MP10390	MP10390	US	16/248,266	1/15/2019			Pending	Using Dipulse Response to Detect Channel Parameters
MP1040	MP1040	US	11/510,006	8/25/2006	7941640	5/10/2011	In Force	Secure Processors Having Encoded Instructions
MP10400	MP10400	US	16/182,160	11/6/2018			Pending	TWO DIMENSIONAL MAGNETIC RECORDING (TDMR) OFF-TRACK PER
MP10403	MP10403	US	16/174,918	10/30/2018			Pending	REGISTER-BASED ASYNCHRONOUS FIFO WITH ASYMMETRIC SIZE
MP10404	MP10404	US	16/176,709	10/31/2018			Pending	Methods and Apparatus for Generation of Physical Layer Protocol Da
MP10404	MP10404WO	WO	PCT/US2018/05	10/31/2018			Pending	METHODS AND APPARATUS FOR GENERATION OF PHYSICAL LAYER P
MP10407	MP10407	US	16/150,356	10/3/2018			Pending	METHOD AND APPARATUS FOR PROCESSING MULTIPLE SERVO CHAN
MP1041	MP1041C1	US	14/246,773	4/7/2014	9405730	8/2/2016	In Force	SYSTEMS AND METHODS FOR A SIGNED MAGNITUDE ADDER IN ONE
MP10410	MP10410	US	16/037,552	7/17/2018			Pending	PHOTOGRAPHY MASK DESIGN RULE CHECK ASSISTANCE
MP10412	MP10412	US	16/051,211	7/31/2018			Pending	BLOCK ACK FRAME WITH FLOW CONTROL
MP10412	MP10412WO	WO	PCT/US2018/04	7/31/2018			Pending	BLOCK ACK FRAME WITH FLOW CONTROL
MP10417	MP10417	US	16/190,991	11/14/2018			Pending	SYSTEMS AND METHODS FOR EFFICIENT DATA RETRIEVAL IN NON-V
MP10419	MP10419	US	16/165,244	10/19/2018	10475482	11/12/2019	In force	METHOD AND APPARATUS FOR CALIBRATING READ-HEAD OFF-TRAC
MP1042	MP1042	US	11/627,250	1/25/2007	7971130	6/28/2011	In Force	MULTI-LEVEL SIGNAL MEMORY WITH LDPC AND INTERLEAVING
MP1042	MP1042C1	US	13/169,790	6/27/2011	8869014	10/21/2014	In Force	MULTI-LEVEL SIGNAL MEMORY WITH LDPC AND INTERLEAVING
MP10421	MP10421	US	16/370,999	3/31/2019			Pending	Plastic MM-wave Waveguide with Integral Electrically-Conductive Tra
MP10421	MP10421WO	WO	PCT/IB2019/052	3/31/2019			Pending	PLASTIC MM-WAVE WAVEGUIDE WITH INTEGRAL ELECTRICALLY-CON

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP10422	MP10422	US	16/144,595	9/27/2018			Pending	APPARATUS AND METHODS FOR A CONFIGURABLE WIRELESS COMMUNICATIONS SYSTEM
MP10423	MP10423	US	16/264,010	1/31/2019			Pending	RAPID SYNC UP MECHANISM FOR TRAFFIC CONTROL BETWEEN TWO NETWORKS
MP10424	MP10424	US	16/427,573	5/31/2019			Pending	DATA TRANSFER INTERFACE WITH REDUCED SIGNALING
MP10427	MP10427	US	16/422,298	5/24/2019			Pending	HARDWARE DATA TRANSPORT IN WIRELESS DATA TRANSCIVER
MP10428	MP10428	US	16/389,020	4/19/2019			Pending	PACKET HANDLING IN DATA RECEIVER WITH HARDWARE DATA TRANSPORT
MP1042D	MP1042DE	DE	07734144.4	3/27/2007			Pending	MULTI-LEVEL SIGNAL MEMORY WITH LDPC AND INTERLEAVING
MP1042F	MP1042FR	FR	07734144.4	3/27/2007			Pending	MULTI-LEVEL SIGNAL MEMORY WITH LDPC AND INTERLEAVING
MP1042I	MP1042IL	IL	194484	3/27/2007	194484	2/1/2013	In Force	Multi-Level Memory with LDPC Bit Interleaved Coded Modulation
MP1042J	MP1042JP	JP	2009-502244	3/27/2007	5356214	9/6/2013	In Force	MULTI-LEVEL MEMORY WITH LDPC BIT INTERLEAVED CODED MODULATION
MP1043	MP1043	US	11/595,053	11/10/2006	7688749	3/30/2010	In Force	Network Interface With Autonegotiation And Cable Length Measure
MP1043C1	MP1043C1	US	12/748,884	3/29/2010	8014313	9/6/2011	In Force	Network Interface With Autonegotiation And Cable Length Measure
MP1043C1a	MP1043C1a	US	13/926,562	6/25/2013	8797909	8/5/2014	In Force	Network Interface With Autonegotiation And Cable Length Measure
MP1043C1C1	MP1043C1C1	US	13/224,920	9/2/2011	8472340	6/25/2013	In Force	Network Interface With Autonegotiation And Cable Length Measure
MP10431	MP10431	US	15/973,572	5/8/2018	10469633	11/5/2019	In Force	Low-Latency Pipeline for Media-To-Ethernet Frame Packaging
MP10431WQ	MP10431WQ	WO	PCT/IB2018/053	5/21/2018			Pending	LOW LATENCY PIPELINE FOR MEDIA-TO-ETHERNET FRAME PACKAGING
MP10432	MP10432	US	16/021,040	6/28/2018			Pending	Network Packet Generator Employing Multiple Header Templates and Multiple Packet Headers
MP10432WQ	MP10432WQ	WO	PCT/IB2018/054	6/28/2018			Pending	NETWORK PACKET GENERATOR EMPLOYING MULTIPLE HEADER TEMPLATES AND MULTIPLE PACKET HEADERS
MP10432WQDE	MP10432WQDE	DE	202018106531	6/28/2018			Pending	NETWORK PACKET GENERATOR EMPLOYING MULTIPLE HEADER TEMPLATES AND MULTIPLE PACKET HEADERS
MP10433	MP10433	US	16/045,781	7/26/2018			Pending	Distributed Checksum Calculation for Communication Packets
MP10433WQ	MP10433WQ	WO	PCT/IB2018/055	7/26/2018			Pending	Distributed Checksum Calculation for Communication Packets
MP10433WQCN	MP10433WQCN	CN	0	7/26/2018			Not Filed	Distributed Checksum Calculation for Communication Packets
MP10433WQDE	MP10433WQDE	DE	21 2018 000 054	7/26/2018			Pending	Distributed Checksum Calculation for Communication Packets
MP10434	MP10434	US	15/984,441	5/21/2018	10437773	10/8/2019	In Force	SPI devices with multi-master capabilities
MP10434DE	MP10434DE	DE	102018214015.4	8/20/2018			Pending	SPI MULTI MASTER CAPABILITY
MP10439	MP10439	US	16/051,084	7/31/2018			Pending	BLOCK ACKNOWLEDGMENT OPERATION
MP10439WQ	MP10439WQ	WO	PCT/US2018/04	7/31/2018			Pending	BLOCK ACKNOWLEDGMENT OPERATION
MP10442	MP10442	US	16/133,381	9/17/2018			Pending	COAXIAL-INTERCONNECT STRUCTURE FOR A SEMICONDUCTOR COMPONENT
MP10442CN	MP10442CN	CN	201811279333.1	10/30/2018			Pending	COAXIAL-INTERCONNECT STRUCTURE FOR A SEMICONDUCTOR COMPONENT
MP10443	MP10443	US	16/169,552	10/24/2018			Pending	Methods and Apparatus for Testing an Integrated Circuit
MP10443CN	MP10443CN	CN	201811285056.4	10/31/2018			Pending	TEST MODE METHOD TO ACCESS AND TEST EMBEDDED SOC INSIDE A PACKAGE
MP1045	MP1045	US	11/605,797	11/29/2006	7836379	11/16/2010	In Force	Method For Computing Buffer ECC
MP10451	MP10451	US	16/175,122	10/30/2018			Pending	SYSTEMS AND METHODS FOR ETHERNET PACKET ERROR DETECTION
MP10452	MP10452	US	16/050,674	7/31/2018			Pending	ACTION FRAME TO INDICATE CHANGE IN BLOCK ACKNOWLEDGMENT
MP10452WQ	MP10452WQ	WO	PCT/US2018/04	7/31/2018			Pending	ACTION FRAME TO INDICATE CHANGE IN BLOCK ACKNOWLEDGMENT

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MP10463	MP10463	US	16/262,616	1/30/2019			Pending	Synchronizing Clocks in a Wireless Network
MP10465	MP10465	US	16/258,731	1/28/2019			Pending	MM-WAVE WAVEGUIDE PHYSICAL LAYER INTERCONNECT FOR AUTO
MP10465	MP10465.C1	US	16/299,154	3/12/2019			Pending	Accessories for MM-Wave Waveguide Physical Layer Interconnect in
MP10465	MP10465.WO	WO	PCT/IB2019/05C	1/28/2019			Pending	MM-WAVE WAVEGUIDE PHYSICAL LAYER INTERCONNECT FOR AUTO
MP10467	MP10467	US	16/264,431	1/31/2019			Pending	SYSTEMS AND METHODS FOR GENERATING AND TRANSMITTING SH
MP10467	MP10467.WO	WO	PCT/US2019/01	1/31/2019			Pending	SYSTEMS AND METHODS FOR GENERATING AND TRANSMITTING SH
MP10468	MP10468	US	16/274,529	2/13/2019			Pending	RADIO-FREQUENCY FRONT END WITH POWER AMPLIFIER DETUNING
MP1047	MP1047	US	11/655,368	1/19/2007	7596731	9/29/2009	In Force	Test Time Reduction Algorithm
MP10472	MP10472	US	16/176,099	10/31/2018			Pending	SYSTEMS AND METHODS FOR WAKE-UP RADIO MULTI-ANTENNA TR
MP10472	MP10472.WO	WO	PCT/US2018/05	10/31/2018			Pending	SYSTEMS AND METHODS FOR WAKE-UP RADIO MULTI-ANTENNA TR
MP10474	MP10474	US	16/262,639	1/30/2019			Pending	SECURE RANGING MEASUREMENT
MP10474	MP10474.WO	WO	PCT/US2019/01	1/30/2019			Pending	SECURE RANGING MEASUREMENT
MP10485	MP10485	US	16/406,894	5/8/2019			Pending	FREQUENCY DIVISION MULTIPLE ACCESS (FDMA) SUPPORT FOR WAK
MP10486	MP10486	US	16/263,845	1/31/2019	10497390	12/3/2019	In force	DECISION-FREE TIMING DISTURBANCE CANCELLATION CAUSED BY H
MP10487	MP10487	US	16/386,319	4/17/2019			Pending	PILOTS FOR WIRELESS ACCESS IN VEHICULAR ENVIRONMENTS
MP10487	MP10487.WO	WO	PCT/IB2019/053	4/17/2019			Pending	Next Generation C-V2X Pilot Design
MP1049	MP1049	US	11/595,142	11/9/2006	8129969	3/6/2012	In Force	Hysteretic Inductive Switching Regulator With Power Supply Compen
MP1049	MP1049C1	US	13/409,294	3/1/2012	8310389	11/13/2012	In Force	Hysteretic Inductive Switching Regulator With Power Supply Compen
MP10491	MP10491	US	16/395,579	4/26/2019			Pending	SYSTEMS AND METHODS FOR DETECTING MOTION USING WIRELESS
MP10495	MP10495	US	16/399,517	4/30/2019			Pending	WIFI MULTI-BAND COMMUNICATION
MP10495	MP10495PR2	US	62/791,519	1/11/2019			Pending	FREQUENCY DIVISION DUPLEX (FDD) OVER CHANNEL AGGREGATION
MP10495	MP10495.WO	WO	PCT/US2019/02	4/30/2019			Pending	WIFI multi-band communication
MP10497	MP10497	US	16/399,782	4/30/2019			Pending	PUNCTURED OPERATING CHANNELS IN WLAN
MP10497	MP10497.WO	WO	PCT/US2019/03	4/30/2019			Pending	PUNCTURED OPERATING CHANNELS IN WLAN
MP1050	MP1050	US	11/585,566	10/24/2006	7465572	12/16/2008	In Force	Three Phase Voltage Tripler
MP1055	MP1055	US	11/784,482	4/6/2007	7974032	7/5/2011	In Force	NON-LINEAR TRANSITION SHIFT IDENTIFICATION AND COMPENSATI
MP1055	MP1055D1	US	13/176,318	7/5/2011	8358480	1/22/2013	In Force	NON-LINEAR TRANSITION SHIFT IDENTIFICATION AND COMPENSATI
MP1056	MP1056C1	US	12/950,779	11/19/2010	8684537	7/9/2013	In Force	Systems And Methods For Data-Path Protection
MP1056	MP1056C1C1	US	13/936,426	7/8/2013	9129654	9/8/2015	In Force	Systems And Methods For Data-Path Protection
MP1057	MP1057	US	11/728,339	3/26/2007	8455691	7/23/2013	In Force	Content Localization In A Network Device
MP1057	MP1057C1	US	13/947,806	7/22/2013	9032460	5/12/2015	In Force	Content Localization In A Network Device
MP1058	MP1058	US	11/657,492	1/24/2007	7436331	10/14/2008	In Force	Rate-28/30 DC-Free RLL Code
MP1059	MP1059	US	11/736,386	4/17/2007	7661058	2/9/2010	In Force	Efficient RAID ECC Controller for RAID Systems
MP1059	MP1059C1	US	11/805,332	5/23/2007	7657823	2/2/2010	In Force	EFFICIENT RAID ECC CONTROLLER FOR RAID SYSTEMS

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MP1059	MP1059C2	US	11/805,344	5/23/2007	8166370	4/24/2012	In Force	EFFICIENT RAID ECC CONTROLLER FOR RAID SYSTEMS
MP1059	MP1059C2a8	US	13/777,538	2/26/2013	9075745	7/7/2015	In Force	System And Method For Adding A Drive To A Storage System Having
MP1059	MP1059C2C1	US	13/454,831	4/24/2012	8386889	2/26/2013	In Force	DRIVE REPLACEMENT TECHNIQUES FOR RAID SYSTEMS
MP1061	MP1061	US	11/582,906	10/18/2006	7514990	4/7/2009	In Force	Very Low Frequency High Pass Filter
MP1061	MP1061C1	US	12/413,654	3/30/2009	8076969	12/13/2011	In Force	APPARATUS FOR ATTENUATING LOW FREQUENCY SIGNALS
MP1062	MP1062	US	11/696,476	4/4/2007	8553720	10/8/2013	In Force	Adaptive Speed Control For MAC-PHY Interfaces
MP1062	MP1062C1	US	14/067,533	10/7/2013	9210107	12/8/2015	In Force	METHOD AND APPARATUS FOR ADJUSTING A RATE AT WHICH DATA
MP1062	MP1062C1C1	US	14/944,630	11/18/2015	9740455	8/22/2017	In Force	APPARATUS AND METHOD FOR ADJUSTING A RATE AT WHICH DATA
MP1062I	MP1062IIL	IL	182676	4/19/2007	182676	10/1/2014	In Force	Adaptive Speed Control for MAC-PHY Interfaces
MP1062T	MP1062TW	TW	96113692	4/18/2007	1418196	12/1/2013	In Force	ADAPTIVE SPEED CONTROL FOR MAC-PHY INTERFACES
MP1062W	MP1062WO	WO	PCT/US2007/00	4/19/2007			Pending	Adaptive Speed Control For MAC-PHY Interfaces
MP1062W	MP1062WOCN	CN	200780023894.1	4/19/2007	ZL2007800228	6/5/2013	In Force	ADAPTIVE SPEED CONTROL FOR MAC-PHY INTERFACES
MP1062W	MP1062WOEP	EP	07775773.0	4/19/2007	EP2016725	8/29/2012	In Force	Adaptive Speed Control for MAC-PHY Interfaces
MP1062W	MP1062WOEPDE	DE	07775773.0	4/19/2007	602007025140	8/29/2012	In Force	Adaptive Speed Control For MAC-PHY Interfaces
MP1062W	MP1062WOEPGB	GB	07775773.0	4/19/2007	2016725	8/29/2012	In Force	Adaptive Speed Control For MAC-PHY Interfaces
MP1062W	MP1062WOKR	KR	1026087028182	4/19/2007	101394392	5/7/2014	In Force	Adaptive Speed Control For MAC-PHY Interfaces
MP1063	MP1063C1	US	12/906,886	10/18/2010	8077419	12/13/2011	In Force	METHOD AND APPARATUS FOR ADJUSTING A GAIN OF A VARIABLE G
MP1064	MP1064	US	11/736,071	4/17/2007	8738013	5/27/2014	In Force	802.11 Mesh Architecture
MP1064	MP1064C1	US	11/736,730	4/18/2007	8014370	9/6/2011	In Force	802.11 MESH ARCHITECTURE
MP1064	MP1064C3	US	14/286,320	5/23/2014	9479428	10/25/2016	In Force	Cumulative Route Metrics For Wireless Mesh Network Routing
MP1064I	MP1064I3D1	JP	2012-065431	3/22/2012	5574190	7/11/2014	In Force	IMPROVED 802.11 MESH ARCHITECTURE
MP1064I	MP1064IP03	JP	2009-507750	4/24/2007	4963318	4/6/2012	In Force	IMPROVED 802.11 MESH ARCHITECTURE
MP1064W	MP1064WO	WO	PCT/US2007/00	4/24/2007			Pending	Improved 802.11 Mesh Architecture
MP1064W	MP1064WOCN	CN	200780023801.1	4/24/2007	ZL2007800238	5/28/2014	In Force	IMPROVED 802.11 MESH ARCHITECTURE
MP1064W	MP1064WOEP	EP	EP07755934.2	4/24/2007	EP2011287	3/29/2017	In Force	Improved 802.11 Mesh Architecture
MP1064W	MP1064WOEPDE	DE	07755934.2	4/24/2007	602007050393	3/29/2017	In Force	Improved 802.11 Mesh Architecture
MP1064W	MP1064WOEPFR	FR	07755934.2	4/24/2007	2011287	3/29/2017	In Force	Improved 802.11 Mesh Architecture
MP1064W	MP1064WOEPGB	GB	07755934.2	4/24/2007	2011287	3/29/2017	In Force	Improved 802.11 Mesh Architecture
MP1064W	MP1064WOKR	KR	1026087028743	4/24/2007	101362776	2/5/2014	In Force	Improved 802.11 Mesh Architecture
MP1065	MP1065	US	11/789,334	4/24/2007	7934143	4/26/2011	In Force	Parity Insertion For Inner Architecture
MP1065	MP1065C1	US	13/089,733	4/19/2011	8140943	3/20/2012	In Force	Parity Insertion For Inner Architecture
MP1065	MP1065C1a8	US	14/035,260	9/24/2013	8788911	7/22/2014	In Force	Parity Insertion For Inner Architecture
MP1068	MP1068	US	11/786,086	4/9/2007	7616142	11/10/2009	In Force	SIGMA-DELTA MODULATED ANALOG-TO-DIGITAL CONVERTER HAVING
MP1069	MP1069	US	11/784,291	4/6/2007	8010883	8/30/2011	In Force	Read Channel Detector For Noise Cancellation

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP1069	MP1069C1	US	13/219,917	8/29/2011	8413032	4/2/2013	In Force	Read Channel Detector For Noise Cancellation
MP1070	MP1070	US	11/809,670	6/1/2007	8028216	9/27/2011	In Force	Embedded Parity Coding For Data Storage
MP1070	MP1070C1	US	11/827,213	7/11/2007	8255765	8/28/2012	In Force	Embedded Parity Coding For Data Storage
MP1070	MP1070C2	US	11/827,206	7/11/2007	8255764	8/28/2012	In Force	Embedded Parity Coding For Data Storage
MP1070	MP1070C3	US	11/827,339	7/11/2007	8156400	4/10/2012	In Force	Embedded Parity Coding For Data Storage
MP1071	MP1071	US	11/799,488	5/1/2007	8080059	1/3/2012	In Force	Multi-Viterbi Receive Channel Decoder
MP1071	MP1071C1	US	13/335,267	12/22/2011	8594246	11/26/2013	In Force	Multi-Viterbi Receive Channel Decoder
MP1071	MP1071C1C1	US	14/088,777	11/25/2013	8873614	10/28/2014	In Force	METHOD AND APPARATUS FOR USING DUAL-VITERBI ALGORITHMS
MP1077	MP1077	US	11/303,785	12/16/2005	7319356	1/15/2008	In Force	MULTIPLEXER CIRCUIT WITH COMBINED LEVEL SHIFTING AND DELAY
MP1082	MP1082	US	11/369,778	3/7/2006	7570725	8/4/2009	In Force	NUMERICALLY CONTROLLED OSCILLATOR
MP1084	MP1084CN	CN	200780014068.1	4/18/2007			Pending	SHARED MEMORY MULTI VIDEO CHANNEL DISPLAY APPARATUS AND
MP1084	MP1084IN	IN	2144/MUMNP/.	10/3/2008			Pending	SHARED MEMORY MULTI VIDEO CHANNEL DISPLAY APPARATUS AND
MP1084J	MP1084JP	JP	2009-506583	4/18/2007			Pending	SHARED MEMORY MULTI VIDEO CHANNEL DISPLAY APPARATUS AND
MP1084K	MP1084KR	KR	10-2008-702656	10/29/2008			Pending	SHARED MEMORY MULTI VIDEO CHANNEL DISPLAY APPARATUS AND
MP1084W	MP1084WO	WO	PCT/US2007/00	4/18/2007			Pending	SHARED MEMORY MULTI VIDEO CHANNEL DISPLAY APPARATUS AND
MP1091	MP1091	US	11/788,250	4/18/2007	7835429	11/16/2010	In Force	TRANSMIT SPECTRUM FOR A DIGITAL SUBSCRIBER LINE SIGNAL
MP1097	MP1097	US	11/729,471	3/29/2007	7724067	5/25/2010	In Force	Enhanced Transmission Gate
MP1098	MP1098	US	11/789,196	4/24/2007	7586429	9/8/2009	In Force	Scrambling System For High Resolution Digital-To-Analog Converter
MP1099	MP1099	US	11/809,373	6/1/2007	7504892	3/17/2009	In Force	Charge-Pump For Phase-Locked Loop
MP1100	MP1100	US	11/738,263	4/20/2007	7649793	1/19/2010	In Force	CHANNEL ESTIMATION FOR MULTI-LEVEL MEMORIES USING PILOT SI
MP1100	MP1100C1	US	12/689,891	1/19/2010	7936630	5/3/2011	In Force	METHOD AND APPARATUS FOR CALIBRATING A READ/WRITE CHANN
MP1100	MP1100C1AA	US	13/752,121	1/28/2013	8531903	9/10/2013	In Force	METHOD AND APPARATUS FOR CALIBRATING A READ/WRITE CHANN
MP1100	MP1100C1C1	US	13/099,997	5/3/2011	8363501	1/29/2013	In Force	METHOD AND APPARATUS FOR CALIBRATING A READ/WRITE CHANN
MP1100	MP1100C12a	US	14/021,712	9/9/2013	8693275	4/8/2014	In Force	METHOD AND APPARATUS FOR CALIBRATING A READ/WRITE CHANN
MP1100S	MP1100S	US	16/257,860	1/25/2019			Pending	APPARATUS AND METHODS FOR TEMPERATURE-BASED MEMORY M
MP11007	MP11007	US	16/545,966	8/20/2019			Pending	PULSE-BASED WRITING FOR MAGNETIC STORAGE MEDIA
MP11008	MP11008	US	16/547,282	8/21/2019			Pending	SYSTEMS AND METHODS FOR RATE SELECTION AND RESOURCE UNIT
MP11009	MP11009	US	16/547,337	8/21/2019			Pending	SYSTEMS AND METHODS FOR DETECTING MOTION BASED ON CHAN
MP1101	MP1101	US	11/800,554	5/4/2007	7589649	9/15/2009	In Force	APPARATUS, METHOD, AND SYSTEM FOR CORRECTION OF BASELINE
MP1101S	MP1101S	US	16/372,905	4/2/2019			Pending	Sub-Device Field-Effect Transistor Architecture for Integrated Circuits
MP11019	MP11019	US	16/262,487	1/30/2019			Pending	RECONFIGURABLE ANALOG FILTER WITH OFFSET COMPENSATION
MP1102	MP1102	US	11/745,254	5/7/2007	7685494	3/23/2010	In Force	ERROR CORRECTION CODING FOR VARYING SIGNAL-TO-NOISE RATIO
MP1102	MP1102C1C1	US	13/179,429	7/8/2011	8663274	3/25/2014	In Force	ERROR CORRECTION CODING FOR VARYING SIGNAL-TO-NOISE RATIO
MP1103	MP1103	US	11/591,326	11/1/2006	7911901	3/22/2011	In Force	Magnetic And Optical Rotating Storage Systems With Audio Monitor

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MP1103	MP1103C1C1	US	13/155,018	6/7/2011	8467279	6/18/2013	In Force	Magnetic And Optical Rotating Storage Systems With Audio Monitori
MP1103	MP1103C2	US	11/652,258	1/11/2007	7896196	2/15/2011	In Force	Magnetic And Optical Rotating Storage Systems With Audio Monitori
MP1103	MP1103C2TW	TW		7/20/2007	1455119	10/1/2014	In Force	Rotating Data Storage Device Having Audio Monitoring Module
MP11032	MP11032	US	16/545,486	8/20/2019			Pending	MULTI-RADIO DEVICE AND METHOD OF OPERATION THEREOF
MP1103T	MP1103TW	TW	96126623	7/20/2007	1433140	4/1/2014	In Force	Magnetic And Optical Rotating Storage Systems With Audio Monitori
MP1103W	MP1103WO	WO	PCT/US2007/01	7/11/2007			Pending	Magnetic And Optical Rotating Storage Systems With Audio Monitori
MP1103W	MP1103WOKR	KR	1020097002546	7/11/2007	101482199	1/7/2015	In Force	Magnetic And Optical Rotating Storage Systems With Audio Monitori
MP1104	MP1104C1	US	12/896,358	10/1/2010	8167740	1/24/2012	In Force	OFFSET LOOP FOR WOBBLE
MP1104	MP1104C1C1	US	13/355,652	1/23/2012	8315135	11/20/2012	In Force	OFFSET LOOP FOR WOBBLE
MP1104	MP11041	US	12/484,417	6/15/2009	8355302	1/15/2013	In Force	OFFSET LOOP FOR WOBBLE
MP11043	MP11043	US	16/527,302	7/31/2019			Pending	METHOD AND APPARATUS FOR AUTHORIZING UNLOCKING OF A DEV
MP11049	MP11049	US	16/263,387	1/31/2019			Pending	SYSTEMS AND METHODS FOR GENERATING METADATA DESCRIBING
MP1105	MP1105	US	09/903,201	7/10/2001	7248381	7/24/2007	In Force	POINT-OF-SALE DEMONSTRATION OF COMPUTER PERIPHERALS
MP1105	MP1105C1	US	11/716,096	3/9/2007	8237957	8/7/2012	In Force	POINT-OF-SALE DEMONSTRATION OF COMPUTER PERIPHERALS
MP11050	MP11050	US	16/518,564	7/22/2019			Pending	ARTIFICIAL INTELLIGENCE-ENABLED SEARCH FOR A STORAGE SYSTEM
MP11058	MP11058	US	16/570,360	9/13/2019			Pending	SYSTEMS AND METHODS FOR UPUNK POWER CONTROL AND RATE A
MP1106	MP1106	US	10/630,506	7/29/2003	7025266	4/11/2006	In Force	DEVICE AND METHOD FOR DIGITIZING A SERIALIZED SCANNER OUTP
MP1106	MP1106GB	GB	G8414307.9	6/25/2004	G82404514	3/14/2007	In Force	DEVICE AND METHOD FOR DIGITIZING A SERIALIZED SCANNER OUTP
MP1106	MP1106RE	US	11/701,272	1/31/2007	RE43511	7/17/2012	In Force	DEVICE AND METHOD FOR DIGITIZING A SERIALIZED SCANNER OUTP
MP11060	MP11060	US	16/264,473	1/31/2019			Pending	STORAGE EDGE CONTROLLER WITH A METADATA COMPUTATIONAL
MP11061	MP11061	US	16/561,047	9/5/2019			Pending	MULTI-PROTOCOL FRAME FORMAT
MP11065	MP11065	US	16/264,248	1/31/2019			Pending	STORAGE AGGREGATOR CONTROLLER WITH METADATA COMPUTATI
MP11066	MP11066	US	16/533,376	8/6/2019			Pending	ENABLING VIRTUAL FUNCTIONS ON STORAGE MEDIA
MP11067	MP11067	US	16/532,272	8/5/2019			Pending	Non-Volatile Memory Switch with Host Isolation
MP11068	MP11068	US	16/533,243	8/6/2019			Pending	VIRTUALIZING ISOLATION AREAS OF SOLID-STATE STORAGE MEDIA
MP1106J	MP1106JP	JP	2004-211476	7/20/2004	JP4582535	9/10/2010	In Force	DEVICE AND METHOD FOR DIGITIZING A SERIALIZED SCANNER OUTP
MP1106J	MP1106JPD1	JP	2010-105857	4/30/2010	4922430	2/10/2012	In Force	DEVICE AND METHOD FOR DIGITIZING A SERIALIZED SCANNER OUTP
MP11070	MP11070	US	16/532,081	8/5/2019			Pending	Managed Switching Between One or More Hosts and Solid State Driv
MP11071	MP11071	US	16/533,399	8/6/2019			Pending	APPARATUS AND METHODS FOR PROVIDING QUALITY OF SERVICE O
MP11072	MP11072	US	16/532,186	8/5/2019			Pending	Modifying NVMe Physical Region Page List Pointers and Data Pointer
MP11073	MP11073	US	16/262,975	1/31/2019			Pending	METADATA GENERATION AT THE STORAGE EDGE
MP11074	MP11074	US	16/262,971	1/31/2019			Pending	METADATA GENERATION FOR MULTIPLE OBJECT TYPES
MP11078	MP11078	US	16/594,025	10/6/2019			Pending	Dispersion Compensation in MM-Wave Communication over Plastic
MP11079	MP11079	US	16/590,581	10/2/2019			Pending	Relay Circuit for Reducing a Voltage Glitch During Device Testing

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP11108	MP11108	US	10/680,018	10/6/2003	8368950	2/5/2013	In Force	METHOD OF PRE-PROCESSING DATA ON A HOST MACHINE FOR A HO
MP1108D	MP1108DE	DE	DE10200403055	6/24/2004	DE10200403055	8/9/2007	In Force	METHOD OF PRE-PROCESSING DATA ON A HOST MACHINE FOR A HO
MP1108J	MP1108JP	JP	2004-291198	10/4/2004			Pending	METHOD OF PRE-PROCESSING DATA ON A HOST MACHINE FOR A HO
MP1108J	MP1108JP01	JP	2011-050630	3/8/2011	5480835	2/21/2014	In Force	METHOD OF PRE-PROCESSING DATA ON A HOST MACHINE FOR A HO
MP11092	MP11092	US	16/571,117	9/15/2019			Pending	Adaptive Gain Control for Indoor Positioning
MP1110	MP1110	US	11/087,453	3/23/2005	7733375	6/8/2010	In Force	SETTING IMAGER PARAMETERS BASED ON CONFIGURATION PATTERN
MP1110	MP1110PR	US	62/770,077	11/20/2018			Pending	PROBLEMS IN SYMMETRIC KEY METHOD FOR MULTI-AP NETWORKS
MP1110	MP1110WO	WO	PCT/IB2019/055	11/19/2019			Pending	CRYPTOGRAPHIC SECURITY IN MULTI-ACCESS POINT NETWORKS
MP11112	MP11112	US	16/261,293	1/29/2019			Pending	WiFi Receiver Architecture
MP11112	MP11112PR	US	62/782,177	12/19/2018			Pending	ARCHITECTURE OF RECEIVER FOR WIRELESS NETWORKS
MP11141	MP11141	US	15/929,191	12/19/2019			Pending	NON-DESTRUCTIVE EYE OPENING MONITOR (EOM) IN ANALOG-TO-D
MP11141	MP11141PR	US	62/782,192	12/19/2018			Pending	NON-DESTRUCTIVE EYE OPENING MONITOR (EOM) IN ANALOG-TO-D
MP11144	MP11144	US	16/715,559	12/16/2019			Pending	Zone Self-Servo Writing With Synchronized Parallel Clocks
MP11116	MP11116	US	11/751,687	5/22/2007	8164773	4/24/2012	In Force	Wireless System-In-Package And Image Processing Control Apparatus
MP11116	MP11116C1	US	13/453,607	4/23/2012	8477357	7/2/2013	In Force	Wireless System-In-Package And Corresponding Wireless Printer Syst
MP11116	MP11116C1aa	US	14/154,480	1/14/2014	9244644	1/26/2016	In Force	Methods For Operating A Circuit Board And An Apparatus Each Havin
MP11116	MP11116C1C1	US	13/927,735	6/26/2013	8630012	1/14/2014	In Force	Apparatus And System On A Chip For Printing Based On Processing Of
MP11116	MP11116CN	CN	200780028360	5/24/2007			Pending	WIRELESS SYSTEM-IN-PACKAGE AND IMAGE PROCESSING CONTROL
MP11116	MP11116Zaa	US	15/001,714	1/20/2016	9665329	5/30/2017	In Force	Printer, A System-In-Package And Method For Wirelessly Initiating Pri
MP11161	MP111161PR	US	62/872,980	7/11/2019			Pending	Phase Roll based Accurate Timing Estimation for 11ax
MP111164	MP111164	US	16/262,355	1/30/2019			Pending	operation with bandwidth-limited devices in a wireless network
MP11164	MP111164DE	DE	1020192012531	1/31/2019			Pending	HE 20MHz ONLY DEVICES IN 5GHz BAND
MP11164	MP111164WO	WO	PCT/US2019/01	1/30/2019			Pending	HE 20MHz ONLY DEVICES IN 5GHz BAND
MP1116W	MP11116WO	WO	PCT/US2007/01	5/24/2007			Pending	Wireless System-In-Package And Image Processing Control Apparatus
MP1116W	MP11116WOEP	EP	07795293.5	5/24/2007	EP2021183	8/4/2010	In Force	Wireless Printer System
MP1116W	MP11116WOEPDE	DE	07795293.5	5/24/2007	602007008231	8/4/2010	In Force	Wireless Printer System
MP1116W	MP11116WOEPFR	FR	77952935	5/24/2007	2021183	8/4/2010	In Force	Wireless Printer System
MP1116W	MP11116WOEPGB	GB	07795293.5	5/24/2007	2021183	8/4/2010	In Force	Wireless Printer System
MP1116W	MP11116WOJP01	JP	2009-512154	5/24/2007			Pending	WIRELESS SYSTEM-IN-PACKAGE AND IMAGE PROCESSING CONTROL
MP1118	MP1118	US	11/368,656	3/6/2006	7428336	9/23/2008	In Force	EFFICIENT DETECTION OF CONSTANT REGIONS OF AN IMAGE
MP1118	MP1118C1aa	US	13/549,036	7/13/2012	8363954	1/29/2013	In Force	EFFICIENT DETECTION OF CONSTANT REGIONS OF AN IMAGE
MP1118	MP1118C1C1	US	13/016,454	1/28/2011	8224096	7/17/2012	In Force	EFFICIENT DETECTION OF CONSTANT REGIONS OF AN IMAGE
MP1119	MP1119	US	11/229,774	9/19/2005	7517039	4/14/2009	In Force	ENABLING INCREASED PRINT SPEED BY ELIMINATING NOZZLE FIRING
MP1119	MP1119CN	CN	200610127592.1	9/19/2006	212006101275	3/21/2012	In Force	ENABLING INCREASED PRINT SPEED BY ELIMINATING NOZZLE FIRING

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Family	JP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP1119J	MP1119JP	JP	2006-249285	9/14/2006	5480460	2/21/2014	In Force	ENABLING INCREASED PRINT SPEED BY ELIMINATING NOZZLE FIRING
MP1119J	MP1119PDI	JP	2013-124041	6/12/2013			Pending	INK SUPPLY FOR A HAND-HELD INK JET PRINTER
MP1120	MP1120CN	CN	CN2006101409	10/17/2006			Pending	SYSTEM AND METHOD FOR INDICATING POSITION OF A MOVEABLE
MP1120J	MP1120JP	JP	2006-274156	10/5/2006			Pending	SYSTEM AND METHOD FOR INDICATING POSITION OF A MOVEABLE
MP1121	MP1121DI	US	12/915,770	10/29/2010	8100510	1/24/2012	In Force	INKJET PRINTHEAD SYSTEM AND METHOD USING LASER-BASED HEAT
MP1122	MP1122CI	US	12/836,758	7/15/2010	8333324	12/18/2012	In Force	METHODS AND SYSTEMS FOR TRANSFERRING INFORMATION BETWE
MP1122	MP1122CIaa	US	14/094,345	12/2/2013	9167115	10/20/2015	In Force	METHODS AND SYSTEMS FOR TRANSFERRING INFORMATION BETW
MP1124	MP1124CI	US	12/643,634	12/21/2009	7918568	4/5/2011	In Force	LID STRUCTURE, APPARATUS AND METHOD FOR DISPLAYING GRAPH
MP1125	MP1125	US	11/400,082	4/7/2006	7798407	9/21/2010	In Force	SCANNING APPARATUS AND METHOD FOR ENTERING A USER INPUT
MP1125	MP1125CI	US	12/871,681	8/30/2010	8486176	7/30/2013	In Force	SCANNING APPARATUS AND METHOD FOR ENTERING A USER INPUT
MP1126	MP1126CI	US	13/225,011	9/2/2011	8223407	7/17/2012	In Force	SHEET MEDIA STACK IMAGING SYSTEM
MP1126	MP1126CIc1	US	13/547,891	7/12/2012	8514469	8/20/2013	In Force	SHEET MEDIA STACK IMAGING SYSTEM
MP1134	MP1134	US	11/741,380	4/27/2007	7535305	5/19/2009	In Force	QUIESCENT CURRENT DETECTING CIRCUIT FOR CLASS AB AMPLIFIER
MP1135	MP1135CI	US	12/842,687	7/23/2010	8085486	12/27/2011	In Force	METHOD FOR INTERFACING NON-VOLATILE MEDIUM CONTROL SYST
MP1138	MP1138	US	11/803,781	5/16/2007	8332201	12/11/2012	In Force	INNOVATIVE VERIFICATION METHODOLOGY FOR DEEPLY EMBEDDED
MP1139	MP1139	US	11/789,170	4/24/2007	7474153	1/6/2009	In Force	Dual Stage Source/Sink Amplifier Circuit with Quiescent Current Dete
MP1139	MP1139CI	US	12/337,955	12/18/2008	7764122	7/27/2010	In Force	DUAL STAGE SOURCE / SINK AMPLIFIER CIRCUIT WITH QUIESCENT C
MP1140	MP1140CI	US	12/730,954	3/24/2010	811436	2/21/2012	In Force	METHODS AND SYSTEMS FOR IMAGE SCALING
MP1140	MP1140CIc1	US	13/399,411	2/17/2012	8306363	11/6/2012	In Force	METHODS AND SYSTEMS FOR IMAGE SCALING
MP1141	MP1141CI	US	12/714,833	3/1/2010	8214706	7/3/2012	In Force	Method And Apparatus For Testing An Electronic Circuit Integrated
MP1141J	MP1141JP	JP	2006-176036	6/27/2006	5179726	1/18/2013	In Force	SEMICONDUCTOR DEVICE
MP1142	MP1142	US	11/789,867	4/25/2007	7836364	11/16/2010	In Force	Circuits, Architectures, Apparatuses, Systems, Methods, Algorithms,
MP1142	MP1142CI	US	12/916,874	11/1/2010	8117510	2/14/2012	In Force	Circuits, Architectures, Apparatuses, Systems, Methods, Algorithms,
MP1142	MP1142CIaa	US	13/608,748	9/10/2012	8435442	7/23/2013	In Force	Circuits, Architectures, Apparatuses, Systems, Algorithms, Software A
MP1142	MP1142CIc1	US	13/371,890	2/13/2012	8266484	9/11/2012	In Force	Circuits, Architectures, Apparatuses, Systems, Methods, Algorithms,
MP1142	MP1142CI2a	US	13/947,836	7/22/2013	8745453	6/3/2014	In Force	Circuits, Architectures, Apparatuses, Systems, Methods, Algorithms,
MP1144	MP1144	US	11/755,665	5/30/2007	7986445	7/26/2011	In Force	PRINT AND SCAN SHARING PAPER PICK AND PAPER MOTION CONTR
MP1144W	MP1144WO	WO	PCT/US2007/01	6/1/2007			Pending	PRINT AND SCAN SHARING PAPER PICK AND PAPER MOTION CONTR
MP1145W	MP1145WO	WO	PCT/US2007/01	6/1/2007			Pending	FLOATING SCAN SENSOR IN A MULTI-FUNCTION PRINTER
MP1146	MP1146CI	US	12/861,887	12/7/2010	8164363	4/24/2012	In Force	ASYMMETRIC SENSE-AMP FLIP-FLOP
MP1147	MP1147	US	11/655,670	1/19/2007	8359416	1/22/2013	In Force	Multi-Mode USB-Satellite Digital Transceiver Port
MP1148	MP1148	US	11/810,762	6/7/2007	8074126	12/6/2011	In Force	Non-Intrusive Eye Monitor System
MP1148	MP1148CI	US	13/300,354	11/18/2011	8327195	12/4/2012	In Force	Non-Intrusive Eye Monitor System
MP1148	MP1148CIc1	US	13/693,579	12/4/2012	8634837	4/8/2014	In Force	NON-INTRUSIVE EYE MONITOR SYSTEM

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP1150	MP1150	US	11/810,832	6/6/2007	7974034	7/5/2011	In Force	SYSTEMS AND METHODS FOR SYNC MARK DETECTION USING CORRE
MP1150	MP1150C1	US	13/160,068	6/14/2011	8279546	10/2/2012	In Force	SYSTEMS AND METHODS FOR SYNC MARK DETECTION USING CORRE
MP1151	MP1151C1	US	12/972,733	12/20/2010	8331206	12/11/2012	In Force	CALIBRATION BASED DC COUPLED ANALOG FRONT END FOR OPTICA
MP1152	MP1152	US	11/752,261	5/22/2007	7719679	6/1/2010	In Force	DYNAMIC BANDWIDTH FILTER FOR AN FM RECEIVER
MP1154	MP1154	US	11/801,317	5/9/2007	7535110	5/19/2009	In Force	Stack Die Packages
MP1154	MP1154C1	US	12/494,264	5/1/2009	7825521	11/2/2010	In Force	Stack Die Packages
MP1154	MP1154WCEPDE	DE	07809502.3	6/13/2007	602007059371	10/16/2019	In Force	Stack Die Packages
MP1154	MP1154WCEPFR	FR	07809502.3	6/13/2007	20832220	10/16/2019	In Force	Stack Die Packages
MP1154	MP1154WCEPGB	GB	07809502.3	6/13/2007	20832220	10/16/2019	In Force	Stack Die Packages
MP1154T	MP1154TW	TW	96121889	6/15/2007	1429050	3/1/2014	In Force	STACK DIE PACKAGES
MP1154W	MP1154WO	WO	PCT/US2007/01	6/13/2007			Pending	Stack Die Packages
MP1154W	MP1154WOCN	CN	2007800304871	6/13/2007	ZL2007800304	4/6/2011	In Force	STACK DIE PACKAGES
MP1154W	MP1154WCEP	EP	07809502.3	6/13/2007			Pending	Stack Die Packages
MP1154W	MP1154WOJP	JP	2009-515464	6/13/2007	5320611	7/26/2013	In Force	STACK DIE PACKAGES
MP1157	MP1157	US	11/803,122	5/11/2007	8284411	10/9/2012	In Force	FASTER MONOCHROME PRINTING FOR IN-LINE ELECTROPHOTOGRA
MP1157	MP1157aaa	US	14/589,580	1/5/2015	9330348	5/3/2016	In Force	FASTER MONOCHROME PRINTING FOR IN-LINE ELECTROPHOTOGRA
MP1157	MP1157C1	US	13/613,465	9/13/2012	8462363	6/11/2013	In Force	FASTER MONOCHROME PRINTING FOR IN-LINE ELECTROPHOTOGRA
MP1158	MP1158C1	US	14/170,188	1/31/2014	9532716	1/10/2017	In Force	NON RASTER ROW PIXEL PROCESSING
MP1160	MP1160	US	11/810,667	6/6/2007	7876746	1/25/2011	In Force	Remote Management For Network Switches
MP1160	MP1160I2	US	11/893,424	8/16/2007	7852768	12/14/2010	In Force	Physical-Layer Device Management For Network Switches
MP1161	MP1161	US	11/801,845	5/11/2007	8161207	4/17/2012	In Force	COMMON BLOCK INTERFACE FOR DATA AND CONTROL WITH HANDS
MP1161	MP1161C1	US	13/446,065	4/13/2012	8443121	5/14/2013	In Force	COMMON BLOCK INTERFACE FOR DATA AND PROTOCOL WITH HAND
MP1163	MP1163	US	11/824,046	6/29/2007	8111259	2/7/2012	In Force	IMAGE PROCESSING APPARATUS HAVING CONTEXT MEMORY CONTR
MP1163	MP1163C1	US	13/287,666	11/2/2011	8234720	10/23/2012	In Force	IMAGE PROCESSING APPARATUS HAVING CONTEXT MEMORY CONTR
MP1164	MP1164C1	US	12/727,088	3/18/2010	8046518	10/25/2011	In Force	USB ISOCRONOUS DATA TRANSFER FOR A HOST BASED LASER PRIN
MP1165	MP1165C1	US	13/023,251	2/8/2011	8762596	6/24/2014	In Force	DIRECT MEMORY ACCESS CONTROLLER WITH MULTIPLE TRANSACTI
MP1167	MP1167C1	US	12/717,587	3/4/2010	7987301	7/26/2011	In Force	DMA CONTROLLER EXECUTING MULTIPLE TRANSACTIONS AT NON-C
MP1168	MP1168	US	11/400,600	4/7/2006	7814329	10/12/2010	In Force	MACsec Implementation
MP1168	MP1168C1	US	12/902,504	10/12/2010	8250370	8/21/2012	In Force	MACSEC IMPLEMENTATION
MP1168	MP1168C1aa	US	14/035,509	9/24/2013	8935534	1/13/2015	In Force	Macsec Implementation
MP1168	MP1168C1C1	US	13/590,979	8/21/2012	8543826	9/24/2013	In Force	MACSEC IMPLEMENTATION
MP1169	MP1169	US	11/818,079	6/12/2007	8027378	9/27/2011	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP1170	MP1170C1	US	12/623,854	11/23/2009	7834800	11/16/2010	In Force	Systems And Methods For Detecting Radar
MP1171	MP1171	US	11/768,622	6/26/2007	8971325	3/3/2015	In Force	Policy System and Method for a Switching Device

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MP1172	MP1172	US	11/768,613	6/26/2007	7782770	8/24/2010	In Force	SYSTEM AND METHOD OF CROSS-CHIP FLOW CONTROL
MP1172	MP1172C1	US	12/860,801	8/20/2010	8085658	12/27/2011	In Force	System and Method of Cross-Chip Flow Control
MP1173	MP1173	US	11/759,062	6/6/2007	8379644	2/19/2013	In Force	System and Method of Processing Management Frames
MP1173	MP1173D1	US	13/768,049	2/15/2013	8879559	11/4/2014	In Force	System and Method of Processing Management Frames
MP1173	MP1173D1C1	US	14/526,954	10/29/2014	9131287	9/8/2015	In Force	System and Method of Processing Management Frames
MP1174	MP1174	US	11/823,588	6/28/2007	8339689	12/25/2012	In Force	EXPOSURE CORRECTION FOR SCANNERS
MP1175	MP1175	US	11/591,325	11/1/2006	7634686	12/15/2009	In Force	File Server For Redundant Array Of Independent Disks (RAID) System
MP1175	MP1175C1	US	11/724,549	3/15/2007	8046629	10/25/2011	In Force	File Server For Redundant Array Of Independent Disks (RAID) System
MP1175	MP1175C1C1	US	13/279,971	10/24/2011	8495416	7/23/2013	In Force	File Server For Redundant Array Of Independent Disks (RAID) System
MP1175	MP1175C2	US	12/637,119	12/14/2009	8066127	8/23/2011	In Force	File Server For Redundant Array Of Independent Disks (RAID) System
MP1175W	MP1175WCN3	CN	201210284451	7/17/2007	ZL2012102844	1/6/2016	In Force	FILE SERVER FOR REDUNDANT ARRAY OF INDEPENDENT DISKS (RAID)
MP1175W	MP1175WO	WO	PCT/US2007/01	7/17/2007			Pending	File Server For Redundant Array Of Independent Disks (RAID) System
MP1175W	MP1175WOCN	CN	200780035418,	7/17/2007	ZL2007800354	9/19/2012	In Force	FILE SERVER FOR REDUNDANT ARRAY OF INDEPENDENT DISKS (RAID)
MP1175W	MP1175WOJP	JP	2009-521760	7/17/2007	5124792	11/9/2012	In Force	FILE SERVER FOR REDUNDANT ARRAY OF INDEPENDENT DISKS (RAID)
MP1177	MP1177	US	11/765,275	6/19/2007	7895488	2/22/2011	In Force	CONTROL OF CLOCK GATE CELLS DURING SCAN TESTING
MP1177	MP1177C1	US	13/014,921	1/27/2011	8443246	5/14/2013	In Force	CONTROL OF CLOCK GATE CELLS DURING SCAN TESTING
MP1177	MP1177C1C1	US	13/892,700	5/13/2013	8689067	4/1/2014	In Force	CONTROL OF CLOCK GATE CELLS DURING SCAN TESTING
MP1179	MP1179C1	US	13/008,160	1/18/2011	9252119	2/2/2016	In Force	BALL GRID ARRAY INCLUDING REDISTRIBUTION LAYER, PACKAGED IN
MP1182	MP1182	US	11/774,979	7/9/2007	8036625	10/11/2011	In Force	METHOD AND APPARATUS FOR MIXING A SIGNAL
MP1183	MP1183	US	11/773,928	7/5/2007	8121662	2/21/2012	In Force	VIRTUAL FM ANTENNA
MP1183T	MP1183TW	TW	96127454	7/27/2007	1451627	9/1/2014	In Force	VIRTUAL FM ANTENNA
MP1184	MP1184	US	11/820,181	6/18/2007	7788343	8/3/2010	In Force	START-UP CIRCUIT FOR BANDGAP REFERENCE
MP1185	MP1185C1	US	13/029,574	2/17/2011	8289334	9/18/2012	In Force	MULTICHIP PACKAGE LEADFRAME INCLUDING ELECTRICAL BUSSING
MP1186	MP1186	US	11/775,757	7/10/2007	7525460	4/28/2009	In Force	TIMING LOOP BASED ON ANALOG TO DIGITAL CONVERTER OUTPUT
MP1186	MP1186L1	US	12/019,430	1/24/2008	7825836	11/2/2010	In Force	LIMIT EQUALIZER OUTPUT BASED TIMING LOOP
MP1186	MP1186L1C1	US	12/877,779	9/8/2010	8274413	9/25/2012	In Force	LIMIT EQUALIZER OUTPUT BASED TIMING LOOP
MP1187	MP1187L1	US	12/359,855	1/26/2009	8358566	1/22/2013	In Force	METHOD AND DEVICE FOR DETECTING A SYNC MARK
MP1187	MP1187L1FZ	US	14/153,784	1/13/2014	9019804	4/28/2015	In Force	TIMING LOOP WITH LARGE PULL-IN RANGE
MP1187	MP1187L1L1	US	12/625,844	11/25/2009	8264933	11/9/2012	In Force	TIMING LOOP WITH LARGE PULL-IN RANGE
MP1188	MP1188C1	US	12/947,690	11/16/2010	8054320	11/8/2011	In Force	HORIZONTAL STRIP COLUMN-FIRST TWO-DIMENSIONAL SCALING
MP1189	MP1189	US	11/654,841	1/18/2007	8099629	1/17/2012	In Force	System-On-A-Chip (SOC) Test Interface Security
MP1189	MP1189C1	US	13/351,040	1/16/2012	8601321	12/3/2013	In Force	System-On-A-Chip (SOC) Test Interface Security
MP1189T	MP1189TW	TW	96125663	7/13/2007	1434156	4/11/2014	In Force	SYSTEM-ON-A-CHIP(SOC) TEST INTERFACE SECURITY
MP1189W	MP1189WD1	JP	2012-061847	3/19/2012	5382555	10/11/2013	In Force	SYSTEM-ON-A-CHIP (SOC) TEST INTERFACE SECURITY

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MP1189W	MP1189WO	WO	PCT/US2007/01	7/11/2007			Pending	System-On-A-Chip (SOC) Test Interface Security
MP1189W	MP1189WOCN	CN	2007800343501	7/11/2007	ZL2007800343	5/30/2012	In Force	SYSTEM-ON-A-CHIP (SOC) TEST INTERFACE SECURITY
MP1189W	MP1189WCEP	EP	EP20070810322	7/11/2007	2041688	12/19/2018	In Force	System-On-A-Chip (SOC) Test Interface Security
MP1189W	MP1189WCEPDE	DE	EP20070810322	7/11/2007	60200705717	12/19/2018	In Force	System-On-A-Chip (SOC) Test Interface Security
MP1189W	MP1189WCEPFR	FR	EP20070810322	7/11/2007	2041688	12/19/2018	In Force	System-On-A-Chip (SOC) Test Interface Security
MP1189W	MP1189WCEPGB	GB	EP20070810322	7/11/2007	2041688	12/19/2018	In Force	System-On-A-Chip (SOC) Test Interface Security
MP1189W	MP1189WQJP	JP	2009-519507	7/11/2007	4962993	4/6/2012	In Force	SYSTEM-ON-A-CHIP (SOC) TEST INTERFACE SECURITY
MP1191	MP1191	US	11/771,944	6/29/2007	8503503	8/6/2013	In Force	Agile Frequency Synthesizer Scheme For Fast Hopping, Low Phase Noise
MP1195	MP1195	US	11/777,758	7/13/2007	7929406	4/19/2011	In Force	POWER SAVING CIRCUIT
MP1195	MP1195C1	US	13/073,007	3/28/2011	8115336	2/14/2012	In Force	POWER SAVING CIRCUIT
MP1197	MP1197	US	11/712,687	2/28/2007	7511549	3/31/2009	In Force	COMPACT HIGH-SPEED, HIGH-RESOLUTION COMPARATOR STRUCTURE
MP1198	MP1198	US	12/012,426	2/1/2008	7907362	3/15/2011	In Force	MAGNETIC DISK CONTROLLER AND METHOD
MP1198J	MP1198JP	JP	2007-023005	2/1/2007	5221044	3/15/2013	In Force	MAGNETIC DISK CONTROLLER AND METHOD
MP1199	MP1199C1	US	13/907,645	5/31/2013	8719685	5/6/2014	In Force	MAGNETIC DISK CONTROLLER AND METHOD
MP1199	MP1199	US	12/024,914	2/1/2008	8458580	6/4/2013	In Force	MAGNETIC DISK CONTROLLER AND METHOD
MP1199J	MP1199JP	JP	2007-023006	2/1/2007	JP5186112	1/25/2013	In Force	MAGNETIC DISK CONTROLLER AND METHOD
MP1207	MP1207	US	08/971,387	11/17/1997	6055285	4/25/2000	In Force	SYNCHRONIZATION CIRCUIT FOR TRANSFERRING POINTER BETWEEN
MP1208	MP1208	US	08/972,117	11/17/1997	6163550	12/19/2000	In Force	STATE DEPENDENT SYNCHRONIZATION CIRCUIT WHICH SYNCHRONIZES
MP1209	MP1209	US	09/030,865	2/26/1998	6151295	11/21/2000	In Force	OFDM RECEIVING SYSTEM
MP1209	MP1209WOJP	JP	2000-533973	2/25/1999			Pending	OFDM FRAME SYNCHRONISATION AND EQUALISATION SYSTEM
MP1209	MP1209P	JP	2007-023007	2/1/2007	JP5064820	8/17/2012	In Force	MAGNETIC DISK CONTROLLER AND METHOD
MP1201	MP1201	US	12/012,321	2/1/2008	7639443	12/29/2009	In Force	MAGNETIC DISK CONTROLLER AND METHOD
MP1201	MP1201C1	US	12/634,300	12/9/2009	8284511	10/9/2012	In Force	MAGNETIC DISK CONTROLLER TO PROCESS PLURAL DATA SETS FOR RECORDING
MP1201	MP1201C1C1	US	13/615,287	9/13/2012	8514507	8/20/2013	In Force	PROCESSING DATA FOR RECORDING TO A STORAGE DEVICE
MP12010	MP12010	US	09/104,731	6/25/1998	6167321	12/26/2000	In Force	INTERFACE MODULE WITH PROTECTION CIRCUIT AND METHOD OF PROVIDING
MP12011	MP12011	US	09/386,109	8/30/1999	6597691	7/22/2003	In Force	HIGH PERFORMANCE SWITCHING
MP12013	MP12013	US	09/249,873	2/16/1999	6317842	11/13/2001	In Force	METHOD AND CIRCUIT FOR RECEIVING DUAL EDGE CLOCKED DATA
MP12014	MP12014	US	09/257,803	2/25/1999	6229845	5/8/2001	In Force	BUS DRIVER WITH DATA DEPENDENT DRIVE STRENGTH CONTROL LOGIC
MP12015	MP12015	US	09/280,566	3/30/1999	6470397	10/22/2002	In Force	SYSTEMS AND METHODS FOR NETWORK AND I/O DEVICE DRIVERS
MP12016	MP12016	US	09/281,369	3/30/1999	6327635	4/12/2001	In Force	ADD-ON CARD WITH AUTOMATIC BUS POWER LINE SELECTION CIRCUIT
MP12017	MP12017	US	09/282,341	3/31/1999	6434630	8/13/2002	In Force	HOT ADAPTER FOR COMBINING I/O COMPLETION REPORTS AND MANAGING
MP12018	MP12018	US	09/328,738	6/9/1999	6564271	5/13/2003	In Force	METHOD AND APPARATUS FOR AUTOMATICALLY TRANSFERRING I/O
MP12019	MP12019	US	09/346,793	2/7/1999	6667359	2/24/2004	In Force	HIGH PERFORMANCE SWITCH FABRIC ELEMENT AND SWITCH SYSTEM
MP12019	MP12019C1	US	10/752,390	1/6/2004	7408927	8/5/2008	In Force	HIGH PERFORMANCE SWITCH FABRIC ELEMENT AND SWITCH SYSTEM

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MP12011	MP1201JP	JP	2007-023008	2/1/2007	JP5064821	8/17/2012	In Force	MAGNETIC DISC CONTROLLER AND METHOD
MP1202	MP1202	US	11/724,882	3/16/2007	8112209	2/23/2012	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP1202	MP1202C1	US	13/354,527	1/20/2012	8326509	11/27/2012	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP1202	MP1202C1C1	US	13/663,910	11/21/2012	8670507	3/11/2014	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP1202	MP1202CN	CN	200780035614.1	7/23/2007	ZL2007800356	5/28/2014	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP12022	MP12022WOUS	US	10/166,570	6/10/2002	7443794	10/28/2008	In Force	FIBRE CHANNEL CREDIT EXTENDER AND REPEATER
MP12023	MP12023	US	09/587,689	6/5/2000	6768218	3/16/2004	In Force	IPSEC PERFORMANCE ENHANCEMENT USING A HARDWARE-BASED
MP12024	MP12024C1	US	11/765,724	6/20/2007	7664398	3/23/2010	In Force	HARDWARE-ENFORCED LOOP-LEVEL HARD ZONING FOR FIBRE CHAN
MP12027	MP12027	US	09/755,642	1/4/2001	6883099	4/19/2005	In Force	SECURE VIRTUAL INTERFACE
MP12027	MP12027C1	US	11/070,858	3/1/2005	7389399	6/17/2008	In Force	SECURE VIRTUAL INTERFACE
MP12028	MP12028	US	09/792,428	2/21/2001	6976174	12/13/2005	In Force	SECURE MULTIPROTOCOL INTERFACE
MP12029	MP12029	US	10/025,512	12/19/2001	6789147	9/7/2004	In Force	INTERFACE FOR A SECURITY COPROCESSOR
MP12029	MP12029EP	EP	02016424.0	7/22/2002	1282025	7/4/2018	In Force	AN INTERFACE FOR A SECURITY COPROCESSOR
MP12029	MP12029EPDE	DE	02016424.0	7/22/2002	60249486.6	7/4/2018	In Force	AN INTERFACE FOR A SECURITY COPROCESSOR
MP12029	MP12029JP	JP	2002-215500	7/24/2002	4298971	7/22/2009	In Force	INTERFACE FOR SECURITY COPROCESSOR
MP12029	MP12029TW	TW	091116472	7/24/2002	576963	2/21/2004	In Force	AN INTERFACE FOR A SECURITY COPROCESSOR
MP1202E	MP1202EP	EP	07796997.0	7/23/2007	EP2047626	7/5/2017	In Force	Concatenation-Assisted Symbol-Level Combining for MIMO Systems
MP1202E	MP1202EPDE	DE	07796997.0	7/23/2007	6.02007E+11	7/5/2017	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP1202E	MP1202EPFR	FR	07796997.0	7/23/2007	2047626	7/5/2017	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP1202E	MP1202EPGB	GB	07796997.0	7/23/2007	2047626	7/5/2017	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP1202T	MP1202TW	TW	96127135	7/25/2007	1442725	6/21/2014	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP1202W	MP1202WO	WO	PCT/US2007/01	7/23/2007			Pending	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP12030	MP12030	US	09/938,166	8/23/2001	6954770	10/11/2005	In Force	RANDOM NUMBER GENERATOR
MP12031	MP12031	US	10/120,266	10/18/2001	7290144	4/3/2007	In Force	ROUTER AND METHODS USING NETWORK ADDRESSES FOR VIRTUALI
MP12031	MP12031D1	US	10/284,273	10/29/2002	7362702	4/22/2008	In Force	ROUTER WITH ROUTING PROCESSORS AND METHODS FOR VIRTUALI
MP12031	MP12031D3	US	10/285,226	10/31/2002	7292567	11/6/2007	In Force	ROUTER AND METHODS FOR DISTRIBUTED VIRTUALIZATION
MP12031	MP1203111	US	11/098,831	4/4/2005	7447197	11/4/2008	In Force	SYSTEM AND METHOD OF PROVIDING NETWORK NODE SERVICES
MP12032	MP12032	US	09/992,637	11/6/2001	7376811	5/20/2008	In Force	METHOD AND APPARATUS FOR PERFORMING COMPUTATIONS AND
MP12034	MP12034	US	10/025,509	12/19/2001	7240203	7/3/2007	In Force	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSIONS
MP12034	MP12034DE	DE	02016423.2	7/22/2002	1292082	12/19/2018	In Force	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSION
MP12034	MP12034EP	EP	02016423.2	7/22/2002	1292082	12/19/2018	In Force	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSION
MP12034	MP12034FR	FR	02016423.2	7/22/2002	1292082	12/19/2018	In Force	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSION
MP12034	MP12034GB	GB	02016423.2	7/22/2002	1292082	12/19/2018	In Force	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSION
MP12034	MP12034JP	JP	2002-215552	7/24/2002			Pending	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSIONS

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MP12034	MP12034TW	TW	091116476	7/24/2002	1243566	11/11/2005	In Force	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSION
MP12035	MP12035	US	10/068,677	2/6/2002	7035889	4/25/2006	In Force	METHOD AND APPARATUS FOR MONTGOMERY MULTIPLICATION
MP12036	MP12036	US	10/052,054	1/17/2002	7076059	7/11/2006	In Force	METHOD AND APPARATUS TO IMPLEMENT THE DATA ENCRYPTION S
MP12037	MP12037	US	10/086,665	2/28/2002	7086292	8/22/2006	In Force	On-Chip Inter-Subsystem Communication
MP12037	MP12037C1	US	11/422,373	6/6/2006	7243179	7/10/2007	In Force	On-Chip Inter-Subsystem Communication
MP12037	MP12037WO3US	US	10/469,467	4/5/2004	7436954	10/14/2008	In Force	Security System With An Intelligent Dma Controller
MP12037	MP12037WOU5	US	10/469,529	8/28/2003	7653763	1/26/2010	In Force	SUBSYSTEM BOOT AND PERIPHERAL DATA TRANSFER ARCHITECTUR
MP12038	MP12038	US	10/092,328	3/6/2002	7260217	8/21/2007	In Force	SPECULATIVE EXECUTION FOR DATA CIPHERING OPERATIONS
MP12039	MP12039	US	10/224,895	9/4/2002	7365567	12/4/2007	In Force	DECOUPLED ARCHITECTURE FOR DATA CIPHERING OPERATIONS
MP1204	MP1204	US	11/821,231	6/21/2007	7984367	7/19/2011	In Force	METHOD FOR ITERATIVE DECODING IN THE PRESENCE OF BURST ERR
MP12040	MP12040	US	10/186,291	6/28/2002	6988150	1/17/2006	In Force	SYSTEM AND METHOD FOR EVENTLESS DETECTION OF NEWLY DELIV
MP12041	MP12041	US	10/600,779	6/20/2003	6864814	3/8/2005	In Force	SYSTEM AND METHOD FOR IMPROVING DYNAMIC RANGE OF ANALO
MP12044	MP12044	US	10/200,393	7/22/2002	7230929	12/6/2007	In Force	METHOD AND SYSTEM FOR DYNAMICALLY ASSIGNING DOMAIN IDEN
MP12044	MP12044-2	US	10/200,487	7/22/2002	7154886	12/26/2006	In Force	METHOD AND SYSTEM FOR PRIMARY BLADE SELECTION IN A MULTI-
MP12044	MP12044-2C1	US	11/608,634	12/8/2006	7388861	6/17/2008	In Force	METHOD AND SYSTEM FOR PRIMARY BLADE SELECTION IN A MULTI-
MP12045	MP12045	US	10/212,425	8/5/2002	7334046	2/19/2008	In Force	SYSTEM AND METHOD FOR OPTIMIZING FRAME ROUTING IN A NET
MP12046	MP12046	US	10/213,237	8/6/2002	7263108	8/28/2007	In Force	DUAL-MODE NETWORK STORAGE SYSTEMS AND METHODS
MP12046	MP12046C1	US	11/845,688	8/27/2007	7688867	3/30/2010	In Force	DUAL-MODE NETWORK STORAGE SYSTEMS AND METHODS
MP12048	MP12048	US	10/241,153	11/9/2002	7397768	8/7/2008	In Force	ZONE MANAGEMENT IN A MULTI-MODULE FIBRE CHANNEL SWITCH
MP12048	MP12048C1	US	11/682,199	3/5/2007	7729288	6/1/2010	In Force	ZONE MANAGEMENT IN A MULTI-MODULE FIBRE CHANNEL SWITCH
MP12049	MP12049	US	10/203,858	3/10/2002	7362717	4/22/2008	In Force	METHOD AND SYSTEM FOR USING DISTRIBUTED NAME SERVERS IN
MP12050	MP12050	US	10/295,976	11/15/2002	6769596	8/3/2004	In Force	METHOD AND SYSTEM FOR REWORKING BALL GRID ARRAYS
MP12053	MP12053	US	10/375,643	2/27/2003	6810440	10/26/2004	In Force	METHOD AND APPARATUS FOR AUTOMATICALLY TRANSFERRING I/O
MP12054	MP12054	US	10/387,151	3/11/2003	6861865	3/1/2005	In Force	APPARATUS AND METHOD FOR REPAIRING LOGIC BLOCKS
MP12054	MP12054D1	US	11/040,266	1/20/2005	7205785	4/17/2007	In Force	APPARATUS AND METHOD FOR REPAIRING LOGIC BLOCKS
MP12055	MP12055	US	10/387,083	3/26/2003	7209531	4/24/2007	In Force	APPARATUS AND METHOD FOR DATA DESKEW
MP12056	MP12056	US	10/410,773	4/10/2003	6959345	10/25/2005	In Force	METHOD AND SYSTEM FOR CALIBRATING SCSI EXPANDERS
MP12057	MP12057	US	10/411,909	4/12/2003	738386	7/8/2008	In Force	TRANSPARENT IPSEC PROCESSING INLINE BETWEEN A FRAMER AND
MP12057	MP12057IPD1	JP	2010-131757	6/9/2010	5074558	11/14/2012	In Force	Network processing using IPsec
MP12057	MP12057WO	WO	PCT/US2004/00	3/30/2004			Pending	TRANSPARENT IPSEC PROCESSING INLINE BETWEEN A FRAMER AND
MP12057	MP12057WOEP	EP	04749529.6	3/30/2004	1614250	8/26/2015	In Force	TRANSPARENT IPSEC PROCESSING INLINE BETWEEN A FRAMER AND
MP12057	MP12057WOEPDE	DE	04749529.6	3/30/2004	602004047762	8/26/2015	In Force	TRANSPARENT IPSEC PROCESSING INLINE BETWEEN A FRAMER AND
MP12057	MP12057WOEPFR	FR	04749529.6	3/30/2004	1614250	8/26/2015	In Force	TRANSPARENT IPSEC PROCESSING INLINE BETWEEN A FRAMER AND
MP12057	MP12057WOJP	JP	2006-509485	3/30/2004			Pending	TRANSPARENT IPSEC PROCESSING INLINE BETWEEN A FRAMER AND

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MP12058	MP12058	US	10/411,944	4/12/2003	7661130	2/9/2010	In Force	APPARATUS AND METHOD FOR ALLOCATING RESOURCES WITHIN A S
MP12059	MP12059	US	10/411,967	4/12/2003	7814310	10/12/2010	In Force	IPSEC PERFORMANCE OPTIMIZATION
MP12059	MP12059WO	WO	PCT/US2004/00	3/31/2004	DNA		Pending	IPSEC PERFORMANCE OPTIMIZATION
MP12060	MP12060	US	10/411,943	4/12/2003	7657933	2/2/2010	In Force	APPARATUS AND METHOD FOR ALLOCATING RESOURCES WITHIN A S
MP12060	MP12060WO	WO	PCT/US2004/01	4/8/2004	DNA		Pending	AN APPARATUS AND METHOD FOR ALLOCATING RESOURCES WITHIN
MP12061	MP12061	US	10/411,945	4/12/2003	7337314	2/26/2008	In Force	APPARATUS AND METHOD FOR ALLOCATING RESOURCES WITHIN A S
MP12061	MP12061WO	WO	PCT/US2004/01	4/7/2004	DNA		Pending	AN APPARATUS AND METHOD FOR ALLOCATING RESOURCES WITHIN
MP12062	MP12062	US	10/417,523	4/17/2003	6961784	11/1/2005	In Force	METHOD AND SYSTEM FOR PROCESSING NON-DATA FRAMES IN HO
MP12063	MP12063	US	10/423,499	4/25/2003	6996785	2/7/2006	In Force	ON-CHIP PACKET-BASED INTERCONNECTIONS USING REPEATERS/RO
MP12063	MP12063C1	US	11/283,210	11/18/2005	7543250	6/2/2009	In Force	ON-CHIP PACKET-BASED INTERCONNECTIONS USING REPEATERS/RO
MP12065	MP12065	US	10/445,695	5/27/2003	7424419	9/9/2008	In Force	METHOD AND SYSTEM FOR PROVIDING A VIRTUAL UNIVERSAL SERIA
MP12066	MP12066	US	10/601,534	6/24/2003	7352778	4/1/2008	In Force	SYNCHRONIZING METHOD AND APPARATUS
MP12066	MP12066CA	CA	2433139	6/23/2003	2433139	3/27/2012	In Force	SYNCHRONIZING METHOD AND APPARATUS
MP12067	MP12067	US	10/619,719	7/15/2003	7515612	4/7/2009	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK DATA PACKETS
MP12067	MP12067C1	US	12/389,308	2/19/2009	7830919	11/9/2010	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK DATA PACKETS
MP12068	MP12068	US	10/620,040	7/15/2003	7403542	7/22/2008	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK DATA PACKETS
MP12070	MP12070	US	10/869,551	7/12/2004	7388843	6/17/2008	In Force	METHOD AND APPARATUS FOR TESTING LOOP PATHWAY INTEGRITY
MP12071	MP12071	US	10/869,256	7/12/2004	7620059	11/17/2009	In Force	METHOD AND APPARATUS FOR ACCELERATING RECEIVE-MODIFY-SE
MP12072	MP12072	US	10/869,259	7/12/2004	7453802	11/18/2008	In Force	METHOD AND APPARATUS FOR DETECTING AND REMOVING ORPHA
MP12072	MP12072C1	US	12/198,644	8/26/2008	7542418	6/2/2009	In Force	METHOD AND APPARATUS FOR DETECTING AND REMOVING ORPHA
MP12073	MP12073	US	10/869,337	7/12/2004	7355966	4/8/2008	In Force	METHOD AND SYSTEM FOR MINIMIZING DISRUPTION IN COMMON-
MP12074	MP12074	US	10/869,635	7/12/2004	7525910	4/28/2009	In Force	METHOD AND SYSTEM FOR NON-DISRUPTIVE DATA CAPTURE IN NET
MP12075	MP12075	US	10/869,588	7/12/2004	7152132	12/19/2006	In Force	METHOD AND APPARATUS FOR IMPROVING BUFFER UTILIZATION IN
MP12076	MP12076	US	10/894,827	7/20/2004	7525983	4/28/2009	In Force	METHOD AND SYSTEM FOR SELECTING VIRTUAL LANES IN FIBRE CHA
MP12076	MP12076D1	US	12/427,966	4/22/2009	8081650	12/20/2011	In Force	METHOD AND SYSTEM FOR SELECTING VIRTUAL LANES IN FIBRE CHA
MP12077	MP12077	US	10/889,267	7/12/2004	7461646	12/9/2008	In Force	METHOD AND SYSTEM FOR FIBRE CHANNEL ARBITRATED LOOP ACCE
MP12078	MP12078	US	10/889,255	7/12/2004	7471635	12/30/2008	In Force	METHOD AND APPARATUS FOR TEST PATTERN GENERATION
MP1208	MP1208C1	US	14/610,822	3/30/2015	9361556	6/7/2016	In Force	PULSE WIDTH MODULATOR FOR A PRINTING DEVICE
MP12080	MP12080	US	10/894,492	7/20/2004	7466709	12/16/2008	In Force	LUN BASED HARD ZONING IN FIBRE CHANNEL SWITCHES
MP12081	MP12081	US	10/895,175	7/20/2004	7522529	4/21/2009	In Force	METHOD AND SYSTEM FOR DETECTING CONGESTION AND OVER SUB
MP12082	MP12082	US	10/674,943	9/30/2003	7234101	6/19/2007	In Force	METHOD AND SYSTEM FOR PROVIDING DATA INTEGRITY IN STORAG
MP12083	MP12083	US	10/894,689	7/20/2004	7477655	1/13/2009	In Force	METHOD AND SYSTEM FOR POWER CONTROL OF FIBRE CHANNEL S
MP12083	MP12083C1	US	12/267,188	11/17/2008	7822061	10/26/2010	In Force	METHOD AND SYSTEM FOR POWER CONTROL OF FIBRE CHANNEL S
MP12084	MP12084.1	US	10/894,491	7/20/2004	7420982	9/2/2008	In Force	METHOD AND SYSTEM FOR KEEPING A FIBRE CHANNEL ARBITRATED

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MP12084	MP12084-1-C1	US	12/191,890	8/14/2008	7822057	10/26/2010	In Force	METHOD AND SYSTEM FOR KEEPING A FIBRE CHANNEL ARBITRATED
MP12084	MP12084-2	US	10/894,536	7/20/2004	7512067	3/31/2009	In Force	METHOD AND SYSTEM FOR CONGESTION CONTROL BASED ON OPTI
MP12084	MP12084-3	US	10/894,587	7/20/2004	7580354	8/25/2009	In Force	MULTI-SPEED CUT THROUGH OPERATION IN FIBRE CHANNEL SWITC
MP12084	MP12084-3-C1	US	12/502,973	7/14/2009	9118586	8/25/2015	In Force	MULTI-SPEED CUT THROUGH OPERATION IN FIBRE CHANNEL SWITC
MP12084	MP12084-4	US	10/894,629	7/20/2004	7447224	11/4/2008	In Force	METHOD AND SYSTEM FOR ROUTING FIBRE CHANNEL FRAMES
MP12084	MP12084-4-C1	US	12/189,502	8/11/2008	7936771	5/3/2011	In Force	METHOD AND SYSTEM FOR ROUTING FIBRE CHANNEL FRAMES
MP12084	MP12084-5	US	10/894,726	7/20/2004	7583597	9/1/2009	In Force	METHOD AND SYSTEM FOR IMPROVING BANDWIDTH AND REDUCIN
MP12085	MP12085	US	10/664,548	9/19/2003	7352701	4/1/2008	In Force	BUFFER TO BUFFER CREDIT RECOVERY FOR IN-LINE FIBRE CHANNEL
MP12086	MP12086	US	10/894,595	7/20/2004	7522522	4/21/2009	In Force	METHOD AND SYSTEM FOR REDUCING LATENCY AND CONGESTION
MP12087	MP12087	US	10/894,627	7/20/2004	7573909	8/11/2009	In Force	METHOD AND SYSTEM FOR PROGRAMMABLE DATA DEPENDANT NE
MP12088	MP12088	US	10/683,622	10/10/2003	7009413	3/7/2006	In Force	SYSTEM AND METHOD FOR TESTING BALL GRID ARRAYS
MP12089	MP12089	US	10/686,471	10/29/2003	7219263	5/15/2007	In Force	METHOD AND SYSTEM FOR MINIMIZING MEMORY CORRUPTION
MP1209	MP1209C1	US	13/205,406	8/8/2011	8233196	7/31/2012	In Force	HALFTONER BLOCK EMPLOYING AVERAGE VALUES AND SHIFTS
MP1209	MP1209C1C1	US	13/561,877	7/30/2012	8467100	6/18/2013	In Force	HALFTONER BLOCK EMPLOYING AVERAGE VALUES AND SHIFTS
MP12090	MP12090	US	10/719,077	11/21/2003	7103504	9/5/2006	In Force	METHOD AND SYSTEM FOR MONITORING EVENTS IN STORAGE AREA
MP12091	MP12091	US	10/723,064	11/26/2003	6949423	9/27/2005	In Force	MOSFET-FUSED NONVOLATILE READ-ONLY MEMORY CELL (MORFO
MP12092	MP12092	US	10/894,586	7/20/2004	7630384	12/8/2009	In Force	METHOD AND SYSTEM FOR DISTRIBUTING CREDIT IN FIBRE CHANNEL
MP12092	MP12092C1	US	12/580,169	10/15/2009	8072988	12/6/2011	In Force	METHOD AND SYSTEM FOR BUFFER-TO-BUFFER CREDIT RECOVERY I
MP12093	MP12093	US	10/894,547	7/20/2004	7684401	3/23/2010	In Force	METHOD AND SYSTEM FOR USING EXTENDED FABRIC FEATURES WIT
MP12093	MP12093C1	US	12/697,994	2/1/2010	7990975	8/2/2011	In Force	METHOD AND SYSTEM FOR USING EXTENDED FABRIC FEATURES WIT
MP12093	MP12093C1D1	US	13/177,314	7/6/2011	8644317	2/4/2014	In Force	METHOD AND SYSTEM FOR USING EXTENDED FABRIC FEATURES WIT
MP12094	MP12094	US	10/894,597	7/20/2004	7406092	7/29/2008	In Force	PROGRAMMABLE PSEUDO VIRTUAL LANES FOR FIBRE CHANNEL SYST
MP12094	MP12094C1	US	12/141,519	6/18/2008	7760752	7/20/2010	In Force	PROGRAMMABLE PSEUDO VIRTUAL LANES FOR FIBRE CHANNEL SYST
MP12095	MP12095	US	10/894,579	7/20/2004	7430175	9/30/2008	In Force	METHOD AND SYSTEM FOR MANAGING TRAFFIC IN FIBRE CHANNEL
MP12095	MP12095C1	US	12/189,497	8/11/2008	7649903	1/19/2010	In Force	METHOD AND SYSTEM FOR MANAGING TRAFFIC IN FIBRE CHANNEL
MP12096	MP12096	US	10/894,546	7/20/2004	7792115	9/7/2010	In Force	METHOD AND SYSTEM FOR ROUTING AND FILTERING NETWORK DAT
MP12097	MP12097	US	10/798,527	3/11/2004	7480293	1/20/2009	In Force	METHOD AND SYSTEM FOR PREVENTING DEADLOCK IN FIBRE CHAN
MP12098	MP12098	US	10/798,468	3/11/2004	7564789	7/21/2009	In Force	METHOD AND SYSTEM FOR REDUCING DEADLOCK IN FIBRE CHANNE
MP12099	MP12099D1	US	11/617,716	12/28/2006	7774374	8/10/2010	In Force	SWITCHING SYSTEMS AND METHODS USING WILDCARD SEARCHING
MP12100	MP12100	US	10/894,978	7/20/2004	7646767	1/12/2010	In Force	METHOD AND SYSTEM FOR PROGRAMMABLE DATA DEPENDANT NE
MP12101	MP12101	US	10/797,977	3/10/2004	7231410	6/12/2007	In Force	REVISION CONTROL SYSTEM FOR LARGE-SCALE SYSTEMS MANAGEM
MP12101	MP12101C1	US	11/748,160	5/14/2007	7647363	1/12/2010	In Force	REVISION CONTROL SYSTEM FOR LARGE-SCALE SYSTEMS MANAGEM
MP12102	MP12102	US	10/806,691	3/23/2004	7445664	9/9/2008	In Force	PCI - EXPRESS SLOT FOR COUPLING PLURAL DEVICES TO A HOST SYST
MP12107	MP12107	US	10/894,732	7/20/2004	7834348	2/22/2011	In Force	METHOD AND SYSTEM FOR CONGESTION CONTROL IN A FIBRE CHAN

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MP12108	MP12108	US	10/948,485	5/17/2004	7493481	2/17/2009	In Force	DIRECT HARDWARE PROCESSING OF INTERNAL DATA STRUCTURE FIE
MP12109	MP12109	US	10/984,917	7/20/2004	7558281	7/7/2009	In Force	METHOD AND SYSTEM FOR CONFIGURING FIBRE CHANNEL PORTS
MP12109	MP12109C1	US	12/473,150	5/27/2009	8005105	8/23/2011	In Force	METHOD AND SYSTEM FOR CONFIGURING FIBRE CHANNEL PORTS
MP12110	MP12110	US	10/847,756	5/18/2004	7669190	2/23/2010	In Force	METHOD AND SYSTEM FOR EFFICIENTLY RECORDING PROCESSOR EV
MP12112	MP12112	US	10/878,595	6/28/2004	7469267	12/23/2008	In Force	METHOD AND SYSTEM FOR HOST DEVICE EVENT SYNCHRONIZATION
MP12113	MP12113	US	29/209,313	7/14/2004	D520,863	5/16/2006	In Force	FIBER OPTIC CABLE RETENTION AND GUIDE CLIP
MP12114	MP12114	US	10/894,529	7/20/2004	7464020	7/22/2008	In Force	INTEGRATED FIBRE CHANNEL FABRIC CONTROLLER
MP12117	MP12117	US	10/927,170	8/25/2004	7308535	12/11/2007	In Force	SELF-TRIGGERING OUTGOING BUFFERS
MP12117	MP12117C1	US	11/933,860	11/1/2007	7560057	3/3/2009	In Force	SELF-TRIGGERING OUTGOING BUFFERS
MP12118	MP12118	US	10/931,851	1/9/2004	7522623	4/21/2009	In Force	METHOD AND SYSTEM FOR EFFICIENTLY USING BUFFER SPACE
MP12118	MP12118C1	US	12/407,153	3/19/2009	7924859	4/12/2011	In Force	METHOD AND SYSTEM FOR EFFICIENTLY USING BUFFER SPACE
MP12119	MP12119	US	10/931,850	1/9/2004	7761608	7/20/2010	In Force	METHOD AND SYSTEM FOR PROCESSING MARKERS, DATA INTEGRITY
MP12120	MP12120	US	10/935,919	9/8/2004	7577772	8/18/2009	In Force	METHOD AND SYSTEM FOR OPTIMIZING DMA CHANNEL SELECTION
MP12122	MP12122	US	10/940,355	9/13/2004	7895390	2/22/2011	In Force	ENSURING BUFFER AVAILABILITY
MP12125	MP12125	US	10/956,955	10/1/2004	7676611	3/9/2010	In Force	METHOD AND SYSTEM FOR PROCESSING OUT OF ORDER FRAMES
MP12126	MP12126	US	10/956,501	10/1/2004	8285299	10/23/2012	In Force	HIGH SPEED FIBRE CHANNEL SWITCH ELEMENT
MP12126	MP12126C1	US	13/604,261	9/5/2012	8774206	7/8/2014	In Force	HIGH SPEED FIBRE CHANNEL SWITCH ELEMENT
MP12126	MP12126EP	EP	05798761.2	9/27/2005	1784952	7/14/2010	In Force	HIGH SPEED FIBER CHANNEL SWITCH ELEMENT
MP12127	MP12127	US	10/956,502	10/1/2004	7593997	9/22/2009	In Force	METHOD AND SYSTEM FOR LUN REMAPPING IN FIBRE CHANNEL NET
MP12128	MP12128	US	10/957,465	10/1/2004	7930377	4/19/2011	In Force	METHOD AND SYSTEM FOR USING BOOT SERVERS IN NETWORKS
MP12129	MP12129	US	10/956,718	10/1/2004	7380030	5/27/2008	In Force	METHOD AND SYSTEM FOR USING AN IN-LINE CREDIT EXTENDER WIT
MP12130	MP12130	US	10/956,717	1/10/2004	7411958	12/8/2008	In Force	METHOD AND SYSTEM FOR TRANSFERRING DATA DIRECTLY BETWEE
MP12131	MP12131	US	10/961,463	8/10/2004	7340167	4/3/2008	In Force	FIBRE CHANNEL TRANSPARENT SWITCH FOR MIXED SWITCH FABRICS
MP12131	MP12131C1	US	12/031,585	2/14/2008	7542676	6/2/2009	In Force	FIBRE CHANNEL TRANSPARENT SWITCH FOR MIXED SWITCH FABRICS
MP12132	MP12132	US	10/963,287	10/11/2004	7512721	3/31/2009	In Force	METHOD AND APPARATUS FOR EFFICIENT DETERMINATION OF STAT
MP12133	MP12133	US	10/984,829	11/22/2004	7398335	7/8/2008	In Force	METHOD AND SYSTEM FOR DMA OPTIMIZATION IN HOST BUS ADAP
MP12134	MP12134	US	11/002,728	11/30/2004	7606998	10/20/2009	In Force	Store Instruction Ordering For Multi-Core Processor
MP12134	MP12134WO	WO	PCT/US2005/03	9/1/2005	DNA		Pending	Store Instruction Ordering For Multi-Core Processor
MP12134	MP12134WOCN	CN	2005800346066	9/1/2005	ZL2005800346	8/26/2009	In Force	Store Instruction Ordering For Multi-Core Processor
MP12134	MP12134WODE	DE	05795368.9	9/1/2005	607005052946	10/25/2017	In Force	Store Instruction Ordering For Multi-Core Processor
MP12134	MP12134WOEP	EP	05795368.9	9/1/2005	1787194	10/25/2017	In Force	Store Instruction Ordering For Multi-Core Processor
MP12134	MP12134WOJP	JP	2007-531266	9/1/2005	JP5197010	2/15/2013	In Force	Store Instruction Ordering For Multi-Core Processor
MP12135	MP12135	US	11/005,490	12/6/2004	7885431	2/22/2011	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WO	WO	PCT/US2005/03	9/1/2005	DNA		Pending	Packet Queuing, Scheduling And Ordering

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MP12135	MP12135WOCN	CN	2005800346009	9/1/2005	Z12005800346	2/8/2012	In Force	Data packet queuing, scheduling and ordering
MP12135	MP12135WOEP	EP	05796391.0	9/1/2005	1787212	2/13/2013	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WOEPDE	DE	05796391.0	9/1/2005	603605038127	2/13/2013	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WOEPFI	FI	05796391.0	9/1/2005	1787212	2/13/2013	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WOEPFR	FR	05796391.0	9/1/2005	1787212	2/13/2013	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WOEPGB	GB	05796391.0	9/1/2005	1787212	2/13/2013	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WOEPSE	SE	05796391.0	9/1/2005		2/13/2013	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WOJP	JP	2007-531240	9/1/2005	JP4723586	4/15/2011	In Force	Packet Queuing, Scheduling And Ordering
MP12136	MP12136	US	11/015,343	12/17/2004	7941585	5/10/2011	In Force	Local Scratchpad And Data Caching System
MP12137	MP12137	US	11/018,055	12/21/2004	7164425	1/16/2007	In Force	METHOD AND SYSTEM FOR HIGH SPEED NETWORK APPLICATION
MP12138	MP12138	US	11/024,002	12/28/2004	7554081	9/22/2009	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WQ	WO	PCT/US2005/03	9/1/2005	DNA		Pending	Direct Access To Low-Latency Memory
MP12138	MP12138WOCN	CN	2005800334834	9/1/2005	Z12005800334	12/8/2010	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEP	EP	05810485.2	9/1/2005	1787193	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEPCH	CH	05810485.2	9/1/2005	1787193	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEPDE	DE	05810485.2	9/1/2005	603605045426	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEPFR	FR	05810485.2	9/1/2005	1787193	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEPGB	GB	05810485.2	9/1/2005	1787193	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEPFI	FI	05810485.2	9/1/2005	1787193	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEPSE	SE	05810485.2	9/1/2005	1787193	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12139	MP12139C1	US	14/159,210	1/20/2014	9141548	9/22/2015	In Force	Method And Apparatus For Managing Write Back Cache
MP12140	MP12140	US	11/037,922	1/18/2005	7519058	4/14/2009	In Force	ADDRESS TRANSLATION IN FIBRE CHANNEL SWITCHES
MP12141	MP12141	US	11/039,189	1/20/2005	7392437	6/24/2008	In Force	METHOD AND SYSTEM FOR TESTING HOST BUS ADAPTERS
MP12143	MP12143WQ	WO	PCT/US2005/03	9/1/2005	DNA		Pending	Multiply Instructions For Modular Exponentiation
MP12147	MP12147	US	11/346,106	2/2/2006	7496745	2/24/2009	In Force	METHOD AND SYSTEM FOR MANAGING STORAGE AREA NETWORKS
MP12149	MP12149	US	11/355,622	2/16/2006	7694047	4/6/2010	In Force	METHOD AND SYSTEM FOR SHARING INPUT/OUTPUT DEVICES
MP1215	MP1215	US	12/050,604	3/18/2008	7855909	12/21/2010	In Force	Calibrating Page Borders In A Phase-Change Memory
MP12150	MP121501	US	11/097,024	4/1/2005	7463991	9/2/2008	In Force	TCP TIME STAMP PROCESSING IN HARDWARE BASED TCP OFFLOAD
MP12153	MP12153	US	11/100,063	6/4/2005	7231480	12/6/2007	In Force	METHOD AND SYSTEM FOR RECEIVER DETECTION IN PCI-EXPRESS DE
MP12154	MP12154	US	11/220,899	9/7/2005	8301788	10/30/2012	In Force	Deterministic Finite Automata (DFA) Instruction
MP12155	MP12155	US	11/221,408	9/7/2005	8392590	3/5/2013	In Force	Deterministic Finite Automata (DFA) Processing
MP12155	MP12155WO	WO	PCT/US2005/03	9/8/2005	DNA		Pending	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOCN	CN	200580034214X	9/8/2005	Z12005800342	2/29/2012	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEP	EP	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing

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MP12155	MP12155WOEPC	CH	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEPDE	DE	05810485.2	9/8/2005	602005038814	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEPFI	FI	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEPFR	FR	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEPGB	GB	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEPJE	JE	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEPSE	SE	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12156	MP12156	US	11/224,728	9/12/2005	8560475	10/15/2013	In Force	Content Search Mechanism That Uses A Deterministic Finite Automata
MP12156	MP12156-2	US	14/040,323	9/27/2013	8818921	8/26/2014	In Force	Content Search Mechanism That Uses A Deterministic Finite Automata
MP12156	MP12156C1	US	14/337,759	7/22/2014	9336328	5/10/2016	In Force	Content search mechanism that uses a deterministic finite automata
MP12156	MP12156C1C1	US	15/134,919	4/21/2016	9652505	5/16/2017	In Force	Content Search Pattern Matching Using Deterministic Finite Automata
MP12157	MP1215711	US	11/335,189	1/18/2006	7558925	7/7/2009	In Force	Selective Replication Of Data Structures
MP12157	MP12157WO	WO	PCT/US2005/03	9/9/2005	DNA		Pending	Selective Replication Of Data Structures
MP12157	MP12157WO2	WO	PCT/US2005/03	9/9/2005	DNA		Pending	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2CH	CH	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2CN	CN	2005800304519	9/9/2005	212005800304	2/1/2012	In Force	Selective replication of data structure
MP12157	MP12157WO2DE	DE	05795143.6	9/9/2005	602005051737	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2DK	DK	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2EP	EP	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2FI	FI	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2FR	FR	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2GB	GB	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2NL	NL	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2SE	SE	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12158	MP12158	US	11/225,373	9/12/2005	7613813	11/3/2009	In Force	METHOD AND APPARATUS FOR REDUCING HOST OVERHEAD IN A SO
MP12158	MP12158C1	US	12/574,263	10/6/2009	7930349	4/19/2011	In Force	Method And Apparatus For Reducing Host Overhead In A Socket Serv
MP12159	MP12159	US	11/218,727	9/2/2005	7535907	5/19/2009	In Force	TCP Engine
MP1216	MP1216	US	11/820,912	6/21/2007	7958301	6/7/2011	In Force	Memory Controller And Method For Memory Pages With Dynamicall
MP1216	MP1216C1	US	13/154,320	6/6/2011	8156271	4/24/2012	In Force	Memory Controller For Setting Page Length And Memory Cell Density
MP1216	MP1216CN	CN	2008800195504	4/10/2008	212008800195	1/2/2013	In Force	MEMORY CONTROLLER
MP12162	MP12162	US	11/126,988	5/11/2005	7447618	11/4/2008	In Force	METHOD AND SYSTEM FOR ASIC SIMULATION
MP12163	MP12163	US	11/132,020	5/18/2005	7802031	9/21/2010	In Force	METHOD AND SYSTEM FOR HIGH SPEED NETWORK APPLICATION
MP12165	MP12165	US	11/140,891	5/31/2005	7168971	1/30/2007	In Force	GASKET RETAINER
MP12166	MP12166	US	11/201,036	8/10/2005	7565580	7/21/2009	In Force	METHOD AND SYSTEM FOR TESTING NETWORK DEVICE LOGIC

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MP12167	MP12167	US	11/222,592	9/9/2005	7230549	12/6/2007	In Force	METHOD AND SYSTEM FOR SYNCHRONIZING BIT STREAMS FOR PCI E
MP12169	MP12169	US	11/223,693	9/9/2005	7639715	12/29/2009	In Force	DEDICATED APPLICATION INTERFACE FOR NETWORK SYSTEMS
MP1216W	MP1216W/CD1	CN	201210473722.1	4/10/2008	21,2012104737	5/18/2016	In Force	MEMORY CONTROLLER
MP1216W	MP1216W/O	WO	PCT/US2008/00	4/10/2008	DNA		Pending	Memory Controller
MP12170	MP12170	US	11/222,936	9/9/2005	7577773	8/18/2009	In Force	METHOD AND SYSTEM FOR DMA OPTIMIZATION
MP12172	MP12172	US	11/252,524	10/18/2005	7447874	4/11/2008	In Force	METHOD AND SYSTEM FOR DESIGNING A FLEXIBLE HARDWARE STAT
MP12173	MP12173	US	11/257,693	10/25/2005	7630876	12/8/2009	In Force	METHOD AND SYSTEM FOR FILTERING UNKNOWN VALUES IN ASIC E
MP12174	MP12174	US	11/291,430	12/1/2005	7920473	4/5/2011	In Force	METHOD AND SYSTEM FOR MANAGING TRANSMIT DESCRIPTORS IN
MP12175	MP12175	US	11/317,315	12/23/2005	7735099	6/8/2010	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK DATA
MP12176	MP12176	US	11/318,116	12/23/2005	8682799	3/25/2014	In Force	Method and system for port licensing in switches
MP12178	MP12178	US	11/328,358	9/1/2006	7594057	9/22/2009	In Force	METHOD AND SYSTEM FOR PROCESSING DMA REQUESTS
MP1218	MP1218	US	11/986,520	11/21/2007	7937526	5/3/2011	In Force	Storage Device Cache With Phase Change Memory
MP12180	MP12180	US	11/343,596	1/31/2006	8464238	6/11/2013	In Force	METHOD AND SYSTEM FOR MANAGING STORAGE AREA NETWORKS
MP12181	MP12181	US	11/357,515	2/17/2006	7668978	2/23/2010	In Force	METHOD AND SYSTEM FOR STORING FRAMES IN NETWORKS
MP12182	MP12182	US	11/362,690	2/27/2006	7518995	4/14/2009	In Force	METHOD AND SYSTEM FOR MANAGING FRAME FLOW IN FIBRE CHA
MP12183	MP12183	US	11/363,365	2/27/2006	7548560	6/16/2009	In Force	METHOD AND SYSTEM FOR CHECKING FRAME-LENGTH IN FIBRE CHA
MP12184	MP12184	US	11/376,703	3/14/2006	8009923	8/30/2011	In Force	METHOD AND SYSTEM FOR MOTION ESTIMATION WITH MULTIPLE V
MP12185	MP12185	US	11/376,955	3/16/2006	7525968	4/28/2009	In Force	METHOD AND SYSTEM FOR AUTO ROUTING FIBRE CHANNEL CLASS F
MP12187	MP12187	US	11/378,838	3/17/2006	7461195	12/2/2008	In Force	METHOD AND SYSTEM FOR DYNAMICALLY ADJUSTING DATA TRANSF
MP12188	MP12188	US	11/396,748	4/3/2006	7681102	3/16/2010	In Force	BYTE LEVEL PROTECTION IN PCI-EXPRESS DEVICES
MP12189	MP12189	US	11/420,704	5/9/2006	7447817	11/4/2008	In Force	METHOD AND SYSTEM FOR PROCESSING ARBITRATION REQUESTS
MP1219	MP1219	US	11/789,971	4/26/2007	8856423	10/7/2014	In Force	Dual-Purpose Nonvolatile Memory For Code And Data Storage
MP12190	MP12190	US	11/453,500	6/15/2006	7660302	2/9/2010	In Force	METHOD AND SYSTEM FOR INTER-FABRIC ROUTING
MP12192	MP12192	US	11/477,161	6/28/2006	7610482	10/27/2009	In Force	METHOD AND SYSTEM FOR MANAGING BOOT TRACE INFORMATION
MP12193	MP12193	US	11/491,357	7/21/2006	7379837	5/27/2008	In Force	METHOD AND SYSTEM FOR TESTING INTEGRATED CIRCUITS
MP12194	MP12194	US	11/493,978	7/27/2006	8185960	5/22/2012	In Force	SYSTEM AND METHOD FOR MANAGING ACCESS TO ADAPTER FEATU
MP12195	MP12195	US	11/461,387	7/31/2006	7764676	7/27/2010	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK INFORMATION
MP12196	MP12196	US	11/502,282	8/10/2006	7657887	11/25/2008	In Force	METHOD AND SYSTEM FOR PROCESSING ASYNCHRONOUS EVENT NO
MP12198	MP12198	US	11/469,377	8/31/2006	7669001	2/23/2010	In Force	METHOD AND SYSTEM FOR USING APPLICATION SPECIFIC INTEGRAT
MP12199	MP12199	US	11/522,672	9/18/2006	7843846	11/30/2010	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK INFORMATION
MP1220	MP1220	US	11/789,605	4/25/2007	8176386	5/8/2012	In Force	Systems And Methods For Processing Streaming Data
MP1220	MP1220C1	US	14/190,836	2/26/2014	8935591	1/13/2015	In Force	SYSTEM AND METHOD TO CORRECT ERRORS IN DATA READ FROM A
MP1220	MP1220D1	US	13/465,964	5/7/2012	8352799	3/5/2013	In Force	Systems And Methods For Arbitrating Use Of Processor Memory
MP1220	MP1220D1C1	US	13/785,803	3/5/2013	8667370	3/4/2014	In Force	Systems And Methods For Arbitrating Use Of Processor Memory

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MP12201	MP12201	US	11/533,873	9/21/2006	7773592	8/10/2010	In Force	METHOD AND SYSTEM FOR ROUTING NETWORK INFORMATION
MP12205	MP12205	US	11/555,180	10/31/2006	8055726	11/8/2011	In Force	METHOD AND SYSTEM FOR WRITING NETWORK DATA
MP12205	MP12205C1	US	13/241,012	9/22/2011	8676928	3/18/2014	In Force	METHOD AND SYSTEM FOR WRITING NETWORK DATA
MP12206	MP12206	US	11/555,123	10/31/2006	8601179	12/3/2013	In Force	METHOD AND SYSTEM FOR DATA TRANSFER
MP12208	MP12208	US	11/560,317	11/15/2006	7613816	11/3/2009	In Force	METHOD AND SYSTEM FOR ROUTING NETWORK INFORMATION
MP1221	MP1221	US	11/728,449	3/26/2007	7619919	11/17/2009	In Force	Multi-Level Memory
MP12210	MP12210	US	11/565,444	11/30/2006	7643505	1/5/2010	In Force	METHOD AND SYSTEM FOR REAL TIME COMPRESSION AND DECOMPRESSION
MP12212	MP12212	US	11/615,329	12/22/2006	7549005	6/16/2009	In Force	SYSTEM AND METHOD FOR MANAGING INTERRUPTS
MP12214	MP12214	US	11/669,573	1/31/2007	7668177	2/23/2010	In Force	METHOD AND SYSTEM FOR QUALITY OF SERVICE IN HOST BUS ADAP
MP12217	MP12217	US	11/669,775	1/31/2007	7668162	2/23/2010	In Force	COMPRESSION AND DECOMPRESSION FOR NETWORK SYSTEMS
MP12217	MP12217C1	US	12/684,054	1/7/2010	7889730	2/15/2011	In Force	COMPRESSION AND DECOMPRESSION FOR NETWORK SYSTEMS
MP12219	MP12219WQBR	BR	P10721380-8	2/28/2007			Pending	METHOD OF ALLOCATING COMMUNICATIONS RESOURCES AND SCH
MP12219	MP12219WCID	ID	WO0200902395	2/28/2007	P0832751	1/21/2013	In Force	METHOD OF ALLOCATING COMMUNICATIONS RESOURCES AND SCH
MP12219	MP12219WCIN	IN	1669/MUJMNPF/	2/28/2007			Pending	METHOD OF ALLOCATING COMMUNICATIONS RESOURCES AND SCH
MP1221T	MP1221TW	TW	97101369	1/14/2008	1470630	1/21/2015	In Force	IMPROVED MULTI-LEVEL MEMORY
MP1221W	MP1221WQ	WO	PCT/US2008/00	1/10/2008	DNA		Pending	Improved Multi-Level Memory
MP1221W	MP1221WQCN	CN	200880002210.	1/10/2008	ZL2008800022	8/8/2012	In Force	IMPROVED MULTI-LEVEL MEMORY
MP1221W	MP1221WQJP	JP	2009-545594	1/10/2008	JP5217048	3/15/2013	In Force	AN IMPROVED MULTI-LEVEL MEMORY AND A METHOD FOR PERFOR
MP1222	MP1222	US	11/820,934	6/21/2007	8046660	10/25/2011	In Force	System And Method For Correcting Errors In Non-Volatile Memory U
MP1222	MP1222D1	US	13/280,168	10/24/2011	8566664	10/22/2013	In Force	System And Method For Correcting Errors In Non-Volatile Memory U
MP12220	MP12220WCIN	IN	1718/MUJMNPF/	3/5/2007			Pending	CHANNEL PROFILER AND METHOD OF PROFILING AN INCOMING SIG
MP12220	MP12220WCJP	JP	2009-552286	3/5/2007			Pending	CHANNEL PROFILER AND METHOD OF PROFILING AN INCOMING SIG
MP12221	MP12221CN	CN	200810081776.1	3/13/2008	ZL2008100817	9/14/2011	In Force	Reliable Network Packet Dispatcher With Interleaving Multi-Port Circ
MP12222	MP12222	US	11/686,417	3/15/2007	7693070	4/6/2010	In Force	CONGESTION REDUCING RELIABLE TRANSPORT PACKET RETRY ENGI
MP12223	MP12223	US	11/742,282	4/30/2007	8387073	2/26/2013	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK PACKETS
MP12224	MP12224	US	11/694,698	3/30/2007	8185715	5/22/2012	In Force	METHOD AND SYSTEM FOR MANAGING METADATA IN STORAGE VIR
MP12225	MP12225WCIN	IN	2098/MUJMNPF/	5/2/2007			Pending	METHOD AND APPARATUS FOR CORRECTING LINEAR ERROR PHASE
MP12225	MP12225WCJP	JP	2010-504875	5/2/2007	JP5460579	4/2/2014	In Force	METHOD AND APPARATUS FOR CORRECTING LINEAR ERROR PHASE
MP12225	MP12225WCUS	US	12/598,544	5/2/2007	8340205	12/25/2012	In Force	METHOD AND APPARATUS FOR CORRECTING LINEAR ERROR PHASE
MP12226	MP12226	US	11/752,212	5/22/2007	8250203	8/21/2012	In Force	METHOD AND SYSTEM FOR NETWORK TRAFFIC MONITORING
MP12227	MP12227	US	11/761,279	6/11/2007	8095978	1/10/2012	In Force	METHOD AND SYSTEM FOR MANAGING PORTS IN A HOST BUS ADAP
MP12228	MP12228	US	11/762,578	6/13/2007	8102769	1/24/2012	In Force	METHOD AND SYSTEM FOR NETWORK COMMUNICATION
MP12228	MP12228C1	US	13/330,085	12/19/2011	8792351	7/29/2014	In Force	METHOD AND SYSTEM FOR NETWORK COMMUNICATION
MP12229	MP12229	US	11/965,037	12/27/2007	8010809	8/30/2011	In Force	METHOD AND SYSTEM FOR SECURING NETWORK DATA

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MP12229	MP12229C1	US	13/190,165	7/25/2011	8251099	9/4/2012	In Force	METHOD AND SYSTEM FOR SECURING NETWORK DATA
MP1222T	MP1222TW	TW	96129103	8/7/2007	1451430	9/1/2014	In Force	System And Method For Correcting Errors In Non-Volatile Memory U
MP1222W	MP1222WO	WO	PCT/US2007/01	8/6/2007	DNA		Pending	System And Method For Correcting Errors In Non-Volatile Memory U
MP1222W	MP1222WOCN	CN	200780029503	8/6/2007	ZL2007800295	10/23/2013	In Force	SYSTEM AND METHOD FOR CORRECTING ERRORS IN NON-VOLATILE
MP12230	MP12230	US	12/111,057	4/28/2008	8904077	12/2/2014	In Force	POWER MANAGEMENT FOR HOST BUS ADAPTERS
MP12232	MP12232	US	11/842,830	8/21/2007	8411677	4/2/2013	In Force	METHOD AND SYSTEM FOR PROCESSING LAYERED NETWORKING PR
MP12234	MP12234	US	11/849,122	8/31/2007	7665658	10/20/2009	In Force	Resistively Loaded Single Stage Differential Amplifier Having Zero Vc
MP12236	MP12236	US	11/854,651	9/28/2007	7903558	3/8/2011	In Force	METHOD AND SYSTEM FOR MONITORING A NETWORK LINK IN NET
MP12236	MP12236C1	US	13/026,007	2/11/2011	8441929	5/14/2013	In Force	METHOD AND SYSTEM FOR MONITORING A NETWORK LINK IN NET
MP12238	MP12238	US	11/982,391	11/1/2007	8086609	12/27/2011	In Force	Graph Caching
MP12238	MP12238C1	US	13/311,244	12/5/2011	9787693	10/10/2017	In Force	Graph Caching
MP12239	MP12239	US	11/982,433	11/1/2007	8819217	8/26/2014	In Force	Intelligent Graph Walking
MP1224	MP1224	US	11/850,732	8/7/2007	7564394	7/21/2009	In Force	BUFFER FOR A/D CONVERSION
MP12240	MP12240WOIN	IN	3312/CHEMP/20	11/6/2007			Pending	METHOD AND SYSTEM FOR DIGITALLY CORRECTING SAMPLING EFFE
MP12241	MP12241	US	12/052,130	3/20/2008	7908404	3/15/2011	In Force	METHOD AND SYSTEM FOR MANAGING NETWORK AND STORAGE D
MP12242	MP12242	US	11/986,975	11/27/2007	7949683	5/24/2011	In Force	Method And Apparatus For Traversing A Compressed Deterministic Fi
MP12242	MP12242WO	WO	PCT/US2008/01	10/7/2008	DNA		Pending	Method And Apparatus For Traversing A Deterministic Finite Automata
MP12242	MP12242WOEP	EP	08853776.6	10/7/2008	2215563	5/4/2016	In Force	Method And Apparatus For Traversing A Deterministic Finite Automata
MP12242	MP12242WOEPDE	DE	08853776.6	10/7/2008	602008044095	5/4/2016	In Force	Method And Apparatus For Traversing A Deterministic Finite Automata
MP12242	MP12242WOEPFI	FI	08853776.6	10/7/2008	2215563	5/4/2016	In Force	Method And Apparatus For Traversing A Deterministic Finite Automata
MP12242	MP12242WOEPFR	FR	08853776.6	10/7/2008	2215563	5/4/2016	In Force	Method And Apparatus For Traversing A Deterministic Finite Automata
MP12242	MP12242WOEPSE	SE	08853776.6	10/7/2008	2215563	5/4/2016	In Force	Method And Apparatus For Traversing A Deterministic Finite Automata
MP12243	MP12243	US	11/986,970	11/27/2007	8180803	5/15/2012	In Force	Deterministic Finite Automata (DFA) Graph Compression
MP12243	MP12243WO	WO	PCT/US2008/01	10/7/2008	DNA		Pending	Deterministic Finite Automata (DFA) Graph Compression
MP12243	MP12243WOEP	EP	08854609.8	10/7/2008	2215565	5/4/2016	In Force	Deterministic Finite Automata (DFA) Graph Compression
MP12243	MP12243WOEPDE	DE	08854609.8	10/7/2008	60200804410X	5/4/2016	In Force	Deterministic Finite Automata (DFA) Graph Compression
MP12243	MP12243WOEPFI	FI	08854609.8	10/7/2008	2215565	5/4/2016	In Force	Deterministic Finite Automata (DFA) Graph Compression
MP12243	MP12243WOEPFR	FR	08854609.8	10/7/2008	2215565	5/4/2016	In Force	Deterministic Finite Automata (DFA) Graph Compression
MP12243	MP12243WOEPSE	SE	08854609.8	10/7/2008	2215565	5/4/2016	In Force	Deterministic Finite Automata (DFA) Graph Compression
MP12245	MP12245	US	11/963,566	12/21/2007	7720064	5/18/2010	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK AND STORAGE D
MP12245	MP12245C1	US	12/751,955	3/31/2010	8225004	7/17/2012	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK AND STORAGE D
MP12246	MP12246	US	11/966,857	12/28/2007	7719969	5/18/2010	In Force	SYSTEM AND METHOD FOR ASSIGNING NETWORK DEVICE PORT ADD
MP12248	MP12248	US	12/019,094	1/24/2008	7669710	10/27/2009	In Force	METHOD AND SYSTEM FOR CREDIT MANAGEMENT IN A NETWORKIN
MP12251	MP12251	US	12/034,913	2/21/2008	7962675	6/14/2011	In Force	METHOD AND SYSTEM FOR COMMUNICATING WITH A HOST BUS AD

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MP12252	MP12252	US	12/392,004	2/24/2009	8677144	3/18/2014	In Force	Secure Software And Hardware Association Technique
MP12253	MP12253	US	12/037,847	2/26/2008	8116206	2/14/2012	In Force	METHOD AND SYSTEM FOR ROUTING FRAMES IN A NETWORK
MP12254	MP12254	US	12/039,604	2/28/2008	7895300	2/22/2011	In Force	SYSTEMS AND METHODS FOR TESTING DEVICE PORTS IN A STORAGE
MP12255	MP12255	US	12/082,246	4/9/2008	8358699	1/22/2013	In Force	METHOD AND SYSTEM FOR SELECTION OF REFERENCE PICTURE AND
MP12256	MP12256C1	US	14/144,252	12/30/2013	8891706	11/18/2014	In Force	SYSTEM AND METHOD FOR OPTIMIZING USE OF CHANNEL STATE INF
MP12256	MP12256WOUS	US	12/988,348	4/17/2008	8619923	12/31/2013	In Force	SYSTEM AND METHOD FOR OPTIMIZING USE OF CHANNEL STATE INF
MP12258	MP12258	US	12/135,973	6/9/2008	7773629	8/10/2010	In Force	METHOD AND SYSTEM FOR NETWORK PORT ADDRESSING
MP12259	MP12259	US	12/214,789	6/20/2008	8471601	6/25/2013	In Force	Single-Ended to Differential Converter
MP12260	MP12260	US	12/489,241	6/22/2009	8226386	10/23/2012	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK PACKETS
MP12260	MP12260C1	US	13/610,095	9/11/2012	8867537	10/21/2014	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK PACKETS
MP12261	MP12261	US	12/165,464	6/30/2008	7765317	7/27/2010	In Force	SYSTEM AND METHODS FOR LOCATING FPDU HEADERS WHEN MARK
MP12262	MP12262	US	12/505,430	7/17/2009	8351442	1/8/2013	In Force	METHOD AND SYSTEM FOR NETWORK COMMUNICATION
MP12264	MP12264	US	12/536,336	8/5/2009	8793399	7/29/2014	In Force	METHOD AND SYSTEM FOR ACCELERATING NETWORK PACKET PROC
MP12265	MP12265	US	12/539,156	8/11/2009	8391300	3/5/2013	In Force	CONFIGURABLE SWITCH ELEMENT AND METHODS THEREOF
MP12265	MP12265C1	US	13/758,543	2/4/2013	8976800	3/10/2015	In Force	CONFIGURABLE SWITCH ELEMENT AND METHODS THEREOF
MP12266	MP12266	US	12/229,617	8/25/2008	8116300	5/8/2012	In Force	Method And Apparatus For Content Based Searching
MP12267	MP12267	US	12/547,414	8/25/2009	8209473	6/12/2012	In Force	EMULATION OF MULTIPLE MIDI MANAGEABLE DEVICES
MP12268	MP12268	US	12/200,649	8/28/2008	7667985	2/23/2010	In Force	COMPUTER NETWORK DEVICE AND METHOD OF ASSEMBLY
MP12269	MP12269	US	12/201,304	8/29/2008	7646602	1/12/2010	In Force	APPARATUS AND METHODS FOR COOLING NETWORK SWITCHES
MP1227	MP1227	US	11/828,975	7/26/2007	8180976	5/29/2012	In Force	HIGH-SPEED INTERFACE FOR HOLOGRAPHIC STORAGE READ CHANNE
MP1227	MP1227C1	US	13/480,288	5/24/2012	8458573	6/4/2013	In Force	HIGH-SPEED INTERFACE FOR READ CHANNEL
MP12270	MP12270	US	12/201,359	8/29/2008	7675750	3/9/2010	In Force	APPARATUS AND METHODS FOR COOLING NETWORK SWITCHES
MP12271	MP12271	US	12/586,169	9/17/2009	7961033	6/14/2011	In Force	DLL-Based Temperature Sensor
MP12273	MP12273	US	12/277,129	11/24/2008	8473523	6/25/2013	In Force	Deterministic Finite Automata Graph Traversal With Nodal Bit Mappi
MP12273	MP12273C1	US	13/905,711	5/30/2013	8886680	11/11/2014	In Force	Deterministic Finite Automata Graph Traversal With Nodal Bit Mappi
MP12273	MP12273C1C1	US	14/516,807	10/17/2014	9495479	11/15/2016	In Force	Traversal With Arc Configuration Information
MP12276	MP12276	US	12/410,294	3/24/2009	7907414	3/15/2011	In Force	NETWORK DEVICE HAVING LEVERS AND ASSOCIATED METHODS
MP12278	MP12278	US	12/556,064	9/9/2009	8088482	11/29/2011	In Force	METHOD AND SYSTEM FOR NETWORK SWITCH ELEMENT
MP12278	MP12278C1	US	13/273,068	10/13/2011	8873546	10/28/2014	In Force	METHOD AND SYSTEM FOR NETWORK SWITCH ELEMENT
MP12278	MP12278WOCN	CN	200980145538.1	11/3/2009	212009801455	5/13/2015	In Force	METHOD AND SYSTEM FOR NETWORK SWITCH ELEMENT
MP12279	MP12279	US	12/412,050	3/26/2009	9094343	7/28/2015	In Force	METHOD AND SYSTEM FOR TAKING A NETWORK PORT OFFLINE
MP1228	MP1228	US	11/890,407	8/6/2007	8085745	1/10/2012	In Force	NON-SEQUENTIAL TRANSFER OF DATA FROM A MEMORY
MP1228	MP1228D1	US	13/331,901	12/20/2011	9053052	6/9/2015	In Force	NON-SEQUENTIAL TRANSFER OF DATA FROM A MEMORY
MP12280	MP12280	US	12/411,176	3/25/2009	7907546	3/15/2011	In Force	METHOD AND SYSTEM FOR PORT NEGOTIATION

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MP12281	MP12281	US	12/615,152	11/9/2009	8116311	2/14/2012	In Force	METHOD AND SYSTEM FOR TAG ARBITRATION IN SWITCHES
MP12282	MP12282	US	12/423,626	4/14/2009	8089971	1/3/2012	In Force	METHOD AND SYSTEM FOR TRANSMITTING FLOW CONTROL INFOR
MP12284	MP12284	US	12/445,873	3/31/2009	8066682	11/15/2011	In Force	METHOD AND SYSTEM FOR MULTI-LEVEL SWITCH CONFIGURATION
MP12288	MP12288	US	12/315,080	11/26/2008	8601139	12/3/2013	In Force	Multiple Core Session Initiation Protocol (SIP)
MP12290	MP12290	US	12/552,222	9/1/2009	8155022	4/10/2012	In Force	METHOD AND SYSTEM FOR CONTROLLING POWER CONSUMPTION I
MP12291	MP12291	US	12/731,065	3/24/2010	8351448	1/8/2013	In Force	METHOD AND SYSTEM FOR EXTENDED PORT ADDRESSING
MP12291	MP12291C1	US	13/711,380	12/11/2012	9071558	6/30/2015	In Force	METHOD AND SYSTEM FOR EXTENDED PORT ADDRESSING
MP12292	MP12292	US	12/410,277	3/24/2009	8068338	11/29/2011	In Force	NETWORK DEVICE WITH BAFFLE FOR REDIRECTING COOLING AIR AN
MP12293	MP12293	US	12/551,302	8/31/2009	8978052	3/10/2015	In Force	SYSTEM AND METHODS FOR INTER-DRIVER COMMUNICATION
MP12295	MP12295	US	12/774,574	5/5/2010	8765351	4/22/2014	In Force	METHOD AND SYSTEM FOR LOAD BALANCING IN NETWORKS
MP12296	MP12296	US	12/471,331	5/22/2009	8069293	11/29/2011	In Force	FLEXIBLE SERVER NETWORK CONNECTION UPGRADE SYSTEMS AND
MP12298	MP12298C1	US	13/447,652	4/16/2012	8996730	3/31/2015	In Force	SYSTEM AND METHOD TO RESTORE MAXIMUM PAYLOAD SIZE IN A N
MP12299	MP12299	US	12/572,835	10/2/2009	8504750	8/6/2013	In Force	SYSTEM AND METHOD TO PROCESS EVENT REPORTING IN AN ADAPT
MP12299	MP12299C1	US	13/836,848	4/4/2013	9043519	5/26/2015	In Force	SYSTEM AND METHOD TO PROCESS EVENT REPORTING IN AN ADAPT
MP12304	MP12304	US	12/544,658	8/20/2009	8065454	11/22/2011	In Force	SYSTEM AND METHOD FOR MAPPING FUNCTIONS TO AND FROM NE
MP12304	MP12304D1	US	13/221,737	8/30/2011	8225018	7/17/2012	In Force	SYSTEM AND METHOD FOR MAPPING FUNCTIONS TO AND FROM NE
MP12305	MP12305	US	12/859,690	8/19/2010	8706905	4/22/2014	In Force	METHOD AND SYSTEM FOR ROUTING INFORMATION IN A NETWORK
MP12306	MP12306	US	12/572,810	10/2/2009	8671228	3/1/2014	In Force	SYSTEM AND METHODS FOR MANAGING VIRTUAL ADAPTER INSTAN
MP12307	MP12307	US	12/651,270	12/31/2009	7970927	6/28/2011	In Force	CONCURRENT TRANSMIT PROCESSING
MP12307	MP12307C1	US	13/111,475	5/19/2011	8275903	9/25/2012	In Force	CONCURRENT TRANSMIT PROCESSING
MP12309	MP12309	US	12/691,629	1/21/2010	9329794	5/3/2016	In Force	SYSTEM AND METHODS FOR DATA MIGRATION
MP1231	MP1231	US	11/833,827	8/3/2007	8699601	4/15/2014	In Force	DISTANCE-LEVEL COMBINING FOR MIMO SYSTEMS WITH HARQ AND
MP1231	MP1231C1	US	14/167,661	1/29/2014	8787486	7/22/2014	In Force	DISTANCE-LEVEL COMBINING FOR MIMO SYSTEMS WITH HARQ AND
MP12310	MP12310	US	12/695,648	1/28/2010	8386194	1/15/2013	In Force	METHOD AND APPARATUS FOR ESTIMATING OVERSHOOT POWER AF
MP12310	MP12310D1	US	13/713,650	12/13/2012	8868949	10/21/2014	In Force	Method And Apparatus For Power Control
MP12310	MP12310D1C1	US	14/488,724	9/17/2014	9703351	7/11/2017	In Force	Method And Apparatus For Power Control
MP12310	MP12310WQ	WO	PCT/US2011/02	1/24/2011	DNA		Pending	Method And Apparatus For Power Control
MP12310	MP12310WCEP	EP	11703077.5	1/24/2011	2539282	11/4/2015	In Force	Method And Apparatus For Power Control
MP12310	MP12310WCEPDE	DE	11703077.5	1/24/2011	602011021124	11/4/2015	In Force	Method And Apparatus For Power Control
MP12310	MP12310WCEPSE	SE	11703077.5	1/24/2011	2539282	11/4/2015	In Force	Method And Apparatus For Power Control
MP12310	MP12310WQJP	JP	2012-551212	1/24/2011	JP5827242	12/2/2015	In Force	METHOD AND APPARATUS FOR POWER CONTROL
MP12310	MP12310WQJPD1	JP	2015-074985	4/1/2015	JP6154843	6/9/2017	In Force	POWER CONTROL METHOD AND POWER CONTROL APPARATUS
MP12313	MP12313	US	12/790,139	3/23/2010	8406152	3/26/2013	In Force	SYSTEM AND METHODS FOR PRESENTING STORAGE
MP12313	MP12313C1	US	13/770,828	2/19/2013	8873430	10/28/2014	In Force	SYSTEM AND METHODS FOR PRESENTING STORAGE

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MP12314	MP12314	US	12/759,316	4/13/2010	8307111	11/6/2012	In Force	SYSTEMS AND METHODS FOR BANDWIDTH SCAVENGING AMONG A
MP12314	MP12314C1	US	13/644,203	10/3/2012	9003038	4/7/2015	In Force	SYSTEMS AND METHODS FOR BANDWIDTH SCAVENGING AMONG A
MP12315	MP12315	US	13/090,882	4/20/2011	8661266	2/25/2014	In Force	SYSTEM AND METHOD FOR SECURE DEVICE KEY STORAGE
MP12316	MP12316	US	12/769,463	4/28/2010	8826271	9/2/2014	In Force	Method And Apparatus For A Virtual System On Chip
MP12316	MP12316C1	US	14/452,873	8/6/2014	9378033	6/28/2016	In Force	Method And Apparatus For A Virtual System On Chip
MP12316	MP12316C1C1	US	15/192,214	6/24/2016	9665300	5/30/2017	In Force	Method And Apparatus For Virtualization
MP12316	MP12316C1C1C1	US	15/499,177	4/27/2017	9823868	11/21/2017	In Force	Method And Apparatus For Virtualization
MP12316	MP12316C2	US	14/452,878	8/6/2014	10146463	12/4/2018	In Force	Method And Apparatus For A Virtual System On Chip
MP12317	MP12317W	TW	96129266	8/8/2007			Pending	DISTANCE-LEVEL COMBINING FOR MIMO SYSTEMS WITH HARQ AND
MP1231W	MP1231WQ	WO	PCT/US2007/01	8/8/2007	DNA		Pending	DISTANCE-LEVEL COMBINING FOR MIMO SYSTEMS WITH HARQ AND
MP12320	MP12320	US	12/774,608	5/5/2010	9781477	10/3/2017	In Force	SYSTEM AND METHOD FOR LOW-LATENCY MULTIMEDIA STREAMING
MP12321	MP12321	US	12/795,563	6/7/2010	8315268	11/20/2012	In Force	METHOD AND SYSTEM FOR COMMUNICATION WITH AN ADAPTER
MP12325	MP12325	US	12/826,105	6/29/2010	8250252	8/21/2012	In Force	SYSTEM AND METHODS FOR USING A DMA MODULE FOR A PLURALI
MP12325	MP12325C1	US	13/489,016	6/5/2012	8392629	3/5/2013	In Force	SYSTEM AND METHODS FOR USING A DMA MODULE FOR A PLURALI
MP12326	MP12326	US	12/914,852	10/28/2010	8478909	7/2/2013	In Force	METHOD AND SYSTEM FOR COMMUNICATION ACROSS MULTIPLE CH
MP12327	MP12327	US	12/848,184	8/1/2010	8544106	9/24/2013	In Force	SYSTEM AND METHOD FOR ENABLING ACCESS TO A PROTECTED HAR
MP12330	MP12330	US	13/216,598	8/24/2011	8441922	5/14/2013	In Force	METHOD AND SYSTEM FOR CONGESTION MANAGEMENT IN NETWO
MP12331	MP12331	US	12/901,409	10/8/2010	8510527	8/13/2013	In Force	SYSTEM AND METHODS FOR MIGRATING INFORMATION FROM SOU
MP12332	MP12332	US	12/902,020	10/11/2010	8301817	10/30/2012	In Force	RING BUS FOR SHARING RESOURCES AMONG MULTIPLE ENGINES
MP12333	MP12333	US	12/901,994	10/11/2010	8386644	2/26/2013	In Force	SYSTEMS AND METHODS FOR EFFICIENTLY PROCESSING LARGE DATA
MP12335	MP12335	US	12/941,621	11/8/2010	8565573	10/22/2013	In Force	SELECTABLE INITIALIZATION FOR ADAPTERS
MP12337	MP12337	US	12/964,295	12/9/2010	9426097	8/23/2016	In Force	FACILITATING COMMUNICATION BETWEEN DEVICES IN A NETWORK
MP12338	MP12338	US	13/325,943	12/14/2011	8977944	3/10/2015	In Force	DRAM Address Protection
MP12338	MP12338C1	US	14/629,770	2/24/2015	9239753	1/19/2016	In Force	DRAM Address Protection
MP12339	MP12339	US	13/326,091	12/14/2011	9556193	3/14/2017	In Force	Messaging With Flexible Transmitt Ordering
MP12339	MP12339D1	US	14/026,293	9/13/2013	9065781	6/23/2015	In Force	Messaging With Flexible Transmitt Ordering
MP12339	MP12339D1C1	US	14/746,674	6/22/2015	9264385	2/16/2016	In Force	Messaging With Flexible Transmitt Ordering
MP12343	MP12343	US	13/369,652	2/9/2012	8535572	9/3/2013	In Force	Level-Up Shifter Circuit
MP12343	MP123431	US	13/968,523	8/16/2013	8896360	11/25/2014	In Force	Level-Up Shifter Circuit For High Speed And Low Power Applications
MP12344	MP12344	US	13/369,562	2/9/2012	8519785	8/27/2013	In Force	Differential Amplifier With Duty Cycle Compensation
MP12345	MP12345	US	13/397,550	2/15/2012	8536944	9/17/2013	In Force	Differential Amplifier With De-Emphasis
MP12346	MP12346	US	13/397,303	2/15/2012	8634509	1/21/2014	In Force	Synchronized Clock Phase Interpolator
MP12347	MP12347	US	13/396,824	2/15/2012	9263151	2/16/2016	In Force	Memory Interface With Selectable Evaluation Modes
MP12348	MP12348	US	13/369,579	2/9/2012	8513994	8/20/2013	In Force	State Machine For Deskew Delay Locked Loop

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MP12349	MP12349	US	13/370,784	2/10/2012	9143140	9/22/2015	In Force	Multi-Function Delay Locked Loop
MP12349	MP12349C1	US	14/639,476	3/5/2015	9306584	4/5/2016	In Force	Multi-Function Delay Locked Loop
MP1235	MP1235	US	11/836,619	8/9/2007	7595745	9/29/2009	In Force	CIRCUIT FOR CONVERTING A VOLTAGE RANGE OF A LOGIC SIGNAL
MP12350	MP12350	US	13/029,018	2/16/2011	8662632	3/25/2014	In Force	SIMULATION SYSTEM AND METHOD THEREOF
MP12352	MP12352	US	13/035,114	2/25/2011	8890732	11/4/2014	In Force	METHOD AND SYSTEM FOR APPLICATION ISOLATION
MP12353	MP12353	US	13/168,395	6/24/2011	9388033	7/19/2016	In Force	Regular Expression Processing Automation
MP12354	MP12354	US	13/087,817	4/15/2011	8456331	6/4/2013	In Force	System And Method Of Compression And Decompression
MP12354	MP12354C1	US	13/888,851	5/7/2013	9054729	6/9/2015	In Force	System And Method Of Compression And Decompression
MP12355	MP12355	US	13/067,051	5/4/2011	8831108	9/9/2014	In Force	Low latency rate control system and method
MP12355	MP12355D1	US	14/480,469	9/8/2014	9445107	9/13/2016	In Force	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355HK	HK	14104540.1	5/4/2012	11914858	11/24/2017	In Force	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355JPD1	JP	2016-029840	2/19/2016	JP6226490	11/8/2017	In Force	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355JPD2	JP	2017-143464	7/25/2017			Pending	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355WO	WO	PCT/US2012/03	5/4/2012	DNA		Pending	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355WOCN	CN	2012800316771	5/4/2012	Z12012800316	2/1/2017	In Force	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355WQJP	JP	2014-509498	5/4/2012			Pending	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355WOKR	KR	10-2013-703224	5/4/2012	101809306	12/14/2017	In Force	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12356	MP12356	US	13/067,050	5/4/2011	9055672	5/5/2015	In Force	ON-DEMAND INTRA-REFRESH FOR END-TO-END CODED VIDEO TRAN
MP12356	MP12356HK	HK	14106723.5	5/4/2012	11932928	12/8/2017	In Force	ON-DEMAND INTRA-REFRESH FOR END-TO-END CODED VIDEO TRAN
MP12356	MP12356WO	WO	PCT/US2012/03	5/4/2012	DNA		Pending	ON-DEMAND INTRA-REFRESH FOR END-TO-END CODED VIDEO TRAN
MP12356	MP12356WOCN	CN	2012800316624	5/4/2012	Z12012800316	2/15/2017	In Force	ON-DEMAND INTRA-REFRESH FOR END-TO-END CODED VIDEO TRAN
MP12356	MP12356WQJP	JP	2014-509499	5/4/2012	JP5784823	9/24/2015	In Force	ON-DEMAND INTRA-REFRESH FOR END-TO-END CODED VIDEO TRAN
MP12356	MP12356WOKR	KR	10-2013-703224	5/4/2012		3/6/2019	In Force	ON-DEMAND INTRA-REFRESH FOR END-TO-END CODED VIDEO TRAN
MP12359	MP12359	US	13/105,401	5/11/2011	8350732	1/8/2013	In Force	Compression With Adjustable Quality/Bandwidth Capability
MP12360	MP12360	US	13/168,450	6/24/2011	9858051	1/2/2018	In Force	Regex Compiler
MP12360	MP12360HK	HK	14106732.4	6/20/2012	1193278	3/9/2018	In Force	Compiler For Regular Expressions
MP12360	MP12360WO	WO	PCT/US2012/04	6/20/2012	DNA		Pending	Compiler For Regular Expressions
MP12360	MP12360WOCN	CN	2012800387992	6/20/2012	Z12012800387	5/3/2017	In Force	Compiler For Regular Expressions
MP12360	MP12360WOCND1	CN	2017102585709	6/20/2012	Z12012800387	5/3/2017	In Force	Compiler For Regular Expressions
MP12360	MP12360WODE	DE	112012002624.1	6/20/2012			Pending	Compiler For Regular Expressions
MP12360	MP12360WOKRD1	KR	1020167020582	6/20/2012		7/17/2018	In Force	Compiler For Regular Expressions
MP12361	MP12361	US	13/168,323	6/24/2011	8990259	3/24/2015	In Force	Anchored Patterns
MP12361	MP12361C1	US	14/632,448	2/26/2015	9514246	12/6/2016	In Force	Anchored Patterns
MP12361	MP12361WO	WO	PCT/US2012/04	6/20/2012	DNA		Pending	Anchored Patterns

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MP12361	MP12361WOKR	KR	1020147001978	6/20/2012		7/14/2015	In Force	Anchored Patterns
MP12362	MP12362	US	13/067,880	7/1/2011	9025665	5/5/2015	In Force	Video encoder bit estimator for macroblock encoding
MP12362	MP12362D1	US	14/329,843	7/11/2014	9094669	7/28/2015	In Force	VIDEO ENCODER BIT ESTIMATOR FOR MACROBLOCK ENCODING
MP12363	MP12363	US	13/181,270	7/12/2011	8488601	7/16/2013	In Force	METHOD AND SYSTEM FOR LINK AGGREGATION
MP12364	MP12364	US	13/181,251	7/12/2011	8467395	6/18/2013	In Force	METHOD AND SYSTEM FOR LINK AGGREGATION
MP12365	MP12365	US	13/193,462	7/28/2011	9071544	6/30/2015	In Force	METHOD AND SYSTEM FOR MANAGING NETWORK ELEMENTS
MP12366	MP12366	US	13/565,271	8/2/2012	10277510	4/30/2019	In Force	System and Method for Storing lookup Request Rules in Multiple M
MP12367	MP12367	US	13/565,784	8/2/2012	8937952	1/20/2015	In Force	Packet Classification
MP12367	MP12367C1	US	13/664,015	10/30/2012	8934488	1/13/2015	In Force	Identifying Duplication in Decision Trees
MP12367	MP12367C11	US	13/831,487	3/14/2013	9208438	12/8/2015	In Force	Duplication in Decision Trees
MP12367	MP12367C2	US	13/686,269	11/27/2012	8937954	1/20/2015	In Force	Decision Tree Level Merging
MP12367	MP12367C3	US	14/570,626	12/15/2014	9191321	11/17/2015	In Force	Packet Classification
MP12367	MP12367HK	HK	14107043.6	8/2/2012	1193686	6/1/2018	In Force	Packet Classification By An Optimised Decision Tree
MP12367	MP12367WQ	WO	PCT/US2012/04	8/2/2012	DNA		Pending	Packet Classification By An Optimised Decision Tree
MP12367	MP12367WQCN	CN	201280048417.1	8/2/2012	21.2012800484	8/25/2017	In Force	Packet Classification By An Optimised Decision Tree
MP12368	MP12368	US	13/565,389	8/2/2012	9586222	3/14/2017	In Force	Method and apparatus encoding a rule for a lookup request in a proc
MP12369	MP12369	US	13/565,406	8/2/2012	9344366	5/17/2016	In Force	System and method for rule matching in a processor
MP12369	MP12369C1	US	15/145,052	5/3/2016	9866540	1/9/2018	In Force	System And Method For Rule Matching In A Processor
MP12370	MP12370	US	13/565,727	8/2/2012	8711861	4/29/2014	In Force	Lookup Front End Packet Input Processor
MP12370	MP12370C1	US	13/599,276	8/30/2012	9729527	8/8/2017	In Force	Lookup Front End Packet Input Processor
MP12370	MP12370C2	US	14/138,931	12/23/2013	9031075	5/12/2015	In Force	Lookup Front End Packet Input Processor
MP12370	MP12370WQ	WO	PCT/US2012/04	8/2/2012	DNA		Pending	LOOKUP FRONT END INPUT PROCESSOR
MP12370	MP12370WOKR	KR	1020147005800	8/2/2012		12/17/2014	In Force	LOOKUP FRONT END INPUT PROCESSOR
MP12371	MP12371	US	13/565,767	8/2/2012	8606959	12/10/2013	In Force	Lookup Front End Packet Output Processor
MP12371	MP12371C1	US	14/082,365	11/18/2013	9497117	11/15/2016	In Force	Lookup Front End Packet Output Processor
MP12371	MP12371WQ	WO	PCT/US2012/04	8/2/2012	DNA		Pending	Lookup Front End Output Processor
MP12371	MP12371WQJP	JP	2014-524092	8/2/2012	JP5657840	1/21/2015	In Force	LOOKUP FRONT END PACKET INPUT PROCESSOR
MP12372	MP12372	US	13/565,741	8/2/2012	9331892	7/12/2016	In Force	Method and apparatus for managing transport operations to a cluste
MP12372	MP12372-2	US	13/565,743	8/2/2012	9319316	4/19/2016	In Force	Method and apparatus for managing transfer of transport operations
MP12372	MP12372-3	US	13/565,746	8/2/2012	9525630	12/20/2016	In Force	Method and Apparatus for Assigning Resources Used To Manage Tra
MP12372	MP12372-4	US	13/565,749	8/2/2012	8954700	2/10/2015	In Force	Method and Apparatus for Managing Processing Thread Migration Be
MP12372	MP12372-4C1	US	14/592,384	1/8/2015	9531690	12/27/2016	In Force	Method and Apparatus for Managing Processing Thread Migration Be
MP12373	MP12373	US	13/565,775	8/2/2012	9137340	9/15/2015	In Force	Incremental Update
MP12373	MP12373CN	CN	2012800482910	8/2/2012	21.2012800482	12/14/2018	In Force	Incremental Update of Rules for Packet Classification

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MP12373	MP12373DE	DE	102014 001 498	2/5/2014			Pending	Incremental Update Heuristics
MP12373	MP12373HK	HK	141114/78.2	2/8/2012			Pending	Incremental Update of Rules for Packet Classification
MP12373	MP1237311	US	13/831,232	3/14/2013	9183244	11/10/2015	In Force	Rule Modification in Decision Trees
MP12373	MP1237312	US	13/831,191	3/14/2013	10229139	3/12/2019	In Force	Incremental Update Heuristics
MP12373	MP12373WO	WO	PCT/US2012/04	8/2/2012	DNA		Pending	Incremental Update of Rules for Packet Classification
MP12374	MP12374	US	13/565,735	8/2/2012	9065860	6/23/2015	In Force	Method And Apparatus For Multiple Access Of Plural Memory Banks
MP12374	MP12374-2	US	13/565,736	8/2/2012	8966152	2/24/2015	In Force	On-Chip Memory (OCM) Physical Bank Parallelism
MP12375	MP12375	US	13/565,459	8/2/2012	8719331	5/6/2014	In Force	Work Migration in A Processor
MP12375	MP12375C1	US	14/240,698	3/31/2014	9614762	4/4/2017	In Force	Work Migration in A Processor
MP12376	MP12376	US	13/565,422	8/2/2012	8472452	6/25/2013	In Force	Lookup Cluster Complex
MP12376	MP12376C1	US	13/901,220	5/23/2013	8995449	3/31/2015	In Force	Lookup Cluster Complex
MP12376	MP12376C1C1	US	14/660,132	3/17/2015	9225643	12/29/2015	In Force	Lookup Cluster Complex
MP12376	MP1237611	US	13/843,353	3/15/2013	8923306	12/30/2014	In Force	Phased Bucket Pre-Fetch in A Network Processor
MP12376	MP1237611C1	US	14/583,968	12/29/2014	9531723	12/27/2016	In Force	Phased Bucket Pre-Fetch in A Network Processor
MP12376	MP12376WO	WO	PCT/US2012/04	8/2/2012	DNA		Pending	Lookup Cluster Complex
MP12376	MP12376WOKR	KR	102014 7005633	8/2/2012		12/17/2014	In Force	Lookup Cluster Complex
MP12377	MP12377	US	13/220,249	8/29/2011	8798047	8/5/2014	In Force	METHODS AND SYSTEMS FOR PROCESSING NETWORK INFORMATION
MP12378	MP12378	US	13/238,485	9/21/2011	8572328	10/29/2013	In Force	METHOD AND SYSTEM FOR MEMORY MANAGEMENT
MP12378	MP12378C1	US	14/036,920	9/25/2013	8918593	12/23/2014	In Force	METHOD AND SYSTEM FOR MEMORY MANAGEMENT
MP12379	MP12379	US	13/433,146	3/28/2012	8953606	2/10/2015	In Force	FLEXIBLE EDGE ACCESS SWITCH AND ASSOCIATED METHODS THEREOF
MP1238	MP1238	US	11/524,789	9/20/2005	8040953	10/18/2011	In Force	RELIABILITY METRIC GENERATION FOR TRELIS-BASED DETECTION A
MP1238	MP1238CN	CN	200780043023.1	9/20/2007	212007800430	3/27/2013	In Force	RELIABILITY METRIC GENERATION FOR TRELIS-BASED DETECTION A
MP12380	MP12380	US	13/397,002	2/15/2012	8873236	10/28/2014	In Force	ELECTRONIC DEVICES HAVING COOLING MODULE WITH DIRECTION-
MP12381	MP12381	US	13/272,975	10/13/2011	9129060	9/8/2015	In Force	QoS Based Dynamic Execution Engine Selection
MP12381	MP12381C1	US	14/836,884	8/18/2015	9495161	11/15/2016	In Force	QoS Based Dynamic Execution Engine Selection
MP12382	MP12382	US	13/272,937	10/13/2011	918769	9/8/2015	In Force	Processor With Dedicated Virtual Functions And Dynamic Assignment
MP12383	MP12383	US	13/274,767	10/17/2011	9465662	10/11/2016	In Force	Processor With Efficient Work Queuing
MP12383	MP12383HK	HK	14109347.5	9/19/2012	1195959	6/15/2018	In Force	Processor With Efficient Work Queuing
MP12383	MP12383WO	WO	PCT/US2012/05	9/19/2012	DNA		Pending	Processor With Efficient Work Queuing
MP12383	MP12383WOCN	CN	2012800564728	9/19/2012	212012800564	9/29/2017	In Force	Processor With Efficient Work Queuing
MP12383	MP12383WOKR	KR	102014 7013222	9/19/2012		7/17/2019	In Force	Processor With Efficient Work Queuing
MP12384	MP12384	US	13/277,613	10/20/2011	8885480	11/11/2014	In Force	Packet Priority in A Network Processor
MP12385	MP12385	US	13/281,059	10/25/2011	9065626	6/23/2015	In Force	Bit Error Rate Impact Reduction
MP12387	MP12387	US	13/280,756	10/25/2011	8850125	9/30/2014	In Force	System And Method To Provide Non-Coherent Access To A Coherent

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MP12387	MP12387C1	US	14/466,384	8/22/2014	9559366	2/14/2017	In Force	System And Method To Provide Non-Coherent Access To A Coherent
MP12388	MP12388	US	13/280,738	10/25/2011	8566757	10/15/2013	In Force	System And Method To Reduce Memory Access Latencies Using Selec
MP12388	MP12388C1	US	14/023,953	9/11/2013	8850101	9/30/2014	In Force	System And Method To Reduce Memory Access Latencies Using Selec
MP12388	MP12388HK	HK	14109348.4	9/16/2014	1135360	4/28/2017	In Force	System And Method To Reduce Memory Access Latencies Using Selec
MP12388	MP12388WO	WO	PCT/US2012/05	9/26/2012	DNA		Pending	System And Method To Reduce Memory Access Latencies Using Selec
MP12388	MP12388WOCN	CN	201280057867X	9/26/2012	Z12012800578	6/29/2016	In Force	System And Method To Reduce Memory Access Latencies Using Selec
MP12388	MP12388WOKR	KR	1020147013564	5/20/2014		4/24/2019	In Force	System And Method To Reduce Memory Access Latencies Using Selec
MP12389	MP12389	US	13/281,052	10/25/2011	8855248	10/7/2014	In Force	Word Boundary lock
MP12389	MP12389C1	US	14/492,557	9/22/2014	9059836	6/16/2015	In Force	Word Boundary lock
MP1238W	MP1238WO	WO	PCT/US2007/02	9/20/2007	DNA		Pending	RELIABILITY METRIC GENERATION FOR TRELLIS-BASED DETECTION A
MP12390	MP12390	US	13/280,841	10/25/2011	9219560	12/22/2015	In Force	Multi-Protocol Serdes Phy Apparatus
MP12390	MP12390HK	HK	14107924.0	9/26/2012	1134544	12/8/2017	In Force	Multi-Protocol Serdes Phy Apparatus
MP12390	MP12390WO	WO	PCT/US2012/05	9/26/2012	DNA		Pending	Multi-Protocol Serdes Phy Apparatus
MP12390	MP12390WOCN	CN	2012800523925	9/26/2012	Z12012800523	11/9/2016	In Force	Multi-protocol serdes phy apparatus
MP12390	MP12390WODE	DE	112012004451	9/26/2012			Pending	Multi-Protocol Serdes Phy Apparatus
MP12390	MP12390WOKR	KR	1020147014165	9/26/2012		8/4/2016	In Force	MULTI-PROTOCOL SERDES PHY APPARATUS
MP12391	MP12391	US	13/280,768	10/25/2011	8473658	6/25/2013	In Force	Input Output Bridging
MP12391	MP12391C1	US	13/905,750	5/30/2013	8555401	11/26/2013	In Force	Input Output Bridging
MP12392	MP12392	US	13/328,919	12/16/2011	9054333	7/28/2015	In Force	SYSTEMS AND METHODS FOR SENDING AND RECEIVING INFORMATI
MP12392	MP12392C1	US	14/747,790	6/23/2015	9401879	7/26/2016	In Force	SYSTEMS AND METHODS FOR SENDING AND RECEIVING INFORMATI
MP12393	MP12393	US	13/283,252	10/27/2011	9966468	2/27/2018	In Force	Packet Traffic Control In A Network Processor
MP12394	MP12394	US	13/284,289	10/28/2011	9612934	4/4/2017	In Force	Network Processor With Distributed Trace Buffers
MP12395	MP12395	US	13/285,773	10/31/2011	9059945	6/16/2015	In Force	Work Request Processor
MP12395	MP12395C1	US	14/670,934	3/27/2015	9529640	12/27/2016	In Force	Work Request Processor
MP12396	MP12396	US	13/403,697	2/23/2012	8853366	11/25/2014	In Force	TOOLS, SYSTEMS, AND METHODS FOR REMOVING CONNECTORS FRO
MP12397	MP12397	US	13/285,629	10/31/2011	9330002	5/3/2016	In Force	Multi-Core Interconnect In A Network Processor
MP12397	MP12397HK	HK	14109343.9	10/29/2012			Pending	Multi-Core Interconnect In A Network Processor
MP12397	MP12397WO	WO	PCT/US2012/06	10/29/2012	DNA		Pending	Multi-Core Interconnect In A Network Processor
MP12397	MP12397WOCN	CN	2012800592395	10/29/2012	Z12012800592	6/21/2019	In Force	Multi-Core Interconnect In A Network Processor
MP12397	MP12397WODE	DE	1120120045511	10/29/2012			Pending	Multi-Core Interconnect In A Network Processor
MP12397	MP12397WOJP	JP	2014-539104	10/29/2012			Pending	Multi-Core Interconnect In A Network Processor
MP12397	MP12397WOJPD1	JP	2017-213851	11/6/2017			Pending	MULTI-CORE INTERCONNECT IN NETWORK PROCESSOR
MP12397	MP12397WOKR	KR	1020147012490	10/29/2012			Pending	Multi-Core Interconnect In A Network Processor
MP12398	MP12398	US	13/303,885	11/23/2011	9203805	12/1/2015	In Force	Reverse NFA Generation And Processing

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MP12398	MP12398C1	US	14/863,484	9/24/2015	9762544	9/12/2017	In Force	Reverse NFA Generation And Processing
MP12401	MP12401WO	WO	PCT/US2013/03	3/20/2013	DNA		Pending	Hardware And Software Association And Authentication
MP12402	MP12402	US	13/447,521	4/16/2012	9164913	10/20/2015	In Force	METHOD AND SYSTEM FOR OFFLOADING COPY ON WRITE OPERATION
MP12403	MP12403	US	13/493,122	5/25/2012	8989220	3/24/2015	In Force	High Speed Variable Bandwidth Ring-Based System
MP12404	MP12404-1	US	13/790,395	3/8/2013	9350807	5/24/2016	In Force	INTELLIGENT ADAPTER FOR PROVIDING STORAGE AREA NETWORK A
MP12404	MP12404-2	US	13/790,471	3/8/2013	9330003	5/3/2016	In Force	INTELLIGENT ADAPTER FOR MAINTAINING CACHE COHERENCY
MP12404	MP12404-3	US	13/790,499	3/8/2013	9567524	11/29/2016	In Force	IN-BAND MANAGEMENT USING AN INTELLIGENT ADAPTER AND ME
MP12404	MP12404-5	US	13/902,427	5/24/2013	9172586	10/27/2015	In Force	METHOD AND SYSTEM FOR WRITING TAG AND DATA
MP12404	MP12404-6	US	13/790,846	3/8/2013	9232005	1/5/2016	In Force	METHODS AND SYSTEMS FOR AN INTELLIGENT STORAGE ADAPTER U
MP12405	MP12405	US	13/594,343	8/24/2012	9229791	1/5/2016	In Force	SYSTEM AND METHOD FOR HIGH SPEED MULTIPLE BUFFER ALLOCAT
MP12406	MP12406	US	13/644,535	10/4/2012	9265662	2/9/2016	In Force	METHOD AND SYSTEM FOR COMMUNICATION BETWEEN A COMPUT
MP12407	MP12407	US	13/649,391	10/11/2012	9727494	8/8/2017	In Force	METHOD AND SYSTEM FOR COMMUNICATION BETWEEN A COMPUT
MP12409	MP12409	US	13/654,308	10/17/2012	9069919	6/30/2015	In Force	METHOD AND SYSTEM FOR ARBITRATION VERIFICATION
MP1241	MP1241	US	11/700,433	1/31/2007	7911326	3/22/2011	In Force	Time Updating And Load Management Systems
MP12410	MP12410	US	14/062,418	10/24/2013	9424226	8/23/2016	In Force	METHOD AND SYSTEM FOR SIGNAL EQUALIZATION IN COMMUNICAT
MP12411	MP12411	US	13/660,928	10/25/2012	8677044	3/18/2014	In Force	METHOD AND SYSTEM FOR COMMUNICATION USING MULTIPLE DM
MP12412	MP12412	US	14/058,887	10/21/2013	9311268	4/12/2016	In Force	METHOD AND SYSTEM FOR COMMUNICATION WITH PERIPHERAL DE
MP12413	MP12413	US	13/924,107	6/21/2013	9338059	5/10/2016	In Force	SYSTEM AND METHODS FOR MANAGING NETWORKS
MP12414	MP12414	US	13/852,885	3/28/2013	9100334	8/4/2015	In Force	METHOD AND SYSTEM FOR VARIABLE LENGTH TAG PROCESSING
MP12415	MP12415	US	13/852,872	3/28/2013	9143415	9/22/2015	In Force	METHOD AND SYSTEM FOR DETECTING LOSS OF SYNCHRONIZATION
MP12416	MP12416	US	13/678,108	11/15/2012	8995455	3/31/2015	In Force	MEMORY DEVICES FOR NETWORK DEVICES AND ASSOCIATED METH
MP12417	MP12417	US	13/678,121	11/15/2012	9225808	12/29/2015	In Force	SYSTEMS AND METHODS FOR PROCESSING INFORMATION BY A NET
MP12418	MP12418	US	13/678,398	11/15/2012	9046941	6/2/2015	In Force	SYSTEM AND METHOD FOR SHARING LOGIC IN A NETWORK DEVICE
MP12419	MP12419	US	13/678,047	11/15/2012	8995425	3/31/2015	In Force	NETWORK DEVICE SCHEDULER AND METHODS THEREOF
MP12419	MP12419D1	US	14/658,544	3/16/2015	9590924	3/7/2017	In Force	NETWORK DEVICE SCHEDULER AND METHODS THEREOF
MP1241T	MP1241TW	TW	97160104	1/2/2008		5/1/2014	In Force	Master Generator For Updating Time Data Of Devices, And Integrate
MP1241W	MP1241WO	WO	PCT/US2007/02	12/28/2007	DNA		Pending	Time Updating And Load Management Systems
MP1242	MP1242	US	11/964,172	12/26/2007	8049599	11/1/2011	In Force	Power Control Device
MP1242	MP1242C1	US	13/286,072	10/31/2011	8164428	4/24/2012	In Force	Power Control Device
MP12420	MP12420	US	13/678,055	11/15/2012	8989191	3/24/2015	In Force	SYSTEMS AND METHODS FOR HARD ZONING IN NETWORKS
MP12421	MP12421	US	13/678,086	11/15/2012	9444713	9/13/2016	In Force	CUT-THROUGH ROUTING FOR NETWORK DEVICES
MP12422	MP12422	US	13/678,161	11/15/2012	8995457	3/31/2015	In Force	SYSTEMS AND METHODS FOR MODIFYING FRAMES IN A NETWORK D
MP12423	MP12423	US	13/678,135	11/15/2012	9172655	10/27/2015	In Force	SYSTEMS AND METHODS FOR QUALITY OF SERVICE IN NETWORKS
MP12424	MP12424	US	13/678,378	11/15/2012	9426063	8/23/2016	In Force	SYSTEMS AND METHODS FOR ROUTING BY NETWORK DEVICES

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MP12425	MP12425	US	13/678,385	11/15/2012	9225672	12/29/2015	In Force	SYSTEMS AND METHODS FOR PACKET GROUPING IN NETWORKS
MP12426	MP12426	US	13/678,377	11/15/2012	8996798	3/31/2015	In Force	SYSTEMS AND METHODS FOR USING TCAMS IN NETWORK DEVICES
MP12427	MP12427	US	13/678,100	11/15/2012	8924764	12/30/2014	In Force	SYSTEMS AND METHODS FOR RATE MATCHING IN NETWORKS UTILIZ
MP12428	MP12428	US	13/678,367	11/15/2012	9071559	6/30/2015	In Force	NETWORK DEVICES HAVING CONFIGURABLE RECEIVE PACKET QUEUE
MP12428	MP12428D1	US	14/749,964	6/25/2015	9282000	3/8/2016	In Force	NETWORK DEVICES HAVING CONFIGURABLE RECEIVE PACKET QUEUE
MP1242T	MP1242TW	TW	96149439	12/21/2007		5/1/2014	In Force	Power Control Device
MP1242W	MP1242WO	WO	PCT/US2007/02	12/26/2007	DNA		Pending	Power Control Device
MP1242W	MP1242WOP	JP	2009-542968	12/26/2007	JP5229505	3/29/2013	In Force	POWER CONTROL DEVICE
MP12431	MP12431	US	13/678,145	11/15/2012	8976667	3/10/2015	In Force	METHOD AND SYSTEM FOR PROGRAMMABLE DELAY BEFORE TRANS
MP12432	MP12432	US	13/830,717	3/14/2013	9282046	3/8/2016	In Force	SMOOTHING FIFO AND METHODS THEREOF
MP12433	MP12433	US	13/678,418	11/15/2012	9172602	10/27/2015	In Force	METHOD AND SYSTEM FOR AUTO-NEGOTIATION
MP12434	MP12434	US	13/678,397	11/15/2012	9049113	6/2/2015	In Force	SYSTEMS AND METHODS FOR PRIMITIVE SEQUENCE CAPTURE AND R
MP12435	MP12435	US	13/678,406	11/15/2012	9172661	10/27/2015	In Force	METHOD AND SYSTEM FOR USING LANE ALIGNMENT MARKERS
MP12436	MP12436	US	13/762,715	2/8/2013	9384113	7/5/2016	In Force	SYSTEMS AND METHODS FOR IDENTIFYING PORT PROTOCOLS USING
MP12438	MP12438	US	13/689,607	11/29/2012	9047151	6/2/2015	In Force	METHODS AND SYSTEMS FOR PROCESSING COMMANDS BY A DEVICE
MP12439	MP12439	US	13/782,673	3/1/2013	9164947	10/20/2015	In Force	METHOD AND SYSTEM FOR INSERTING COOKIES IN I/O COMMANDS
MP1244	MP1244	US	11/775,152	7/9/2007	7941643	5/10/2011	In Force	MULTI-THREAD PROCESSOR WITH MULTIPLE PROGRAM COUNTERS
MP12441	MP12441	US	13/834,838	3/15/2013	8993608	2/10/2015	In Force	METHOD AND SYSTEM FOR FRAME AGGREGATION
MP12442	MP12442	US	14/100,856	12/9/2013	9047208	6/2/2015	In Force	METHOD AND SYSTEM OF CONFIGURING VIRTUAL FUNCTION IN PERI
MP12444	MP12444	US	13/755,770	1/31/2013	8948203	2/3/2015	In Force	METHOD AND SYSTEM FOR USING ASYMMETRIC TRANSFER RATES IN R
MP12446	MP12446	US	14/194,567	2/28/2014	9331942	5/3/2016	In Force	APPARATUS AND METHOD FOR PROCESSING ALTERNATELY CONFIGU
MP12446	MP12446C1	US	15/140,424	4/27/2016	9729447	8/8/2017	In Force	APPARATUS AND METHOD FOR PROCESSING ALTERNATELY CONFIGU
MP12446	MP12446CN	CN	201410090589.3	3/12/2014	6373021	12/7/2018	In Force	APPARATUS AND METHOD FOR PROCESSING ALTERNATELY CONFIGU
MP12446	MP12446HK	HK	15102553.8	3/12/2014	6190754		Pending	APPARATUS AND METHOD FOR PROCESSING ALTERNATELY CONFIGU
MP12446	MP12446JP	JP	2014-47788	3/11/2014	JP6373021	8/15/2018	In Force	Apparatus and method for processing longest prefix match table conf
MP12446	MP12446KR	KR	10-2014-002858	3/11/2014			Pending	APPARATUS AND METHOD FOR PROCESSING ALTERNATELY CONFIGU
MP12446	MP12446TW	TW	103108614	3/12/2014		12/21/2018	In Force	APPARATUS AND METHOD FOR PROCESSING ALTERNATELY CONFIGU
MP12447	MP12447	US	14/142,612	12/27/2013	9256380	2/9/2016	In Force	APPARATUS AND METHOD FOR PACKET MEMORY DATA PATH PROCE
MP12448	MP12448	US	13/830,492	3/14/2013	9706564	7/11/2017	In Force	Apparatus and Method for Media Access Control Scheduling with a P
MP12449	MP12449	US	13/831,415	3/14/2013	10083200	9/25/2018	In Force	Batch Incremental Update
MP1244W	MP1244WO	WO	PCT/US2007/01	8/14/2007	DNA		Pending	A Multi-Thread Processor with Multiple Program Counters
MP12450	MP12450	US	13/830,395	3/14/2013	9237581	1/12/2016	In Force	APPARATUS AND METHOD FOR MEDIA ACCESS CONTROL SCHEDULE
MP12450	MP12450CN	CN	201410145120.4	3/11/2014	Z12014101451	6/11/2019	In Force	APPARATUS AND METHOD FOR MEDIA ACCESS CONTROL SCHEDULE
MP12450	MP12450KR	KR	10-2014-003024	3/14/2014			Pending	APPARATUS AND METHOD FOR MEDIA ACCESS CONTROL SCHEDULE

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MP12451	MP12451-2	US	14/207,928	3/13/2014	9430511	8/30/2016	In Force	Merging Independent Writes, Separating Dependent And Independent
MP12451	MP12451-3	US	14/207,933	3/13/2014	10293144	3/12/2019	In Force	NSP Manager
MP12452	MP12452	US	13/841,743	3/15/2013	9531647	12/27/2016	In Force	Multi-Host Processing
MP12453	MP12453	US	13/886,798	5/17/2013	9555003	3/14/2017	In Force	Compiler With Mask Nodes
MP12455	MP12455	US	13/844,451	3/15/2013	9112767	8/13/2015	In Force	Method And An Accumulator Scoreboard For Out-Of-Order Rule Resp
MP12456	MP12456	US	13/840,867	3/15/2013	9135939	11/24/2015	In Force	Scope In Decision Trees
MP12456	MP12456C1	US	14/922,449	10/26/2015	10460250	10/29/2019	In force	Scope In Decision Trees
MP12457	MP12457	US	13/843,992	3/15/2013	9130819	9/8/2015	In Force	Method And Apparatus For Scheduling Rule Matching In A Processo
MP12457	MP12457CN	CN	201410097575X	3/14/2014	ZL2014100975	4/12/2017	In Force	Scheduling Method And Apparatus For Scheduling Rule Matching In
MP12457	MP12457HK	HK	14110989.6	3/14/2014	1197478	3/9/2018	In Force	Scheduling Method And Apparatus For Scheduling Rule Matching In A
MP12458	MP12458	US	13/843,308	3/15/2013	9276846	3/1/2016	In Force	Packet Extraction Optimization In A Network Processor
MP12459	MP12459	US	13/843,782	3/15/2013	9152494	10/6/2015	In Force	Method And Apparatus For Data Packet Integrity Checking In A Proce
MP1245W	MP1245WO	WO	PCT/US2007/01	6/14/2007	1204166		Pending	Instruction Dispatching Method and Apparatus
MP1246	MP1246	US	11/833,127	8/2/2007	7886131	2/8/2011	In Force	MULTITHREAD PROCESSOR WITH THREAD BASED THROTTLING
MP12460	MP12460	US	14/201,692	3/7/2014	9264357	2/16/2016	In Force	APPARATUS AND METHOD FOR TABLE SEARCH WITH CENTRALIZED
MP12460	MP12460CN	CN	201410182891.1	4/30/2014	ZL2014101826	4/10/2018	In Force	APPARATUS AND METHOD FOR TABLE SEARCH WITH CENTRALIZED
MP12460	MP12460HK	HK	15104281.3	5/5/2015	1204166	7/12/2019	In Force	APPARATUS AND METHOD FOR TABLE SEARCH WITH CENTRALIZED
MP12460	MP12460JP	JP	2014-92766	4/28/2014	JP6190754	8/30/2017	In Force	Apparatus and method for searching a table using a centralized mem
MP12460	MP12460TW	TW	103115489	4/30/2014			Pending	APPARATUS AND METHOD FOR TABLE SEARCH WITH CENTRALIZED
MP12462	MP12462	US	13/902,453	5/24/2013	9747227	8/29/2017	In Force	METHOD AND SYSTEM FOR TRANSMITTING INFORMATION FROM A
MP12463	MP12463	US	13/906,207	5/30/2013	10019203	7/10/2018	In Force	METHOD AND SYSTEM FOR PROCESSING WRITE REQUESTS
MP12464	MP12464	US	13/921,090	6/18/2013			Pending	Apparatus and Method for Uniquely Enumerating Paths in a Parse Tr
MP12464	MP12464CN	CN	201410270176.4	6/17/2014	ZL2014102701	5/5/2019	In Force	APPARATUS AND METHOD FOR UNIQUELY ENUMERATING PATHS IN
MP12464	MP12464JP	JP	2014-113339	6/10/2014	JP6383578	8/29/2018	In Force	APPARATUS AND METHOD FOR UNIQUELY ENUMERATING PATHS IN
MP12464	MP12464KR	KR	10-2014-007435	6/18/2014			Pending	APPARATUS AND METHOD FOR UNIQUELY ENUMERATING PATHS IN
MP12464	MP12464TW	TW	103120983	6/18/2014			Pending	APPARATUS AND METHOD FOR UNIQUELY ENUMERATING PATHS IN
MP12465	MP12465	US	13/912,911	6/20/2013	8898346	11/25/2014	In Force	METHOD AND SYSTEM FOR CONFIGURING NETWORK DEVICES
MP12467	MP12467	US	13/949,095	7/23/2013	9411400	8/9/2016	In Force	METHODS AND SYSTEMS FOR ADVERTISING AVAILABLE CREDIT AT M
MP12468	MP12468CN	CN	201410452112.4	7/24/2014			Pending	NETWORK INTERFACE CARD WITH VIRTUAL SWITCH AND TRAFFIC FL
MP12468	MP12468EP	EP	14177898.5	7/21/2014			Pending	NETWORK INTERFACE CARD WITH VIRTUAL SWITCH AND TRAFFIC FL
MP12468	MP12468HK	HK	15107379.9	7/24/2014			Pending	NETWORK INTERFACE CARD WITH VIRTUAL SWITCH AND TRAFFIC F
MP12468	MP12468JP	JP	2014-151627	7/25/2014			Pending	NETWORK INTERFACE CARD WITH VIRTUAL SWITCH AND TRAFFIC FL
MP12468	MP12468KR	KR	10-2014-009265	7/22/2014			Pending	NETWORK INTERFACE CARD WITH VIRTUAL SWITCH AND TRAFFIC FL
MP12469	MP12469	US	13/954,661	7/30/2013	8977786	3/10/2015	In Force	METHOD AND SYSTEM FOR PROCESSING INFORMATION AT PERIPHE

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MP1247	MP1247	US	11/888,075	8/13/2007	7870372	1/11/2011	In Force	INTERRUPT HANDLING
MP1247	MP1247C1	US	12/987,716	1/10/2011	8198866	5/29/2012	In Force	INTERRUPT HANDLING
MP1247	MP1247C1C1	US	13/479,788	5/24/2012	8473728	6/25/2013	In Force	INTERRUPT HANDLING
MP12472	MP12472	US	14/200,939	3/7/2014	9264023	2/16/2016	In Force	Scannable Flop With A Single Storage Element
MP12473	MP12473	US	14/217,935	3/18/2014	9130549	9/8/2015	In Force	Multiplexer Flop
MP12474	MP12474	US	14/015,929	8/30/2013	9426166	8/23/2016	In Force	Method And Apparatus For Processing Finite Automata
MP12474	MP12474CN	CN	20141043321980	8/28/2014	ZL20141043321	12/14/2018	In Force	Method And Apparatus For Processing Finite Automata
MP12474	MP12474HK	HK	15108607.1	8/28/2014			Pending	Method And Apparatus For Processing Finite Automata
MP12475	MP12475	US	14/014,803	8/30/2013	9154455	10/6/2015	In Force	METHOD AND SYSTEM FOR DETERMINING DROP ELIGIBILITY OF NETWORK
MP12476	MP12476	US	14/015,601	8/30/2013	8963601	2/24/2015	In Force	Clock Gated Delay Line Based On Setting Value
MP12477	MP12477	US	14/186,978	2/21/2014	9563399	2/7/2017	In Force	Generating A Non-Deterministic Finite Automata (NFA) Graph For Regular Expression
MP12477	MP12477CN	CN	201410431555.1	8/28/2014	ZL2014104315	1/4/2019	In Force	Generating a non-deterministic finite automata (NFA) graph for regular expression
MP12477	MP12477HK	HK	15108608.0	8/28/2014			Pending	Generating A Non-Deterministic Finite Automata (NFA) Graph For Regular Expression
MP12477	MP12477KR	KR	1020140114469	8/29/2014		4/21/2016	In Force	GENERATING A NFA (Non-Deterministic finite automata) GRAPH FOR REGULAR EXPRESSION
MP12478	MP12478	US	14/186,913	2/21/2014	9507563	11/29/2016	In Force	System And Method To Traverse A Non-Deterministic Finite Automaton
MP12478	MP12478CN	CN	2014104332025	8/28/2014	ZL2014104332	4/23/2019	In Force	System and method to traverse a non-deterministic finite automata (NFA) graph for regular expression
MP12478	MP12478HK	HK	15112237.1	8/28/2014			Pending	SYSTEM AND METHOD TO TRAVERSE A NON-DETERMINISTIC FINITE AUTOMATA
MP12479	MP12479	US	14/015,248	8/30/2013	9426165	8/23/2016	In Force	Method And Apparatus For Compilation Of Finite Automata
MP12479	MP12479CN	CN	2014104333210	8/28/2014	ZL2014104333	3/23/2018	In Force	Method And Apparatus For Compilation Of Finite Automata
MP12479	MP12479HK	HK	15108609.9	8/28/2014		7/5/2019	In Force	Method And Apparatus For Compilation Of Finite Automata
MP1248	MP1248	US	11/775,172	7/9/2007	8046775	10/25/2011	In Force	EVENT-BASED BANDWIDTH ALLOCATION MODE SWITCHING METHOD
MP1248	MP1248C1	US	13/279,163	10/21/2011	8424021	4/16/2013	In Force	EVENT-BASED BANDWIDTH ALLOCATION MODE SWITCHING METHOD
MP1248	MP1248C1C1	US	13/853,163	4/15/2013	8799929	8/5/2014	In Force	METHOD AND APPARATUS FOR BANDWIDTH ALLOCATION MODE SWITCHING
MP12480	MP12480	US	14/015,607	8/30/2013	9335784	5/10/2016	In Force	Clock Distribution Circuit With Distributed Delay Locked Loop
MP12480	MP12480CN	CN	201410438613.1	8/29/2014	ZL2014104386	1/23/2018	In Force	Distributed Delay Locked Loop
MP12480	MP12480HK	HK	15107380.6	8/29/2014			Pending	Distributed Delay Locked Loop
MP12481	MP12481	US	14/142,623	12/27/2013	9509585	11/29/2016	In Force	APPARATUS AND METHOD FOR TIME STAMPING PACKETS ACROSS A NETWORK
MP12482	MP12482	US	14/142,631	12/27/2013	9498539	9/6/2016	In Force	APPARATUS AND METHOD FOR OPTIMIZING THE NUMBER OF ACCESSES TO A MEMORY
MP12483	MP12483	US	14/142,643	12/27/2013	9009364	4/14/2015	In Force	APPARATUS AND METHOD FOR ACCELERATED PAGE LINK LIST PROCESSING
MP12483	MP12483C1	US	14/676,791	4/1/2015	9262369	2/16/2016	In Force	APPARATUS AND METHOD FOR ACCELERATED PAGE LINK LIST PROCESSING
MP12484	MP12484	US	14/142,653	12/27/2013	9736069	8/15/2017	In Force	Method for Storing and Retrieving Packets in High Bandwidth and Low Latency Networks
MP12485	MP12485	US	14/034,200	9/23/2013	9281953	3/8/2016	In Force	SYSTEMS AND METHODS FOR ROUTING MULTICAST PACKETS
MP12486	MP12486	US	14/034,006	9/23/2013	9256491	2/9/2016	In Force	METHOD AND SYSTEM FOR DATA INTEGRITY
MP12487	MP12487	US	14/036,709	9/25/2013	9568542	2/14/2017	In Force	Memory Interface With Integrated Tester

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MP12488	MP12488TW	TW	103132756	9/23/2014			Pending	SEMICONDUCTOR WITH VIRTUALIZED COMPUTATION AND SWITCH
MP12488	MP12488WO	WO	PCT/US2014/05	9/17/2014	DNA		Pending	SEMICONDUCTOR WITH VIRTUALIZED COMPUTATION AND SWITCH
MP12489	MP12489	US	14/038,156	9/26/2013	9639476	5/2/2017	In Force	Merged TLB Structure For Multiple Sequential Address Translations
MP12489	MP12489CN	CN	20141049384014	9/25/2014	ZL2014104984	2/6/2018	In Force	Merged TLB Structure For Multiple Sequential Address Translations
MP12489	MP12489HK	HK	15107954.2	8/18/2015			Pending	Merged TLB Structure For Multiple Sequential Address Translations
MP12489	MP12489KR	KR	1020140129272	9/26/2014		1/14/2016	In Force	Merged TLB Structure For Multiple Sequential Address Translations
MP1248W	MP1248WO	WO	PCT/US2007/01	8/14/2007	DNA		Pending	Event Based Bandwidth Allocation Mode Switching Method and App
MP1249	MP1249	US	11/836,044	8/8/2007	8082737	10/4/2011	In Force	METHODS AND APPARATUS FOR HANDLING SWITCHING AMONG TH
MP1249	MP1249C1	US	13/247,030	9/28/2011	8478972	7/2/2013	In Force	METHODS AND APPARATUS FOR HANDLING SWITCHING AMONG TH
MP12490	MP12490	US	14/038,383	9/26/2013	9208103	12/8/2015	In Force	Translation Bypass In Multi-Stage Address Translation
MP12490	MP12490CN	CN	2014104980134	9/25/2014	ZL2014104980	6/8/2018	In Force	Translation Bypass In Multi-Stage Address Translation
MP12490	MP12490HK	HK	15109895.0	9/25/2014		8/2/2019	In Force	Translation Bypass In Multi-Stage Address Translation
MP12490	MP12490KR	KR	10-2014-012924	9/26/2014		2/23/2016	In Force	Translation Bypass In Multi-Stage Address Translation
MP12491	MP12491	US	14/038,189	9/26/2013	9645941	5/9/2017	In Force	Collapsed Address Translation With Multiple Page Sizes
MP12491	MP12491C1	US	15/475,718	3/31/2017	10042778	8/7/2018	In Force	Collapsed Address Translation With Multiple Page Sizes
MP12491	MP12491DE	DE	102014014076	9/25/2014			Pending	Collapsed Address Translation With Multiple Page Sizes
MP12492	MP12492	US	14/038,225	9/26/2013	9268694	2/23/2016	In Force	Maintenance Of Cache And Tags In A Translation Lookaside Buffer
MP12494	MP12494	US	14/040,431	9/27/2013	9507369	11/29/2016	In Force	Dynamically Adjusting Supply Voltage Based On Monitored Chip Tem
MP12494	MP12494CN	CN	2014105034421	9/26/2014			Pending	Dynamically Adjusting Supply Voltage Based On Monitored Chip Tem
MP12494	MP12494CND1	CN	201810444294.1	9/26/2014			Pending	Dynamically Adjusting Supply Voltage Based On Monitored Chip Tem
MP12494	MP12494DE	DE	102014014494	9/25/2014			Pending	Dynamically Adjusting Supply Voltage Based On Monitored Chip Tem
MP12494	MP12494HK	HK	15107953.3	9/26/2014			Pending	Dynamically Adjusting Supply Voltage Based On Monitored Chip Tem
MP12494	MP12494JP	JP	2014-192983	9/22/2014	JP6002728	9/9/2016	In Force	DYNAMIC ADJUSTMENT OF SUPPLY VOLTAGE BASED ON MONITORE
MP12495	MP12495	US	14/040,577	9/27/2013	9413568	8/9/2016	In Force	Method And Apparatus For Calibrating An Input Interface
MP12496	MP12496	US	14/040,563	9/27/2013	9087567	7/21/2015	In Force	Method And Apparatus For Amplifier Offset Calibration
MP12497	MP12497	US	14/040,532	9/27/2013	9496012	11/15/2016	In Force	Method and Apparatus For Reference Voltage Calibration In A Single-
MP12498	MP12498	US	14/039,846	9/27/2013	9490033	11/8/2016	In Force	Auto-Blow Memory Repair
MP12499	MP12499	US	14/041,961	9/30/2013	9041432	5/26/2015	In Force	Clock Multiplexing And Repeater Network
MP1249E	MP1249E	EP	07016002.3	8/14/2007	1890225	4/4/2012	In Force	METHODS AND APPARATUS FOR HANDLING SWITCHING AMONG TH
MP1249E	MP1249EPDE	DE	07016002.3	8/14/2007		4/4/2012	In Force	METHOD, APPARATUS AND COMPUTER PROGRAM PRODUCT FOR H
MP1249E	MP1249EPFR	FR	07016002.3	8/14/2007	1890225	4/4/2012	In Force	METHOD, APPARATUS AND COMPUTER PROGRAM PRODUCT FOR H
MP1249E	MP1249EPGB	GB	07016002.3	8/14/2007	1890225	4/4/2012	In Force	METHOD, APPARATUS AND COMPUTER PROGRAM PRODUCT FOR H
MP1249H	MP1249HK	HK	HK08108651.5	8/5/2008		12/28/2012	In Force	METHODS AND APPARATUS FOR HANDLING SWITCHING AMONG TH
MP1249W	MP1249WO	WO	PCT/US2007/01	8/14/2007	DNA		Pending	Methods and Apparatus for Handling Switching Among Threads withi

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MP12500	MP12500	US	14/042,111	9/30/2013	9264383	2/16/2016	In Force	SYSTEMS AND METHODS FOR QUALITY OF SERVICE FOR LINK AGGREGATION
MP12501	MP12501	US	14/042,403	9/30/2013	10110393	10/23/2018	In Force	Protocol Switching over Multi-Network Interface
MP12502	MP12502	US	14/045,674	10/3/2013	9390023	7/12/2016	In Force	Method And Apparatus For Conditional Storing Of Data Using A Compression
MP12502	MP12502KR	KR	10-2014-013446	10/6/2014	101587362	1/14/2016	In Force	Method And Apparatus For Conditional Storing Of Data Using A Compression
MP12503	MP12503	US	14/045,602	10/3/2013	9501243	11/22/2016	In Force	Method And Apparatus For Supporting Wide Operations Using Atomic
MP12503	MP12503CN	CN	201410521310.1	9/30/2014	212014105213	3/23/2018	In Force	Method And Apparatus For Supporting Wide Operations Using Atomic
MP12503	MP12503HK	HK	15107951.5	9/30/2014	1267437	7/5/2019	In Force	Method And Apparatus For Supporting Wide Operations Using Atomic
MP12503	MP12503TW	TW	103133879	9/30/2014	1670648	9/1/2019	In Force	Method And Apparatus For Supporting Wide Operations Using Atomic
MP12504	MP12504	US	14/045,644	10/3/2013	9281034	3/8/2016	In Force	Data Strobe Generation
MP12505	MP12505	US	14/046,879	10/4/2013	9721627	8/1/2017	In Force	Method and Apparatus for Aligning Signals
MP12506	MP12506	US	14/048,849	10/8/2013	9170880	10/27/2015	In Force	METHOD AND SYSTEM FOR DATA INTEGRITY IN TCAMs
MP12507	MP12507	US	14/051,290	10/10/2013	9253120	2/2/2016	In Force	SYSTEMS AND METHODS FOR HIGH SPEED DATA PROCESSING AT A NETWORK
MP12508	MP12508	US	14/067,815	10/30/2013	9465406	10/11/2016	In Force	TIMERS AND METHODS THEREOF FOR COMPUTING DEVICES
MP12509	MP12509	US	14/080,389	11/14/2013	9923715	4/26/2016	In Force	Method And Apparatus To Represent A Processor Context With Fewer
MP12509	MP12509CN	CN	2014106415662	11/13/2014	212014106415	5/4/2018	In Force	Method And Apparatus To Represent A Processor Context With Fewer
MP12509	MP12509HK	HK	15107950.6	11/13/2014	1207436	7/12/2019	In Force	Method And Apparatus To Represent A Processor Context With Fewer
MP12509	MP12509KR	KR	1020140157977	11/13/2014	101694591006	1/3/2017	In Force	Method And Apparatus To Represent A Processor Context With Fewer
MP12509	MP12509TW	TW	103138879	11/10/2014	1541661	7/11/2016	In Force	Method And Apparatus To Represent A Processor Context With Fewer
MP1251	MP1251	US	11/893,163	8/14/2007	7751139	7/6/2010	In Force	CIRCUITS, SYSTEMS, AND METHODS FOR CALIBRATION OF BIAS ACR
MP12510	MP12510	US	14/083,894	11/19/2013	9363193	6/7/2016	In Force	VIRTUALIZED NETWORK INTERFACE FOR TCP REASSEMBLY BUFFER A
MP12510	MP12510DE	DE	102014012618.1	8/22/2014			Pending	Virtualized network interface for TCP recombination buffer allocation
MP12511	MP12511	US	14/088,082	11/22/2013	9223518	12/29/2015	In Force	METHOD AND SYSTEM FOR REMOTE CACHE DIRECT DATA PLACEMENT
MP12512	MP12512	US	14/151,748	1/9/2014	9058463	6/16/2015	In Force	SYSTEMS AND METHODS FOR SPECIFYING, MODELING, IMPLEMENTING
MP12512	MP12512TW	TW	103141564	12/1/2014	1634445	9/1/2018	In Force	Systems and methods for specifying, modeling, implementing and ve
MP12513	MP12513	US	14/106,841	12/15/2013	9313029	4/12/2016	In Force	VIRTUALIZED NETWORK INTERFACE FOR REMOTE DIRECT MEMORY
MP12514	MP12514	US	14/106,827	12/15/2013			Pending	VIRTUALIZED NETWORK INTERFACE FOR LOCKDOWN AND OVERLAY
MP12515	MP12515	US	14/140,503	12/25/2013	9379992	6/28/2016	In Force	METHOD AND AN APPARATUS FOR VIRTUALIZATION OF A QUALITY-
MP12515	MP12515CN	CN	2014107420206	12/5/2014	212014107420	7/28/2017	In Force	Method and an apparatus for virtualization of quality-of-service
MP12515	MP12515HK	HK	15108376	12/5/2014	12077198	4/20/2018	In Force	A METHOD AND AN APPARATUS FOR VIRTUALIZATION OF A QUALITY
MP12515	MP12515KR	KR	1020140164241	11/24/2014	101698648006	1/16/2017	In Force	A METHOD AND AN APPARATUS FOR VIRTUALIZATION OF QUALITY-
MP12515	MP12515TW	TW	103137163	10/28/2014	1571076	2/11/2017	In Force	A METHOD AND AN APPARATUS FOR VIRTUALIZATION OF A QUALITY
MP12516	MP12516	US	14/140,488	12/25/2013	9306916	4/5/2016	In Force	SYSTEM AND A METHOD FOR A REMOTE DIRECT MEMORY ACCESS O
MP12517	MP12517	US	14/140,494	12/25/2013	9977737	5/22/2018	In Force	METHOD AND AN APPARATUS FOR MEMORY ADDRESS ALIGNMENT
MP12517	MP12517CN	CN	201410756095X	12/10/2014	212014107560	8/2/2019	In Force	Method and apparatus for memory address alignment

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MP12517	MP12517HK	HK	15109030.6	12/10/2014			Pending	A METHOD AND AN APPARATUS FOR MEMORY ADDRESS ALIGNME
MP12517	MP12517TW	TW	103137161	10/28/2014	1627529	6/21/2018	In Force	A METHOD AND AN APPARATUS FOR MEMORY ADDRESS ALIGNME
MP12518	MP12518	US	14/140,585	12/26/2013	9665508	5/30/2017	In Force	METHOD AND AN APPARATUS FOR CONVERTING INTERRUPTS INTO
MP12519	MP12519	US	14/141,076	12/26/2013	9380209	7/12/2016	In Force	SYSTEM FOR AND METHOD OF COMBINING CMOS INVERTERS OF M
MP1252	MP1252	US	11/893,162	8/14/2007	7848043	12/7/2010	In Force	CIRCUITS, SYSTEMS, AND METHODS FOR LOW NOISE BIASING OF MA
MP12520	MP12520	US	14/141,096	12/26/2013	9443053	9/13/2016	In Force	SYSTEM FOR AND METHOD OF PLACING CLOCK STATIONS USING VA
MP12521	MP12521	US	14/141,104	12/26/2013	9305129	4/5/2016	In Force	SYSTEM FOR AND METHOD OF TUNING CLOCK NETWORKS CONSTR
MP12521	MP12521HK	HK	15107952.4	8/18/2015			Pending	SYSTEM AND METHOD FOR TUNING CLOCK NETWORKS
MP12521	MP12521JP	JP	2014-263371	12/25/2014	JP6544923	6/28/2019	In Force	SYSTEM AND METHOD OF TUNING CLOCK NETWORKS CONSTRUCTE
MP12521	MP12521KR	KR	1020140191072	12/26/2014			Pending	SYSTEM FOR AND METHOD OF TUNING CLOCK NETWORKS CONSTRU
MP12521	MP12521TW	TW	103145439	12/25/2014	1661325	6/1/2019	In Force	SYSTEM FOR AND METHOD OF TUNING CLOCK NETWORKS CONSTRU
MP12522	MP12522	US	14/142,511	12/27/2013	9620213	4/11/2017	In Force	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12522	MP12522CN	CN	201410638433.4	12/29/2014	Z12014108384	8/9/2019	In Force	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12522	MP12522D1	US	15/446,259	3/1/2017	9957799	4/24/2018	In Force	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12522	MP12522D2	US	15/446,297	3/1/2017	9952800	4/24/2018	In Force	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12522	MP12522D2C1	US	15/923,851	3/16/2018			Pending	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12522	MP12522HK	HK	15111512.9	12/29/2014			Pending	A MATRIX OF ON-CHIP ROUTERS INTERCONNECTING A PLURALITY OF
MP12522	MP12522JP	JP	2014-263373	12/25/2014			Pending	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12522	MP12522KR	KR	1020140191978	12/29/2014			Pending	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12522	MP12522TW	TW	103145450	12/25/2014	1659303	5/11/2019	In Force	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12523	MP12523	US	14/142,497	12/27/2013	9548945	1/17/2017	In Force	MATRIX OF ON-CHIP ROUTERS INTERCONNECTING A PLURALITY OF P
MP12523	MP12523CN	CN	201410838063.4	12/29/2014	Z12014083806	8/9/2019	In Force	Matrix of on-chip routers interconnecting a plurality of processing en
MP12523	MP12523HK	HK	15111519.2	12/29/2014			Pending	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12523	MP12523JP	JP	2014-263372	12/25/2014	JP6556450	7/19/2019	In Force	A MATRIX OF ON-CHIP ROUTERS INTERCONNECTING A PLURALITY OF
MP12523	MP12523KR	KR	1020140192064	12/29/2014			Pending	A MATRIX OF ON-CHIP ROUTERS INTERCONNECTING A PLURALITY OF
MP12523	MP12523TW	TW	103145445	12/25/2014	1661702	6/1/2019	In Force	A MATRIX OF ON-CHIP ROUTERS INTERCONNECTING A PLURALITY OF
MP12524	MP12524	US	14/141,987	12/27/2013	9491099	11/8/2016	In Force	LOOK-ASIDE PROCESSOR UNIT WITH INTERNAL AND EXTERNAL ACCE
MP12524	MP12524CN	CN	2014107568006	12/10/2014	Z12014107568	11/23/2018	In Force	LOOK-ASIDE PROCESSOR UNIT WITH INTERNAL AND EXTERNAL ACCE
MP12524	MP12524HK	HK	15108374.2	12/10/2014			Pending	A LOOK-ASIDE PROCESSOR UNIT WITH INTERNAL AND EXTERNAL AC
MP12524	MP12524KR	KR	1020140160344	11/17/2014			Pending	A LOOK-ASIDE PROCESSOR UNIT WITH INTERNAL AND EXTERNAL AC
MP12524	MP12524TW	TW	103137160	10/28/2014	1652623	3/1/2019	In Force	A LOOK-ASIDE PROCESSOR UNIT WITH INTERNAL AND EXTERNAL AC
MP12525	MP12525	US	14/141,681	12/27/2013	9460033	10/4/2016	In Force	Apparatus and Method for Interrupt Collecting and Reporting Status
MP12526	MP12526	US	14/143,586	12/30/2013	9419943	8/16/2016	In Force	Method And Apparatus For Processing Of Finite Automata
MP12526	MP12526CN	CN	201410432859X	8/28/2014	Z12014104328	6/5/2018	In Force	Method And Apparatus For Processing Of Finite Automata

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP12526	MP12526HK	HK	15112773.1	8/28/2014	1212119	8/2/2019	In Force	Method And Apparatus For Processing Of Finite Automata
MP12527	MP12527	US	14/144,260	12/30/2013	9880844	1/30/2018	In Force	METHOD AND APPARATUS FOR PARALLEL AND CONDITIONAL DATA
MP12527	MP12527CN	CN	2014108431994	12/30/2014	Z12014108431	5/28/2019	In Force	Method and apparatus for parallel and conditional data manipulation
MP12527	MP12527HK	HK	15109646.2	12/30/2014			Pending	METHOD AND APPARATUS FOR PARALLEL AND CONDITIONAL DATA
MP12527	MP12527JP	JP	2014-267000	12/29/2014	6537823	6/14/2019	In Force	METHOD AND APPARATUS FOR PARALLEL AND CONDITIONAL DATA
MP12527	MP12527KR	KR	1020140194027	12/30/2014			Pending	METHOD AND APPARATUS FOR PARALLEL AND CONDITIONAL DATA
MP12528	MP12528	US	14/144,270	12/30/2013	9379963	6/28/2016	In Force	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKING
MP12528	MP12528CN	CN	201410843934.1	12/30/2014			Pending	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKING
MP12528	MP12528D1	US	15/167,704	5/27/2016	10609273	6/26/2018	In Force	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKING
MP12528	MP12528D101	US	15/991,810	5/29/2018			Pending	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKING
MP12528	MP12528HK	HK	15111520.9	12/30/2014			Pending	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKING
MP12528	MP12528JP	JP	2014-267001	12/29/2014	6537824	6/14/2019	In Force	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKING
MP12528	MP12528KR	KR	1020140194209	12/30/2014			Pending	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKING
MP12528	MP12528TW	TW	103145662	12/26/2014	1661685	6/1/2019	In Force	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKING
MP12529	MP12529	US	14/145,374	12/31/2013	9275336	3/1/2016	In Force	METHOD AND SYSTEM FOR SKIPPING OVER GROUP(S) OF RULES BAS
MP1253	MP1253	US	11/840,745	8/17/2007	7852168	12/14/2010	In Force	POWER-EFFICIENT BIASING CIRCUIT
MP12530	MP12530	US	14/145,918	12/31/2013	9544402	1/10/2017	In Force	MULTI-RULE APPROACH TO ENCODING A GROUP OF RULES
MP12534	MP12534	US	14/150,622	1/8/2014	10447823	10/15/2019	In Force	PACKET PARSING ENGINE
MP12534	MP12534WO	WO	PCT/US2015/10	1/6/2015			Pending	PACKET PARSING ENGINE
MP12535	MP12535	US	14/150,650	1/8/2014	9307057	4/5/2016	In Force	METHODS AND SYSTEMS FOR RESOURCE MANAGEMENT IN A SINGLE
MP12536	MP12536	US	14/150,550	1/8/2014	9268855	2/23/2016	In Force	PROCESSING REQUEST KEYS BASED ON A KEY SIZE SUPPORTED BY U
MP12537	MP12537	US	14/150,572	1/8/2014	9432284	8/30/2016	In Force	METHOD AND APPARATUS FOR COMPILING SEARCH TREES FOR PRO
MP12538	MP12538	US	14/150,761	1/8/2014	9667446	5/30/2017	In Force	CONDITION CODE APPROACH FOR COMPARING RULE AND PACKET D
MP12539	MP12539TW	TW	104100216	1/6/2015	1593256	7/21/2017	In Force	METHODS AND SYSTEMS FOR FLEXIBLE PACKET CLASSIFICATION
MP12539	MP12539WO	WO	PCT/US2015/03	1/6/2015			Pending	METHODS AND SYSTEMS FOR FLEXIBLE PACKET CLASSIFICATION
MP1254	MP1254C1	US	13/034,613	2/24/2011	8232841	7/31/2012	In Force	Amplifier With Output Filtering
MP12540	MP12540	US	14/150,664	1/8/2014	9513926	12/6/2016	In Force	FLOATING MASK GENERATION FOR NETWORK PACKET FLOW
MP12541	MP12541	US	14/150,635	1/8/2014	10284630	5/7/2019	In Force	METHODS AND SYSTEMS FOR DISTRIBUTION OF PACKETS AMONG P
MP12543	MP12543	US	14/152,817	1/10/2014	9647947	5/9/2017	In Force	BLOCK MASK REGISTER KEY PROCESSING BY COMPILING DATA STRU
MP12544	MP12544	US	14/165,100	1/27/2014	9454305	9/27/2016	In Force	METHOD AND SYSTEM FOR MANAGING STORAGE RESERVATION
MP12545	MP12545	US	14/166,082	1/28/2014	9378036	6/28/2016	In Force	METHOD AND SYSTEM FOR COMMUNICATION IN A VIRTUAL MACHI
MP12546	MP12546	US	14/169,830	1/31/2014	9602532	3/21/2017	In Force	Method And Apparatus For Optimizing Finite Automata Processing
MP12546	MP12546KR	KR	10-2014-011384	8/29/2014	101633649	6/21/2016	In Force	Method And Apparatus For Optimizing Finite Automata Processing
MP12547	MP12547	US	14/169,967	1/31/2014	9904630	2/27/2018	In Force	Finite Automata Processing Based On A Top Of Stack (TOS) Memory

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MP12547	MP12547CN	CN	2014104332877	8/28/2014	212014104332	9/25/2018	In Force	Finite Automata Processing Based On A Top Of Stack (TOS) Memory
MP12547	MP12547HK	HK	16101361.1	8/28/2014			Pending	Finite Automata Processing Based On A Top Of Stack (TOS) Memory
MP12548	MP12548	US	14/170,955	2/3/2014			Pending	METHOD AND AN APPARATUS FOR WORK PACKET QUEUING, SCHED
MP12548	MP12548WO	WO	PCT/US2015/14	2/2/2015			Pending	METHOD AND AN APPARATUS FOR WORK PACKET QUEUING, SCHED
MP12549	MP12549	US	14/171,108	2/3/2014	9838471	12/5/2017	In Force	METHOD AND AN APPARATUS FOR WORK REQUEST ARBITRATION IN
MP12549	MP12549WO	WO	PCT/US2015/01	2/2/2015			Pending	METHOD AND AN APPARATUS FOR WORK REQUEST ARBITRATION IN
MP12550	MP12550	US	14/171,290	2/3/2014	9811467	11/7/2017	In Force	METHOD AND AN APPARATUS FOR PRE-FETCHING AND PROCESSING
MP12550	MP12550WO	WO	PCT/US2015/01	2/2/2015			Pending	A METHOD AND AN APPARATUS FOR PRE-FETCHING AND PROCESSING
MP12550	MP12550WCN	CN	201560063167.1	2/2/2015			Pending	A method and an apparatus for pre-fetching and processing work fo
MP12551	MP12551	US	14/186,743	2/21/2014	9692715	6/27/2017	In Force	Multiple Ethernet Ports and Port Types Using A Shared Data Path
MP12551	MP12551C1	US	15/601,493	5/22/2017	10404623	9/3/2019	In Force	Multiple Ethernet Ports And Port Types Using A Shared Data Path
MP12551	MP12551CN	CN	201510072689.1	2/11/2015	104869176B	4/12/2019	In Force	Multiple Ethernet Ports and Port Types Using A Shared Data Path
MP12551	MP12551HK	HK	15109893.2	2/11/2015			Pending	Multiple Ethernet Ports and Port Types Using A Shared Data Path
MP12551	MP12551KR	KR	10-2015-002345	2/16/2015	101664658	10/4/2016	In Force	Multiple Ethernet Ports and Port Types Using A Shared Data Path
MP12551	MP12551TW	TW	104103687	2/4/2015	1551101	9/21/2016	In Force	AN INTERFACE UNIT BASED ON MULTIPLE ETHERNET PORTS AND PO
MP12552	MP12552	US	14/650,554	2/24/2015	9729320	8/8/2017	In Force	APPARATUS AND METHOD FOR SOFTWARE ENABLED ACCESS TO PR
MP12552	MP12552CN	CN	201510172120.1	2/25/2015			Pending	APPARATUS AND METHOD FOR SOFTWARE ENABLED ACCESS TO PR
MP12552	MP12552HK	HK	15111508.5	2/25/2015			Pending	APPARATUS AND METHOD FOR SOFTWARE ENABLED ACCESS TO PR
MP12552	MP12552TW	TW	104105809	2/24/2015	1633458	8/21/2018	In Force	APPARATUS AND METHOD FOR SOFTWARE ENABLED ACCESS TO PR
MP12553	MP12553	US	14/191,163	2/26/2014	9431105	8/30/2016	In Force	Method And Apparatus For Memory Access Management
MP12554	MP12554	US	14/192,100	2/27/2014	9430968	11/8/2016	In Force	CDR Voter With Improved Frequency Offset Tolerance
MP12555	MP12555	US	14/194,049	2/28/2014	9432288	8/30/2016	In Force	System On Chip Link Layer Protocol
MP12556	MP12556	US	14/193,793	2/28/2014	9471416	10/18/2016	In Force	Partitioned Error Code Computation
MP12557	MP12557	US	14/193,933	2/28/2014	9397938	7/19/2016	In Force	Packet Scheduling In A Network Processor
MP12557	MP12557TW	TW	104105253	2/16/2015	1559706	11/21/2016	In Force	Packet Scheduling In A Network Processor
MP12557	MP12557WO	WO	PCT/US2014/07	12/30/2014			Pending	Packet Scheduling In A Network Processor
MP12557	MP12557WCN	CN	201480076494.1	12/30/2014	212014800764	4/20/2018	In Force	Packet Scheduling In A Network Processor
MP12558	MP12558	US	14/193,895	2/28/2014	9680742	6/13/2017	In Force	Packet Output Processing
MP12558	MP12558TW	TW	104104215	2/9/2015	1566511	1/11/2017	In Force	Packet Output Processing
MP1256	MP1256	US	11/857,081	9/18/2007	9516178	12/6/2016	In Force	Multiple Media Format Support For Printers
MP12560	MP12560	US	14/193,899	2/28/2014	9483100	11/1/2016	In Force	Method And Apparatus For Power Gating Hardware Components In
MP12560	MP12560CN	CN	201510064815.1	2/6/2015	212015100648	5/4/2018	In Force	Method And Apparatus For Power Gating Hardware Components In
MP12560	MP12560HK	HK	15111507.6	6/2/2015	1210879	7/12/2019	In Force	Method And Apparatus For Power Gating Hardware Components In
MP12560	MP12560TW	TW	104105811	2/24/2015	1618355	3/11/2018	In Force	Method And Apparatus For Power Gating Hardware Components In

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MP12561	MP12561	US	14/194,038	2/28/2014	9559982	1/31/2017	In Force	Packet Shaping In A Network Processor
MP12561	MP12561TW	TW	104104959	2/13/2015	1668975	8/11/2019	In Force	Circuit and method for packet shaping in a network processor
MP12561	MP12561WO	WO	PCT/US2014/07	12/30/2014			Pending	Packet Shaping In A Network Processor
MP12561	MP12561WOCN	CN	201480076493.1	12/30/2014	ZL2014800764	7/13/2018	In Force	Packet Shaping In A Network Processor
MP12562	MP12562	US	14/193,658	2/28/2014	9588920	3/7/2017	In Force	METHOD AND SYSTEM FOR PORT TRUNKING
MP12564	MP12564	US	14/201,594	3/7/2014	9529532	12/27/2016	In Force	Method And Apparatus For Memory Allocation In A Multi-Node System
MP12564	MP12564HK	HK	1710445.1	12/30/2014			Pending	Method And Apparatus For Memory Allocation In A Multi-Node System
MP12564	MP12564TW	TW	104104217	2/9/2015	1519958	2/1/2016	In Force	Method And Apparatus For Memory Allocation In A Multi-Node System
MP12564	MP12564WO	WO	PCT/US2014/07	12/30/2014			Pending	Method And Apparatus For Memory Allocation In A Multi-Node System
MP12564	MP12564WOCN	CN	201480076878.1	12/30/2014	ZL2014800768	3/1/2019	In Force	Method and apparatus for memory allocation in multi-node system
MP12565	MP12565	US	14/201,513	3/7/2014	9372800	6/21/2016	In Force	Inter-Chip Interconnect Protocol For A Multi-Chip System
MP12565	MP12565TW	TW	104107158	3/6/2015	1541649	7/11/2016	In Force	System and method of inter-chip interconnect protocol for a multi-chip
MP12566	MP12566	US	14/201,557	3/7/2014			Pending	Method And System For Ordering I/O Access In A Multi-Node Environ
MP12566	MP12566TW	TW	104105256	2/16/2015	1547870	9/1/2016	In Force	Method And System For Ordering I/O Access In A Multi-Node Environ
MP12567	MP12567	US	14/201,541	3/7/2014	9411644	8/9/2016	In Force	Method And System For Work Scheduling In A Multi-Chip System
MP12567	MP12567D1	US	15/200,587	7/1/2016	10169080	1/1/2019	In Force	Method For Work Scheduling In A Multi-Chip System
MP12567	MP12567TW	TW	104105255	2/16/2015	1543073	7/21/2016	In Force	Method And System For Work Scheduling In A Multi-Chip System
MP12569	MP12569	US	14/230,768	3/31/2014	9384008	7/5/2016	In Force	METHOD AND SYSTEM FOR OPERATING SYSTEM RECOVERY FROM A
MP1256W	MP1256WO	WO	PCT/182007/002	9/19/2007			Pending	Multiple Media Format Support For Printers
MP1257	MP1257	US	11/775,605	7/10/2007	7974351	7/5/2011	In Force	METHOD FOR DETECTING A PERIODIC SIGNAL
MP1257	MP1257C1	US	13/175,476	7/1/2011	8208571	6/26/2012	In Force	METHOD FOR DETECTING A PERIODIC SIGNAL
MP12571	MP12571	US	14/252,390	4/14/2014	9438561	9/6/2016	In Force	Processing Of Finite Automata Based On A Node Cache
MP12572	MP12572	US	14/252,299	4/14/2014	10110558	10/23/2018	In Force	Processing Of Finite Automata Based On Memory Hierarchy
MP12573	MP12573	US	14/252,354	4/14/2014	9823895	11/21/2017	In Force	Memory Management For Finite Automata Processing
MP12574	MP12574	US	14/252,293	4/14/2014	10802326	6/19/2018	In Force	Compilation Of Finite Automata Based On Memory Hierarchy
MP12574	MP12574CN	CN	2014104334020	8/28/2014	ZL2014104334	11/13/2018	In Force	Compilation Of Finite Automata Based On Memory Hierarchy
MP12574	MP12574HK	HK	16102603.7	8/28/2014			Pending	Compilation Of Finite Automata Based On Memory Hierarchy
MP12575	MP12575	US	14/268,121	5/27/2014	9355206	5/31/2016	In Force	SYSTEM AND METHOD FOR AUTOMATED FUNCTIONAL COVERAGE G
MP12575	MP12575TW	TW	104108422	3/17/2015	1627547	6/21/2018	In Force	System and method for automated functional coverage generation a
MP12576	MP12576	US	14/264,927	4/29/2014	9483290	11/1/2016	In Force	METHOD AND SYSTEM FOR VIRTUAL MACHINE COMMUNICATION
MP12577	MP12577	US	14/264,957	4/29/2014	9229893	1/5/2016	In Force	SYSTEMS AND METHODS FOR MANAGING DIRECT MEMORY ACCESS
MP12578	MP12578	US	14/300,552	6/10/2014	9264567	3/22/2016	In Force	SYSTEMS AND METHODS FOR ENABLING ACCESS TO EXTENSIBLE STO
MP12578	MP12578-4	US	14/496,916	9/25/2014	9819739	11/14/2017	In Force	SYSTEMS AND METHODS FOR SUPPORTING HOT PLUGGING OF REM
MP12578	MP12578-5	US	14/537,758	11/10/2014	9430268	8/30/2016	In Force	SYSTEMS AND METHODS FOR SUPPORTING MIGRATION OF VIRTUAL

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MP12578	MP1257811	US	15/041,892	2/11/2016	9529773	12/27/2016	In Force	SYSTEMS AND METHODS FOR ENABLING ACCESS TO EXTENSIBLE RE
MP12578	MP125781TW	TW	104106790	3/4/2015	1621023	4/11/2018	In Force	SYSTEMS AND METHODS FOR SUPPORTING HOT PLUGGING OF REM
MP12578	MP125781TW-3	TW	104106793	3/4/2015	1637613	10/1/2018	In Force	SYSTEMS AND METHODS FOR ENABLING ACCESS TO EXTENSIBLE STO
MP12578	MP125781TW-5	TW	104111571	4/10/2015	1647573	1/11/2019	In Force	Systems and methods for supporting migration of virtual machines ac
MP12579	MP12579	US	14/289,533	5/28/2014			Pending	METHOD AND APPARATUS FOR FLEXIBLE AND EFFICIENT ANALYTICS I
MP12579	MP12579CN	CN	201510257702.1	5/19/2015			Pending	METHOD AND APPARATUS FOR FLEXIBLE AND EFFICIENT ANALYTICS I
MP12579	MP12579HK	HK	1610885.3	5/19/2015			Pending	METHOD AND APPARATUS FOR FLEXIBLE AND EFFICIENT ANALYTICS
MP12579	MP12579IN	IN	835/DEL/2015	3/25/2015			Pending	METHOD AND APPARATUS FOR FLEXIBLE AND EFFICIENT ANALYTICS
MP12579	MP12579JP	JP	2015-107430	5/27/2015			Pending	METHOD AND APPARATUS FOR FLEXIBLE AND EFFICIENT ANALYTICS
MP12579	MP12579KR	KR	1020150073195	5/26/2015			Pending	METHOD AND APPARATUS FOR FLEXIBLE AND EFFICIENT ANALYTICS I
MP12580	MP12580	US	14/289,548	5/28/2014	9773036	9/26/2017	In Force	METHOD AND APPARATUS FOR TABLE AGING IN A NETWORK SWITC
MP12580	MP12580CN	CN	2015102546804	5/18/2015			Pending	METHOD AND APPARATUS FOR TABLE AGING IN A NETWORK SWITC
MP12580	MP12580D1	US	15/675,336	8/11/2017	10216780	2/26/2019	In Force	METHOD AND APPARATUS FOR TABLE AGING IN A NETWORK SWITC
MP12580	MP12580HK	HK	1610887.1	5/18/2015			Pending	METHOD AND APPARATUS FOR TABLE AGING IN A NETWORK SWITC
MP12581	MP12581-4	US	14/723,858	5/28/2015	9571279	2/14/2017	In Force	SYSTEMS AND METHODS FOR SECURED BACKUP OF HARDWARE SEC
MP12581	MP12581TW-5	TW	104119512	6/17/2015	1632797	8/11/2018	In Force	SYSTEMS AND METHODS FOR SECURED BACKUP OF HARDWARE SEC
MP12582	MP12582	US	14/302,351	6/11/2014	9413357	8/9/2016	In Force	HIERARCHICAL STATISTICALLY MULTIPLEXED COUNTERS AND A MET
MP12582	MP12582CN	CN	2015103201878	6/11/2015			Pending	HIERARCHICAL STATISTICALLY MULTIPLEXED COUNTERS AND A MET
MP12582	MP12582D1	US	15/202,428	7/5/2016	10038448	7/31/2018	In Force	HIERARCHICAL STATISTICALLY MULTIPLEXED COUNTERS AND A METH
MP12582	MP12582D2	US	16/019,780	6/27/2018			Pending	HIERARCHICAL STATISTICALLY MULTIPLEXED COUNTERS AND A METH
MP12582	MP12582HK	HK	16108891.5	6/11/2015			Pending	HIERARCHICAL STATISTICALLY MULTIPLEXED COUNTERS AND A MET
MP12582	MP12582TW	TW	104112176	4/16/2015	1656476	4/11/2019	In Force	HIERARCHICAL STATISTICALLY MULTIPLEXED COUNTERS AND ARCHIT
MP12583	MP12583CN	CN	201510253417.1	5/18/2015			Pending	COUNTER WITH OVERFLOW FIFO AND A METHOD THEREOF
MP12583	MP12583HK	HK	1610886.2	5/18/2015			Pending	COUNTER WITH OVERFLOW FIFO AND A METHOD THEREOF
MP12584	MP12584	US	14/302,706	6/12/2014	9423980	8/23/2016	In Force	METHODS AND SYSTEMS FOR AUTOMATICALLY ADDING INTELLIGEN
MP12585	MP12585	US	14/309,659	6/19/2014	9635146	4/25/2017	In Force	METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLLAP
MP12585	MP12585C1	US	15/457,970	3/13/2017			Pending	METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLLAP
MP12585	MP12585CN	CN	2015102724093	5/25/2015			Pending	A METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLL
MP12585	MP12585EP	EP	15172929.0	6/19/2015	2958287	6/19/2019	In Force	A METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLL
MP12585	MP12585EPDE	DE	15172929.0	6/19/2015	602015030226	6/19/2019	In Force	A METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLL
MP12585	MP12585HK	HK	1610884.4	5/25/2015			Pending	A METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLL
MP12585	MP12585KR	KR	1020150086042	6/17/2015			Pending	METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLLAP
MP12585	MP12585TW	TW	104111755	4/13/2015	1664843	7/1/2019	In Force	A METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLL
MP12586	MP12586	US	14/309,726	6/19/2014	9516145	12/6/2016	In Force	METHOD OF EXTRACTING DATA FROM PACKETS AND AN APPARATUS

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MP12586	MP12586CN	CN	201510236939.1	5/11/2015			Pending	A METHOD OF EXTRACTING DATA FROM PACKETS AND AN APPARAT
MP12586	MP12586HK	HK	16108889.9	5/11/2015			Pending	A METHOD OF EXTRACTING DATA FROM PACKETS AND AN APPARAT
MP12586	MP12586JP	JP	2015-122563	6/18/2015			Pending	METHOD OF EXTRACTING DATA FROM PACKETS, AND APPARATUS T
MP12586	MP12586KR	KR	1020150087785	6/19/2015			Pending	A METHOD OF EXTRACTING DATA FROM PACKETS AND AN APPARAT
MP12587	MP12587	US	14/309,802	6/19/2014	9628385	4/18/2017	In Force	METHOD OF IDENTIFYING INTERNAL DESTINATIONS OF NETWORK P
MP12587	MP12587C1	US	15/497,959	3/13/2017	10397113	8/27/2019	In Force	METHOD OF IDENTIFYING INTERNAL DESTINATIONS OF NETWORK P
MP12587	MP12587CN	CN	2015102296103	5/7/2015			Pending	METHOD OF IDENTIFYING INTERNAL DESTINATIONS OF NETWORK P
MP12587	MP12587HK	HK	16108883.5	5/7/2015			Pending	A METHOD OF IDENTIFYING INTERNAL DESTINATIONS OF NETWORK
MP12587	MP12587TW	TW	104110829	4/2/2015	1660609	5/21/2019	In Force	A method of identifying internal destinations of network packets an
MP12588	MP12588	US	14/309,789	6/19/2014	9742694	8/22/2017	In Force	METHOD OF DYNAMICALLY RENUMBERING PORTS AND AN APPARAT
MP12588	MP12588C1	US	15/653,021	7/18/2017			Pending	METHOD OF DYNAMICALLY RENUMBERING PORTS AND AN APPARAT
MP12589	MP12589	US	14/309,773	6/19/2014	10650833	8/14/2018	In Force	METHOD OF REDUCING LATENCY IN A FLEXIBLE PARSER AND AN APP
MP12589	MP12589CN	CN	2015102297708	5/7/2015			Pending	METHOD OF REDUCING LATENCY IN A FLEXIBLE PARSER AND AN APP
MP12589	MP12589HK	HK	16108892.4	5/7/2015			Pending	A METHOD OF REDUCING LATENCY IN A FLEXIBLE PARSER AND AN A
MP12590	MP12590	US	14/309,763	6/19/2014			Pending	METHOD OF HANDLING LARGE PROTOCOL LAYERS FOR CONFIGURAB
MP12590	MP12590CN	CN	2015102297799	5/7/2015			Pending	METHOD OF HANDLING LARGE PROTOCOL LAYERS FOR CONFIGURAB
MP12590	MP12590HK	HK	16108890.6	5/7/2015			Pending	A METHOD OF HANDLING LARGE PROTOCOL LAYERS FOR CONFIGUR
MP12591	MP12591	US	14/309,739	6/19/2014	9438703	9/6/2016	In Force	METHOD OF FORMING A HASH INPUT FROM PACKET CONTENTS AN
MP12591	MP12591CN	CN	2015102367984	5/11/2015			Pending	A METHOD OF FORMING A HASH INPUT FROM PACKET CONTENTS A
MP12591	MP12591HK	HK	16108888.0	5/11/2015			Pending	A METHOD OF FORMING A HASH INPUT FROM PACKET CONTENTS A
MP12591	MP12591JP	JP	2015-122564	6/18/2015			Pending	METHOD OF FORMING HASH INPUT FROM PACKET CONTENTS, AND
MP12591	MP12591KR	KR	1020150087800	6/19/2015			Pending	METHOD OF FORMING A HASH INPUT FROM PACKET CONTENTS AN
MP12592	MP12592	US	14/309,679	6/19/2014	9531849	12/27/2016	In Force	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12592	MP12592CN	CN	2015102764264	5/26/2015			Pending	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12592	MP12592EP	EP	15172935.7	6/19/2015			Pending	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12592	MP12592HK	HK	16108882.6	7/26/2016			Pending	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12592	MP12592IN	IN	841/DEL/2015	3/26/2015			Pending	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12592	MP12592JP	JP	2015-122562	6/18/2015			Pending	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12592	MP12592KR	KR	1020150086078	6/17/2015			Pending	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12593	MP12593	US	14/309,650	6/19/2014	9531848	12/27/2016	In Force	METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO ENA
MP12593	MP12593CN	CN	2015102726703	5/25/2015			Pending	A METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO EN
MP12593	MP12593EP	EP	15172917.5	6/19/2015	2958286	5/1/2019	In Force	A METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO EN
MP12593	MP12593EPDE	DE	15172917.5	6/19/2015	60201502917.7	5/1/2019	In Force	A METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO EN
MP12593	MP12593HK	HK	16108894.2	5/25/2015			Pending	A METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO EN

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MP12593	MP12593IN	IN	847/DEL/2015	3/26/2015			Pending	A METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO EN
MP12593	MP12593JP	JP	2015-122561	6/18/2015			Pending	METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO ENA
MP12593	MP12593KR	KR	1020150084526	6/15/2015			Pending	METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO ENA
MP12594	MP12594	US	14/309,633	6/19/2014	9497294	11/15/2016	In Force	METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY STRU
MP12594	MP12594CN	CN	2015102722163	5/25/2015			Pending	A METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY ST
MP12594	MP12594EP	EP	15172911.8	6/19/2015	2958296	3/27/2019	In Force	A METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY ST
MP12594	MP12594EPDE	DE	15172911.8	6/19/2015	602015027021	3/27/2019	In Force	A METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY ST
MP12594	MP12594HK	HK	16104736.3	4/26/2016			Pending	A METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY ST
MP12594	MP12594IN	IN	836/DEL/2015	3/25/2015			Pending	A METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY ST
MP12594	MP12594JP	JP	2015-122560	6/18/2015			Pending	METHOD AND DEVICE FOR IDENTIFYING STRUCTURE OF PACKET BY
MP12594	MP12594KR	KR	1020150083631	6/12/2015			Pending	METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY STRU
MP12595	MP12595	US	14/309,619	6/19/2014	9473601	10/18/2016	In Force	METHOD OF REPRESENTING A GENERIC FORMAT HEADER USING CO
MP12595	MP12595CN	CN	2015102724360	5/25/2015			Pending	A METHOD OF REPRESENTING A GENERIC FORMAT HEADER USING C
MP12595	MP12595HK	HK	16107161.0	5/25/2015			Pending	A METHOD OF REPRESENTING A GENERIC FORMAT HEADER USING C
MP12596	MP12596	US	14/309,603	6/19/2014	9961167	5/1/2018	In Force	METHOD OF MODIFYING PACKETS TO A GENERIC FORMAT FOR ENAB
MP12596	MP12596CN	CN	2015102765888	5/26/2015			Pending	Method of modifying packets to a generic format for enabling progra
MP12596	MP12596EP	EP	15172942.3	6/19/2015	2958288	5/1/2019	In Force	A METHOD OF MODIFYING PACKETS TO A GENERIC FORMAT FOR EN
MP12596	MP12596EPDE	DE	15172942.3	6/19/2015	60201502917	5/1/2019	In Force	A METHOD OF MODIFYING PACKETS TO A GENERIC FORMAT FOR EN
MP12596	MP12596HK	HK	16108893.3	5/26/2015			Pending	A METHOD OF MODIFYING PACKETS TO A GENERIC FORMAT FOR EN
MP12596	MP12596IN	IN	834/DEL/2015	3/25/2015			Pending	A METHOD OF MODIFYING PACKETS TO A GENERIC FORMAT FOR EN
MP12596	MP12596JP	JP	2015-122559	6/18/2015			Pending	Method of modifying packets to a generic format for enabling progra
MP12596	MP12596KR	KR	1020150084520	6/15/2015			Pending	METHOD OF MODIFYING PACKETS TO A GENERIC FORMAT FOR ENAB
MP12598	MP12598	US	14/312,130	6/23/2014	9436654	9/6/2016	In Force	METHODS AND SYSTEMS FOR PROCESSING TASK MANAGEMENT FUN
MP1260	MP1260	US	11/840,460	8/17/2007	7863842	1/4/2011	In Force	MOTOR SPINDLE CONTROL SYSTEM AND METHOD
MP1260	MP1260C1	US	12/965,259	12/10/2010	8314581	11/20/2012	In Force	MOTOR SPINDLE CONTROL SYSTEM AND METHOD
MP1260	MP1260C1C1	US	13/663,341	10/29/2012	8497647	7/30/2013	In Force	MOTOR SPINDLE CONTROL SYSTEM AND METHOD
MP12600	MP12600	US	14/325,841	7/8/2014	9785403	10/10/2017	In Force	Engine Architecture For Processing Finite Automata
MP12600	MP12600C1	US	15/703,638	9/13/2017	10466964	11/5/2019	In force	Engine Architecture For Processing Finite Automata
MP12600	MP12600CN	CN	2014104323187	8/28/2014	212014104323	3/1/2019	In Force	Engine Architecture For Processing Finite Automata
MP12600	MP12600HK	HK	15107653.6	8/28/2014			Pending	Engine Architecture For Processing Finite Automata
MP12601	MP12601	US	14/663,912	3/20/2015	9692560	6/27/2017	In Force	METHODS AND SYSTEMS FOR RELIABLE NETWORK COMMUNICATIO
MP12602	MP12602	US	14/328,951	7/11/2014			Pending	MANAGING INSTRUCTION ORDER IN A PROCESSOR PIPELINE
MP12602	MP12602TW	TW	104114685	5/8/2015	1658407	5/1/2019	In Force	MANAGING INSTRUCTION ORDER IN A PROCESSOR PIPELINE
MP12603	MP12603	US	14/328,923	7/11/2014			Pending	MANAGING INSTRUCTION ORDER IN A PROCESSOR PIPELINE

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MP12603	MP12603TW	TW	104110835	4/2/2015	1659357	5/11/2019	In Force	MANAGING INSTRUCTION ORDER IN A PROCESSOR PIPELINE
MP12603	MP12603TWD1	TW	108111505	4/2/2015			Pending	MANAGING INSTRUCTION ORDER IN A PROCESSOR PIPELINE
MP12604	MP12604	US	14/351,105	7/14/2014	10198389	2/5/2019	In Force	BASEBOARD INTERCONNECTION DEVICE, SYSTEM AND METHOD
MP12606	MP12606	US	14/339,086	7/23/2014	9477424	10/25/2016	In Force	METHODS AND SYSTEMS FOR USING AN INTELLIGENT STORAGE ADA
MP12607	MP12607	US	14/724,273	5/28/2015	9819515	11/14/2017	In Force	INTEGRATED FABRIC ADAPTER AND ASSOCIATED METHODS THEREO
MP12609	MP12609	US	14/447,732	7/31/2014	9678779	6/13/2017	In Force	METHOD AND AN APPARATUS FOR CO-PROCESSOR DATA PLANE VIR
MP1261	MP1261C1	US	12/827,517	6/30/2010	7986114	7/26/2011	In Force	Fast Startup Supply For Chip Initialization
MP12610	MP12610	US	14/464,535	8/20/2014	9477414	10/25/2016	In Force	METHODS AND SYSTEMS FOR IMPROVED CACHING WITH DATA REC
MP12611	MP12611	US	14/470,309	8/27/2014	9311021	4/12/2016	In Force	METHODS AND SYSTEMS FOR PERFORMING A READ AHEAD OPERAT
MP12612	MP12612	US	14/470,337	8/27/2014	9330010	5/3/2016	In Force	METHODS AND SYSTEMS FOR IMPROVED DATA CACHING IN A VIRTU
MP12613	MP12613	US	14/473,524	8/29/2014	9936003	4/3/2018	In Force	METHOD AND SYSTEM FOR TRANSMITTING INFORMATION IN A NET
MP12614	MP12614	US	14/038,549	9/26/2013	9671844	6/6/2017	In Force	Method And Apparatus For Managing Global Chip Power On A Multic
MP12614	MP12614C1	US	15/459,531	4/27/2017	10152102	12/11/2018	In Force	Method And Apparatus For Managing Global Chip Power On A Multic
MP12614	MP12614C1C1	US	16/210,987	12/5/2018			Pending	Method And Apparatus For Managing Global Chip Power On A Multic
MP12614	MP12614CN	CN	2014104983609	9/25/2014	21.2014104983	7/21/2017	In Force	Method and apparatus for managing global chip power on multicore
MP12614	MP12614CND1	CN	2017105321604	9/25/2014			Pending	Method And Apparatus For Managing Global Chip Power On A Multic
MP12614	MP12614DE	DE	10 2014 014 301	9/25/2014	10 2014 014 31	8/16/2018	In Force	Method And Apparatus For Managing Global Chip Power On A Multic
MP12614	MP12614HK	HK	15109894.1	9/25/2014	12092108	5/4/2018	In Force	Method And Apparatus For Managing Global Chip Power On A Multic
MP12614	MP12614JP	JP	2014-192982	9/22/2014	6169547	7/7/2017	In Force	METHOD AND APPARATUS FOR MANAGING GLOBAL CHIP POWER O
MP12614	MP12614KR	KR	20140128554	9/25/2014	10170100400	11/23/2017	In Force	Method And Apparatus For Managing Global Chip Power On A Multic
MP12615	MP12615	US	14/494,187	9/23/2014	9729338	8/8/2017	In Force	FAST HARDWARE SWITCHOVER IN A CONTROL PATH IN A NETWORK
MP12615	MP12615C1	US	15/642,141	7/5/2017	10341130	7/2/2019	In Force	FAST HARDWARE SWITCHOVER IN A CONTROL PATH IN A NETWORK
MP12616	MP12616	US	14/494,291	9/23/2014	9565136	2/7/2017	In Force	MULTICAST REPLICATION ENGINE OF A NETWORK ASIC AND METHO
MP12617	MP12617	US	14/494,168	9/23/2014	9813327	7/11/2017	In Force	HIERARCHICAL HARDWARE LINKED LIST APPROACH FOR MULTICAST
MP12617	MP12617C1	US	15/725,643	10/5/2017	10205649	2/12/2019	In Force	HIERARCHICAL HARDWARE LINKED LIST APPROACH FOR MULTICAST
MP12617	MP12617C1D1	US	16/232,882	12/26/2018			Pending	HIERARCHICAL HARDWARE LINKED LIST APPROACH FOR MULTICAST
MP12618	MP12618	US	14/493,865	9/23/2014	9882678	1/30/2018	In Force	Method and Apparatus for Improving Data Integrity Using Comprese
MP12619	MP12619	US	14/494,229	9/23/2014	9766418	9/12/2017	In Force	SESSION BASED PACKET MIRRORING IN A NETWORK ASIC
MP12619	MP12619C1	US	15/672,082	8/8/2017	10417067	9/17/2019	In Force	SESSION BASED PACKET MIRRORING IN A NETWORK ASIC
MP1262	MP1262C1	US	14/305,312	6/16/2014	9313825	4/12/2016	In Force	Multi-Mode Handheld Apparatus
MP12620	MP12620	US	14/493,897	9/23/2014	9503218	11/22/2016	In Force	Method and Apparatus for Quantizing Soft Information using Non-Lin
MP12620	MP12620-2	US	14/493,953	9/23/2014	9768912	9/19/2017	In Force	Method and Apparatus for Quantizing Soft Information using Linear
MP12621	MP12621	US	14/498,580	9/26/2014	9460017	10/4/2016	In Force	METHODS AND SYSTEMS FOR EFFICIENT CACHE MIRRORING
MP12622	MP12622	US	14/716,689	5/19/2015	10129162	11/13/2018	In Force	SYSTEMS AND METHODS FOR DEFINING STORAGE

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MP12623	MP12623	US	14/870,675	9/30/2015			Pending	SYSTEMS AND METHODS FOR ALLOWING FLEXIBLE CHIP CONFIGURATION
MP12624	MP12624	US	14/514,708	10/15/2014	9696590	6/27/2017	In Force	FLEXIBLE INSTRUCTION EXECUTION IN A PROCESSOR PIPELINE
MP12624	MP12624TW	TW	103138148	11/4/2014	613590	2/1/2018	In Force	FLEXIBLE INSTRUCTION EXECUTION IN A PROCESSOR PIPELINE
MP12625	MP12625	US	14/514,596	10/15/2014	9747109	8/29/2017	In Force	FLEXIBLE INSTRUCTION EXECUTION IN A PROCESSOR PIPELINE
MP12625	MP12625TW	TW	103138147	11/4/2014	613589	2/1/2018	In Force	FLEXIBLE INSTRUCTION EXECUTION IN A PROCESSOR PIPELINE
MP12626	MP12626	US	14/521,333	10/22/2014			Pending	APPARATUS AND A METHOD OF DETECTING ERRORS ON REGISTERS
MP12627	MP12627	US	14/521,354	10/22/2014	9960324	6/5/2018	In Force	TWO MODES OF A CONFIGURATION INTERFACE OF A NETWORK ASIC
MP12627	MP12627D1	US	15/969,681	5/2/2018			Pending	METHOD OF IMPLEMENTING A NETWORK ASIC IN A NETWORK DEVICE
MP12628	MP12628	US	14/521,359	10/22/2014	9542342	1/10/2017	In Force	SMART HOLDING REGISTERS TO ENABLE MULTIPLE REGISTER ACCESS
MP12629	MP12629	US	14/521,367	10/22/2014	10078605	9/18/2018	In Force	MULTIPLE-INTERRUPT PROPAGATION SCHEME IN A NETWORK ASIC
MP1263	MP1263	US	11/840,682	8/17/2007	8441751	5/14/2013	In Force	DIBIT PULSE EXTRACTION METHODS AND SYSTEMS
MP12630	MP12630	US	14/530,388	10/31/2014	9936021	4/3/2018	In Force	METHODS AND SYSTEMS FOR A MULTI-FUNCTION ADAPTER
MP12631	MP12631	US	14/667,485	3/24/2015	9934177	4/3/2018	In Force	METHODS AND SYSTEMS FOR ACCESSING STORAGE USING A NETWORK
MP12632	MP12632	US	14/536,937	11/10/2014	10116564	10/30/2018	In Force	HYBRID WILDCARD MATCH TABLE
MP12632	MP12632CN	CN	2015107573136	11/9/2015			Pending	HYBRID WILDCARD MATCH TABLE
MP12632	MP1263211	US	14/932,791	11/4/2015			Pending	HYBRID WILDCARD MATCH TABLE
MP12632	MP12632TW	TW	104134823	10/23/2015	1667960	8/1/2019	In Force	HYBRID WILDCARD MATCH TABLE
MP12633	MP12633	US	14/538,484	11/11/2014	9665790	5/9/2017	In Force	Adder Decoder
MP12634	MP12634	US	14/540,414	11/13/2014	9858222	1/2/2018	In Force	Register Access Control Among Multiple Devices
MP12635	MP12635	US	14/540,175	11/13/2014	10013385	7/3/2018	In Force	Programmable Validation Of Transaction Requests
MP12636	MP12636	US	14/540,436	11/13/2014	10002099	6/19/2018	In Force	Arbitrated Access To Resources Among Multiple Devices
MP12637	MP12637	US	14/540,379	11/13/2014	9569362	2/14/2017	In Force	Programmable Ordering And Prefetch
MP12638	MP12638	US	14/540,656	11/13/2014			Pending	Independent Ordering of Independent Transactions
MP12639	MP12639	US	14/542,350	11/14/2014	10110515	10/23/2018	In Force	PACKET SCHEDULING USING HIERARCHICAL SCHEDULING PROCESS
MP12640	MP12640	US	14/542,039	11/14/2014	9417655	8/16/2016	In Force	FREQUENCY DIVISION CLOCK ALIGNMENT
MP12640	MP12640TW	TW	104112338	4/17/2015	1642277	11/21/2018	In Force	FREQUENCY DIVISION CLOCK ALIGNMENT
MP12641	MP12641	US	14/542,298	11/14/2014	10291540	5/14/2019	In Force	METHOD AND APPARATUS FOR PERFORMING A WEIGHTED QUEUE S
MP12642	MP12642	US	14/541,807	11/14/2014	9697137	7/4/2017	In Force	FILTERING TRANSLATION LOOKASIDE BUFFER INVALIDATIONS
MP12644	MP12644	US	14/541,902	11/14/2014	9665505	5/30/2017	In Force	MANAGING BUFFERED COMMUNICATION BETWEEN SOCKETS
MP12644	MP12644TW	TW	104110836	4/2/2015	1658362	5/1/2019	In Force	MANAGING BUFFERED COMMUNICATION BETWEEN SOCKETS
MP12645	MP12645	US	14/541,882	11/14/2014	10007524	6/26/2018	In Force	MANAGING HISTORY INFORMATION FOR BRANCH PREDICTION
MP12645	MP12645TW	TW	104112340	4/17/2015	1648624	1/21/2019	In Force	MANAGING HISTORY INFORMATION FOR BRANCH PREDICTION
MP12646	MP12646	US	14/541,977	11/14/2014	9470719	10/18/2016	In Force	TESTING SEMICONDUCTOR DEVICES
MP12647	MP12647	US	14/616,580	2/6/2015	9904511	2/27/2018	In Force	HIGH PERFORMANCE SHIFTER CIRCUIT

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MP12648	MP12648	US	14/542,060	11/14/2014	9601181	3/21/2017	In Force	CONTROLLED MULTI-STEP DE-ALIGNMENT OF CLOCKS
MP12649	MP12649	US	14/542,136	11/14/2014	9910776	3/6/2018	In Force	INSTRUCTION ORDERING FOR IN-PROGRESS OPERATIONS
MP12649	MP12649C1	US	15/890,921	2/7/2018	10339054	7/2/2019	In Force	INSTRUCTION ORDERING FOR IN-PROGRESS OPERATIONS
MP12649	MP12649TW	TW	104110197	3/30/2015	1664571	7/1/2019	In Force	INSTRUCTION ORDERING FOR IN-PROGRESS OPERATIONS
MP12649	MP12649TWD1	TW	108117952	5/24/2019			Pending	INSTRUCTION ORDERING FOR IN-PROGRESS OPERATIONS
MP12650	MP12650	US	14/542,118	11/14/2014	9870328	1/16/2018	In Force	MANAGING BUFFERED COMMUNICATION BETWEEN CORES
MP12651	MP12651	US	14/542,188	11/14/2014	9613679	4/4/2017	In Force	CONTROLLED DYNAMIC DE-ALIGNMENT OF CLOCKS
MP12652	MP12652	US	14/542,202	11/14/2014	9561099	11/22/2016	In Force	MANAGING SKEW IN DATA SIGNALS WITH MULTIPLE MODES
MP12652	MP1265211	US	14/672,787	3/30/2015	9667672	3/28/2017	In Force	MANAGING SKEW IN DATA SIGNALS WITH ADJUSTABLE STROBE
MP12653	MP12653	US	14/541,952	11/14/2014	9570128	2/14/2017	In Force	MANAGING SKEW IN DATA SIGNALS
MP12654	MP12654	US	14/940,982	11/13/2015	10078601	9/18/2018	In Force	APPROACH FOR INTERFACING A PIPELINE WITH TWO OR MORE INTE
MP12655	MP12655	US	14/541,841	11/14/2014	9703669	7/11/2017	In Force	APPARATUS AND METHOD FOR DISTRIBUTED INSTRUCTION TRACE I
MP12656	MP12656	US	14/542,485	11/14/2014	10116772	10/30/2018	In Force	NETWORK SWITCHING WITH CO-RESIDENT DATA-PLANE AND NETW
MP12656	MP12656D1	US	16/132,424	9/16/2018			Pending	NETWORK SWITCHING WITH CO-RESIDENT DATA-PLANE AND NETW
MP12657	MP12657	US	14/542,383	11/14/2014			Pending	APPARATUS AND METHOD FOR A MULTI-ENTITY SECURE SOFTWARE
MP12658	MP12658	US	14/541,726	11/14/2014	9684606	6/20/2017	In Force	TRANSLATION LOOKASIDE BUFFER INVALIDATION SUPPRESSION
MP12658	MP12658TW	TW	104113187	4/24/2015	1646421	1/1/2019	In Force	TRANSLATION LOOKASIDE BUFFER INVALIDATION SUPPRESSION
MP12659	MP12659	US	14/541,616	11/14/2014	9405702	8/2/2016	In Force	CACHING TLB TRANSLATIONS USING A UNIFIED PAGE TABLE WALKER
MP12659	MP12659TW	TW	104110648	4/1/2015	1641946	11/21/2018	In Force	CACHING TLB TRANSLATIONS USING A UNIFIED PAGE TABLE WALKER
MP1266	MP1266	US	11/836,641	8/9/2007	8122331	2/21/2012	In Force	MEDIA DEFECT COMPENSATION SYSTEM AND METHOD
MP12660	MP12660	US	14/541,498	11/14/2014	9501425	11/22/2016	In Force	TRANSLATION LOOKASIDE BUFFER MANAGEMENT
MP12660	MP12660TW	TW	104110402	3/31/2015			Pending	TRANSLATION LOOKASIDE BUFFER MANAGEMENT
MP12661	MP12661	US	14/541,685	11/14/2014	10394730	8/27/2019	In Force	DISTRIBUTED INTERRUPT SCHEME IN A MULTI-PROCESSOR SYSTEM
MP12662	MP12662	US	14/541,971	11/14/2014	9404970	8/2/2016	In Force	DEBUG INTERFACE FOR MULTIPLE CPU CORES
MP12663	MP12663	US	14/542,528	11/14/2014	9813342	11/7/2017	In Force	METHOD AND SYSTEM FOR IMPROVED LOAD BALANCING OF RECEIV
MP12664	MP12664	US	14/941,182	11/13/2015	10303514	5/28/2019	In Force	SHARING RESOURCES IN A MULTI-CONTENT COMPUTING SYSTEM
MP12665	MP12665	US	14/940,585	11/13/2015			Pending	IMPLEMENTING 128-BIT SIMD OPERATIONS ON A 64-BIT DATAPATH
MP12665	MP12665CN	CN	2015107789847	11/13/2015			Pending	IMPLEMENTING 128-BIT SIMD OPERATIONS ON A 64-BIT DATAPATH
MP12665	MP12665TW	TW	104137305	11/12/2015			Pending	IMPLEMENTING 128-BIT SIMD OPERATIONS ON A 64-BIT DATAPATH
MP12666	MP12666	US	14/542,509	11/14/2014	10050896	8/14/2018	In Force	MANAGEMENT OF AN OVER-SUBSCRIBED SHARED BUFFER
MP12667	MP12667	US	14/541,769	11/14/2014	9928193	3/27/2018	In Force	DISTRIBUTED TIMER SUBSYSTEM
MP12668	MP12668	US	14/940,981	11/13/2015	9933809	4/3/2018	In Force	Automatic Data Rate Matching
MP12669	MP12669	US	14/940,538	11/13/2015			Pending	CARRY CHAIN FOR SIMD OPERATIONS
MP1267	MP1267	US	11/894,179	8/20/2007	7873377	1/18/2011	In Force	Background Scan Process For Wireless Devices

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MP1267	MP1267C1	US	13/008,574	1/18/2011	8391907	3/5/2013	In Force	BACKGROUND SCAN PROCESS FOR WIRELESS DEVICES
MP1267	MP1267C1C1	US	13/785,825	3/5/2013	9029552	4/28/2015	In Force	BACKGROUND SCAN PROCESS FOR WIRELESS DEVICES
MP12670	MP12670	US	14/541,917	11/14/2014	9824058	11/21/2017	In Force	Bypass FIFO For Multiple Virtual Channels
MP12671	MP12671	US	14/941,082	11/13/2015	9678717	6/13/2017	In Force	DISTRIBUTING RESOURCE REQUESTS IN A COMPUTING SYSTEM
MP12672	MP12672	US	14/542,216	11/14/2014	9411361	8/9/2016	In Force	FREQUENCY DIVISION CLOCK ALIGNMENT USING PATTERN SELECTIO
MP12673	MP12673	US	14/542,065	11/14/2014	9568944	2/14/2017	In Force	DISTRIBUTED TIMER SUBSYSTEM ACROSS MULTIPLE DEVICES
MP12674	MP12674	US	14/940,679	11/13/2015	9763722	7/11/2017	In Force	METHOD AND SYSTEM FOR COMPRESSING DATA FOR A TRANSLATIO
MP12674	MP12674C1	US	15/598,719	5/18/2017	9772952	9/26/2017	In Force	METHOD AND SYSTEM FOR COMPRESSING DATA FOR A TRANSLATIO
MP12675	MP12675	US	14/609,164	1/29/2015	9740807	8/22/2017	In Force	METHOD TO MEASURE EDGE-RATE TIMING PENALTY OF DIGITAL IN
MP12676	MP12676	US	14/548,040	11/19/2014	9674303	6/6/2017	In Force	METHODS AND SYSTEMS FOR EFFICIENT DATA TRANSMISSION IN A D
MP12677	MP12677-1	US	14/681,978	4/8/2015	9571412	2/14/2017	In Force	SYSTEMS AND METHODS FOR HARDWARE ACCELERATED TIMER IMP
MP12677	MP12677-2	US	14/939,982	11/12/2015	10684719	9/25/2018	In Force	SYSTEMS AND METHODS FOR HARDWARE ACCELERATED METERING
MP12677	MP12677TW	TW	104118717	6/10/2015	1631941	8/1/2018	In Force	SYSTEMS AND METHODS FOR HARDWARE ACCELERATED TIMER IMP
MP12678	MP12678	US	14/577,448	12/19/2014	986657	1/9/2018	In Force	NETWORK SWITCHING WITH LAYER 2 SWITCH COUPLED CO-RESIDEN
MP12679	MP12679	US	14/585,761	12/30/2014	9356023	7/19/2016	In Force	METHODS AND SYSTEMS FOR PARALLEL DISTRIBUTED COMPUTATIO
MP12680	MP12680	US	14/593,898	1/9/2015	9483207	11/1/2016	In Force	METHODS AND SYSTEMS FOR EFFICIENT CACHING USING AN INTELLI
MP12681	MP12681	US	14/597,030	1/14/2015	9952979	4/24/2018	In Force	METHODS AND SYSTEMS FOR DIRECT MEMORY ACCESS OPERATIONS
MP12682	MP12682	US	14/609,945	1/30/2015	9426084	8/23/2016	In Force	METHODS AND SYSTEMS FOR CONTROLLING TRANSMISSION RATES I
MP12683	MP12683	US	14/634,446	2/27/2015	9660614	3/21/2017	In Force	AUTOMATED FLIP-FLOP INSERTIONS IN PHYSICAL DESIGN WITHOUT
MP12684	MP12684	US	14/617,699	2/9/2015	9778315	10/3/2017	In Force	TESTBENCH BUILDER, SYSTEM, DEVICE AND METHOD HAVING AGEN
MP12685	MP12685	US	14/617,610	2/9/2015	9330227	5/3/2016	In Force	TESTBENCH BUILDER, SYSTEM, DEVICE AND METHOD INCLUDING A
MP12686	MP12686	US	14/617,546	2/9/2015	10682538	9/25/2018	In Force	TESTBENCH BUILDER, SYSTEM, DEVICE AND METHOD
MP12686	MP12688	US	14/617,645	2/9/2015	9566982	11/29/2016	In Force	TESTBENCH BUILDER, SYSTEM, DEVICE AND METHOD INCLUDING A
MP12689	MP12689	US	14/632,200	2/26/2015	9590897	3/7/2017	In Force	METHODS AND SYSTEMS FOR NETWORK DEVICES AND ASSOCIATED
MP1269	MP1269	US	11/893,543	8/16/2007	8122234	2/21/2012	In Force	Flash Memory Backup System For Network Devices
MP12691	MP12691	US	14/638,266	3/4/2015	9720773	8/1/2017	In Force	MANAGING REUSE INFORMATION IN CACHES
MP12691	MP12691TW	TW	104113188	4/24/2015	1641947	11/21/2018	In Force	MANAGING REUSE INFORMATION IN CACHES
MP12692	MP12692	US	14/638,194	3/4/2015	10013360	7/3/2018	In Force	MANAGING REUSE INFORMATION WITH MULTIPLE TRANSLATION ST
MP12694	MP12694	US	14/637,533	3/4/2015	9678159	6/13/2017	In Force	COMMUNICATION AND CONTROL TOPOLOGY FOR EFFICIENT TESTIN
MP12694	MP12694TW	TW	104118042	3/13/2015	1631355	8/1/2018	In Force	COMMUNICATION AND CONTROL TOPOLOGY FOR EFFICIENT TESTIN
MP12696	MP12696	US	15/065,799	3/9/2016	10602218	6/19/2018	In Force	DESIGN AND VERIFICATION OF A MULTICHIP COHERENCE PROTOCOL
MP12697	MP12697	US	14/662,405	3/19/2015	9471509	10/18/2016	In Force	MANAGING ADDRESS-INDEPENDENT PAGE ATTRIBUTES
MP12697	MP12697TW	TW	104113191	4/24/2015	1648625	1/21/2019	In Force	MANAGING ADDRESS-INDEPENDENT PAGE ATTRIBUTES
MP12698	MP12698	US	14/664,680	3/20/2015	9606020	3/21/2017	In Force	REPEATER INSERTIONS PROVIDING REDUCED ROUTING PERTURBATI

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MP12699	MP12699	US	14/667,568	3/24/2015	9960253	2/20/2018	In Force	PHANTOM QUEUE LINK LEVEL LOAD BALANCING SYSTEM, METHOD
MP12699	MP12699C1	US	15/862,509	1/4/2018	10103993	10/16/2018	In Force	PHANTOM QUEUE LINK LEVEL LOAD BALANCING SYSTEM, METHOD
MP12699	MP12699C1C1	US	16/126,644	9/10/2018			Pending	PHANTOM QUEUE LINK LEVEL LOAD BALANCING SYSTEM, METHOD
MP12699	MP12699C1C1C1	US	16/694,481	11/25/2019			Pending	PHANTOM QUEUE LINK LEVEL LOAD BALANCING SYSTEM, METHOD
MP1270	MP1270	US	11/825,556	7/6/2007	8036113	10/11/2011	In Force	Packet Sampling Using Rate-Limiting Mechanisms
MP12700	MP12700	US	14/667,488	3/24/2015	10419571	9/17/2019	In Force	PACKET PROCESSOR FORWARDING DATABASE CACHE
MP12700	MP12700CN	CN	201610176470.1	3/24/2016			Pending	PACKET PROCESSOR FORWARDING DATABASE CACHE
MP12700	MP12700HK	HK	17106372.6	3/24/2016			Pending	PACKET PROCESSOR FORWARDING DATABASE CACHE
MP12700	MP12700TW	TW	105108783	3/22/2016			Pending	PACKET PROCESSOR FORWARDING DATABASE CACHE
MP12701	MP12701	US	14/672,025	3/27/2015	9817067	11/14/2017	In Force	TESTBENCH BUILDER, SYSTEM, DEVICE AND METHOD INCLUDING LA
MP12702	MP12702	US	14/672,006	3/27/2015	10282315	5/7/2019	In Force	SOFTWARE ASSISTED HARDWARE CONFIGURATION FOR SOFTWARE
MP12703	MP12703	US	14/671,900	3/27/2015			Pending	METHOD AND APPARATUS FOR BYPASS ROUTING OF MULTICAST DA
MP12704	MP12704	US	14/671,983	3/27/2015	9547041	1/17/2017	In Force	TESTBENCH BUILDER, SYSTEM, DEVICE AND METHOD WITH PHASE S
MP12705	MP12705	US	14/671,993	3/27/2015	9372772	6/21/2016	In Force	CO-VERIFICATION OF HARDWARE AND SOFTWARE, A UNIFIED APPR
MP12706	MP12706	US	14/672,016	3/27/2015	10096963	6/26/2018	In Force	PACKET TRACKING IN A VERIFICATION ENVIRONMENT
MP12707	MP12707	US	14/673,835	3/30/2015	9582215	2/28/2017	In Force	PACKET PROCESSING SYSTEM, METHOD AND DEVICE UTILIZING ME
MP12707	MP12707-2	US	14/673,836	3/30/2015	9612950	4/4/2017	In Force	CONTROL PATH SUBSYSTEM, METHOD AND DEVICE UTILIZING MEM
MP12707	MP12707-3	US	14/673,840	3/30/2015	9652171	5/16/2017	In Force	DATAPATH SUBSYSTEM, METHOD AND DEVICE UTILIZING MEMORY S
MP12707	MP12707-3C1	US	15/463,946	4/10/2017	10061513	8/28/2018	In Force	PACKET PROCESSING SYSTEM, METHOD AND DEVICE UTILIZING ME
MP12708	MP12708	US	14/673,807	3/30/2015	10003551	6/19/2018	In Force	PACKET MEMORY SYSTEM, METHOD AND DEVICE FOR PREVENTING
MP12708	MP12708CN	CN	2016101932888	3/30/2016			Pending	PACKET MEMORY SYSTEM, METHOD AND DEVICE FOR PREVENTING
MP12708	MP12708DE	DE	10 2016 003 679	3/24/2016			Pending	A PACKET MEMORY SYSTEM, METHOD AND DEVICE FOR PREVENTIN
MP12708	MP12708HK	HK	17103793.4	3/30/2016			Pending	A PACKET MEMORY SYSTEM, METHOD AND DEVICE FOR PREVENTIN
MP12708	MP12708KR	KR	2016-0036000	3/25/2016			Pending	A PACKET MEMORY SYSTEM, METHOD AND DEVICE FOR PREVENTIN
MP12708	MP12708TW	TW	105109655	3/28/2016			Pending	PACKET MEMORY SYSTEM, METHOD AND DEVICE FOR PREVENTING
MP12709	MP12709	US	14/673,813	3/30/2015	9747226	8/29/2017	In Force	PACKET PROCESSING SYSTEM, METHOD AND DEVICE TO OPTIMIZE P
MP12709	MP12709CN	CN	201610188963.1	3/29/2016			Pending	PACKET PROCESSING SYSTEM, METHOD AND DEVICE TO OPTIMIZE P
MP12709	MP12709HK	HK	17103792.5	3/29/2016			Pending	A PACKET PROCESSING SYSTEM, METHOD AND DEVICE TO OPTIMIZE
MP12709	MP12709TW	TW	105109656	3/28/2016			Pending	A PACKET PROCESSING SYSTEM, METHOD AND DEVICE TO OPTIMIZE
MP1271	MP1271C1	US	13/117,974	5/27/2011	8271701	9/18/2012	In Force	CONCURRENT INPUT/OUTPUT CONTROL AND INTEGRATED ERROR M
MP12710	MP12710	US	14/673,819	3/30/2015			Pending	PACKET PROCESSING SYSTEM, METHOD AND DEVICE HAVING REDUC
MP12711	MP12711	US	14/672,874	3/30/2015	9349434	5/24/2016	In Force	VARIABLE STROBE FOR ALIGNMENT OF PARTIALLY INVISIBLE DATA SI
MP12713	MP12713	US	14/675,356	3/31/2015	9792400	10/17/2017	In Force	DETERMINATION OF FLIP-FLOP COUNT IN PHYSICAL DESIGN
MP12714	MP12714	US	14/675,710	3/31/2015	9836283	12/5/2017	In Force	COMPILER ARCHITECTURE FOR PROGRAMMABLE APPLICATION SPEC

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MP12714	MP12714C1	US	15/804,835	11/6/2017	10466976	11/5/2019	In force	COMPILER ARCHITECTURE FOR PROGRAMMABLE APPLICATION SPEC
MP12715	MP12715	US	14/675,702	3/31/2015	9864584	1/9/2018	In force	CODE GENERATOR FOR PROGRAMMABLE NETWORK DEVICES
MP12716	MP12716	US	14/675,696	3/31/2015	9870204	1/16/2018	In force	ALGORITHM TO ACHIEVE OPTIMAL LAYOUT OF INSTRUCTION TABLES
MP12717	MP12717	US	14/675,692	3/31/2015	9864583	1/9/2018	In force	ALGORITHM TO DERIVE LOGIC EXPRESSION TO SELECT EXECUTION B
MP12718	MP12718	US	14/675,307	3/31/2015	9547733	1/17/2017	In force	IDENTIFYING INVERSION ERROR IN LOGIC EQUIVALENCE CHECK
MP12719	MP12719	US	14/675,682	3/31/2015	9582251	2/28/2017	In force	ALGORITHM TO ACHIEVE OPTIMAL LAYOUT OF DECISION LOGIC ELE
MP1272	MP1272	US	11/765,889	6/20/2007	7538967	5/26/2009	In force	METHOD AND APPARATUS FOR ESTIMATING A POSITION OF A HEAD
MP12720	MP12720	US	14/675,450	3/31/2015			In force	METHOD AND APPARATUS FOR USING MULTIPLE LINKED MEMORY
MP12720	MP12720C1	US	16/594,962	10/7/2019			Pending	METHOD AND APPARATUS FOR USING MULTIPLE LINKED MEMORY
MP12720	MP12720CN	CN	2015102909668	5/29/2015			Pending	Method and apparatus for using multiple linked memory lists
MP12720	MP12720HK	HK	17101140.8	5/29/2015			Pending	METHOD AND APPARATUS FOR USING MULTIPLE LINKED MEMORY L
MP12720	MP12720IN	IN	1284/DEL/2015	5/7/2015			Pending	METHOD AND APPARATUS FOR USING MULTIPLE LINKED MEMORY L
MP12720	MP12720JP	JP	2015-176003	6/31/2015	6535253	6/7/2019	In force	METHOD AND APPARATUS FOR USING MULTIPLE LINKED MEMORY L
MP12720	MP12720KR	KR	1020150067244	5/14/2015			Pending	METHOD AND APPARATUS FOR USING MULTIPLE LINKED MEMORY L
MP12720	MP12720TW	TW	104124865.0	7/31/2015			Pending	Method and apparatus for using multiple linked memory lists
MP12721	MP12721	US	14/675,403	3/31/2015			Pending	APPROACH FOR LOGIC SIGNAL GROUPING AND RTL GENERATION USI
MP12722	MP12722	US	14/675,342	3/31/2015	10303626	5/28/2019	In force	APPROACH FOR CHIP-LEVEL FLOP INSERTION AND VERIFICATION BAS
MP12723	MP12723	US	14/675,667	3/31/2015	9666781	3/28/2017	In force	PARSER ENGINE PROGRAMMING TOOL FOR PROGRAMMABLE NETW
MP12724	MP12724	US	14/675,674	3/31/2015	9864582	1/9/2018	In force	CODE PROCESSOR TO BUILD ORTHOGONAL EXECUTION BLOCKS FOR
MP12725	MP12725	US	14/675,728	3/31/2015	9954551	4/24/2018	In force	BARREL COMPACTOR SYSTEM, METHOD AND DEVICE
MP12725	MP12725C1	US	15/917,284	3/9/2018			Pending	BARREL COMPACTOR SYSTEM, METHOD AND DEVICE
MP12726	MP12726	US	14/675,734	3/31/2015	9584635	2/28/2017	In force	BARREL COMPACTOR SYSTEM, METHOD AND DEVICE HAVING CELL C
MP12727	MP12727	US	14/676,679	4/1/2015	9871733	1/16/2018	In force	POLICER ARCHITECTURE
MP12728	MP12728	US	14/673,828	3/30/2015	9666942	3/28/2017	In force	PACKET PROCESSING SYSTEM, METHOD AND DEVICE UTILIZING A PO
MP12728	MP12728C1	US	15/434,917	2/16/2017	10289575	5/14/2019	In force	PACKET PROCESSING SYSTEM, METHOD AND DEVICE UTILIZING A PO
MP12728	MP12728C1C1	US	16/370,746	3/29/2019			Pending	PACKET PROCESSING SYSTEM, METHOD AND DEVICE UTILIZING A PO
MP12729	MP12729	US	14/678,789	4/3/2015	9509362	11/29/2016	In force	Method and Apparatus for Handling Modified Constellation Mapping
MP1273	MP1273C1	US	13/108,151	5/16/2011	8189728	5/29/2012	In force	JITTER MEASUREMENT
MP12730	MP12730	US	14/678,836	4/3/2015	9825799	11/21/2017	In force	METHOD AND APPARATUS FOR DISCARDING UNUSED POINTS FROM
MP12731	MP12731	US	14/698,581	4/28/2015	9720733	8/1/2017	In force	METHODS AND SYSTEMS FOR CONTROL BLOCK ROUTING
MP12732	MP12732	US	15/152,164	5/11/2016	10095558	10/9/2018	In force	SYSTEMS AND METHODS FOR OFFLOADING INLINE SSL PROCESSING
MP12733	MP12733	US	14/805,161	7/21/2015	9588839	3/7/2017	In force	METHODS AND SYSTEMS FOR USING SHARED LOGIC AT NETWORK D
MP12734	MP12734	US	14/830,045	8/19/2015	10216666	2/26/2019	In force	CACHING METHODS AND SYSTEMS USING A NETWORK INTERFACE C
MP12735	MP12735	US	15/246,440	8/24/2016	10250571	4/2/2019	In force	SYSTEMS AND METHODS FOR OFFLOADING IPSEC PROCESSING TO A

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MP12736	MP12736	US	14/838,056	8/27/2015	9985887	5/29/2018	In Force	METHOD AND APPARATUS FOR PROVIDING A LOW LATENCY TRANS
MP12736	MP12736.2	US	14/838,088	8/27/2015			Pending	Method and Apparatus For Providing A Low Latency Transmission Sys
MP12736	MP127362C1	US	16/699,520	11/29/2019			Pending	Method and Apparatus For Providing A Low Latency Transmission Sys
MP12736	MP12736DIV	US	15/957,566	4/19/2018			Pending	Method and Apparatus for Providing A Low Latency Transmission Sys
MP12737	MP12737	US	15/248,876	8/26/2016	10425234	9/24/2019	In Force	SYSTEMS AND METHODS FOR PERFECT FORWARD SECRECY (PFS) TR
MP12738	MP12738	US	14/840,323	8/31/2015	10216430	2/26/2019	In Force	LOCAL ORDERING OF INSTRUCTIONS IN A COMPUTING SYSTEM
MP12738	MP12738C1	US	16/240,104	1/4/2019			Pending	LOCAL INSTRUCTION ORDERING BASED ON MEMORY DOMAINS
MP12739	MP12739	US	15/265,252	9/14/2016	10615740	7/17/2018	In Force	SYSTEMS AND METHODS FOR OFFLOADING LINK AGGREGATION TO
MP12741	MP12741	US	14/864,323	9/24/2015	9860524	10/24/2017	In Force	SWITCHING METHODS AND SYSTEMS FOR A NETWORK INTERFACE CP
MP12742	MP12742	US	14/874,460	10/4/2015	9753859	9/5/2017	In Force	INPUT OUTPUT VALUE PREDICTION WITH PHYSICAL OR VIRTUAL AD
MP12743	MP12743	US	14/919,508	10/21/2015	9753852	9/5/2017	In Force	METHODS AND SYSTEMS FOR MANAGING ADDRESS LIST CONTROL B
MP12744	MP12744	US	15/291,475	10/12/2016	10602216	6/19/2018	In Force	SYSTEMS AND METHODS FOR DYNAMIC REGRESSION TEST GENERATI
MP12745	MP12745	US	14/969,950	12/15/2015	9946671	4/17/2018	In Force	METHODS AND SYSTEMS FOR PROCESSING READ AND WRITE REQUE
MP12746	MP12746	US	15/000,840	1/19/2016	9673753	6/6/2017	In Force	Voltage-Controlled Oscillator With Improved Tuning Curve Linearizat
MP1275	MP1275	US	11/894,864	8/21/2007	7957259	6/7/2011	In Force	MODE DETECTION FOR DVB RECEIVER
MP12753	MP12753	US	15/013,139	2/2/2016	10057614	6/26/2018	In Force	METHOD AND APPARATUS FOR DETERMINING METRIC FOR SELECTIV
MP12754	MP12754	US	15/067,139	3/10/2016	9825884	11/21/2017	In Force	PROTOCOL INDEPENDENT PROGRAMMABLE SWITCH (PIPS) SOFTWARE
MP12754	MP12754C1	US	15/786,900	10/18/2017			Pending	PROTOCOL INDEPENDENT PROGRAMMABLE SWITCH (PIPS) FOR SCF
MP12754	MP12754CN	CN	201680015083.1	9/11/2017			Pending	Protocol independent programmable switch (PIPS) for software defin
MP12754	MP12754DE	DE	11 2016 001 193	3/11/2016			Pending	Protocol-independent, programmable switch for software-defined da
MP12754	MP12754TW	TW	105107740	3/14/2016			Pending	Protocol independent programmable switch (PIPS) for software defin
MP12757	MP12757	US	15/088,302	4/1/2016	9779028	10/3/2017	In Force	MANAGING TRANSLATION INVALIDATION
MP12758	MP12758	US	15/088,354	4/1/2016	9772943	9/26/2017	In Force	MANAGING SYNONYMS IN VIRTUAL-ADDRESS CACHES
MP12759	MP12759	US	15/099,552	4/14/2016			Pending	METHOD AND APPARATUS FOR SHARED MULTI-PORT MEMORY ACC
MP12760	MP12760	US	15/136,330	4/22/2016	10235211	3/19/2019	In Force	Method And Apparatus For Dynamic Virtual System On Chip
MP12760	MP12760CN	CN	201710266560.1	4/21/2017			Pending	DISPLAY CONTROL FOR CELLULAR PHONE
MP12760	MP12760DE	DE	102017206710.1	4/20/2017			Pending	Method And Apparatus For Dynamic Virtual System On Chip
MP12760	MP12760JP	JP	2017-083643	4/20/2017			Pending	METHOD AND DEVICE FOR KINETIC VIRTUAL SYSTEM ON CHIP
MP12760	MP12760KR	KR	10-2017-604451	4/6/2017			Pending	Method And Apparatus For Dynamic Virtual System On Chip
MP12760	MP12760TW	TW	106113271	4/20/2017			Pending	Method And Apparatus For Dynamic Virtual System On Chip
MP12761	MP12761	US	15/143,302	4/29/2016	9904305	2/27/2018	In Force	Voltage Regulator With Adaptive Bias Network
MP12762	MP12762	US	15/143,282	4/29/2016	9590797	3/7/2017	In Force	Edge Rate Control Calibration
MP12763	MP12763	US	15/582,408	4/28/2017	10409911	9/10/2019	In Force	SYSTEMS AND METHODS FOR TEXT ANALYTICS PROCESSOR
MP12766	MP12766	US	15/582,420	4/28/2017			Pending	SYSTEMS AND METHODS FOR DEEP LEARNING PROCESSOR

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MP12767	MP12767	US	15/590,519	5/9/2017			Pending	SYSTEMS AND METHODS FOR VIRTIO BASED OPTIMIZATION OF DATA
MP12768	MP12768	US	15/154,994	5/14/2016			Pending	METHOD AND APPARATUS FOR EFFICIENT AND FLEXIBLE DIRECT ME
MP12769	MP12769	US	15/420,343	1/31/2017	10050624	8/14/2018	In Force	Process-Compensated Level-Up Shifter Circuit
MP12770	MP12770	US	15/159,729	5/19/2016	10027457	7/17/2018	In Force	METHODS AND APPARATUS FOR PROVIDING SOFT AND BLIND COMB
MP12770	MP12770-2	US	15/378,750	12/14/2016	10469233	11/5/2019	In force	Methods and Apparatus for Providing Soft and Blind Combining for P
MP12770	MP12770-2C1	US	16/583,243	9/25/2019			Pending	Methods and Apparatus for Providing Soft and Blind Combining for P
MP12771	MP12771	US	15/593,235	5/11/2017			Pending	SYSTEMS AND METHODS FOR VECTORIZED FFT FOR MULTI-DIMENSIO
MP12772	MP12772	US	15/469,466	5/31/2016	10129219	11/13/2018	In Force	METHODS AND SYSTEMS FOR SECURING DATA STORED AT A STORAGE
MP12774	MP12774	US	15/193,535	6/27/2016	10223279	3/5/2019	In Force	MANAGING VIRTUAL-ADDRESS CACHES FOR MULTIPLE MEMORY PA
MP12775	MP12775	US	15/222,184	7/28/2016			Pending	ADMISSION CONTROL FOR MEMORY ACCESS REQUESTS
MP12776	MP12776	US	15/269,072	9/19/2016	10013357	7/3/2018	In Force	MANAGING MEMORY ACCESS REQUESTS WITH PREFETCH FOR STRE
MP12776	MP12776C1	US	16/011,173	6/18/2018			Pending	MANAGING MEMORY ACCESS REQUESTS WITH PREFETCH FOR STRE
MP12777	MP12777	US	15/272,332	9/21/2016	10210135	2/19/2019	In Force	Methods and Apparatus for Providing a Programmable Mixed-Radix
MP12777	MP127771	US	15/282,015	10/12/2016	10311018	6/4/2019	In Force	METHODS AND APPARATUS FOR A VECTOR MEMORY SUBSYSTEM FO
MP12777	MP1277711C1	US	16/272,470	2/11/2019			Pending	Method and Apparatus for A Vector Memory Subsystem for Use with
MP12777	MP127771111	US	15/347,663	11/9/2016	10349251	7/9/2019	In Force	Methods and Apparatus for Twiddle Factor Generation for Use with a
MP12777	MP127771111C1	US	16/506,820	7/9/2019			Pending	Methods and Apparatus for Twiddle Factor Generation for Use with a
MP12779	MP12779	US	15/335,816	10/27/2016	9668808	7/4/2017	In Force	Phase Measurement And Correction Circuitry
MP1278	MP1278	US	11/874,581	10/18/2007	7904644	3/8/2011	In Force	DISK CHANNEL SYSTEM WITH SECTOR REQUEST QUEUE
MP12780	MP12780	US	15/363,995	11/29/2016	10275261	4/30/2019	In Force	METHODS AND SYSTEMS FOR MESSAGE LOGGING AND RETRIEVAL IN
MP12781	MP12781	US	15/379,207	12/14/2016	10140250	11/27/2018	In Force	Methods and Apparatus for Providing an FFT Engine Using a Reconfig
MP12783	MP12783	US	15/398,617	1/4/2017			Pending	Methods and Apparatus for Configuring a Front End to Process Multi
MP12786	MP12786	US	15/496,294	4/25/2017	10158353	12/18/2018	In Force	Duty Cycle Correction Method
MP12787	MP12787	US	15/498,373	4/26/2017	10171207	1/1/2019	In Force	Methods and Apparatus for Control Bit Detection
MP12788	MP12788	US	15/497,356	4/26/2017	10263759	4/16/2019	In Force	Signal Presence Detection Circuit And Method
MP12788	MP12788C1	US	16/281,822	2/21/2019			Pending	Signal Presence Detection Circuit And Method
MP12788	MP12788DE	DE	102018206379.1	4/25/2018			Pending	Signal Presence Detection Circuit And Method
MP12789	MP12789	US	15/586,867	5/4/2017	10130381	3/12/2019	In Force	Frequency Divider
MP1279	MP1279C1	US	13/084,449	4/11/2011	8274749	9/25/2012	In Force	DATA WEDGE FORMAT TABLE SYNCHRONIZATION
MP12790	MP12790	US	15/588,015	5/5/2017	10459770	10/29/2019	In Force	METHOD AND APPARATUS FOR PORT ACCESS MANAGEMENT AT A DI
MP12791	MP12791	US	15/592,087	5/10/2017	10171278	1/1/2019	In Force	Methods and Apparatus for Frequency Offset Estimation
MP12791	MP12791C1	US	16/237,635	12/31/2018			Pending	Methods and Apparatus for Frequency Offset Estimation
MP12792	MP12792	US	15/594,497	6/2/2017			Pending	Methods And Apparatus For A Unified Baseband Architecture
MP12793	MP12793	US	15/595,667	5/15/2017	10423215	9/24/2019	In Force	Methods and Apparatus for Adaptive Power Profiling in a Baseband P

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MP12793	MP12793C1	US	16/546,153	8/20/2019			Pending	Methods and Apparatus for Adaptive Power Profiling in A baseband P
MP12796	MP12796	US	15/596,860	5/16/2017	10419481	9/17/2019	In Force	METHODS AND SYSTEMS FOR OVERLAPPING PROTECTION DOMAIN I
MP12797	MP12797	US	15/600,706	5/20/2017			Pending	METHOD AND APPARATUS FOR LOAD BALANCING OF JOBS SCHEDULE
MP12798	MP12798	US	15/602,774	5/23/2017	9966964	5/8/2018	In Force	Multi-Phase Divider
MP12799	MP12799	US	15/608,852	5/30/2017			Pending	FLOWLET SCHEDULER FOR MULTICORE NETWORK PROCESSORS
MP12800	MP12800	US	15/609,225	5/31/2017			Pending	MANAGING LOCK AND UNLOCK OPERATIONS USING OPERATION PRE
MP12801	MP12801	US	15/609,217	5/31/2017	10445096	10/15/2019	In Force	MANAGING LOCK AND UNLOCK OPERATIONS USING TRAFFIC PRIOR
MP12801	MP128011	US	15/697,736	9/7/2017	10331500	6/25/2019	In Force	MANAGING FAIRNESS FOR LOCK AND UNLOCK OPERATIONS USING
MP12802	MP12802	US	15/609,211	5/31/2017	10248420	4/2/2019	In Force	MANAGING LOCK AND UNLOCK OPERATIONS USING ACTIVE SPINNING
MP12805	MP12805	US	15/894,732	2/12/2018			Pending	TIMESTAMP-BASED PACKET SWITCHING USING A TRIE DATA STRUCT
MP12806	MP12806	US	15/631,085	6/23/2017	10282299	5/7/2019	In Force	MANAGING CACHE PARTITIONS BASED ON CACHE USAGE INFORMAT
MP12807	MP12807	US	15/670,841	8/7/2017	10020929	7/10/2018	In Force	METHODS AND SYSTEMS FOR DATA ALIGNMENT IN NETWORK DEVIC
MP12808	MP12808	US	15/683,376	8/22/2017			Pending	Method and Apparatus for Uplink Control Channel Detection
MP12809	MP12809	US	15/683,020	8/31/2017	10362438	7/23/2019	In Force	Method and Apparatus for Coordinated Multipoint Receiver Processi
MP1281	MP1281	US	11/893,810	8/17/2007	7876860	1/25/2011	In Force	ENHANCEMENTS OF SOFT DECODERS AND DETECTORS ON ASYMMETRIC
MP1281	MP1281C1	US	13/012,407	1/24/2011	8149959	4/3/2012	In Force	ENHANCEMENTS OF SOFT DECODERS AND DETECTORS ON ASYMMETRIC
MP12811	MP12811	US	15/721,236	9/29/2017	10222817	3/5/2019	In Force	Method And Circuit For Low Voltage Current-Mode Bandgap
MP12811	MP12811CN	CN					Pending	Method And Circuit For Low Voltage Current-Mode Bandgap
MP12811	MP12811TW	TW	107131736	9/10/2018			Pending	Method And Circuit For Low Voltage Current-Mode Bandgap
MP12812	MP12812	US	15/716,082	9/26/2017	10389481	8/20/2019	In Force	Methods and Apparatus for Calculating Transport Block (TB) Cyclic Re
MP12813	MP12813	US	15/719,171	9/28/2017	10448377	10/15/2019	In Force	Methods and Apparatus for Control Channel Detection in an Uplink S
MP12813	MP12813C1	US	16/571,164	9/15/2019			Pending	Methods and Apparatus for Control Channel Detection in an Uplink S
MP12814	MP12814	US	15/719,973	9/29/2017	10312920	6/4/2019	In Force	Baseline Wander Compensation
MP12815	MP12815	US	15/721,334	9/29/2017	10291386	5/14/2019	In Force	Serializer/Deserializer (SerDes) Lanes with Lane-by-Lane Data Rate Ind
MP12815	MP12815C1	US	16/352,180	3/13/2019	10461917	10/29/2019	In Force	Serializer/Deserializer (SerDes) Lanes with Lane-by-Lane Data Rate Ind
MP12815	MP12815C1C1	US	16/569,445	9/12/2019			Pending	Serializer/Deserializer (SerDes) Lanes with Lane-by-Lane Data Rate Ind
MP12815	MP12815WC	WO	PCT/US2018/05	9/10/2018			Pending	Serializer/Deserializer (SerDes) Lanes with Lane-by-Lane Data Rate Ind
MP12816	MP12816	US	15/789,420	10/20/2017			Pending	Multi-Termination Scheme Interface
MP12816	MP12816DE	DE	10 2018 217 921	10/19/2018			Pending	Multi-Termination Scheme Interface
MP12817	MP12817	US	15/796,645	10/27/2017			Pending	Self-Biased Current Trimmer With Digital Scaling Input
MP12818	MP12818	US	15/805,631	11/7/2017	10141949	11/27/2018	In Force	Modular Serializer And Deserializer
MP12818	MP12818DE	DE	10 2018 218 151	10/24/2018			Pending	Modular Serializer And Deserializer
MP12819	MP12819	US	15/825,942	11/29/2017			Pending	Method And Apparatus For Multi-Band Voltage-Controlled Oscillator
MP1282	MP1282	US	11/893,936	8/17/2007	8316287	11/20/2012	In Force	LOW-DENSITY PARITY CHECK CODES FOR HOLOGRAPHIC STORAGE

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MP1282	MP1282C1	US	13/475,848	5/18/2012	8489977	7/16/2013	In Force	LOW-DENSITY PARITY CHECK CODES FOR HOLOGRAPHIC STORAGE
MP12820	MP12820	US	16/165,577	10/19/2018	10396803	8/27/2019	In Force	Clock and Data Recovery of Sub-Rate Data
MP12822	MP12822	US	15/875,611	1/19/2018			Pending	ISSUING INSTRUCTIONS BASED ON RESOURCE CONFLICT CONSTRAINT
MP12823	MP12823	US	15/880,219	1/25/2018			Pending	MANAGING PREDICTOR SELECTION FOR BRANCH PREDICTION
MP12824	MP12824	US	15/880,188	1/25/2018			Pending	MANAGING BRANCH PREDICTION INFORMATION FOR DIFFERENT CO
MP12824	MP12824DE	DE	102019200599.1	1/17/2019			Pending	MANAGING BRANCH PREDICTION INFORMATION FOR DIFFERENT CO
MP12824	MP1282411	US	15/963,243	4/26/2018			Pending	MANAGING OBSCURED BRANCH PREDICTION INFORMATION
MP12826	MP12826	US	15/934,804	3/23/2018			Pending	EXTERNAL DOS BI-DIRECTIONAL LOOPBACK WITH USE OF FEED FOR
MP1283	MP1283	US	11/842,717	8/21/2007	7940849	5/10/2011	In Force	METHOD AND APPARATUS FOR ESTIMATION OF CARRIER FREQUENCY
MP12840	MP12840	US	16/009,826	6/15/2018			Pending	Combined Conditional Branch and Indirect Branch Target Predictor
MP12842	MP12842	US	16/209,739	12/4/2018			Pending	Complex I/O Value Prediction for Multiple Values with Physical or Vir
MP12843	MP12843	US	16/029,031	7/6/2018			Pending	Limiting Backpressure With Bad Actors
MP12844	MP12844	US	16/029,184	7/6/2018			Pending	Method To Limit Packet Fetching With Uncertain Packet Sizes To Con
MP12845	MP12845	US	16/191,838	11/15/2018			Pending	Transmitter Tuning Using Receiver Gradient
MP12845	MP12845KR	KR	10-2019-014593	11/14/2019			Pending	Transmitter Tuning Using Receiver Gradient
MP12846	MP12846	US	16/158,443	10/12/2018			Pending	Configuration Cache For The ARM SMMLUv3
MP12847	MP12847	US	16/696,789	11/26/2019			Pending	System and Method For Performing a Failure Assessment of an Integr
MP12847	MP12847PR	US	62/771,466	11/26/2018			Pending	System and Method For Performing a Failure Assessment of an Integr
MP12848	MP12848	US	16/709,367	12/10/2019			Pending	Traversing A Variable Delay Line In A Deterministic Number Of Clock
MP12848	MP12848PR	US	62/778,386	12/12/2018			Pending	Traversing A Variable Delay Line In A Deterministic Number Of Clock
MP12849	MP12849	US	16/528,311	7/31/2019			Pending	DLL-Based Clocking Architecture with Programmable Delay at Phase
MP12849	MP12849PR	US	62/786,777	12/31/2018			Pending	DLL-Based Clocking Architecture with Programmable Delay at Phase
MP1285	MP1285	US	11/893,546	8/16/2007	7864784	9/28/2010	In Force	CABLE TESTER
MP1285	MP1285C1	US	12/887,910	9/22/2010	8416699	4/9/2013	In Force	CABLE TESTER
MP12850	MP12850	US	16/209,709	12/4/2018			Pending	Glitch-Free PLL Multiplexer
MP12850	MP12850PR	US	62/770,293	11/21/2018			Pending	Glitch-Free PLL Multiplexer
MP12851	MP12851PR	US	62/778,375	12/12/2018			Pending	Droop Detection and Mitigation
MP12852	MP12852	US	16/528,381	7/31/2019			Pending	Clocking Architecture for DVSs with Low-Frequency DLL Locking
MP12852	MP12852PR	US	62/786,790	12/31/2018			Pending	Clocking Architecture for DVSs with Low-Frequency DLL Locking
MP12853	MP12853	US	16/039,922	7/19/2018	10418125	9/17/2019	In Force	WRITE AND READ COMMON LEVELING FOR 4-BIT WIDE DRAMS
MP12853	MP12853C1	US	16/536,183	8/8/2019			Pending	WRITE AND READ COMMON LEVELING FOR 4-BIT WIDE DRAMS
MP12854	MP12854	US	16/049,314	7/30/2018			Pending	Preventing Information Leakage In Out-Of-Order Machines Due To M
MP12854	MP12854CN	CN	201910691268.1	7/29/2019			Pending	Preventing Information Leakage In Out-Of-Order Machines Due To M
MP12855	MP12855	US	16/589,527	10/1/2019			Pending	MERGE EXECUTION UNIT FOR MICROINSTRUCTIONS

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MP12857	MP12857	US	16/264,458	1/31/2019			Pending	PAIR MERGE EXECUTION UNITS FOR MICROINSTRUCTIONS
MP12858	MP12858	US	16/054,627	8/3/2018			Pending	VOO-BASED NETWORK SWITCH ARCHITECTURE USING MULTI-STAGE
MP1286	MP1286	US	11/895,186	8/23/2007	8139472	3/20/2012	In Force	BIT SWAP ARCHITECTURE IN DIGITAL SUBSCRIBER LINE COMMUNICA
MP12862	MP12862	US	16/115,117	8/28/2018			Pending	COMPRESSING LIKE MAGNITUDE PARTIAL PRODUCTS IN MULTIPLY A
MP12863	MP12863	US	16/568,698	9/12/2019			Pending	METHOD OF IMPROVING L1 CACHE PERFORMANCE WITH LARGE PR
MP12864	MP12864	US	16/128,369	9/11/2018			Pending	METHODS AND SYSTEMS FOR DISTRIBUTING MEMORY REQUESTS
MP12866	MP12866	US	16/129,107	9/12/2018			Pending	Low Latency Interconnect Protocol for Coherent Multi-Chip Commu
MP12867	MP12867	US	16/189,119	11/13/2018			Pending	FLEXIBLE RESOURCE ASSIGNMENT TO PHYSICAL AND VIRTUAL FUNC
MP12868	MP12868	US	16/199,852	11/26/2018			Pending	PREFETCHING DATA TO REDUCE CACHE MISSES
MP12869	MP12869	US	16/199,936	11/26/2018			Pending	INCREASING THE LOOKAHEAD AMOUNT FOR PREFETCHING
MP12872	MP12872	US	16/142,698	9/26/2018			Pending	Secure Low-latency Chip-to-Chip Communication
MP12873	MP12873	US	16/142,923	9/26/2018			Pending	Secure In-line Received Network Packet Processing
MP12873	MP12873CN	CN	201910913261.1	9/25/2019			Pending	Secure In-line Received Network Packet Processing
MP12874	MP12874	US	16/142,871	9/26/2018			Pending	Secure In-line Network Packet Transmittal
MP12874	MP12874CN	CN	201910913894.1	9/25/2019			Pending	Secure In-line Network Packet Transmittal
MP12876	MP12876	US	15/419,942	1/30/2017	10318643	2/26/2019	In Force	APPARATUS AND METHOD FOR SCALABLE AND FLEXIBLE ACCESS CO
MP12877	MP12877	US	14/540,868	11/13/2014	9485179	11/1/2016	In Force	APPARATUS AND METHOD FOR SCALABLE AND FLEXIBLE TABLE SEAR
MP12877	MP12877TW	TW	104104344	2/10/2015			Pending	APPARATUS AND METHOD FOR SCALABLE AND FLEXIBLE TABLE SEAR
MP12878	MP12878	US	14/540,927	11/13/2014	9553829	1/24/2017	In Force	APPARATUS AND METHOD FOR FAST SEARCH TABLE UPDATE IN A NE
MP12878	MP12878TW	TW	104104960	2/10/2015	1632792	8/11/2018	In Force	APPARATUS AND METHOD FOR FAST SEARCH TABLE UPDATE IN A NE
MP12879	MP12879	US	14/617,644	2/9/2015	9571395	2/14/2017	In Force	RECONFIGURABLE INTERCONNECT ELEMENT WITH LOCAL LOOKUP T
MP12879	MP12879TW	TW	104110649	4/1/2015	1629887	7/11/2018	In Force	RECONFIGURABLE INTERCONNECT ELEMENT WITH LOCAL LOOKUP T
MP1288	MP1288	US	11/846,292	8/28/2007	7511649	3/31/2009	In Force	CIRCUIT FOR CONVERTING A VOLTAGE RANGE OF A LOGIC SIGNAL
MP12880	MP12880	US	14/628,058	2/20/2015	10903676	6/19/2018	In Force	METHOD AND APPARATUS FOR GENERATING PARALLEL LOOKUP RE
MP12880	MP12880TW	TW	104110834	4/2/2015	1665894	7/11/2019	In Force	METHOD AND APPARATUS FOR GENERATING PARALLEL LOOKUP RE
MP12881	MP12881	US	14/632,709	2/26/2015	10430472	10/1/2019	In Force	APPARATUS AND METHOD FOR COLLECTING RESPONSES TO A PLUR
MP12881	MP12881TW	TW	104110654	4/1/2015	1665988	7/11/2019	In Force	APPARATUS AND METHOD FOR COLLECTING RESPONSES TO A PLUR
MP12882	MP12882	US	14/615,246	3/31/2015	9553819	1/24/2017	In Force	SYSTEMS AND METHODS FOR TIMING ADJUSTMENT OF METADATA
MP12883	MP12883	US	14/657,921	3/13/2015	10901999	6/19/2018	In Force	SYSTEM AND METHOD FOR CONFIGURING A PLURALITY OF REGISTER
MP12883	MP12883TW	TW	104110651	4/1/2015	1656474	4/11/2019	In Force	SYSTEM AND METHOD FOR CONFIGURING A PLURALITY OF REGISTER
MP12884	MP12884	US	14/806,894	7/23/2015	9916274	3/13/2018	In Force	APPARATUS AND METHOD FOR ON-CHIP CROSSBAR DESIGN IN A NE
MP12885	MP12885	US	14/850,873	9/10/2015	9823960	11/21/2017	In Force	APPARATUS AND METHOD FOR PARALLEL CRC UNITS FOR VARIABLE-
MP12887	MP12887	US	15/067,987	2/19/2016	9870173	1/16/2018	In Force	APPARATUS AND METHOD FOR OPTIMIZED N-WRITE/1-READ PORT
MP12888	MP12888	US	15/140,394	4/27/2016	9948482	4/17/2018	In Force	APPARATUS AND METHOD FOR ENABLING FLEXIBLE KEY IN A NETWO

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MP1289	MP1289	US	11/840,557	8/17/2007	8255621	9/11/2012	In Force	WI-FI BASED GEO-LOCATION CONNECTIVITY
MP1289	MP1289C1C1	US	14/617,844	4/2/2015	9324105	4/26/2016	In Force	METHOD AND APPARATUS TO BUY AND SELL ITEMS VIA A LOCAL ARE
MP12891	MP12891	US	15/419,962	1/30/2017	10091137	10/2/2018	In Force	APPARATUS AND METHOD FOR SCALABLE AND FLEXIBLE WILDCARD
MP12892	MP12892	US	16/584,584	9/26/2019			Pending	System and Method for Isolating Work within a Virtualized Scheduler
MP12893	MP12893	US	16/553,688	8/28/2019			Pending	System and Method for Queuing Work within a Virtualized Scheduler
MP12897	MP12897	US	15/613,889	6/5/2017			Pending	PROGRAMMABLE HARDWARE SCHEDULER FOR DIGITAL PROCESSING
MP12898	MP12898	US	15/613,760	6/5/2017			Pending	METHOD AND APPARATUS FOR SCHEDULING ARBITRATION AMONG
MP1290	MP1290	US	11/848,795	8/31/2007	8213197	7/3/2012	In Force	SWITCHING CAPACITOR POWER SUPPLY
MP12900	MP12900	US	15/588,240	5/5/2017			Pending	METHOD AND APPARATUS FOR JOB PRE-SCHEDULING BY DISTRIBUT
MP12901	MP12901	US	15/602,620	5/23/2017			Pending	RE-ORDERING BUFFER FOR A DIGITAL MULTI-PROCESSOR SYSTEM WI
MP12907	MP12907	US	16/040,249	7/19/2018	10497413	12/3/2019	In force	WRITE AND READ COMMON LEVELING FOR 4-BIT WIDE DRAMS
MP12908	MP12908	US	14/279,712	5/16/2014	9501245	11/22/2016	In Force	SYSTEMS AND METHODS FOR NVME CONTROLLER VIRTUALIZATION
MP12908	MP12908TW	TW	104107519	3/10/2015	1625674	6/1/2018	In Force	SYSTEMS AND METHODS FOR NVME CONTROLLER VIRTUALIZATION
MP12915	MP12915	US	14/542,393	11/14/2014	10447608	10/15/2019	In Force	PACKET SCHEDULING USING HIERARCHICAL SCHEDULING PROCESS
MP1292	MP1292	US	11/894,417	8/20/2007	7486215	2/3/2009	In Force	APPARATUS, METHOD, AND CIRCUIT FOR BASELINE LOOP ANALOG C
MP1292	MP1292D1	US	12/339,062	12/18/2008	7755522	7/13/2010	In Force	APPARATUS, METHOD, AND CIRCUIT FOR BASELINE LOOP ANALOG C
MP12921	MP12921	US	13/184,199	7/15/2011	8843764	9/23/2014	In Force	Secure Software And Hardware Association Technique
MP12921	MP12921C1	US	14/340,225	7/24/2014	9602282	3/21/2017	In Force	Secure Software And Hardware Association Technique
MP12922	MP12922	US	16/544,203	8/19/2019			Pending	System and Method for Mining Digital Currency in a Blockchain Netw
MP12923	MP12923	US	16/447,865	6/20/2019			Pending	System and Method for Bitcoin Mining with Reduced Power
MP12926	MP12926	US	16/140,936	9/25/2018			Pending	MANAGING LOW-LEVEL INSTRUCTIONS AND CORE INTERACTIONS IN
MP12927	MP12927	US	16/171,881	10/26/2018			Pending	MANAGING PIPELINE INSTRUCTION INSERTION FOR RECEIVED EXTER
MP12928	MP12928	US	16/204,722	11/29/2018			Pending	MANAGING TRANSLATION LOOKASIDE BUFFER ENTRIES BASED ON A
MP12929	MP12929	US	16/234,813	12/28/2018			Pending	MANAGING TRACE INFORMATION STORAGE USING PIPELINE INSTRU
MP1293	MP1293	US	11/844,090	8/23/2007	8441752	5/14/2013	In Force	DIBIT PULSE EXTRACTION METHODS AND SYSTEMS
MP1294	MP1294D1	US	14/813,333	10/2/2015	9536562	1/3/2017	In Force	METHOD AND APPARATUS FOR IMITATING A DEFECT IN A READ-BAC
MP12943	MP12943	US	16/447,431	6/20/2019			Pending	System and Method for Instruction Mapping in an Out-of-Order Proc
MP12944	MP12944	US	16/525,519	7/29/2019			Pending	System and Method for Strong Load Ordering
MP12945	MP12945	US	16/425,209	5/29/2019			Pending	Shared Mid-Level Data Cache
MP12946	MP12946	US	16/425,435	5/29/2019			Pending	Partitioning in a Processor Cache
MP12947	MP12947	US	16/425,462	5/29/2019			Pending	Mid-Level Instruction Cache
MP12948	MP12948	US	16/427,678	5/31/2019			Pending	Managing Load and Store Instructions for Memory Barrier Handling
MP12948	MP12948DE	DE	1020192685664	6/12/2019			Pending	Managing Load and Store Instructions for Memory Barrier Handling
MP12949	MP12949	US	16/532,654	8/6/2019			Pending	MANAGING POTENTIAL FAULTS FOR SPECULATIVE PAGE TABLE ACCE

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MP12950	MP12950	US	16/447,470	6/20/2019			Pending	System and Method for Instruction Unwinding in an Out-of-Order Pr
MP12956	MP12956	US	11/889,111	9/4/2007	780854	9/23/2010	In Force	Calibration Circuit For Voltage Mode Biasing Of Magnetoresistive Hea
MP1297	MP1297	US	11/849,937	9/4/2007	8259691	9/4/2012	In Force	SYSTEM AND METHOD FOR IDENTIFYING AN ACCESS POINT
MP1297	MP1297C1	US	13/601,411	8/31/2012	8605699	12/10/2013	In Force	SYSTEM AND METHOD FOR IDENTIFYING AN ACCESS POINT
MP1297W	MP1297WO	WO	PCT/US2007/07	9/5/2007			Pending	SYSTEM AND METHOD TO COMMUNICATE TO A WIRELESS DEVICE
MP1298	MP1298	US	11/775,444	7/10/2007	7757070	7/13/2010	In Force	MULTITHREAD HANDLING METHODS, APPARATUSES, AND SYSTEM F
MP1298	MP1298C1	US	12/831,984	7/7/2010	8082427	12/20/2011	In Force	MULTITHREAD HANDLING
MP1298	MP1298C1aa	US	13/932,889	7/1/2013	8850169	9/30/2014	In Force	DISABLING THREADS IN MULTITHREAD ENVIRONMENT
MP1298	MP1298C1C1	US	13/330,305	12/19/2011	8478971	7/2/2013	In Force	MULTITHREAD HANDLING
MP1299	MP1299	US	11/851,237	9/6/2007	7751495	7/6/2010	In Force	EQUAL POWER OUTPUT SPATIAL SPREADING MATRIX FOR USE IN A
MP1299	MP1299AAAA	US	14/320,098	6/30/2014	9143219	9/22/2015	In Force	EQUAL POWER OUTPUT SPATIAL SPREADING MATRIX FOR USE IN A
MP1299	MP1299C1	US	12/788,081	5/26/2010	8238466	8/7/2012	In Force	EQUAL POWER OUTPUT SPATIAL SPREADING MATRIX FOR USE IN A
MP1299	MP1299C1AA	US	13/932,862	7/1/2013	8767863	7/1/2014	In Force	EQUAL POWER OUTPUT SPATIAL SPREADING MATRIX FOR USE IN A
MP1299	MP1299C1C1	US	13/567,763	8/6/2012	8477869	7/2/2013	In Force	EQUAL POWER OUTPUT SPATIAL SPREADING MATRIX FOR USE IN A
MP1300	MP1300	US	11/775,661	7/10/2007	7904703	3/8/2011	In Force	METHOD AND APPARATUS FOR IDLING AND WAKING THREADS BY A
MP1301	MP1301	US	12/100,144	4/9/2008	8261049	9/4/2012	In Force	DETERMINATIVE BRANCH PREDICTION INDEXING
MP13022	MP13022PR	US	62/854,802	5/30/2019			Pending	SPATIAL CONFIGURATION SUBFIELD ENCODING FORMAT (HE-SIG-B P
MP13023	MP13023PR	US	62/884,616	8/8/2019			Pending	Test Bed Optimization using Ethernet Relay
MP13027	MP13027PR	US	62/823,507	3/25/2019			Pending	NEXT GENERATION X-V2X FRAME FORMAT
MP1304	MP1304	US	11/849,671	9/4/2007	7839593	11/23/2010	In Force	MAGNETO-RESISTIVE BIASING METHODS AND SYSTEMS
MP1305	MP1305	US	11/899,477	9/6/2007	7840753	11/23/2010	In Force	Hard Disk Drive As Hybrid Drive
MP1306	MP1306	US	11/851,115	9/6/2007	7813436	10/12/2010	In Force	FRAME SYNCHRONIZATION METHOD AND APPARATUS
MP1306	MP1306C1	US	12/899,928	10/7/2010	7995666	8/9/2011	In Force	FRAME SYNCHRONIZATION METHOD AND APPARATUS
MP1306	MP1306C1AA	US	13/942,255	7/15/2013	8811462	8/19/2014	In Force	FRAME SYNCHRONIZATION METHOD AND APPARATUS
MP1306	MP1306C1C1	US	13/194,093	7/29/2011	8488698	7/16/2013	In Force	FRAME SYNCHRONIZATION METHOD AND APPARATUS
MP13074	MP13074PR	US	62/902,047	9/18/2019			Pending	Priority Based Adaptive Frequency Hopping (AFH) Map Selection
MP13084	MP13084PR	US	62/876,284	7/19/2019			Pending	Low Complexity Method of sub-stream power allocation
MP1309	MP1309	US	11/835,936	8/8/2007	8164845	4/24/2012	In Force	METHOD AND APPARATUS FOR ASYMMETRY CORRECTION IN MAGN
MP1309	MP1309C1	US	13/453,584	4/23/2012	8456774	6/4/2013	In Force	Compensating Asymmetries Of Signals Using Piece-Wise Linear Appro
MP13098	MP13098PR	US	62/866,487	6/25/2019			Pending	MULTI-BAND OPERATION: BASIC SERVICE SET (BSS) AND STA ASSOCI
MP13098	MP13098PR2	US	62/892,447	8/27/2019			Pending	MULTI-BAND OPERATION: BASIC SERVICE SET (BSS) AND STA ASSOCI
MP1310	MP1310	US	11/590,936	11/1/2006	7747293	6/29/2010	In Force	DISPLAY CONTROL FOR CELLULAR PHONE
MP1310	MP1310C1	US	11/653,546	1/16/2007	7797024	9/14/2010	In Force	DISPLAY CONTROL FOR CELLULAR PHONE
MP1310	MP1310C1WO	WO	PCT/US2007/02	10/16/2007			Pending	DISPLAY CONTROL FOR CELLULAR PHONE

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MP1310	MP1310C3	US	11/653,549	1/16/2007	8204553	6/19/2012	In Force	DISPLAY CONTROL FOR CELLULAR PHONE
MP1310	MP1310C3TW	TW	96138524	10/15/2007	1435581	4/21/2014	In Force	DISPLAY CONTROL FOR CELLULAR PHONE
MP1310	MP1310IN	IN	2076C/ENP2007	10/16/2007			Pending	Display Control For Cellular Phone
MP1310T	MP1310TW	TW	96138505	10/12/2007	1420882	12/21/2013	In Force	DISPLAY CONTROL FOR CELLULAR PHONE
MP1310W	MP1310WO	WO	PCT/US2007/02	10/16/2007			Pending	Display Control For Cellular Phone
MP1310W	MP1310WCN	CN	2007800387489	10/16/2007			Pending	Display Control For Cellular Phone
MP1310W	MP1310WCEP	EP	EP07839605.8	10/16/2007	EP2082562	4/10/2013	In Force	Display Control for Cellular Phone
MP1310W	MP1310WUDE	DE	07839605.8	10/16/2007	602007029735	4/10/2013	In Force	DISPLAY CONTROL FOR CELLULAR PHONE
MP1310W	MP1310WUFR	FR	07839605.8	10/16/2007	2082562	4/10/2013	In Force	DISPLAY CONTROL FOR CELLULAR PHONE
MP1310W	MP1310WUGB	GB	07839605.8	10/16/2007	2082562	4/10/2013	In Force	DISPLAY CONTROL FOR CELLULAR PHONE
MP1311	MP1311	US	11/853,421	9/11/2007	8126098	2/28/2012	In Force	MULTI-RAKE RECEIVER
MP1311	MP1311aas	US	14/217,910	3/18/2014	8976917	3/10/2015	In Force	METHOD AND APPARATUS FOR FILTERING AND COMBINING MULTIP
MP1311	MP1311aasa	US	14/643,475	3/10/2015	9143191	9/22/2015	In Force	METHOD AND APPARATUS FOR FILTERING AND COMBINING MULTIP
MP1311	MP1311C1	US	13/406,105	2/27/2012	8477893	7/2/2013	In Force	MULTI-RAKE RECEIVER
MP1311	MP1311C1C1	US	13/932,536	7/1/2013	8675795	3/18/2014	In Force	APPARATUSES FOR ADJUSTING A BANDWIDTH AND COEFFICIENT VA
MP1311	MP1311CN	CN	2007800420051	9/12/2007	212007800420	12/12/2012	In Force	MULTI-RAKE RECEIVER
MP13112	MP13112PR	US	62/902,092	9/18/2019			Pending	Method of Carrier Frequency Offset (CFO) Estimation Using Pilot Ton
MP1311W	MP1311W/D1	JP	2013-178047	8/29/2013	5681998	1/23/2015	In Force	MULTI-RAKE RECEIVER
MP1311W	MP1311WO	WO	PCT/US2007/01	9/12/2007			Pending	Multi-Rake Receiver
MP1311W	MP1311WCEP	EP	07838060.7	9/12/2007	EP2060011	4/25/2012	In Force	Multi-Rake Receiver
MP1311W	MP1311WCEPDE	DE	60 2007 022 314	9/12/2007	602007022314	4/25/2012	In Force	Multi-Rake Receiver
MP1311W	MP1311WCEPFR	FR	07838060.7	9/12/2007	2060011	4/25/2012	In Force	Multi-Rake Receiver
MP1311W	MP1311WCEPGB	GB	07838060.7	9/12/2007	2060011	4/25/2012	In Force	Multi-Rake Receiver
MP1311W	MP1311WOJP	JP	2009-527462	9/12/2007	5357031	9/6/2013	In Force	MULTI-RAKE RECEIVER
MP1312	MP1312	US	11/900,972	9/14/2007	8068319	11/29/2011	In Force	CIRCUITS, SYSTEMS, ALGORITHMS AND METHODS FOR ESD PROTECT
MP1313	MP1313	US	11/866,167	10/2/2007	8364881	1/29/2013	In Force	FLASH MEMORY CONTROLLER AND METHODS OF PROGRAMMING A
MP13130	MP13130PR	US	62/893,073	8/28/2019			Pending	MULTI-BAND OPERATION: AGGREGATE MAC PROTOCOL DATA UNIT (
MP13132	MP13132PR	US	62/897,120	9/6/2019			Pending	EXTREME HIGH THROUGHPUT (EHT) MULTI-ACCESS POINT SYNC FRA
MP13133	MP13133PR	US	62/897,130	9/6/2019			Pending	NEXT-GENERATION VEHICULAR (NGV) MIDAMBLE FORMAT
MP1313J	MP1313JP	JP	2009-530970	10/2/2007			Pending	FLASH MEMORY CONTROL INTERFACE
MP1313J	MP1313JPD1	JP	2012-26596	12/4/2012	5879651	2/12/2016	In Force	FLASH MEMORY CONTROL INTERFACE
MP1313K	MP1313KR	KR	1020097006661	10/2/2007	101373793	3/6/2014	In Force	Flash Memory Control Interface
MP1313T	MP1313TWA4	TW	103119673	10/4/2007	1525628	3/11/2016	In Force	Flash Memory Control Interface
MP1313W	MP1313WO	WO	PCT/IB2007/004	10/2/2007			Pending	Flash Memory Control Interface

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MP1316	MP1316	US	11/900,728	9/13/2007	7756015	7/13/2010	In Force	FAST FAILOVER RECOVERY FOR STACKABLE NETWORK SWITCHES
MP1316	MP1316C1	US	12/830,649	7/6/2010	8305878	11/6/2012	In Force	FAST FAILOVER RECOVERY FOR STACKABLE NETWORK SWITCHES
MP1316	MP1316C1aa	US	14/223,070	3/24/2014	8908499	12/9/2014	In Force	SYSTEM AND METHOD FOR HANDLING PACKETS IN A STACKABLE NE
MP1316	MP1316C1C1	US	13/669,756	11/6/2012	8661603	3/25/2014	In Force	FAST FAILOVER RECOVERY FOR STACKABLE NETWORK SWITCHES
MP1319	MP1319	US	11/855,082	9/13/2007	8315193	11/20/2012	In Force	AD-HOC NETWORK POWER SAVE SYSTEM AND METHOD
MP1319	MP1319C1	US	13/619,595	9/14/2012	8830891	9/9/2014	In Force	AD-HOC NETWORK POWER SAVE SYSTEM AND METHOD
MP1319	MP1319CND1	CN	201210162268.1	9/13/2007	Z1.2012101622	12/16/2015	In Force	AD-HOC NETWORK POWER SAVE SYSTEM AND METHOD
MP1319E	MP1319EP	EP	EP07638142.3	9/13/2007	EP2069953	1/25/2017	In Force	AD-HOC Network Power Save System and Method
MP1319E	MP1319EPDE	DE	07838142.3	9/13/2007	602007049676	1/25/2017	In Force	AD-HOC NETWORK POWER SAVE SYSTEM AND METHOD
MP1319E	MP1319EPFR	FR	07838142.3	9/13/2007	2069953	1/25/2017	In Force	AD-HOC NETWORK POWER SAVE SYSTEM AND METHOD
MP1319E	MP1319EPGB	GB	07838142.3	9/13/2007	2069953	1/25/2017	In Force	AD-HOC NETWORK POWER SAVE SYSTEM AND METHOD
MP1319J	MP1319JP	JP	2009-528282	9/13/2007	5090455	9/21/2012	In Force	AD-HOC NETWORK POWER SAVE SYSTEM AND METHOD
MP1319K	MP1319KR	KR	7007680-2009	9/13/2007	10-1499118	2/27/2015	In Force	AD-HOC NETWORK POWER SAVE SYSTEM AND METHOD
MP1319T	MP1319TW	TW	96134384	9/14/2007	1426740	2/11/2014	In Force	AD-HOC NETWORK POWER SAVE SYSTEM AND METHOD
MP1319W	MP1319WOCN	CN	2007800424373	9/13/2007	Z1.2007800424	7/4/2012	In Force	AD-HOC NETWORK POWER SAVE SYSTEM AND METHOD
MP1320	MP1320C1aa	US	15/050,179	2/22/2016	9851400	12/26/2017	In Force	SYSTEM ON A CHIP SERIAL COMMUNICATION INTERFACE METHOD A
MP1320	MP1320C1C1	US	14/293,475	6/2/2014	9268661	2/23/2016	In Force	SYSTEM ON A CHIP SERIAL COMMUNICATION INTERFACE METHOD A
MP1321	MP1321	US	11/903,051	9/20/2007	7872372	1/18/2011	In Force	POWER CIRCUIT
MP1321	MP1321D1	US	13/008,682	1/18/2011	8188784	5/29/2012	In Force	POWER CIRCUIT
MP1326	MP1326	US	11/857,231	9/18/2007	7916663	3/29/2011	In Force	ESTABLISHMENT OF AD-HOC NETWORKS BETWEEN MULTIPLE DEVIC
MP1326	MP1326C1	US	13/052,291	3/21/2011	8547873	10/1/2013	In Force	ESTABLISHMENT OF AD-HOC NETWORKS BETWEEN MULTIPLE DEVIC
MP1326	MP1326C1C1	US	14/042,002	9/30/2013	9025493	5/5/2015	In Force	ESTABLISHMENT OF AD-HOC NETWORKS BETWEEN MULTIPLE DEVIC
MP1326	MP1326CN	CN	200780042744.1	9/18/2007	Z1.2007800427	1/4/2012	In Force	ESTABLISHMENT OF AD-HOC NETWORKS BETWEEN MULTIPLE DEVIC
MP1326E	MP1326EP	EP	EP07642688.9	9/18/2007	2064829	4/25/2018	In Force	ESTABLISHMENT OF AD-HOC NETWORKS BETWEEN MULTIPLE DEVICE
MP1326E	MP1326EPDE	DE	EP07642688.9	9/18/2007	602007054693	4/25/2018	In Force	ESTABLISHMENT OF AD-HOC NETWORKS BETWEEN MULTIPLE DEVICE
MP1326E	MP1326EPFR	FR	EP07642688.9	9/18/2007	2064829	4/25/2018	In Force	ESTABLISHMENT OF AD-HOC NETWORKS BETWEEN MULTIPLE DEVICE
MP1326E	MP1326EPGB	GB	EP07642688.9	9/18/2007	2064829	4/25/2018	In Force	ESTABLISHMENT OF AD-HOC NETWORKS BETWEEN MULTIPLE DEVICE
MP1326J	MP1326JP	JP	2009-528528	9/18/2007	JP5298391	6/28/2013	In Force	ESTABLISHMENT OF AD-HOC NETWORKS BETWEEN MULTIPLE DEVIC
MP1326K	MP1326KR	KR	KR10-2009-7006	9/18/2007	KR10-1434613	8/20/2014	In Force	ESTABLISHMENT OF AD-HOC NETWORKS BETWEEN MULTIPLE DEVICE
MP1326W	MP1326WO	WO	PCT/US2007/07	9/18/2007			Pending	ESTABLISHMENT OF AD-HOC NETWORKS BETWEEN MULTIPLE DEVIC
MP1327	MP1327	US	11/857,297	9/18/2007	7729439	6/1/2010	In Force	CALIBRATION CORRECTION FOR IMPLICIT BEAMFORMING IN A WIRE
MP1327	MP1327C1	US	12/785,234	5/21/2010	7978781	7/12/2011	In Force	CALIBRATION CORRECTION FOR IMPLICIT BEAMFORMING IN A WIRE
MP1327	MP1327C1AA	US	13/784,393	3/4/2013	8855228	10/7/2014	In Force	CALIBRATION CORRECTION FOR IMPLICIT BEAMFORMING IN A WIRE
MP1327	MP1327C1C1	US	13/178,387	7/7/2011	8391397	3/5/2013	In Force	CALIBRATION CORRECTION FOR IMPLICIT BEAMFORMING IN A WIRE

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MP1327J	MP1327JP	JP	2009-528529	9/18/2007	5133346	11/16/2012	In Force	CALIBRATION CORRECTION FOR IMPLICIT BEAMFORMING IN A WIRE
MP1327T	MP1327TW	TW	96134974	9/19/2007	1443985	7/1/2014	In Force	CALIBRATION CORRECTION FOR IMPLICIT BEAMFORMING IN A WIRE
MP1327W	MP1327WO	WO	PCT/US2007/07	9/18/2007			Pending	CALIBRATION CORRECTION FOR IMPLICIT BEAMFORMING IN A WIRE
MP1327W	MP1327WOCN	CN	2007800345373	9/18/2007	ZL2007800345	12/12/2012	In Force	CALIBRATION CORRECTION FOR IMPLICIT BEAMFORMING IN A WIRE
MP1327W	MP1327WCEP	EP	07842701.0	9/18/2007	2064818	7/26/2017	In Force	Calibration Correction for Implicit Beamforming in a Wireless MIMO
MP1327W	MP1327WCEPDE	DE	07842701.0	9/18/2007	602007051786	7/26/2017	In Force	CALIBRATION CORRECTION FOR IMPLICIT BEAMFORMING IN A WIRE
MP1327W	MP1327WCEPFR	FR	07842701.0	9/18/2007	2064818	7/26/2017	In Force	CALIBRATION CORRECTION FOR IMPLICIT BEAMFORMING IN A WIRE
MP1327W	MP1327WCEPGB	GB	07842701.0	9/18/2007	2064818	7/26/2017	In Force	CALIBRATION CORRECTION FOR IMPLICIT BEAMFORMING IN A WIRE
MP1327W	MP1327WGPJP	JP	2009-528529	9/18/2007	5133346	11/16/2012	In Force	CALIBRATION CORRECTION FOR IMPLICIT BEAMFORMING IN A WIRE
MP1328	MP1328	US	11/901,435	9/17/2007	8630255	1/14/2014	In Force	WIRELESS ADAPTER WITH AUXILIARY RADIO CHANNEL FOR ADVANC
MP1328	MP1328C1	US	14/151,490	1/9/2014	9357457	5/31/2016	In Force	METHOD AND APPARATUS FOR SCANNING OF CHANNELS SIMULTAN
MP1329	MP1329	US	11/857,717	9/19/2007	7793180	9/7/2010	In Force	SCAN ARCHITECTURE FOR FULL CUSTOM BLOCKS
MP1329	MP1329C1	US	13/093,114	4/25/2011	8151153	4/3/2012	In Force	SCAN ARCHITECTURE FOR FULL CUSTOM BLOCKS
MP1329	MP1329C1C1	US	13/416,712	3/9/2012	8281195	10/2/2012	In Force	SCAN ARCHITECTURE FOR FULL CUSTOM BLOCKS
MP1329	MP1329D1	US	12/849,385	8/3/2010	7954025	5/31/2011	In Force	SCAN ARCHITECTURE FOR FULL CUSTOM BLOCKS
MP1330	MP1330	US	11/848,078	8/30/2007	7816803	10/19/2010	In Force	POWER LINE CONTROL SYSTEM
MP1332	MP1332	US	11/866,754	10/3/2007	8050200	11/1/2011	In Force	Opportunistic 40 MHz Mode Of Transmission In Wireless Transmitter
MP1332	MP1332C1	US	13/279,520	10/24/2011	8441967	5/14/2013	In Force	Opportunistic 40 MHz Mode Of Transmission In Wireless Transmitter
MP1332	MP1332C1C1	US	13/891,784	5/10/2013	9014207	4/21/2015	In Force	Opportunistic 40 MHz Mode Of Transmission In Wireless Transmitter
MP1332T	MP1332TW	TW	96137323	10/4/2007	1513220	12/11/2015	In Force	OPPORTUNISTIC 40 MHZ MODE OF TRANSMISSION IN WIRELESS TRA
MP1332W	MP1332WO	WO	PCT/US2007/02	10/4/2007			Pending	OPPORTUNISTIC 40MHZ MODE OF TRANSMISSION IN WIRELESS TRA
MP1339	MP1339CN	CN	2007101541573	9/19/2007	ZL2007101541	6/26/2013	In Force	TRAINING METHOD FOR A SELF-ADAPTED EQUALIZER IN A RECEIVER
MP1340	MP1340CN	CN	2007101541624	9/19/2007	ZL2007101541	3/13/2013	In Force	BASEBAND DEMODULATING SYSTEM AND METHOD WITH DIFFEREN
MP1341	MP1341CN	CN	2007101541611	9/19/2007	ZL2007101541	7/3/2013	In Force	APPROXIMATE PHASE ANGLE CALCULATOR AND APPROXIMATE PHA
MP1342	MP1342CN	CN	2007101541631	9/19/2007	ZL2007101541	11/13/2013	In Force	BIT ERROR RATE ESTIMATION METHOD IN A RECEIVER OF A WIRELES
MP1343	MP1343CN	CN	2007101541584	9/19/2007	ZL2007101541	1/9/2013	In Force	A COMMUNICATION SYSTEM WITH CARRIER OFFSET ESTIMATION FE
MP1344	MP1344CN	CN	2007101541641	9/19/2007	ZL2007101541	8/14/2013	In Force	A MOBILE COMMUNICATION DEVICE AND A METHOD FOR BURST DE
MP1345	MP1345CN	CN	2007101541601	9/19/2007	ZL2007101541	3/26/2014	In Force	APPARATUS AND METHOD FOR CORRECTING ERRORS USING CYCLIC
MP1346	MP1346CN	CN	2007101541591	9/19/2007	ZL2007101541	9/5/2012	In Force	APPARATUS AND METHOD FOR ADJUSTING FREQUENCY OFFSET
MP1347	MP1347CN	CN	2007101541651	9/19/2007	ZL2007101541	6/19/2013	In Force	A MOBILE COMMUNICATION DEVICE AND A METHOD FOR REDUCIN
MP1348	MP1348	US	11/859,434	9/21/2007	7471113	12/30/2008	In Force	LOW CROWBAR CURRENT SLEW RATE CONTROLLED DRIVER
MP1351	MP1351	US	11/862,729	9/27/2007	8312345	11/13/2012	In Force	FORWARD ERROR CORRECTING CODE ENCODER APPARATUS.
MP1351	MP1351D1C1	US	14/140,125	12/24/2013	9432057	8/30/2016	In Force	FORWARD ERROR CORRECTING CODE ENCODER AND DECODER MET
MP1352	MP1352	US	11/906,261	10/1/2007	7589561	9/15/2009	In Force	Tolerant CMOS Receiver

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MP1353	MP1353	US	11/864,003	9/28/2007	7981688	2/1/2011	In Force	METHOD AND APPARATUS FOR CONTROLLING A LOCAL OSCILLATOR
MP1355	MP1355	US	11/904,117	9/26/2007	7646559	1/12/2010	In Force	Self-Servo Write Switching System
MP1359	MP1359	US	11/867,356	10/4/2007	8032812	10/4/2011	In Force	Error Correction Decoding Methods And Apparatus
MP1359W	MP1359WO	WO	PCT/US2007/02	10/4/2007		4/20/2010	Pending	Error Correction Decoding Methods And Apparatus
MP1360	MP1360	US	11/866,673	10/3/2007	7700475	4/20/2010	In Force	PILLAR STRUCTURE ON BUMP PAD
MP1361	MP1361	US	11/865,627	10/1/2007	7805664	9/28/2010	In Force	LIKELIHOOD METRIC GENERATION FOR TRELLIS-BASED DETECTION A
MP1362	MP1362	US	11/870,158	10/10/2007	9137341	9/15/2015	In Force	APPARATUS AND METHOD FOR COMMUNICATING TRANSMIT PARA
MP1364	MP1364	US	11/973,856	10/10/2007	8155308	4/10/2012	In Force	Advanced Encryption System Hardware Architecture
MP1364	MP1364C1	US	13/442,202	4/9/2012	8750498	6/10/2014	In Force	Method And Apparatus For Encoding Data In Accordance With The A
MP1364	MP1364C1C1	US	14/289,735	6/9/2014	9350534	5/24/2016	In Force	Method And Apparatus For Pipelined Byte Substitution In Encryption
MP1366	MP1366	US	11/871,110	10/11/2007	7622880	11/24/2009	In Force	VOICE-COIL MOTOR CONTROL WITH ZERO-CURRENT SENSING
MP1367	MP1367	US	11/973,150	10/5/2007	7961797	6/14/2011	In Force	NONLINEAR VITERBI COMPLEXITY REDUCTION
MP1368	MP1368	US	11/867,858	10/5/2007	8171380	5/1/2012	In Force	ADAPTIVE SYSTEMS AND METHODS FOR STORING AND RETRIEVING
MP1368	MP1368C1	US	13/459,013	4/27/2012	8347187	1/1/2013	In Force	ADAPTIVE SYSTEMS AND METHODS FOR STORING AND RETRIEVING
MP1368	MP1368C1C1	US	13/727,914	12/27/2012	8578248	11/5/2013	In Force	ADAPTIVE SYSTEMS AND METHODS FOR STORING AND RETRIEVING
MP1368T	MP1368TW	TW	96138035	10/11/2007	1381273	1/1/2013	In Force	ADAPTIVE SYSTEMS AND METHODS FOR STORING AND RETRIEVING
MP1368W	MP1368WO	WO	PCT/US2007/06	10/9/2007			Pending	ADAPTIVE SYSTEMS AND METHODS FOR STORING AND RETRIEVING
MP1368W	MP1368WOKR	KR	7009287/2009	10/9/2007	10-1094421	12/8/2011	In Force	ADAPTIVE SYSTEMS AND METHODS FOR STORING AND RETRIEVING
MP1369	MP1369	US	11/868,649	10/8/2007	7974251	7/5/2011	In Force	Wireless Networks For Vehicles
MP1369	MP1369C1	US	13/175,586	7/1/2011	8605696	12/10/2013	In Force	Wireless Networks For Vehicles
MP1369	MP1369C1C1	US	14/101,627	12/10/2013	9119014	8/25/2015	In Force	Method And Apparatus For Supporting Wireless Communication In A
MP1369W	MP1369WCEP	EP	78392982	10/8/2007			Pending	Wireless Network In A Vehicle
MP1369W	MP1369WFOCA	WO	PCT/US2007/02	10/8/2007			Pending	Wireless Networks For Vehicles
MP1370	MP1370	US	11/868,950	10/8/2007	8167486	1/31/2012	In Force	Flexible Queue Controller Reserve List
MP1371	MP1371	US	11/868,949	10/8/2007	8050263	11/1/2011	In Force	Device And Process For Efficient Multicasting
MP1371	MP1371C1	US	13/285,811	10/31/2011	9050008	6/9/2015	In Force	Device And Process For Efficient Multicasting
MP1372	MP1372	US	11/974,141	10/11/2007	7774688	8/10/2010	In Force	Hardware Efficient Decoding System For Bose, Ray-Chaudhuri, Hocqu
MP1374	MP1374	US	11/871,777	10/12/2007	8139772	3/20/2012	In Force	SYSTEM AND METHOD FOR ESTIMATING CHANNEL CONDITION BASE
MP1374	MP1374C1	US	13/419,766	3/14/2012	8948402	2/3/2015	In Force	SYSTEM AND METHOD FOR ESTIMATING CHANNEL CONDITION BASE
MP1375	MP1375	US	11/871,755	10/12/2007	8083841	1/10/2012	In Force	SYSTEM AND METHOD FOR FASTER RESPONSE TIME IN STEREO/MO
MP1375	MP1375C1C1	US	14/530,036	10/31/2014	9161145	10/13/2015	In Force	SYSTEM AND METHOD FOR FASTER RESPONSE TIME IN STEREO/MO
MP1376	MP1376	US	11/871,751	10/12/2007	8139771	3/20/2012	In Force	SYSTEM AND METHOD FOR SWITCHING A MODE OF A RECEIVER USI
MP1376	MP1376C1	US	13/419,730	3/14/2012	8953809	2/10/2015	In Force	SYSTEM AND METHOD FOR SWITCHING A MODE OF A RECEIVER USI
MP1377	MP1377	US	11/871,773	10/12/2007	8295495	10/23/2012	In Force	SYSTEM AND METHOD FOR DETECTING RATED MAXIMUM SYSTEM D

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MP1377	MP1377C1	US	13/618,082	9/14/2012	9356638	5/31/2016	In Force	SYSTEM AND METHOD FOR DETECTING RATED MAXIMUM SYSTEM D
MP1378	MP1378	US	11/872,673	10/15/2007	8572302	10/29/2013	In Force	CONTROLLER FOR STORAGE DEVICE WITH IMPROVED BURST EFFICIE
MP1379	MP1379	US	11/870,833	10/11/2007	7787324	8/31/2010	In Force	Processor Instruction Cache With Dual-Read Modes
MP1379	MP1379C1	US	12/888,341	8/25/2010	8089823	1/3/2012	In Force	Processor Instruction Cache With Dual-Read Modes
MP1379	MP1379I1	US	12/061,296	4/12/2008	8027218	9/27/2011	In Force	Processor Instruction Cache With Dual-Read Modes
MP1379	MP1379IAA	US	13/657,502	10/22/2012	8526257	9/3/2013	In Force	PROCESSOR WITH MEMORY DELAYED BIT LINE PRECHARGING
MP1379	MP1379I1C1	US	13/245,551	9/26/2011	8255110	10/23/2012	In Force	Processor Instruction Cache With Dual-Read Modes
MP1379I	MP1379I1TW	TW	97113712	4/15/2008	1456586	10/11/2014	In Force	Processor Instruction Cache With Dual-Read Modes
MP1379I	MP1379I1WO	WO	PCT/US2008/00	4/4/2008			Pending	Processor Instruction Cache With Dual-Read Modes
MP1379I	MP1379ITW	TW	96138542	10/15/2007	1446359	7/21/2014	In Force	Processor Instruction Cache With Dual-Read Modes
MP1379W	MP1379WO	WO	PCT/US2007/02	10/12/2007			Pending	Processor Instruction Cache With Dual-Read Modes
MP1380	MP1380	US	11/907,676	10/16/2007	8054717	11/8/2011	In Force	DEFECT DETECTION DESIGN
MP1380	MP1380C1	US	13/255,658	9/19/2011	8315132	11/20/2012	In Force	DEFECT DETECTION DESIGN
MP1380	MP1380C1C1	US	13/675,631	11/13/2012	8634286	1/21/2014	In Force	DEFECT DETECTION DESIGN
MP1383	MP1383CN	CN	200780038650	10/15/2007			Pending	NETWORK CONNECTION FAST RECOVERY
MP1383I	MP1383TW	TW	96138831	10/16/2007	1424711	1/21/2014	In Force	NETWORK CONNECTION FAST RECOVERY
MP1383W	MP1383WO	WO	PCT/US2007/06	10/15/2007			Pending	NETWORK CONNECTION FAST RECOVERY
MP1383W	MP1383WOCN	CN	2007800386481	10/15/2007			Pending	NETWORK CONNECTION FAST RECOVERY
MP1383W	MP1383WOEP	EP	7854062.2	10/15/2007			Pending	NETWORK CONNECTION FAST RECOVERY
MP1383W	MP1383WOJP	JP	2009-534625	10/15/2007	534794	10/4/2013	In Force	NETWORK CONNECTION FAST RECOVERY
MP1384	MP1384C1	US	12/960,804	12/6/2010	8164972	4/24/2012	In Force	Address Decoder
MP1385	MP1385	US	11/873,797	10/17/2007	7557739	7/7/2009	In Force	Four-To-Six Modulation Encoder
MP1386	MP1386	US	11/871,770	10/12/2007	8271863	9/18/2012	In Force	FORWARD DECISION AIDED NONLINEAR VITERBI DETECTOR
MP1386W	MP1386WO	WO	PCT/US2007/02	10/18/2007			Pending	FORWARD DECISION AIDED NONLINEAR VITERBI DETECTOR
MP1388	MP1388	US	11/947,581	11/29/2007	7958433	6/7/2011	In Force	METHODS AND SYSTEMS FOR STORING DATA IN MEMORY USING ZO
MP1388	MP1388C1	US	13/151,000	6/1/2011	8341503	12/25/2012	In Force	METHODS AND SYSTEMS FOR STORING DATA IN MEMORY USING ZO
MP1388	MP1388C1C1	US	13/722,710	12/20/2012	8799724	8/5/2014	In Force	METHODS AND SYSTEMS FOR STORING DATA IN MEMORY USING ZO
MP1389	MP1389	US	12/022,746	1/30/2008	8623492	4/23/2013	In Force	ERROR CORRECTING CODE PREDICATION SYSTEM AND METHOD
MP1389I	MP1389ITW	TW	97104492	2/5/2008	1476776	3/11/2015	In Force	ERROR CORRECTING CODE PREDICATION SYSTEM AND METHOD
MP1389W	MP1389WO	WO	PCT/US2008/00	2/1/2008			Pending	ERROR CORRECTING CODE PREDICATION SYSTEM AND METHOD
MP1390	MP1390	US	11/974,853	10/15/2007	7525347	4/28/2009	In Force	DIFFERENTIAL PEAK DETECTOR
MP1394	MP1394	US	11/975,980	10/23/2007	7881680	2/1/2011	In Force	Predictive Transmitter Calibration
MP1394	MP1394C1	US	13/017,908	1/31/2011	8055089	1/10/2012	In Force	Predictive Transmitter Calibration
MP1395	MP1395	US	11/975,215	10/18/2007	7920536	4/5/2011	In Force	MORE ROBUST DATA TRANSFER THROUGH DETECTION OF HIDDEN N

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MP1396	MP1396C1	US	12/844,861	7/28/2010	8411687	4/2/2013	In Force	METHOD AND APPARATUS FOR MANAGING TRAFFIC THROUGH A NE
MP1396	MP1396C1C1	US	13/855,583	4/2/2013	8948188	2/3/2015	In Force	Method And Apparatus For Managing Traffic Through A Network Swi
MP1398	MP1398	US	11/957,396	11/8/2007	9178713	11/3/2015	In Force	OPTICAL LINE TERMINATION IN A PASSIVE OPTICAL NETWORK
MP1399	MP1399	US	11/948,753	11/30/2007	7818389	10/19/2010	In Force	PACKET BUFFER APPARATUS AND METHOD
MP1399	MP1399C1	US	12/898,343	10/5/2010	8019825	9/13/2011	In Force	PACKET BUFFER APPARATUS AND METHOD
MP1399	MP1399C1C1	US	13/228,936	9/9/2011	8326938	12/4/2012	In Force	PACKET BUFFER APPARATUS AND METHOD
MP1401	MP1401	US	11/938,019	11/9/2007	7991296	8/2/2011	In Force	METHOD AND APPARATUS FOR DATA FRAME SYNCHRONIZATION A
MP1401	MP1401C1	US	13/195,197	8/1/2011	8406633	3/26/2013	In Force	METHOD AND APPARATUS FOR DATA FRAME SYNCHRONIZATION A
MP1402	MP1402	US	11/948,575	11/30/2007	8268815	6/26/2012	In Force	BIT ACCURATE UPSTREAM BURST TRANSMISSION PHASE METHOD F
MP1402	MP1402D1	US	13/523,610	6/14/2012	8913895	12/16/2014	In Force	METHOD AND APPARATUS FOR CONTROLLING TIMING OF A TRANS
MP1405	MP1405	US	11/876,225	10/22/2007	9501492	11/22/2016	In Force	COMBINATION JOURNALING/NON-JOURNALING FILE SYSTEM
MP1406	MP1406	US	11/874,610	10/18/2007	7975163	7/5/2011	In Force	APPARATUS AND METHOD FOR MASKING A CLOCK SIGNAL
MP1407	MP1407	US	11/931,422	10/31/2007	7741900	6/22/2010	In Force	BIAS SETTING DEVICE
MP1407	MP1407C1	US	12/787,072	5/25/2010	8081025	12/20/2011	In Force	BIAS SETTING DEVICE
MP1411	MP1411	US	11/924,431	10/25/2007	9308455	4/12/2016	In Force	SYSTEM AND METHOD FOR GAMING IN AN AD-HOC NETWORK
MP1413	MP1413C11	US	13/164,232	6/20/2011	8493826	7/23/2013	In Force	METHOD AND APPARATUS FOR RECORDING
MP1415	MP1415C1	US	12/788,563	5/27/2010	7961002	6/14/2011	In Force	METHOD AND APPARATUS FOR MATCHING AN IMPEDANCE OF A DRI
MP1416	MP1416	US	11/926,835	10/29/2007	8725142	5/13/2014	In Force	CHANNEL SCANNING METHOD AND APPARATUS
MP1419	MP1419C2TW	TW	96139016	10/18/2007	1444857	7/11/2014	In Force	Low Power Optical Mouse
MP1419W	MP1419WO	WO	PCT/US2007/02	10/25/2007			Pending	Low Power Optical Mouse
MP1420	MP1420	US	11/935,815	11/6/2007	8073894	12/6/2011	In Force	COMPACT SWITCHED-CAPACITOR FIR FILTER IMPLEMENTATION
MP1420	MP1420D1	US	13/291,715	11/8/2011	8768998	7/1/2014	In Force	COMPACT SWITCHED-CAPACITOR FIR FILTER IMPLEMENTATION
MP1421	MP1421	US	11/929,073	10/30/2007	8166379	4/24/2012	In Force	CALCULATING SOFT INFORMATION FROM A MULTI-LEVEL MODULAT
MP1423	MP1423	US	11/932,829	10/31/2007	7941590	5/10/2011	In Force	ADAPTIVE READ AND WRITE SYSTEMS AND METHODS FOR MEMORY
MP1423	MP1423C1	US	13/095,094	4/27/2011	8799556	8/5/2014	In Force	ADAPTIVE READ AND WRITE SYSTEMS AND METHODS FOR MEMORY
MP1423	MP14231	US	12/350,068	1/7/2009	8725929	5/13/2014	In Force	ADAPTIVE READ AND WRITE SYSTEMS AND METHODS FOR MEMORY
MP1423	MP142311C1	US	14/274,583	5/9/2014	9147491	9/29/2015	In Force	ADAPTIVE READ AND WRITE SYSTEMS AND METHODS FOR MEMORY
MP1423K	MP1423KR	KR	7011381/2009	11/5/2007	10-1058568	8/16/2011	In Force	ADAPTIVE READ AND WRITE SYSTEMS AND METHODS FOR MEMORY
MP1423T	MP1423TW	TW	96141922	11/6/2007	1380309	12/21/2012	In Force	ADAPTIVE READ AND WRITE SYSTEMS AND METHODS FOR MEMORY
MP1423W	MP1423WO	WO	PCT/US2007/08	11/5/2007			Pending	ADAPTIVE READ AND WRITE SYSTEMS AND METHODS FOR MEMORY
MP1423W	MP1423WOCN	CN	200780041025+	11/5/2007	ZL2007800410	2/27/2013	In Force	ADAPTIVE READ AND WRITE SYSTEMS AND METHODS FOR MEMORY
MP1424	MP1424	US	11/936,484	11/7/2007	8356064	1/15/2013	In Force	DISCRETE FOURIER TRANSFORM CALCULATION METHOD AND APPAR
MP1424	MP1424C1	US	13/741,141	1/14/2013	9164959	10/20/2015	In Force	DISCRETE FOURIER TRANSFORM CALCULATION METHOD AND APPAR
MP1425	MP1425	US	11/934,351	11/2/2007	8266510	9/11/2012	In Force	HIGH-THROUGHPUT PIPELINED AND SCALABLE ARCHITECTURE FOR

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MP1425	MP1425C1	US	13/603,783	9/5/2012	8578229	11/5/2013	In Force	HIGH-THROUGHPUT PIPELINED AND SCALABLE ARCHITECTURE FOR
MP1425	MP1425D1	US	13/586,540	8/15/2012	8936784	1/6/2015	In Force	APPARATUS AND METHOD FOR FINDING LIKELY COMBINATIONS OF
MP1426	MP1426	US	11/935,866	11/6/2007	8209582	6/26/2012	In Force	SYSTEMS AND METHODS FOR OPTIMIZING A PRODUCT CODE STRUC
MP1426	MP1426C1	US	13/485,957	6/1/2012	8382790	3/5/2013	In Force	SYSTEMS AND METHODS FOR OPTIMIZING A PRODUCT CODE STRUC
MP1426	MP1426C1C1	US	13/773,945	2/22/2013	8595587	11/26/2013	In Force	SYSTEMS AND METHODS FOR OPTIMIZING A PRODUCT CODE STRUC
MP1427	MP1427	US	11/982,937	11/6/2007	8024631	9/20/2011	In Force	Scan Testing System And Method
MP1427	MP1427C1	US	13/225,278	9/16/2011	8214704	7/3/2012	In Force	Scan Testing System And Method
MP1428	MP1428	US	09/785,920	2/17/2001	6624535	9/23/2003	In Force	DIGITALLY CONTROLLING THE OUTPUT VOLTAGE OF A PLURALITY OF
MP1429	MP1429	US	09/695,343	6/29/2001	6944784	9/13/2005	In Force	FLIP-FLOP HAVING MULTIPLE CLOCK SOURCES AND METHOD THERE
MP1430E	MP1430EP	EP	02780305.5	9/12/2002	1428131	11/2/2006	In Force	MULTIPLE CHANNEL INTERFACE FOR COMMUNICATIONS BETWEEN
MP1430T	MP1430TW	TW	91119491	8/28/2002		4/9/2004	In Force	MULTIPLE CHANNEL INTERFACE FOR COMMUNICATIONS BETWEEN
MP1431	MP1431	US	09/919,518	7/31/2001	7257844	8/14/2007	In Force	SYSTEM AND METHOD FOR ENHANCED PIRACY PROTECTION IN A WI
MP1431	MP1431C1	US	11/682,840	8/6/2007	7836515	11/16/2010	In Force	SYSTEM AND METHOD FOR ENHANCED PIRACY PROTECTION IN A WI
MP1431	MP1431C1a8	US	13/473,142	5/16/2012	8561211	10/15/2013	In Force	SYSTEM AND METHOD FOR ENHANCED PIRACY PROTECTION IN A WI
MP1431	MP1431C1C1	US	12/943,418	11/10/2010	8185965	5/22/2012	In Force	SYSTEM AND METHOD FOR ENHANCED PIRACY PROTECTION IN A WI
MP1432	MP1432	US	09/962,424	9/25/2001	7339910	3/4/2008	In Force	POWER SAVING IN COMMUNICATION TERMINALS
MP1432	MP1432C1	US	12/010,624	1/28/2008	7646748	1/12/2010	In Force	POWER SAVING IN COMMUNICATION TERMINALS
MP1432	MP1432CN	CN	02818604.4	9/5/2002	Z12002028186	5/12/2010	In Force	POWER SAVING IN COMMUNICATION TERMINALS
MP1432	MP1432GB	GB	02799569.5	9/5/2002	1430609	5/17/2006	In Force	POWER SAVING IN COMMUNICATION TERMINALS
MP1432D	MP1432DE	DE	02799569.5	9/5/2002	02799569.5	5/17/2006	In Force	POWER SAVING IN COMMUNICATION TERMINALS
MP1432E	MP1432EP	EP	EP02799569.5	9/5/2002	EP1430609	5/17/2006	In Force	Power saving in communication terminals
MP1432H	MP1432HK	HK	4105370.5	7/21/2004	HK1063385	12/1/2006	In Force	POWER SAVING IN COMMUNICATION TERMINALS
MP1432M	MP1432MY	MY	P120023110	8/22/2002	MY-134307-A	12/31/2007	In Force	POWER SAVING IN COMMUNICATION TERMINALS
MP1432W	MP1432WO	WO	PCT/US2002/02	9/5/2002			Pending	POWER SAVING IN COMMUNICATION TERMINALS
MP1433	MP1433	US	10/039,072	1/2/2002	7133477	11/7/2006	In Force	ROBUST LOW COMPLEXITY MULTI-ANTENNA ADAPTIVE MINIMUM
MP1433	MP1433CN	CN	2826660.9	12/31/2002	Z12002028266	6/11/2008	In Force	ROBUST LOW COMPLEXITY MULTI-ANTENNA ADAPTIVE MINIMUM
MP1433T	MP1433TW	TW	91136555	12/18/2002	1234405	6/11/2005	In Force	ROBUST LOW-COMPLEXITY MULTI-ANTENNA ADAPTIVE MINIMUM
MP1433W	MP1433WO	WO	PCT/US2002/04	12/31/2002			Pending	ROBUST LOW COMPLEXITY MULTI-ANTENNA ADAPTIVE MINIMUM
MP1433W	MP1433WOSG	SG	200404003-6	12/31/2002	105721	1/31/2007	In Force	ROBUST LOW COMPLEXITY MULTI-ANTENNA ADAPTIVE MINIMUM
MP1434	MP1434C1	US	12/700,100	2/4/2010	8335229	12/18/2012	In Force	METHOD FOR SUPPORTING MULTIPLE DEVICES ON A HIGH SPEED PH
MP1435	MP1435C1	US	12/637,303	12/14/2009	8320348	11/27/2012	In Force	METHOD AND APPARATUS FOR TRANSMITTING PACKETS ASSOCIATE
MP1435	MP1435C1a8	US	14/149,546	1/7/2014	9154399	10/6/2015	In Force	CONDUIT-SHARING APPARATUS, SYSTEMS, AND METHODS
MP1435	MP1435C1C1	US	13/686,607	11/27/2012	8625563	1/7/2014	In Force	CONDUIT-SHARING APPARATUS, SYSTEMS, AND METHODS
MP1436	MP1436	US	10/726,828	12/3/2003	7457838	11/25/2008	In Force	METHODS AND APPARATUS FOR PERFORMING CALCULATIONS USIN

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MP1436	MP1436CN	CN	200480036215.1	11/17/2004	ZL2004800362	11/25/2009	In Force	METHODS AND APPARATUS FOR PERFORMING CALCULATIONS USING
MP1436	MP1436CND1	CN	200910179405.1	11/17/2004	ZL2009101794	11/25/2015	In Force	METHODS AND APPARATUS FOR PERFORMING CALCULATIONS USING
MP1436	MP1436D1	US	11/657,300	1/24/2007	7555508	6/30/2009	In Force	METHODS AND APPARATUS FOR PERFORMING CALCULATIONS USING
MP1436	MP1436D1C1	US	12/492,961	6/26/2009	8275819	9/25/2012	In Force	METHODS AND APPARATUS FOR PERFORMING CALCULATIONS USING
MP1436E	MP1436EP	EP	EP04811444.1	11/17/2004	EP1690181	7/17/2013	In Force	Methods and Apparatuses for Function Approximation and Evaluation
MP1436E	MP1436EPDE	DE	4811444.1	11/17/2004	1660181	7/17/2013	In Force	METHOD AND APPARATUSSES FOR FUNCTION APPROXIMATION AND
MP1436E	MP1436EPFR	FR	4811444.1	11/17/2004	1660181	7/17/2013	In Force	METHOD AND APPARATUSSES FOR FUNCTION APPROXIMATION AND
MP1436E	MP1436EPGB	GB	4811444.1	11/17/2004	1660181	7/17/2013	In Force	METHOD AND APPARATUSSES FOR FUNCTION APPROXIMATION AND
MP1436H	MP1436HK	HK	HK07106753.7	11/17/2004			Pending	METHODS AND APPARATUS FOR PERFORMING CALCULATIONS USING
MP1436W	MP1436WO	WO	PCT/US2004/03	11/17/2004			Pending	METHODS AND APPARATUS FOR PERFORMING CALCULATIONS USING
MP1437	MP1437C1	US	12/796,504	6/8/2010	8601145	12/3/2013	In Force	INTER-PROCESSOR COMMUNICATION LINK WITH MANAGEABILITY P
MP1437	MP1437C1C1	US	14/091,958	11/27/2013	9262375	2/16/2016	In Force	INTER-PROCESSOR COMMUNICATION LINK WITH MANAGEABILITY P
MP1438	MP1438	US	10/782,474	2/19/2004	7373111	5/13/2008	In Force	COMMUNICATION ACCESS APPARATUS, SYSTEMS, AND METHODS
MP1438	MP1438C1	US	12/149,438	5/1/2008	7951427	5/31/2011	In Force	COMMUNICATION ACCESS APPARATUS SYSTEMS, AND METHODS
MP1439	MP1439	US	10/788,725	2/26/2004	7437512	10/14/2008	In Force	LOW POWER SEMI-TRACE INSTRUCTION/TRACE HYBRID CACHE WITH
MP1440	MP1440	US	10/819,022	4/5/2004	7451271	11/11/2008	In Force	PHYSICALLY-TAGGED CACHE WITH VIRTUALLY-TAGGED FILL BUFFERS
MP1440	MP1440C1	US	12/256,766	10/23/2008	7761661	7/20/2010	In Force	PHYSICALLY-TAGGED CACHE WITH VIRTUAL FILL BUFFERS
MP1441	MP1441	US	10/816,268	3/31/2004	7159096	1/2/2007	In Force	Method And Apparatus To Perform Memory Management
MP1442	MP1442	US	10/744,035	12/23/2003	7059985	8/29/2006	In Force	USING A PROCESSOR TO PROGRAM A SEMICONDUCTOR MEMORY
MP1442	MP1442C1	US	11/486,588	7/14/2006	7594065	9/22/2009	In Force	USING A PROCESSOR TO PROGRAM A SEMICONDUCTOR MEMORY
MP1442	MP1442C2	US	11/499,340	8/4/2006	7325087	1/29/2008	In Force	USING A PROCESSOR TO PROGRAM A SEMICONDUCTOR MEMORY
MP1443	MP1443	US	10/812,564	3/30/2004	7698739	4/13/2010	In Force	UPDATING CODE WITH VALIDATION
MP1445	MP1445	US	09/967,543	9/28/2001	6954872	10/11/2005	In Force	REGISTERING EVENTS WHILE CLOCKING MULTIPLE DOMAINS
MP1446	MP1446	US	10/066,274	1/31/2002	7065131	6/20/2006	In Force	PROCESSOR AND METHOD FOR WEIGHT DETECTION IN A CLOSED LO
MP1446	MP1446CN	CN	3803071.3	1/31/2003	ZL2003038030	10/16/2009	In Force	PROCESSOR AND METHOD FOR WEIGHT DETECTION IN A CLOSED LO
MP1446E	MP1446EP	EP	03701643.3	1/31/2003	1468502	1/21/2009	In Force	PROCESSOR AND METHOD FOR WEIGHT DETECTION IN A CLOSED LO
MP1446E	MP1446EPDE	DE	03701643.3	1/31/2003	1468502	1/21/2009	In Force	PROCESSOR AND METHOD FOR WEIGHT DETECTION IN A CLOSED LO
MP1446E	MP1446EPFR	FR	03701643.3	1/31/2003	1468502	1/21/2009	In Force	PROCESSOR AND METHOD FOR WEIGHT DETECTION IN A CLOSED LO
MP1446E	MP1446EPGB	GB	03701643.3	1/31/2003	1468502	1/21/2009	In Force	PROCESSOR AND METHOD FOR WEIGHT DETECTION IN A CLOSED LO
MP1446J	MP1446JP	JP	2003-563070	1/31/2003	JP4067493	1/18/2008	In Force	PROCESSOR AND METHOD FOR WEIGHT DETECTION IN A CLOSED LO
MP1446K	MP1446KR	KR	10-2004-701186	1/31/2003	6993388	12/26/2006	In Force	PROCESSOR AND METHOD FOR WEIGHT DETECTION IN A CLOSED LO
MP1446S	MP1446SG	SG	200403971.5	1/31/2003	105701	9/28/2007	In Force	PROCESSOR AND METHOD FOR WEIGHT DETECTION IN A CLOSED LO
MP1448	MP1448	US	10/050,237	1/15/2002	7392045	6/24/2008	In Force	METHOD AND APPARATUS FOR SEARCHING FOR A BASE STATION USI
MP1448	MP1448C1	US	12/155,219	5/30/2008	8280365	10/2/2012	In Force	METHOD AND APPARATUS FOR SEARCHING FOR A BASE STATION USI

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MP1449	MP1449	US	10/145,432	5/13/2002	7305050	12/4/2007	In Force	Method And Apparatus For Processing Signals Received From A Chan
MP1449	MP1449C1	US	11/989,251	12/4/2007	8681899	3/25/2014	In Force	Method And Apparatus For Processing Signals Received From A Chan
MP1450	MP14501	US	10/322,365	12/17/2002	7260842	10/9/2007	In Force	WIRELESS COMMUNICATION DEVICE AND METHOD FOR COMMUNIC
MP1450	MP14501C2	US	11/973,499	10/9/2007	7515883	4/7/2009	In Force	Wireless Communication Device And Method For Communicating In
MP1450A	MP1450AUN	DE	02788360.2	12/16/2002	1456966	6/10/2009	In Force	METHOD AND APPARATUS FOR QUALITY MEASURE TARGET VALUE B
MP1450A	MP1450AUR	FR	02788360.2	12/16/2002	1456966	6/10/2009	In Force	METHOD AND APPARATUS FOR QUALITY MEASURE TARGET VALUE B
MP1450A	MP1450AUT	GB	02788360.2	12/16/2002	1456966	6/10/2009	In Force	METHOD AND APPARATUS FOR QUALITY MEASURE TARGET VALUE B
MP1450I	MP1450IKR	KR	1020047609455	12/16/2002	106632511	9/28/2006	In Force	Wireless Communication Device And Method For Communicating In
MP1450I	MP1450IWO	WO	PCT/IB2002/005	12/16/2002			Pending	Wireless Communication Device And Method For Communicating In
MP1450J	MP1450JP	JP	2003553745	12/16/2002			Pending	Method And Apparatus For Generating A Quality Measure Target Val
MP1450K	MP1450KR	KR	1020047609403	12/16/2002			Pending	Method And Apparatus For Generating A Quality Measure Target Val
MP1450S	MP1450SG	SG	2004032710	12/16/2002			Pending	Method And Apparatus For Generating A Quality Measure Target Val
MP1452	MP1452	US	10/076,957	2/19/2002	7463671	12/9/2008	In Force	RAKE RECEIVER INTERFACE
MP1452	MP1452C1	US	12/256,928	10/23/2008	7924908	4/12/2011	In Force	RAKE RECEIVER INTERFACE
MP1452	MP1452CN	CN	3804144.8	1/15/2003			Pending	RAKE RECEIVER INTERFACE
MP1452	MP1452CND1	CN	201010271593.1	1/15/2003	212010102715	7/16/2014	In Force	RAKE RECEIVER INTERFACE
MP1452E	MP1452EP	EP	03704964.0	1/15/2003	1476954	7/11/2012	In Force	Rake Receiver Interface
MP1452E	MP1452EPDE	DE	03704964.0	1/15/2003	1476954	7/11/2012	In Force	RAKE RECEIVER INTERFACE
MP1452E	MP1452EPGB	GB	03704964.0	1/15/2003	1476954	7/11/2012	In Force	RAKE RECEIVER INTERFACE
MP1452M	MP1452MY	MY	P10030155	1/17/2003	MY134697.A	12/31/2007	In Force	RAKE RECEIVER INTERFACE
MP1453	MP1453	US	10/175,763	6/20/2002	6791210	9/14/2004	In Force	CYCLIC REGULATION APPARATUS, SYSTEM, AND METHOD
MP1455	MP1455	US	10/176,503	6/21/2002	7020873	3/28/2006	In Force	Apparatus And Method For Vectorization Of Detected Saturation And
MP1456	MP1456	US	10/286,800	11/4/2002	7796752	9/14/2010	In Force	CIPHER IMPLEMENTATION
MP1456	MP1456C1	US	12/881,167	9/13/2010	8660265	2/25/2014	In Force	CIPHER IMPLEMENTATION
MP1457	MP1457	US	10/218,853	8/13/2002	6976117	12/13/2005	In Force	SNOOPY VIRTUAL LEVEL 1 CACHE TAG
MP1458	MP1458	US	10/174,668	6/18/2002	6944713	9/13/2005	In Force	LOW POWER SET ASSOCIATIVE CACHE
MP1458	MP1458CN	CN	3814430.1	6/10/2003	212003445095	11/26/2008	In Force	Low Power Set Associative Cache
MP1458E	MP1458EP	EP	EP03737084.8	6/12/2003			Pending	Low Power Set Associative Cache
MP1458H	MP1458HK	HK	5103654.6	4/28/2005			Pending	Low Power Set Associative Cache
MP1458T	MP1458TW	TW	92116414	6/17/2003	1233302	11/11/2005	In Force	Low Power Set Associative Cache
MP1459	MP1459	US	10/119,003	6/25/2002	6566926	5/20/2003	In Force	HYSTERETIC SELF-BIASED AMPLIFIER
MP1460	MP1460	US	10/056,983	1/25/2002	7058116	6/6/2006	In Force	NOVEL RECEIVER ARCHITECTURE FOR CDMA RECEIVER DOWNLINK
MP1460	MP1460D1	US	11/422,550	6/6/2006	8724597	5/13/2014	In Force	NOVEL CDMA RECEIVER DOWNLINK ARCHITECTURE FOR SOFT-HAND
MP1460F	MP1460FEDE	DE	03702804.0	1/24/2003	1470652	12/10/2008	In Force	PILOT TRACKING RECEIVER FOR CDMA DOWNLINK

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MP1460F	MP1460FEFR	FR	03702804.0	1/24/2003	1470652	12/10/2008	In Force	PILOT TRACKING RECEIVER FOR CDMA DOWNLINK
MP1460F	MP1460FEGB	GB	03702804.0	1/24/2003	1470652	12/10/2008	In Force	PILOT TRACKING RECEIVER FOR CDMA DOWNLINK
MP1460I	MP1460IEP	EP	03702804.0	1/24/2003	EP1470652	12/10/2008	In Force	Pilot tracking receiver for CDMA downlink
MP1460W	MP1460WVO	WO	PCT/IB2003/00C	1/24/2003			Pending	PILOT TRACKING RECEIVER FOR CDMA DOWNLINK
MP1461	MP1461	US	10/193,082	7/10/2002	6834683	5/17/2005	In Force	MULTI-MOUSE ACTIONS STYLUS
MP1462	MP1462	US	10/261,677	10/2/2002	7505478	3/17/2009	In Force	METHOD AND APPARATUS OF DE-MULTIPLEXING DATA
MP1462	MP1462C1	US	12/400,705	3/9/2009	8331393	12/11/2012	In Force	METHOD AND APPARATUS OF DE-MULTIPLEXING DATA
MP1464	MP1464	US	10/254,974	9/26/2002	7203253	4/10/2007	In Force	METHOD AND APPARATUS OF CROSS-CORRELATION
MP1464	MP1464C1	US	11/624,550	1/18/2007	7466769	12/16/2008	In Force	METHOD AND APPARATUS OF CROSS-CORRELATION
MP1464	MP1464C1AA	US	13/589,824	8/30/2012	8576954	11/5/2013	In Force	METHOD AND APPARATUS OF CROSS-CORRELATION
MP1464	MP1464C1C1	US	12/268,973	11/11/2008	8259851	9/4/2012	In Force	METHOD AND APPARATUS OF CROSS-CORRELATION
MP1464	MP1464CN	CN	3801773.3	8/8/2003	ZL03801773.3	8/5/2009	In Force	METHOD AND APPARATUS OF CROSS-CORRELATION
MP1464E	MP1464EP	EP	03798672.6	8/8/2003	1550277	12/9/2015	In Force	METHOD AND APPARATUS OF CROSS-CORRELATION
MP1464E	MP1464EPDE	DE	03798672.6	8/8/2003	1550277	12/9/2015	In Force	METHOD AND APPARATUS OF CROSS-CORRELATION
MP1464E	MP1464EPR	FR	03798672.6	8/8/2003	1550277	12/9/2015	In Force	METHOD AND APPARATUS OF CROSS-CORRELATION
MP1464E	MP1464EPGB	GB	03798672.6	8/8/2003	1550277	12/9/2015	In Force	METHOD AND APPARATUS OF CROSS-CORRELATION
MP1464M	MP1464MY	MY	PI20033137	8/18/2003	MY-136527-A	10/31/2008	In Force	METHOD AND APPARATUS OF CROSS-CORRELATION
MP1465	MP1465	US	10/215,756	8/9/2002	6986023	1/10/2006	In Force	Conditional execution of coprocessor instruction based on main proc
MP1465	MP1465faa	US	12/539,944	8/12/2009	8131981	3/6/2012	In Force	SIMD PROCESSOR PERFORMING FRACTIONAL MULTIPLY OPERATION
MP1465	MP1465I1	US	10/263,246	10/2/2002	7047393	5/16/2006	In Force	Multimedia coprocessor control mechanism including alignment or
MP1465	MP1465IHD1	US	11/351,950	2/10/2005	7356676	4/8/2008	In Force	EXTRACTING ALIGNED DATA FROM TWO SOURCE REGISTERS WITHO
MP1465	MP1465I2	US	10/262,195	9/30/2002	7213128	5/1/2007	In Force	STORING AND TRANSFERRING SIMD SATURATION HISTORY FLAGS AN
MP1465	MP1465I2C1	US	11/797,067	4/30/2007	7373488	5/13/2008	In Force	PROCESSING FOR ASSOCIATED DATA SIZE SATURATION FLAG HISTOR
MP1465	MP1465I2I1	US	11/173,408	6/30/2005	7392368	6/24/2008	In Force	CROSS MULTIPLY AND ADD INSTRUCTION AND MULTIPLY AND SUBT
MP1465F	MP1465FIP1	JP	2008-161911	6/20/2008	JP5586128	8/1/2014	In Force	Multimedia Coprocessor Control Mechanism Including Alignment or
MP1465F	MP1465FIP2	JP	2011-016901	1/28/2011	JP5634898	10/24/2014	In Force	Multimedia Coprocessor Control Mechanism Including Alignment or
MP1465F	MP1465FIP3	JP	2014-156777	7/31/2014			Pending	Multimedia Coprocessor Control Mechanism Including Alignment or
MP1465F	MP1465FMP	JP	2004-527842	8/6/2003			Pending	Multimedia Coprocessor Control Mechanism Including Alignment or
MP1465I	MP1465I1AU	AU	2003256870	8/6/2003			Pending	MULTIMEDIA COPROCESSOR CONTROL MECHANISM INCLUDING ALL
MP1465I	MP1465I1CN	CN	03823993.0	8/6/2003	ZL03823993.0	6/11/2008	In Force	MULTIMEDIA COPROCESSOR CONTROL MECHANISM INCLUDING ALL
MP1465I	MP1465I1PCT	WO	PCT/US2003/02	8/6/2003			Pending	MULTIMEDIA COPROCESSOR CONTROL MECHANISM INCLUDING ALL
MP1465I	MP1465I1TW	TW	92121859	8/8/2003	261201	9/1/2006	In Force	MULTIMEDIA COPROCESSOR CONTROL MECHANISM INCLUDING ALL
MP1466I	MP1466IP	JP	2004517588	5/22/2003			Pending	METHOD AND APPARATUS TO TRANSFER INFORMATION
MP1466S	MP1466SG	SG	200407272.4	5/22/2003	1,085,25	2/28/2007	In Force	METHOD AND APPARATUS TO TRANSFER INFORMATION

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MP1466T	MP1466TW	TW	92117431	6/26/2003	1290679	12/1/2007	In Force	METHOD AND APPARATUS TO TRANSFER INFORMATION
MP1466W	MP1466WO	WO	PCT/US2003/01	5/22/2003			Pending	METHOD AND APPARATUS TO TRANSFER INFORMATION
MP1466W	MP1466WOCN	CN	3820010.4	5/22/2003			Pending	METHOD AND APPARATUS TO TRANSFER INFORMATION
MP1467	MP1467	US	10/262,360	9/30/2002	7219176	5/15/2007	In Force	SYSTEM AND APPARATUS FOR EARLY FIXED LATENCY SUBTRACTIVE D
MP1467	MP1467C1	US	11/734,401	4/12/2007	7406553	7/29/2008	In Force	SYSTEM AND APPARATUS FOR EARLY FIXED LATENCY SUBTRACTIVE D
MP1467	MP1467D1	US	11/724,765	3/16/2007	7406552	7/29/2008	In Force	SYSTEMS AND METHODS FOR EARLY FIXED LATENCY SUBTRACTIVE D
MP1468	MP1468	US	10/329,125	12/23/2002	6801470	10/5/2004	In Force	DIGITAL REGULATION CIRCUIT
MP1469	MP1469	US	10/603,860	6/26/2003	7336753	2/26/2008	In Force	TRANSMITTER
MP1470	MP1470	US	10/327,957	12/26/2002	7163729	9/5/2006	In Force	METHOD AND APPARATUS OF MEMORY MANAGEMENT
MP1470	MP1470C1	US	11/487,331	8/2/2006	7415583	8/19/2008	In Force	METHOD AND APPARATUS FOR FREEING MEMORY
MP1470	MP1470C1C1	US	11/779,198	7/17/2007	7640405	12/29/2009	In Force	METHOD AND APPARATUS FOR FREEING MEMORY
MP1470	MP1470CN	CN	200380107374.1	12/23/2003	ZL2003801073	2/13/2013	In Force	MEMORY AND APPARATUS OF MEMORY MANAGEMENT
MP1470	MP1470GB	GB	03796848.4	12/23/2003	1576760	5/19/2010	In Force	METHOD AND APPARATUS OF MEMORY MANAGEMENT
MP1470D	MP1470DE	DE	03796848.4	12/23/2003	1576760	5/19/2010	In Force	METHOD AND APPARATUS OF MEMORY MANAGEMENT
MP1470E	MP1470EP	EP	03796848.4	12/23/2003	1576760	5/19/2010	In Force	MEMORY AND APPARATUS OF MEMORY MANAGEMENT
MP1470F	MP1470FR	FR	03796848.4	12/23/2003	1576760	5/19/2010	In Force	METHOD AND APPARATUS OF MEMORY MANAGEMENT
MP1471	MP1471C1	US	11/501,490	8/8/2006	7266646	9/4/2007	In Force	LEAST MEAN SQUARE DYNAMIC CACHE-LOCKING
MP1473	MP1473	US	10/337,058	12/24/2002	7509257	3/24/2009	In Force	METHOD AND APPARATUS FOR ADAPTING REFERENCE TEMPLATES
MP1474	MP1474	US	10/324,264	12/18/2002	7391437	6/24/2008	In Force	Image Sensor Interface
MP1475	MP1475	US	10/336,668	1/3/2003	8352248	1/8/2013	In Force	SPEECH COMPRESSION METHOD AND APPARATUS
MP1475	MP1475C1	US	13/753,602	1/3/2013	8639503	1/28/2014	In Force	SPEECH COMPRESSION METHOD AND APPARATUS
MP1476	MP1476	US	10/336,847	1/3/2003	6952754	10/4/2005	In Force	PREDECODE APPARATUS, SYSTEMS, AND METHODS
MP1478	MP1478	US	10/318,689	12/12/2002	6774696	8/10/2004	In Force	LEVEL SHIFTER AND VOLTAGE TRANSLATOR
MP1479	MP1479	US	10/747,802	12/29/2003	6947328	9/20/2005	In Force	VOLTAGE LEVEL SHIFTER
MP1480	MP1480	US	10/612,291	6/30/2003	7038687	5/2/2006	In Force	SYSTEM AND METHOD FOR HIGH-SPEED COMMUNICATIONS BETWE
MP1481	MP1481	US	10/388,384	3/11/2003	7310710	12/18/2007	In Force	REGISTER FILE WITH INTEGRATED ROUTING TO EXECUTION UNITS F
MP1482	MP1482	US	10/654,544	9/2/2003	7099968	8/29/2006	In Force	SYSTEM AND METHOD FOR GENERATING BUS REQUESTS IN ADVANC
MP1483	MP1483	US	10/687,853	10/30/2003	7482860	9/23/2008	In Force	UNIFIED MMSE EQUALIZATION AND MULTI-USER DETECTION APPRO
MP1483	MP1483C1	US	12/015,187	1/16/2008	8144748	3/27/2012	In Force	METHOD AND APPARATUS FOR EQUALIZING CDMA SIGNALS
MP1483	MP1483C1C1	US	13/450,635	3/26/2012	8548028	10/1/2013	In Force	METHOD AND APPARATUS FOR EQUALIZING CDMA SIGNALS
MP1483	MP1483CND1	CN	201010228297.1	10/27/2004	ZL2010102282	11/20/2013	In Force	METHOD, APPARATUS AND SYSTEM FOR CDMA COMMUNICATIONS
MP1483T	MP1483TW	TW	93132736	10/28/2004	1256783	3/6/2006	In Force	A UNIFIED MMSE EQUALIZATION AND MULTI-USER DETECTION APPR
MP1483W	MP1483WO	WO	PCT/US2604/03	10/27/2004			Pending	A UNIFIED MMSE EQUALIZATION AND MULTI-USER DETECTION APPR
MP1483W	MP1483WOCN	CN	200480039667.1	10/27/2004	ZL2004800396	9/8/2010	In Force	UNIFIED MMSE EQUALIZATION AND MULTI-USER DETECTION APPRO

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MP1483W	MP1483WOEP	EP	4818321.4	10/27/2004			Pending	UNIFIED MMSE EQUALIZATION AND MULTI-USER DETECTION APPRO
MP1483W	MP1483WOEPHK	HK	6109016.5	10/27/2004			Pending	UNIFIED MMSE EQUALIZATION AND MULTI-USER DETECTION APPRO
MP1484	MP1484	US	10/749,693	12/29/2003	7702023	4/20/2010	In Force	TRANSMITTER OPERATIONS FOR INTERFERENCE MITIGATION
MP1484W	MP1484WO	WO	PCT/US2004/04	12/20/2004			Pending	TRANSMITTER OPERATIONS FOR INTERFERENCE MITIGATION
MP1484W	MP1484WOEP	EP	04815154.2	12/20/2004	1700383	8/25/2010	In Force	TRANSMITTER OPERATIONS FOR INTERFERENCE MITIGATION
MP1484W	MP1484WOEPDE	DE	04815154.2	12/20/2004	1700383	8/25/2010	In Force	TRANSMITTER OPERATIONS FOR INTERFERENCE MITIGATION
MP1484W	MP1484WOEPGB	GB	04815154.2	12/20/2004	1700383	8/25/2010	In Force	TRANSMITTER OPERATIONS FOR INTERFERENCE MITIGATION
MP1484W	MP1484WOEPHK	HK	6110522	9/21/2006	1088459	11/5/2010	In Force	TRANSMITTER OPERATIONS FOR INTERFERENCE MITIGATION
MP1485	MP1485	US	10/430,083	5/5/2003	7398068	7/8/2008	In Force	DUAL ANTENNA SYSTEM HAVING ONE PHASE LOCK LOOP
MP1485	MP1485C1	US	12/048,033	3/13/2008	8170518	5/1/2012	In Force	DUAL ANTENNA SYSTEM HAVING ONE PHASE LOCK LOOP
MP1485	MP1485C1C1	US	13/459,508	4/30/2012	8301099	10/30/2012	In Force	METHOD AND APPARATUS FOR DOWNCONVERTING A PLURALITY OF
MP1485	MP1485CN	CN	20048011899.1	3/24/2004	ZL200480118	1/5/2011	In Force	DUAL ANTENNA SYSTEM HAVING ONE PHASE LOCK LOOP
MP1485E	MP1485EPDE	DE	04760500.1	3/24/2004	602604052996	8/1/2018	In Force	Dual Antenna System Having One Phase Lock Loop
MP1485E	MP1485EPFR	FR	04760500.1	3/24/2004	1625666	8/1/2018	In Force	Dual Antenna System Having One Phase Lock Loop
MP1485E	MP1485EPGB	GB	04760500.1	3/24/2004	1625666	8/1/2018	In Force	Dual Antenna System Having One Phase Lock Loop
MP1485K	MP1485KR	KR	2005-7010922	11/3/2005	10-0810337	2/27/2008	In Force	Dual Antenna System Having One Phase Lock Loop
MP1485T	MP1485TW	TW	93108505	3/29/2004	1244829	10/12/2007	In Force	Dual Antenna System Having One Phase Lock Loop
MP1487	MP1487	US	10/602,509	6/24/2003	7082508	7/25/2006	In Force	DYNAMIC TTB LOCKING BASED ON PAGE USAGE METRIC
MP1488	MP1488	US	10/435,231	5/9/2003	7035757	4/25/2006	In Force	THREE-DIMENSIONAL POSITION CALIBRATION OF AUDIO SENSORS A
MP1488	MP1488CN	CN	200410034740.1	5/9/2004	ZL2004100347	9/9/2009	In Force	THREE-DIMENSIONAL POSITION CALIBRATION OF AUDIO SENSORS A
MP1488	MP1488RE	US	12/110,216	4/25/2008	RE44737	1/28/2014	In Force	THREE-DIMENSIONAL POSITION CALIBRATION OF AUDIO SENSORS A
MP1488E	MP1488EPD1	EP	11009918.1	3/19/2004			Pending	THREE-DIMENSIONAL POSITION CALIBRATION OF AUDIO SENSORS A
MP1488W	MP1488WO	WO	PCT/US2004/00	3/19/2004			Pending	THREE-DIMENSIONAL POSITION CALIBRATION OF AUDIO SENSORS A
MP1488W	MP1488WOEP	EP	4760779.1	3/19/2004	1623314	5/9/2012	In Force	THREE-DIMENSIONAL POSITION CALIBRATION OF AUDIO SENSORS A
MP1488W	MP1488WOEPDE	DE	4760779.1	3/19/2004	6026040	5/9/2012	In Force	THREE-DIMENSIONAL POSITION CALIBRATION OF AUDIO SENSORS A
MP1488W	MP1488WOEPFR	FR	4760779.1	3/19/2004	1623314	5/9/2012	In Force	THREE-DIMENSIONAL POSITION CALIBRATION OF AUDIO SENSORS A
MP1488W	MP1488WOEPGB	GB	4760779.1	3/19/2004	1623314	5/9/2012	In Force	THREE-DIMENSIONAL POSITION CALIBRATION OF AUDIO SENSORS A
MP1488W	MP1488WOEPHK	HK	6101794	3/19/2004			Pending	THREE-DIMENSIONAL POSITION CALIBRATION OF AUDIO SENSORS A
MP1488W	MP1488WOEPSE	SE	200506757.4	3/19/2004	116322	9/30/2009	In Force	THREE-DIMENSIONAL POSITION CALIBRATION OF AUDIO SENSORS A
MP1489	MP1489	US	10/726,885	12/3/2003	7162609	1/9/2007	In Force	TRANSLATION LOOKASIDE BUFFER PREDICTION MECHANISM
MP1489	MP1489C1	US	11/528,791	9/27/2006	7434027	10/7/2008	In Force	TRANSLATION LOOKASIDE BUFFER PREDICTION MECHANISM
MP1489	MP1489C1C1	US	12/286,360	9/30/2008	7987337	7/26/2011	In Force	Translation Lookaside Buffer Prediction Mechanism
MP1490	MP1490	US	08/129,664	9/30/1993	5399918	3/21/1995	In Force	Large Fan-in, Dynamic, BiCMOS Logic Gate
MP1490I	MP1490IP	JP	1994259047	9/29/1994	JP3517459	1/30/2004	In Force	Large Fan-in, Dynamic, BiCMOS Logic Gate

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MP1492	MP1492	US	10/750,256	12/29/2003	7428645	9/23/2008	In Force	METHODS AND APPARATUS TO SELECTIVELY POWER FUNCTIONAL U
MP1492	MP1492C1	US	12/232,646	9/22/2008	8112643	2/7/2012	In Force	METHODS AND APPARATUS TO SELECTIVELY POWER FUNCTIONAL U
MP1492	MP1492C1aa	US	14/280,971	5/19/2014	9146607	9/29/2015	In Force	METHODS AND APPARATUS TO SELECTIVELY POWER FUNCTIONAL U
MP1492	MP1492C1C1	US	13/362,358	1/31/2012	8732490	5/20/2014	In Force	METHODS AND APPARATUS TO SELECTIVELY POWER FUNCTIONAL U
MP1493	MP1493	US	10/734,117	12/15/2003	7912145	3/22/2011	In Force	FILTER FOR A MODULATOR AND METHODS THEREOF
MP1493	MP1493GB	GB	04749927.2	4/22/2004	1712049	6/9/2010	In Force	A FILTER FOR A MODULATOR AND METHODS THEREOF
MP1493D	MP1493DE	DE	04749927.2	4/22/2004	1712049	6/9/2010	In Force	A FILTER FOR A MODULATOR AND METHODS THEREOF
MP1493E	MP1493EP	EP	04749927.2	4/22/2004	1712049	6/9/2010	In Force	A filter for a Modulator and Methods Thereof
MP1493F	MP1493FR	FR	04749927.2	4/22/2004	1712049	6/9/2010	In Force	A FILTER FOR A MODULATOR AND METHODS THEREOF
MP1493T	MP1493TW	TW	93111781	4/27/2004	1257205	6/21/2006	In Force	A filter for a Modulator and Methods Thereof
MP1494	MP1494	US	10/620,470	7/15/2003	7428649	9/23/2008	In Force	Power Supply Delivery For Leakage Suppression Modes
MP1494	MP1494C1	US	12/284,524	9/23/2008	7865625	9/28/2010	In Force	Power Supply Delivery For Leakage Suppression Modes
MP1494	MP1494C1AA	US	13/668,724	11/5/2012	8589711	11/19/2013	In Force	Power Supply Delivery For Leakage Suppression Modes
MP1494	MP1494C1C1	US	12/887,930	9/22/2010	8307232	11/6/2012	In Force	Power Supply Delivery For Leakage Suppression Modes
MP1495	MP1495	US	10/742,728	12/19/2003	7587712	9/8/2009	In Force	END-TO-END ARCHITECTURE FOR MOBILE CLIENT JIT PROCESSING O
MP1498	MP1498	US	10/879,259	6/30/2004	7469205	12/23/2008	In Force	APPARATUS AND METHODS FOR PRONUNCIATION LEXICON COMPRE
MP1499	MP1499	US	10/835,083	4/29/2004	7945900	5/17/2011	In Force	DEBUGGING TOOL FOR DEBUGGING MULTI-THREADED PROGRAMS
MP1500	MP1500C1	US	12/588,652	10/22/2009	8479183	7/2/2013	In Force	CACHING RUN-TIME VARIABLES IN OPTIMIZED CODE
MP1500	MP1500CN	CN	200480042902.1	4/30/2004	ZL2004800429	10/21/2009	In Force	CACHING RUN-TIME VARIABLES IN OPTIMIZED CODE
MP1500H	MP1500HK	HK	7110407.9	4/30/2004			Pending	CACHING RUN-TIME VARIABLES IN OPTIMIZED CODE
MP1500W	MP1500WO	WO	PCT/CN2004/00	4/30/2004			Pending	CACHING RUN-TIME VARIABLES IN OPTIMIZED CODE
MP1501	MP1501	US	10/797,771	3/10/2004	7143220	11/28/2006	In Force	APPARATUS AND METHOD FOR GRANTING CONCURRENT OWNERSHI
MP1501	MP1501C1	US	11/501,572	8/8/2006	7428607	9/23/2008	In Force	Apparatus And Method For Arbitrating Heterogeneous Agents In On-
MP1501	MP1501C1C1	US	12/284,396	9/22/2008	7765349	7/27/2010	In Force	Apparatus And Method For Arbitrating Heterogeneous Agents In On-
MP1502	MP1502	US	10/802,586	3/17/2004	7904893	3/8/2011	In Force	POWER AND/OR ENERGY OPTIMIZED COMPILE/EXECUTION
MP1503	MP1503	US	10/903,971	7/29/2004	7447961	11/4/2008	In Force	Inversion Of Scan Clock For Scan Cells
MP1503	MP1503C1	US	12/290,262	10/29/2008	7613967	11/3/2009	In Force	Inversion Of Scan Clock For Scan Cells
MP1505	MP1505	US	10/826,459	4/16/2004	7353244	1/4/2008	In Force	DUAL-MULTIPLY-ACCUMULATOR OPERATION OPTIMIZED FOR EVEN
MP1505	MP1505C1	US	12/042,100	3/4/2008	8051121	11/1/2011	In Force	Dual Multiply-Accumulator Operation Optimized for Even and Odd M
MP1505	MP1505C1C1	US	13/285,830	10/31/2011	8756267	6/17/2014	In Force	Dual-Multiply-Accumulator Operation Optimized for Even and Odd M
MP1506	MP1506	US	10/880,762	6/30/2004	7302600	11/27/2007	In Force	POWER SUPPLY DETECTION METHOD, APPARATUS, AND SYSTEM
MP1506	MP1506C1	US	11/945,146	11/26/2007	7770044	8/3/2010	In Force	METHOD AND APPARATUS FOR WAKING UP A CIRCUIT
MP1510	MP1510	US	10/821,485	4/9/2004	7492369	2/17/2009	In Force	Loading An Internal Frame Buffer From An External Frame Buffer
MP1510	MP1510aC1	US	13/236,544	9/19/2011	8237724	8/7/2012	In Force	Loading An Internal Frame Buffer From An External Frame Buffer

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MP1510	MP1510C1	US	12/351,372	1/9/2009	7755633	7/13/2010	In Force	Loading An Internal Frame Buffer From An External Frame Buffer
MP1511	MP1511	US	10/819,788	4/7/2004	7562209	7/14/2009	In Force	SUPPORTING DIFFERENT INSTRUCTION SET ARCHITECTURES DURING
MP1512	MP1512	US	10/879,482	6/30/2004	7546140	6/9/2009	In Force	DEVICE, SYSTEM AND METHOD FOR MULTI-PROFILE WIRELESS COM
MP1512	MP1512C1	US	12/472,306	5/26/2009	8165628	4/24/2012	In Force	DEVICE, SYSTEM AND METHOD FOR MULTI-PROFILE WIRELESS COM
MP1513	MP1513	US	10/880,767	6/28/2004	7129751	10/31/2006	In Force	LOW-LEAKAGE LEVEL SHIFTER WITH INTEGRATED FIREWALL AND ME
MP1513	MP1513C1	US	11/460,885	7/28/2006	7342417	3/11/2008	In Force	LOW-LEAKAGE LEVEL SHIFTER WITH INTEGRATED FIREWALL AND ME
MP1513	MP1513C1D1	US	11/798,602	5/15/2007	7471106	12/30/2008	In Force	LOW-LEAKAGE LEVEL SHIFTER WITH INTEGRATED FIREWALL AND ME
MP1515	MP1515C1	US	11/845,553	8/27/2007	7492201	2/17/2009	In Force	TWO-LATCH CLOCKED-LSSD FLIP-FLOP
MP1516	MP1516	US	10/927,682	8/27/2004	7475219	1/6/2009	In Force	Serially Indexing A Cache Memory
MP1516	MP1516C1C1	US	12/942,212	11/9/2010	8171200	5/1/2012	In Force	Serially Indexing A Cache Memory
MP1517	MP1517	US	10/949,330	9/27/2004	8144806	3/27/2012	In Force	DEVICE, SYSTEM AND METHOD OF I/Q MISMATCH CORRECTION
MP1517	MP1517C1	US	13/425,179	3/20/2012	8428180	4/23/2013	In Force	METHOD AND APPARATUS FOR CORRECTING I/Q MISMATCH IN A WI
MP1519	MP1519	US	10/954,648	9/30/2004	7275204	9/25/2007	In Force	DISTRIBUTED RING CONTROL CIRCUITS FOR VITERBI TRACEBACK
MP1519	MP1519C1	US	11/860,493	9/24/2007	7840885	11/23/2010	In Force	DISTRIBUTED RING CONTROL CIRCUITS FOR VITERBI TRACEBACK
MP1519J	MP1519JP	JP	2007-533502	1/31/2007			Pending	DISTRIBUTED RING CONTROL CIRCUITS FOR VITERBI TRACEBACK
MP1519W	MP1519WO	WO	PCT/US2005/03	9/1/2005			Pending	DISTRIBUTED RING CONTROL CIRCUITS FOR VITERBI TRACEBACK
MP1520	MP1520	US	11/014,805	12/20/2004	7379718	5/27/2008	In Force	METHOD AND APPARATUS TO MANAGE POWER CONSUMPTION OF
MP1520	MP1520C1	US	12/127,678	5/27/2008	8112048	2/7/2012	In Force	METHOD AND APPARATUS TO MANAGE POWER CONSUMPTION OF
MP1520	MP1520CN	CN	200580043761.4	11/15/2005	ZL2005800437	10/28/2009	In Force	METHOD AND APPARATUS TO MANAGE POWER CONSUMPTION OF
MP1520H	MP1520HK	HK	HK08102577.9	11/15/2005			Pending	Method and Apparatus to Manage Power Consumption of a Semicon
MP1520J	MP1520JP	JP	2007-544675	11/15/2005	JP5060305	8/10/2012	In Force	METHOD AND APPARATUS TO MANAGE POWER CONSUMPTION OF
MP1520K	MP1520KR	KR	7014002/2007	11/15/2005	931030	12/2/2009	In Force	Method and Apparatus to Manage Power Consumption of a Semicon
MP1521	MP1521Faa	US	12/709,736	2/22/2010	8076956	12/13/2011	In Force	Relatively Low Standby Power
MP1521	MP1521I1	US	11/132,618	5/18/2005	7443205	10/28/2008	In Force	Relatively Low Standby Power
MP1522	MP1522	US	10/974,493	10/27/2004	8239563	8/7/2012	In Force	Method And Apparatus For Using Multiple Links At A Handheld Devic
MP1522	MP1522C1	US	13/568,698	8/7/2012	8443100	5/14/2013	In Force	Method And Apparatus For Using Multiple Links At A Handheld
MP1523	MP1523	US	08/537,155	9/28/1995	5905902	5/18/1999	In Force	Programmable State Machine Employing A Cache-Like Arrangement
MP1524	MP1524	US	09/035,039	4/3/1998	6266766	7/24/2001	In Force	METHOD AND APPARATUS FOR INCREASING THROUGHPUT WHEN A
MP1524	MP1524C1	US	09/872,174	5/31/2001	6408378	6/18/2002	In Force	Multi-Bit Scoreboarding To Handle Write-After-Write Hazards And Eli
MP1524D	MP1524DE	DE	199830983	3/31/1999	19983098	3/17/2005	In Force	Multi-Bit Scoreboarding To Handle Write-After-Write Hazards And Eli
MP1524W	MP1524WO	WO	PCT/US1999/00	3/31/1999			Pending	Multi-Bit Scoreboarding To Handle Write-After-Write Hazards And Eli
MP1524W	MP1524WODE	DE	19983098.3	3/31/1999	19983098.3	3/17/2005	In Force	MULTI-BIT SCOREBOARDING TO HANDLE WRITE-AFTER-WRITE HAZA
MP1525	MP1525	US	08/937,631	9/26/1997	6057586	5/2/2000	In Force	Method And Apparatus For Employing A Light Shield To Modulate Pix
MP1525	MP1525C1	US	09/802,464	3/9/2001	6933168	8/23/2005	In Force	Method And Apparatus For Employing A Light Shield To Modulate Pix

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MP1525	MP1525D1	US	09/524,081	3/13/2000	6235549	5/22/2001	In Force	Method And Apparatus for Employing A Light Shield To Modulate Pix
MP1526	MP1526	US	09/164,134	9/30/1998	6237087	5/22/2001	In Force	METHOD AND APPARATUS FOR SPEEDING SEQUENTIAL ACCESS OF A
MP1528	MP1528	US	09/109,259	6/30/1998	6115476	9/5/2000	In Force	ACTIVE DIGITAL AUDIO/VIDEO SIGNAL MODIFICATION TO CORRECT F
MP1528	MP1528CN	CN	998099171	3/4/1999	Z1998099171	3/9/2005	In Force	Active Digital/Video Signal Modification To Correct For Playback Syst
MP1528D	MP1528DE	DE	199833346	3/4/1999	19983334	9/4/2008	In Force	Active Digital Audio/Video Signal Modification To Correct For Playbac
MP1528P	MP1528PCT	WO	PCT/US1999/00	3/4/1999			Pending	Active Digital Audio/Video Signal Modification To Correct For Playba
MP1528T	MP1528TW	TW	88107953	5/6/1999		2/1/2001	In Force	Active Digital Audio/Video Signal Modification To Correct For Playba
MP1528W	MP1528WOCN	CN	998099171	3/4/1999	Z1199809917	3/9/2005	In Force	ACTIVE DIGITAL AUDIO/VIDEO SIGNAL MODIFICATION TO CORRECT
MP1528W	MP1528WODE	DE	199833346	3/4/1999	199833346	9/4/2008	In Force	ACTIVE DIGITAL AUDIO/ VIDEO SIGNAL MODIFICATION TO CORRECT
MP1529	MP1529	US	09/272,766	3/18/1999	6201428	3/13/2001	In Force	5-VOLT TOLERANT 3-VOLT DRIVE PUSH-PULL BUFFER/DRIVER
MP1530	MP1530	US	09/444,538	11/22/1999	6282139	8/28/2001	In Force	REGISTER FILE WITH IMPROVED NOISE IMMUNITY AND ASPECT RATIO
MP1531	MP1531	US	09/449,283	11/24/1999	6543022	4/1/2003	In Force	METHOD AND APPARATUS TO ENCODE DIGITAL SIGNALS
MP1532	MP1532	US	09/476,444	12/30/1999	6516384	2/4/2003	In Force	METHOD AND APPARATUS TO PERFORM A ROUND ROBIN AND LOCK
MP1532	MP1532CN	CN	8180415	11/27/2000	Z1808180415	4/4/2007	In Force	A METHOD AND APPARATUS TO PERFORM A ROUND ROBIN AND LO
MP1532H	MP1532HKD1	HK	81008459	11/27/2000			Pending	A METHOD AND APPARATUS TO PERFORM A ROUND ROBIN AND LO
MP1532T	MP1532TW	TW	89128436	1/9/2001		5/22/2003		A METHOD AND APPARATUS TO PERFORM A ROUND ROBIN AND LO
MP1532W	MP1532WO	WO	PCT/US2000/04	11/27/2000			Pending	A METHOD AND APPARATUS TO PERFORM A ROUND ROBIN AND LO
MP1532W	MP1532WCEPGB	GB	215661	11/27/2000	2374178	8/25/2004	In Force	A METHOD AND APPARATUS TO PERFORM A ROUND ROBIN AND LO
MP1532W	MP1532WCIJP	JP	2001-550561	11/27/2000			Pending	A METHOD AND APPARATUS TO PERFORM A ROUND ROBIN AND LO
MP1532W	MP1532WCKR	KR	27008325	11/27/2000	476446	3/3/2005	In Force	METHOD AND APPARATUS TO PERFORM A ROUND ROBIN AND LOCK
MP1533	MP1533	US	09/464,023	12/15/1999	6388933	9/4/2002	In Force	TAP CONNECTIONS FOR CIRCUITS WITH LEAKAGE SUPPRESSION CAP
MP1533	MP1533D1	US	09/941,829	8/29/2001	6388315	5/14/2002	In Force	TAP CONNECTIONS FOR CIRCUITS WITH LEAKAGE SUPPRESSION CAP
MP1533	MP1533D1RE	US	11/802,556	5/23/2007	RE42776	10/4/2011	In Force	TAP CONNECTIONS FOR CIRCUITS WITH LEAKAGE SUPPRESSION CAP
MP1533	MP1533RE	US	11/802,763	5/24/2007	RE43,326	4/24/2012	In Force	TAP CONNECTIONS FOR CIRCUITS WITH LEAKAGE SUPPRESSION CAP
MP1534	MP1534	US	10/815,567	3/31/2004	7529947	5/5/2009	In Force	DETERMINING POWER CONSUMPTION OF AN APPLICATION
MP1535	MP1535	US	09/602,667	6/26/2000	6549037	4/15/2003	In Force	APPARATUS AND CIRCUIT HAVING REDUCED LEAKAGE CURRENT AND
MP1537	MP1537	US	08/671,896	6/28/1996	6097704	1/8/2000	In Force	SYSTEM FOR COMMUNICATING DIGITAL INFORMATION BETWEEN A
MP1539	MP1539	US	09/981,631	10/16/2001	7003051	2/21/2006	In Force	TIME DELAY ESTIMATION IN A TRANSMITTER
MP1540	MP1540	US	09/867,028	5/30/2001	6959275	10/25/2005	In Force	SYSTEM AND METHOD FOR ENHANCING THE INTELLIGIBILITY OF REC
MP1540	MP1540AAAA	US	13/340,383	12/29/2011	8407045	3/26/2013	In Force	ENHANCING THE INTELLIGIBILITY OF RECEIVED SPEECH IN A NOISY E
MP1540	MP15401aa	US	12/617,540	11/12/2009	8080576	1/3/2012	In Force	ENHANCING THE INTELLIGIBILITY OF RECEIVED SPEECH IN A NOISY E
MP1540	MP15401TW	TW	91111450	5/29/2002		8/20/2004	In Force	ENHANCING THE INTELLIGIBILITY OF RECEIVED SPEECH IN A NOISY E
MP1540T	MP1540TW	TW	91111450	5/29/2002		8/20/2004	In Force	ENHANCING THE INTELLIGIBILITY OF RECEIVED SPEECH IN A NOISY E

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MP1540W	MP1540WO	WO	PCT/US2002/01	5/17/2002			Pending	ENHANCING THE INTELLIGIBILITY OF RECEIVED SPEECH IN A NOISY E
MP1541	MP1541	US	09/746,675	12/22/2000	6867956	3/15/2005	In Force	ELECTROSTATIC DISCHARGE PROTECTION DEVICE AND METHOD THE
MP1543	MP1543	US	10/057,493	1/24/2002	6775748	8/10/2004	In Force	METHODS AND APPARATUS FOR TRANSFERRING CACHE BLOCK OWN
MP1543	MP1543I1	US	10/073,492	2/11/2002	7100001	8/29/2006	In Force	METHODS AND APPARATUS FOR CACHE INTERVENTION
MP1543	MP1543I1C1	US	11/084,286	3/18/2005	7062613	6/13/2006	In Force	METHODS AND APPARATUS FOR CACHE INTERVENTION
MP1543	MP1543I1I1	US	10/303,931	11/25/2002	6983348	1/3/2006	In Force	METHODS AND APPARATUS FOR CACHE INTERVENTION
MP1547	MP1547	US	11/936,418	11/7/2007	8122314	2/21/2012	In Force	DEFECT RECOVERY FOR ITERATIVELY-DECODED DATA CHANNEL
MP1547	MP1547C1	US	13/362,790	1/31/2012	8382714	12/11/2012	In Force	DEFECT RECOVERY FOR ITERATIVELY-DECODED DATA CHANNEL
MP1547	MP1547C1C1	US	13/669,979	11/6/2012	8464119	6/11/2013	In Force	DEFECT RECOVERY FOR ITERATIVELY-DECODED DATA CHANNEL
MP1548	MP1548	US	11/937,389	11/8/2007	8255763	8/28/2012	In Force	ERROR CORRECTION SYSTEM USING AN ITERATIVE PRODUCT CODE
MP1549	MP1549	US	11/936,578	11/7/2007	8145983	3/27/2012	In Force	METHODS AND APPARATUS FOR IDENTIFICATION OF LIKELY ERRORS
MP1549	MP1549C1	US	13/407,901	2/29/2012	8601354	12/3/2013	In Force	METHODS AND APPARATUS FOR IDENTIFICATION OF LIKELY ERRORS
MP1550	MP1550	US	10/097,202	3/12/2002	6639827	10/28/2003	In Force	Low Standby Power Using Shadow Storage
MP1551	MP1551	US	11/938,722	11/12/2007	7957332	6/7/2011	In Force	MEMORY EFFICIENT FILTERING
MP1551	MP1551C1A4	US	14/936,000	9/11/2015	9426745	8/23/2016	In Force	MEMORY EFFICIENT FILTERING
MP1551	MP1551C1C1	US	14/216,556	3/17/2014	9185653	11/10/2015	In Force	MEMORY EFFICIENT FILTERING
MP1553	MP1553	US	11/936,531	11/7/2007	8059763	11/15/2011	In Force	APPROXIMATE SOFT-INFORMATION COMPUTATION IN MULTI-LEVEL
MP1554	MP1554	US	11/983,708	11/9/2007	8127309	2/28/2012	In Force	Secure Digital Input/Output Interface System
MP1554	MP1554C1	US	13/405,946	2/27/2012	8234661	7/31/2012	In Force	Secure Digital Input/Output Interface System
MP1554	MP1554C1C1	US	13/563,323	7/31/2012	8832719	9/9/2014	In Force	Secure Digital Input/Output Interface System
MP1556	MP1556	US	11/937,854	11/9/2007	8380203	2/19/2013	In Force	ENHANCED WLAN ASSOCIATION FOR ROAMING
MP1556	MP1556C1	US	13/769,614	2/18/2013	9226335	12/29/2015	In Force	ENHANCED WLAN ASSOCIATION FOR ROAMING
MP1557	MP1557	US	11/983,856	11/13/2007	7739568	6/15/2010	In Force	Scan Testing System For Circuits Under Test
MP1558	MP1558	US	11/932,126	10/31/2007	9128722	11/3/2015	In Force	ROAMING METHOD AND APPARATUS FOR VOIP HANDSET
MP1559	MP1559	US	11/940,802	11/15/2007	8175262	5/8/2012	In Force	PER TONE COPROCESSOR ARCHITECTURE
MP1560	MP1560	US	11/940,805	11/15/2007	8437244	5/7/2013	In Force	CROSSTALK CANCELLER INITIALIZATION
MP1560	MP1560C1	US	13/888,188	5/6/2013	8948505	9/30/2014	In Force	CROSSTALK CANCELLER INITIALIZATION
MP1564	MP1564	US	11/936,631	11/7/2007	8788641	7/22/2014	In Force	SMALL FORM FACTOR INTERFACE MODULE
MP1568	MP1568	US	11/944,093	11/21/2007	8045456	10/25/2011	In Force	HIERARCHICAL PORT-BASED RATE LIMITING
MP1568	MP1568C1	US	13/278,916	10/21/2011	8493847	7/23/2013	In Force	HIERARCHICAL PORT-BASED RATE LIMITING
MP1569	MP1569	US	12/025,387	2/4/2008	8345702	1/1/2013	In Force	Method And Apparatus For Flexible Interface Bypass Options In Switc
MP1569	MP1569C1	US	13/730,478	12/28/2012	8885654	11/11/2014	In Force	Method And Apparatus For Flexible Interface Bypass Options In Switc
MP1569T	MP1569TW	TW	97104920	2/12/2008	1434554	4/11/2014	In Force	Method And Apparatus For Flexible Interface Bypass Options In Switc
MP1569W	MP1569WO	WO	PCT/US2008/00	2/7/2008			Pending	Method And Apparatus For Flexible Interface Bypass Options In Switc

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MP1570	MP1570	US	12/012,196	1/31/2008	8379559	2/19/2013	In Force	Wireless Multicast Proxy
MP1570	MP1570C1	US	13/769,696	2/18/2013	8937898	1/20/2015	In Force	Wireless Multicast Proxy
MP1570W	MP1570WQ	WO	PCT/US2008/00	2/7/2008			Pending	Wireless Multicast Proxy
MP1570W	MP1570WOCN	CN	2008800044843	2/7/2008	ZL2008800044	11/14/2012	In Force	WIRELESS MULTICAST PROXY
MP1570W	MP1570WCEP	EP	08725275.5	2/7/2008	EP2127206	5/4/2011	In Force	WIRELESS MULTICAST PROXY
MP1570W	MP1570WQJP	JP	2009-549101	2/7/2008	5176121	1/18/2013	In Force	
MP1570W	MP1570WUN	DE	08725275.5	2/7/2008	2127206	5/4/2011	In Force	
MP1570W	MP1570WUR	FR	08725275.5	2/7/2008	2127206	5/4/2011	In Force	
MP1570W	MP1570WUT	GB	08725275.5	2/7/2008	2127206	5/4/2011	In Force	Wireless Multicast Proxy
MP1571	MP1571	US	11/945,165	11/26/2007	7974299	7/5/2011	In Force	METHODS AND APPARATUS FOR SWITCHING TRANSMISSION CHANNELS
MP1571	MP1571C1	US	13/175,527	7/1/2011	8542698	9/24/2013	In Force	METHODS AND APPARATUS FOR SWITCHING TRANSMISSION CHANNELS
MP1571	MP1571C1C1	US	14/031,974	9/19/2013	8861541	10/14/2014	In Force	METHODS AND APPARATUS FOR SCANNING TRANSMISSION CHANNELS
MP1572	MP1572C1	US	14/076,811	11/11/2013	9137286	9/15/2015	In Force	STREAMING TRAFFIC CLASSIFICATION METHOD AND APPARATUS
MP1575	MP1575C1	US	13/023,164	2/8/2011	8116178	2/14/2012	In Force	Servo Accelerator System For Optical Drives
MP1576	MP1576	US	11/945,151	11/26/2007	8411709	4/2/2013	In Force	Use of previously buffered state information to decode in a hybrid a
MP1576	MP1576C1	US	13/836,896	3/15/2013	8948216	2/3/2015	In Force	Use of previously buffered state information to Decode in an Hybrid
MP1578	MP1578	US	11/944,980	11/26/2007	7848263	12/7/2010	In Force	SIMPLIFIED AUTO-CONFIGURATION AND SERVICE DISCOVERY IN AD-
MP1578	MP1578C1	US	12/952,061	11/22/2010	8755306	6/7/2014	In Force	Simplified Auto-Configuration and Service Discovery in 802.11 Ad-Ho
MP1578	MP1578C1a8	US	14/967,942	12/14/2015	9585087	2/28/2017	In Force	METHOD AND APPARATUS FOR PERMITTING A DEVICE TO AUTOMAT
MP1578	MP1578C1C1	US	14/305,526	6/16/2014	9237067	1/12/2016	In Force	METHOD AND APPARATUS FOR PERMITTING A DEVICE TO AUTOMAT
MP1578	MP1578CN	CN	2007800422871	11/27/2007	ZL2007800422	4/7/2013	In Force	SIMPLIFIED AUTO-CONFIGURATION AND SERVICE DISCOVERY IN AD-
MP1578	MP1578CNC1	CN	2012103533174	11/27/2007	ZL2012103533	8/3/2016	In Force	SIMPLIFIED AUTO-CONFIGURATION AND SERVICE DISCOVERY IN AD-
MP1578E	MP1578EP	EP	EP07870904.5	11/27/2007	EP2098044	3/24/2010	In Force	Simplified Auto-Configuration and Service Discovery in Ad-Hoc Netw
MP1578E	MP1578EPDE	DE	07870904.5	11/27/2007	2098044	3/24/2010	In Force	SIMPLIFIED AUTO-CONFIGURATION AND SERVICE DISCOVERY IN AD-
MP1578E	MP1578EPRR	FR	07870904.5	11/27/2007	2098044	3/24/2010	In Force	SIMPLIFIED AUTO-CONFIGURATION AND SERVICE DISCOVERY IN AD-
MP1578E	MP1578EPGB	GB	07870904.5	11/27/2007	2098044	3/24/2010	In Force	SIMPLIFIED AUTO-CONFIGURATION AND SERVICE DISCOVERY IN AD-
MP1578T	MP1578TW	TW	96145222	11/28/2007	1452874	9/11/2014	In Force	SIMPLIFIED AUTO-CONFIGURATION AND SERVICE DISCOVERY IN AD-
MP1578W	MP1578WO	WO	PCT/US2007/02	11/27/2007			Pending	SIMPLIFIED AUTO-CONFIGURATION AND SERVICE DISCOVERY IN AD-
MP1579	MP1579	US	11/944,103	11/21/2007	9332496	5/3/2016	In Force	Enhanced IEEE Power Save in Ad Hoc Wireless Mode
MP1579T	MP1579TW	TW	96145222	11/28/2007	1469556	1/11/2015	In Force	METHOD FOR SAVING POWER BY A DEVICE IN COMMUNICATION WI
MP1580	MP1580	US	11/946,707	11/28/2007	8744023	6/3/2014	In Force	INTERFERENCE WHITENING FILTERS FOR MINO MAXIMUM LIKEIHO
MP1580	MP1580D1	US	14/275,287	5/12/2014	9014320	4/21/2015	In Force	INTERFERENCE WHITENING FILTERS FOR MINO MAXIMUM LIKEIHO
MP1581	MP1581	US	11/996,872	11/27/2007	8180961	5/29/2012	In Force	System And Method For Using Pilot Signals in Non-Volatile Memory
MP1581	MP1581C1	US	13/480,040	5/24/2012	8429488	4/23/2013	In Force	System And Method For Using Pilot Signals in Non-Volatile Memory

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MP1581	MP1581C1aa	US	14/158,219	1/17/2014	8874997	10/28/2014	In Force	System And Method For Combining Pilot Data With User Data Using
MP1581	MP1581C1C1	US	13/867,609	4/22/2013	8635507	1/21/2014	In Force	System And Method For Using Pilot Signals In Non-Volatile Memory
MP1582	MP1582	US	11/939,891	11/14/2007	8046776	10/25/2011	In Force	Method And Apparatus For Transferring Firmware Between An Opera
MP1582	MP1582C1	US	13/280,017	10/24/2011	8261257	9/4/2012	In Force	Method And Apparatus For Transferring Firmware Between An Opera
MP1583	MP1583	US	11/943,793	11/21/2007	8294248	10/23/2012	In Force	CHIP ON LEADS
MP1583	MP1583CN	CN	200780050697	11/26/2007	212007800506	12/12/2012	In Force	CHIP ON LEADS
MP1583T	MP1583TW	TW	96145395	11/29/2007	1446563	7/21/2014	In Force	CHIP ON LEADS
MP1583W	MP1583WO	WO	PCT/US2007/08	11/26/2007			Pending	CHIP ON LEADS
MP1586	MP1586	US	12/121,581	5/15/2008	8152372	4/10/2012	In Force	METHODS FOR MONITORING CHIP TEMPERATURE DURING TEST
MP1588	MP1588	US	11/937,350	11/8/2007	7596012	9/29/2009	In Force	WRITE-ASSIST AND POWER-DOWN CIRCUIT FOR LOW POWER SRAM
MP1588	MP1588C1	US	12/547,182	8/25/2009	7835217	11/16/2010	In Force	WRITE-ASSIST AND POWER-DOWN CIRCUIT FOR LOW POWER SRAM
MP1588	MP1588C1AA	US	13/673,666	11/9/2012	8582387	11/12/2013	In Force	METHOD AND APPARATUS FOR SUPPLYING POWER TO A STATIC RAM
MP1588	MP1588C1C1	US	12/946,534	11/15/2010	8310894	11/13/2012	In Force	WRITE-ASSIST AND POWER-DOWN CIRCUIT FOR LOW POWER SRAM
MP1589	MP1589	US	11/950,168	12/4/2007	8664915	3/4/2014	In Force	Plug-In Vehicle
MP1589	MP1589C1	US	14/196,399	3/4/2014	9187005	11/17/2015	In Force	System And Method For Charging Batteries Of Vehicles And Returnin
MP1589T	MP1589TW	TW	96146614	12/6/2007	1462431	11/21/2014	In Force	Plug-In Vehicle
MP1589W	MP1589WO	WO	PCT/US2007/02	12/6/2007			Pending	Plug-In Vehicle
MP1590	MP1590	US	11/999,416	12/5/2007	7773334	8/10/2010	In Force	Single-Pass Spiral Self-Servo-Write
MP1590	MP1590C1	US	12/853,328	8/10/2010	8031428	10/4/2011	In Force	Single-Pass Spiral Self-Servo-Write
MP1595	MP1595	US	11/949,424	12/3/2007	7639057	12/29/2009	In Force	Clock Gater System
MP1595	MP1595C1	US	12/645,773	12/23/2009	7990199	8/2/2011	In Force	CLOCK GATER SYSTEM
MP1596	MP1596	US	11/953,552	12/10/2007	7827323	11/2/2010	In Force	System And Method For Peripheral Device Communications
MP1596	MP1596C1	US	12/917,390	11/1/2010	8127053	2/28/2012	In Force	System And Method For Peripheral Device Communications
MP1596	MP1596C1C1	US	13/406,007	2/27/2012	8386666	2/26/2013	In Force	System And Method For Peripheral Device Communications
MP1596W	MP1596WO	WO	PCT/IB2007/003	12/10/2007			Pending	System And Method For Peripheral Device Communications
MP1597T	MP1597TW	TW	96147279	12/11/2007			Pending	HYBRID N/A-VOLATILE SOLID STATE MEMORY SYSTEM
MP1597W	MP1597WO	WO	PCT/US2007/02	12/11/2007			Pending	Hybrid Non-Volatile Solid State Memory System
MP1597W	MP1597WOCN	CN	2007800458732	12/11/2007			Pending	Hybrid Non-Volatile Solid State Memory System
MP1597W	MP1597WODE	DE	1120070030364	12/11/2007			Pending	Hybrid Non-Volatile Solid State Memory System
MP1597W	MP1597WQJP	JP	2009-540332	12/11/2007			Pending	HYBRID NON-VOLATILE SOLID STATE MEMORY SYSTEM
MP1599	MP1599	US	12/001,357	12/11/2007	7551028	6/23/2009	In Force	Signal Generators For Current-Mode Three-Level Digital Amplifiers
MP1600	MP1600	US	11/942,601	11/19/2007	7724161	5/25/2010	In Force	TRUNCATION FOR THREE-LEVEL DIGITAL AMPLIFIER
MP1601	MP1601	US	11/955,088	12/12/2007	8036377	10/11/2011	In Force	METHOD AND APPARATUS OF HIGH SPEED ENCRYPTION AND DECRY
MP1601	MP1601C1	US	13/267,988	10/7/2011	8494155	7/23/2013	In Force	METHOD AND APPARATUS OF HIGH SPEED ENCRYPTION AND DECRY

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP1602	MP1602	US	11/955,223	12/12/2007	9064135	6/23/2015	In Force	Hardware Implemented Key Management System and Method
MP1603	MP1603	US	11/999,482	12/5/2007	8224272	7/17/2012	In Force	SYSTEM FOR SIGNAL METRIC BASED RECEIVE SELECTION
MP1603	MP1603C1	US	13/549,887	7/16/2012	8483634	7/9/2013	In Force	SYSTEM FOR SIGNAL METRIC BASED RECEIVE SELECTION
MP1604	MP1604	US	11/994,823	12/12/2007	8301976	10/30/2012	In Force	Defect Detector For Holographic Storage Systems
MP1606	MP1606	US	11/999,846	12/6/2007	8144630	3/27/2012	In Force	APPARATUS, SYSTEMS, METHODS, ALGORITHMS AND SOFTWARE FO
MP1607	MP1607	US	12/001,107	12/10/2007	8046571	10/25/2011	In Force	System-On-A-Chip (SOC) Security Using One-Time Programmable M
MP1607	MP1607D1	US	13/279,832	10/24/2011	8285980	10/9/2012	In Force	System-On-A-Chip (SOC) Security Using One-Time Programmable M
MP1607	MP1607D1aa	US	14/028,819	9/17/2013	8751786	6/10/2014	In Force	METHOD AND INTEGRATED CIRCUIT FOR LOADING AND EXECUTING
MP1607	MP1607D1C1	US	13/647,045	10/8/2012	8559216	9/17/2013	In Force	System-On-A-Chip (SOC) Security Using One-Time Programmable M
MP1608	MP1608	US	12/002,121	12/14/2007	7839672	11/23/2010	In Force	Phase Change Memory Array Circuits And Methods of Manufacture
MP1608	MP1608C1	US	12/952,613	11/23/2010	7952920	5/31/2011	In Force	Phase Change Memory Array Circuits And Methods of Manufacture
MP1610	MP1610	US	11/957,213	12/14/2007	8144358	3/27/2012	In Force	PROVIDING DEMONSTRATION PAGE INFORMATION IN AN IMAGING
MP1610	MP1610C1	US	13/424,188	3/19/2012	8355157	1/15/2013	In Force	METHOD AND APPARATUS FOR STORING DEMONSTRATION PAGE IN
MP1611	MP1611	US	11/958,092	12/17/2007	8289599	10/16/2012	In Force	MECHANICAL GALVANOMETER TILT CORRECTION
MP1612	MP1612	US	11/956,958	12/14/2007	7943896	5/17/2011	In Force	MECHANICAL GALVANOMETER TILT CORRECTION CONTROL
MP1612	MP1612C1	US	13/098,554	5/2/2011	8237103	8/7/2012	In Force	MECHANICAL GALVANOMETER TILT CORRECTION CONTROL
MP1613	MP1613	US	11/964,696	12/26/2007	9466596	10/11/2016	In Force	GEOMETRY OF MOS DEVICE WITH LOW ON-RESISTANCE
MP1613	MP1613CN	CN	200780051801	12/26/2007	212007800519	7/4/2012	In Force	GEOMETRY OF MOS DEVICE WITH LOW ON-RESISTANCE
MP1613	MP1613CND1	CN	201210147239	12/26/2007	212012101472	9/16/2015	In Force	GEOMETRY OF MOS DEVICE WITH LOW ON-RESISTANCE
MP1613E	MP1613EP	EP	07866042.0	12/26/2007	2100334	4/13/2016	In Force	GEOMETRY OF MOS DEVICE WITH LOW ON-RESISTANCE
MP1613E	MP1613EPDE	DE	07866042.0	12/26/2007	602007045885	4/13/2016	In Force	GEOMETRY OF MOS DEVICE WITH LOW ON-RESISTANCE
MP1613E	MP1613EPFR	FR	07866042.0	12/26/2007	2100334	4/13/2016	In Force	GEOMETRY OF MOS DEVICE WITH LOW ON-RESISTANCE
MP1613E	MP1613EPGB	GB	07866042.0	12/26/2007	2100334	4/13/2016	In Force	GEOMETRY OF MOS DEVICE WITH LOW ON-RESISTANCE
MP1613J	MP1613JP	JP	2009-544252	12/26/2007	5360829	9/13/2013	In Force	GEOMETRY OF MOS DEVICE WITH LOW ON-RESISTANCE
MP1613J	MP1613JPD1	JP	2013-098733	5/8/2013	5726230	4/10/2015	In Force	
MP1613T	MP1613TW	TW	96150732	12/28/2007	1456758	10/11/2014	In Force	GEOMETRY OF MOS DEVICE WITH LOW ON-RESISTANCE
MP1613W	MP1613WO	WO	PCT/US2007/08	12/26/2007			Pending	GEOMETRY OF MOS DEVICE WITH LOW ON-RESISTANCE
MP1614	MP1614	US	11/957,254	12/14/2007	8112220	2/21/2012	In Force	MEMORY USAGE IN IMAGING DEVICES
MP1614	MP1614C1	US	13/397,469	2/15/2012	8230197	7/24/2012	In Force	MEMORY USAGE IN IMAGING DEVICES
MP1614	MP1614C1aa	US	13/945,744	7/18/2013	9159005	10/13/2015	In Force	Method and Apparatus for Optimizing Memory Usage in an Imaging
MP1614	MP1614C1C1	US	13/549,026	7/13/2012	8495333	7/23/2013	In Force	METHOD AND APPARATUS FOR OPTIMIZING MEMORY USAGE IN AN
MP1615	MP1615	US	11/957,269	12/14/2007	7710777	5/4/2010	In Force	SEMI-VOLATILE NAND FLASH MEMORY
MP1615	MP1615aaaa	US	13/868,119	5/6/2013	8793609	6/3/2014	In Force	METHOD AND APPARATUS FOR INCREASING DATA RETENTION CAPA
MP1615	MP1615C1	US	12/772,819	5/3/2010	8000142	8/16/2011	In Force	SEMI-VOLATILE NAND FLASH MEMORY

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MP1615	MP1615C1AA	US	13/397,504	2/15/2012	8437189	5/7/2013	In Force	METHOD AND APPARATUS FOR STORING DATA IN A NAND FLASH ME
MP1615	MP1615C1C1	US	13/207,277	8/10/2011	8125969	2/21/2012	In Force	SEMI-VOLATILE NAND FLASH MEMORY
MP1615	MP1615Z2a	US	14/278,932	5/15/2014	9099182	8/4/2015	In Force	METHOD AND APPARATUS FOR SELECTIVE USE OF WEAR-LEVELING T
MP1616	MP1616	US	11/962,071	12/20/2007	8046601	10/25/2011	In Force	CLOSED LOOP VOLTAGE CONTROL USING ADJUSTABLE DELAY LINES
MP1616	MP1616RED1	US	14/480,075	9/8/2014	RE46782	4/10/2018	In Force	CLOSED LOOP VOLTAGE CONTROL USING ADJUSTABLE DELAY LINES
MP1617	MP1617C1	US	12/897,572	10/4/2010	8131877	3/6/2012	In Force	Method for Flexible Modifications to a Packet
MP1618	MP1618	US	11/949,544	12/3/2007	8144997	3/27/2012	In Force	METHOD FOR ENHANCED IMAGE DECODING
MP1618	MP1618D1	US	13/427,113	3/22/2012	8563802	8/6/2013	In Force	Method and Apparatus for Processing an Image
MP1619	MP1619C1	US	12/731,754	3/25/2010	8072275	12/6/2011	In Force	DIGITAL RING OSCILLATOR
MP1620	MP1620	US	11/964,311	12/26/2007	7576666	8/18/2009	In Force	Jitter Correction
MP1621	MP1621	US	12/001,457	12/11/2007	9509512	11/29/2016	In Force	Message Digest Generator
MP1622	MP1622	US	11/957,245	12/14/2007	7899110	3/1/2011	In Force	BIT SYNC FOR RECEIVER WITH MULTIPLE ANTENNAS
MP1622	MP1622C1	US	13/031,867	2/22/2011	8369387	2/5/2013	In Force	BIT SYNC FOR RECEIVER WITH MULTIPLE ANTENNAS
MP1622	MP1622C1aa	US	14/231,306	3/31/2014	8948231	2/3/2015	In Force	DETERMINING TIMING INFORMATION IN A RECEIVER
MP1623	MP1623	US	11/951,725	12/6/2007	8254514	8/28/2012	In Force	EARLY-LATE GATE TIMING RECOVERY
MP1624	MP1624	US	11/963,294	12/21/2007	7970066	6/28/2011	In Force	Tracking Automatic Gain Control Of Orthogonal Frequency Domain
MP1624	MP1624C1	US	13/169,691	6/27/2011	8254503	8/28/2012	In Force	Tracking Automatic Gain Control Of Orthogonal Frequency Domain
MP1624	MP1624C1aa	US	13/937,752	7/22/2013	8831148	9/9/2014	In Force	Tracking Automatic Gain Control Of Orthogonal Frequency Domain
MP1624	MP1624C1C1	US	13/596,513	8/28/2012	8494097	7/23/2013	In Force	Tracking Automatic Gain Control Of Orthogonal Frequency Domain
MP1625	MP1625	US	11/949,487	12/3/2007	8015224	9/6/2011	In Force	ENTROPY SOURCE FOR RANDOM NUMBER GENERATION
MP1626	MP1626	US	12/006,085	12/28/2007	7877101	1/25/2011	In Force	Locating A WLAN Station Using Signal Propagation Delay
MP1626	MP1626aaa	US	13/741,607	1/15/2013	8682351	3/25/2014	In Force	Method And Apparatus For Locating A WLAN Station Based On A Pro
MP1626	MP1626aaaa	US	14/224,342	3/25/2014	9046596	6/2/2015	In Force	Systems And Methods For Determining A Distance Between A First D
MP1626	MP1626C1	US	13/013,500	1/25/2011	7996020	8/9/2011	In Force	Locating A WLAN Station Using Signal Propagation Delay
MP1626	MP1626C1C1	US	13/204,262	8/5/2011	8355739	1/15/2013	In Force	Method and Apparatus For Locating A WLAN Station Based On A Pro
MP1629	MP1629	US	12/002,610	12/18/2007	8005155	8/23/2011	In Force	Frame Synchronization In Orthogonal Frequency-Division Multiplexin
MP1629	MP1629C1	US	13/214,766	8/22/2011	8160169	4/17/2012	In Force	Frame Synchronization In Orthogonal Frequency-Division Multiplexin
MP1629	MP1629C1aa	US	13/872,740	4/29/2013	8711990	4/29/2014	In Force	Frame Synchronization In Orthogonal Frequency-Division Multiplexin
MP1629	MP1629C1C1	US	13/447,738	4/16/2012	8433003	4/30/2013	In Force	Frame Synchronization In Orthogonal Frequency-Division Multiplexin
MP1630	MP1630	US	11/955,005	12/12/2007	8583981	11/12/2013	In Force	CONCATENATED CODES FOR HOLOGRAPHIC STORAGE
MP1630	MP1630P	JP	2007-334466	12/26/2007	5055578	8/10/2012	In Force	CONCATENATED CODES FOR HOLOGRAPHIC STORAGE
MP1631	MP1631C1D1	US	13/090,623	4/20/2011	8166216	4/24/2012	In Force	Floating Frame Timing Circuits For Network Devices
MP1632	MP1632	US	11/951,251	12/5/2007	9411431	8/9/2016	In Force	TRACKING A POSITION IN RELATION TO A SURFACE
MP1632T	MP1632TW	TW	96149708	12/24/2007	1436027	5/1/2014	In Force	TRACKING A POSITION IN RELATION TO A SURFACE

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MP1634	MP1634	US	11/955,209	12/12/2007	8226194	7/24/2012	In Force	PRINTING ON PLANAR OR NON-PLANAR PRINT SURFACE WITH HAND
MP1635	MP1635	US	11/968,558	1/2/2008	8043015	10/25/2011	In Force	DETECTING EDGE OF A PRINT MEDIUM WITH A HANDHELD IMAGE T
MP1636	MP1636	US	11/966,009	12/28/2007	7978541	7/12/2011	In Force	High Speed Interface For Multi-Level Memory
MP1636	MP1636D1	US	13/179,827	7/11/2011	8432753	4/30/2013	In Force	High Speed Interface For Multi-Level Memory
MP1636	MP1636D1C1	US	13/872,729	4/29/2013	8638619	1/28/2014	In Force	High Speed Interface For Multi-Level Memory
MP1636T	MP1636TW	TW	96151646	12/31/2007	1453755	9/21/2014	In Force	High Speed Interface For Multi-Level Memory
MP1636W	MP1636WO	WO	PCT/US2007/02	12/28/2007			Pending	High Speed Interface For Multi-Level Memory
MP1638	MP1638	US	11/966,831	12/28/2007	8073471	12/6/2011	In Force	SAVING POWER IN WIRELESS COMMUNICATION DEVICES
MP1638	MP1638C1	US	13/310,507	12/2/2011	8380229	2/19/2013	In Force	SAVING POWER IN WIRELESS COMMUNICATION DEVICES
MP1639	MP1639	US	11/988,528	2/1/2008	8077343	12/13/2011	In Force	DETERMINING END OF PRINT JOB IN HANDHELD IMAGE TRANSLATIO
MP1639	MP1639C1	US	13/323,740	12/12/2011	8462379	6/11/2013	In Force	DETERMINING END OF PRINT JOB IN HANDHELD IMAGE TRANSLATIO
MP1640	MP1640C1	US	13/113,248	5/23/2011	8229503	7/24/2012	In Force	SCANNER FOR A MOBILE DEVICE
MP1640	MP1640C1C1	US	13/556,104	7/23/2012	8423083	4/16/2013	In Force	HANDHELD SCANNING DEVICE
MP1641	MP1641	US	11/955,240	12/12/2007	8006740	8/16/2011	In Force	IMAGE TRANSLATION DEVICE FOR A MOBILE DEVICE
MP1642	MP1642C1	US	14/159,343	1/20/2014	9205671	12/8/2015	In Force	PRINTER FOR A MOBILE DEVICE
MP1644	MP1644C1	US	14/444,191	7/28/2014	9213512	12/15/2015	In Force	STORING AND REMOVING DEVICE DRIVERS IN MEMORY IN IMAGING
MP1646	MP1646	US	11/970,443	1/7/2008	7953382	5/31/2011	In Force	CLEAN SPOT DETECTION FOR FM TRANSMISSION
MP1646	MP1646C1	US	13/117,460	5/27/2011	8330860	11/27/2012	In Force	CLEAN SPOT DETECTION FOR FM TRANSMISSION
MP1646	MP16461	US	12/638,879	12/15/2009	8509687	8/13/2013	In Force	QUIET SPOT DETECTION FOR FM TRANSMISSION
MP1647	MP1647	US	11/970,133	1/7/2008	8116706	2/14/2012	In Force	METHOD AND APPARATUS FOR CALIBRATING A BANDPASS FILTER
MP1648	MP1648	US	11/968,556	1/2/2008	8627167	1/7/2014	In Force	METHODS AND APPARATUS FOR PROVIDING MULTI-LAYERED CODIN
MP1648	MP1648C1	US	14/136,101	12/20/2013	8910013	12/9/2014	In Force	METHODS AND APPARATUS FOR PROVIDING MULTI-LAYERED CODIN
MP1651	MP1651	US	11/970,964	1/8/2008	8286059	10/9/2012	In Force	WORD-SERIAL CYCLIC CODE ENCODER
MP1654	MP1654	US	12/008,339	1/10/2008	7711873	5/4/2010	In Force	Bandwidth Control And Power Saving By Interface Aggregation
MP1655	MP1655	US	11/972,567	10/1/2008	7793175	7/9/2010	In Force	AUTOMATED SCAN TESTING OF DDR SDRAM
MP1655	MP1655C1	US	12/876,654	9/7/2010	7971111	6/28/2011	In Force	AUTOMATED SCAN TESTING OF DDR SDRAM
MP1655	MP1655C111	US	13/114,981	5/24/2011	8522090	8/27/2013	In Force	Automated Scan Testing of a System-on-Chip (SoC)
MP1656	MP1656	US	11/956,088	12/13/2007	7962775	6/14/2011	In Force	METHODS AND APPARATUS FOR POWER MODE CONTROL FOR PDA
MP1656	MP1656C1	US	13/157,456	6/10/2011	8826052	9/2/2014	In Force	METHODS AND APPARATUS FOR POWER MODE CONTROL FOR PDA
MP1657	MP1657CN	CN	2008800020681	1/10/2008	212008800020	3/13/2013	In Force	DISCERNING BETWEEN ORIGINAL MEDIA AND BLANK MEDIA IN A CO
MP1657E	MP1657EP	EP	08713707.1	1/10/2008			Pending	DISCERNING BETWEEN ORIGINAL MEDIA AND BLANK MEDIA IN A CO
MP1657J	MP1657JP	JP	2009-545685	1/10/2008			Pending	DISCERNING BETWEEN ORIGINAL MEDIA AND BLANK MEDIA IN A CO
MP1657W	MP1657WO	WO	PCT/US2608/05	1/10/2008			Pending	Discerning Between Original Media and Blank Media in a Common Fe
MP1659	MP1659	US	11/972,462	1/10/2008	8472066	6/25/2013	In Force	USAGE MAPS IN IMAGE DEPOSITION DEVICES

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP1660	MP1660	US	12/013,313	1/11/2008	8342627	1/1/2013	In Force	ADAPTIVE FILTERING SCHEME IN HANDHELD POSITIONING DEVICE
MP1661	MP1661	US	11/967,768	12/31/2007	8869152	10/21/2014	In Force	Methods And Procedures To Dynamically Adjust Processor Frequency
MP1662	MP1662	US	12/013,118	1/11/2008	10082846	9/25/2018	In Force	Temperature Sensing System
MP1662T	MP1662TW	TW	97101225	1/11/2008	1434032	4/11/2014	In Force	Temperature Sensing System
MP1662W	MP1662WO	WO	PCT/US2008/00	1/11/2008			Pending	Temperature Sensing System
MP1662W	MP1662WOCN	CN	200880002160	1/11/2008			Pending	TEMPERATURE SENSING SYSTEM
MP1664	MP1664	US	11/951,062	12/5/2007	8312341	11/13/2012	In Force	INTERLEAVED ERROR CORRECTION CODING FOR CHANNELS WITH N
MP1664	MP1664D1	US	13/673,804	11/9/2012	8458557	6/4/2013	In Force	INTERLEAVED ERROR CORRECTION CODING FOR CHANNELS WITH N
MP1665	MP1665	US	12/132,422	6/3/2008	8209580	6/26/2012	In Force	ERROR CORRECTION CODING FOR VARYING SIGNAL-TO-NOISE RATIO
MP1666	MP1666	US	11/925,605	10/26/2007	7849345	12/7/2010	In Force	DDR CONTROL
MP1667	MP1667	US	12/008,963	1/15/2008	8160045	4/17/2012	In Force	Beacon Miss Prevention in Power Save Modes Using Timing Synchron
MP1667	MP1667C1	US	13/447,437	4/16/2012	8358642	1/22/2013	In Force	Beacon Miss Prevention in Power Save Modes Using Timing Synchron
MP1667	MP1667C1C1	US	13/747,179	1/22/2013	8976721	3/10/2015	In Force	Beacon Miss Prevention in Power Save Modes Using Timing Synchron
MP1668	MP1668	US	12/014,556	1/15/2008	8042027	10/18/2011	In Force	BM/LLR COMPUTATION FOR MULTI-DIMENSIONAL TCM/BCM
MP1668	MP1668C1	US	13/274,943	10/17/2011	8327244	12/4/2012	In Force	BM/LLR COMPUTATIONS FOR MULTI-DIMENSIONAL TCM/BCM CODE
MP1668	MP1668C1C1	US	13/666,323	11/27/2012	8612837	12/17/2013	In Force	BM/LLR COMPUTATIONS FOR MULTI-DIMENSIONAL TCM/BCM CODE
MP1670	MP1670	US	12/015,159	1/16/2008	8051363	11/1/2011	In Force	ABSORB DECODE ALGORITHM FOR 10GBASE-T L0PC DECODER
MP1670	MP1670C1	US	13/769,446	10/7/2011	8361069	2/19/2013	In Force	ABSORB DECODE ALGORITHM FOR 10GBASE-T L0PC DECODER
MP1670	MP1670C1C1	US	13/738,325	1/10/2013	8495462	7/23/2013	In Force	ABSORB DECODE ALGORITHM FOR 10GBASE-T L0PC DECODER
MP1671	MP1671	US	12/015,913	1/17/2008	8051365	11/1/2011	In Force	VITERBI DECODER METHOD AND APPARATUS WITH RI DETECTOR IN
MP1671	MP1671aaaa	US	14/244,557	4/3/2014	9037955	5/19/2015	In Force	VITERBI DECODER METHOD AND APPARATUS WITH RI DETECTOR IN
MP1671	MP1671C1	US	13/282,910	10/27/2011	8261172	9/4/2012	In Force	VITERBI DECODER METHOD AND APPARATUS WITH RI DETECTOR IN
MP1671	MP1671C1aa	US	13/925,581	6/24/2013	8767147	4/22/2014	In Force	VITERBI DECODER METHOD AND APPARATUS WITH RI DETECTOR IN
MP1671	MP1671C1C1	US	13/601,404	8/31/2012	8473830	6/25/2013	In Force	VITERBI DECODER METHOD AND APPARATUS WITH RI DETECTOR IN
MP1673	MP1673	US	12/016,833	1/18/2008	8396654	3/12/2013	In Force	SENSOR POSITIONING IN HANDHELD IMAGE TRANSLATION DEVICE
MP1674	MP1674	US	12/022,046	1/29/2008	8174944	5/8/2012	In Force	WRITE STRATEGY CALIBRATION FOR OPTICAL DRIVES
MP1674	MP1674C1	US	13/456,877	4/26/2012	8488424	7/16/2013	In Force	WRITE STRATEGY CALIBRATION FOR OPTICAL DRIVES
MP1675	MP1675	US	12/015,132	1/16/2008	8314961	11/20/2012	In Force	METHOD AND APPARATUS FOR PRINTING RASTER PAGE
MP1677	MP1677	US	11/970,965	1/8/2008	8045612	10/25/2011	In Force	FAST INVERSE INTEGER TRANSFORM FOR VIDEO DECODING
MP1678	MP1678	US	12/055,470	3/26/2008	7888834	10/5/2010	In Force	Incremental Memory Refresh
MP1678	MP1678C1	US	12/893,542	9/29/2010	8031531	10/4/2011	In Force	Incremental Memory Refresh
MP1678	MP1678C1aa	US	13/682,347	11/20/2012	8644080	2/4/2014	In Force	Incremental Memory Refresh
MP1678	MP1678C1C1	US	13/251,885	10/3/2011	8315106	11/20/2012	In Force	Incremental Memory Refresh
MP1679	MP1679	US	12/018,933	1/24/2008	8130872	3/6/2012	In Force	Frequency And Q-Factor Tunable Filters Using Frequency Translatable

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MP1679	MP1679D1	US	13/412,753	3/6/2012	8565349	10/22/2013	In Force	Frequency And Q-Factor Tunable Filters Using Frequency Translatable
MP1679	MP1679D1C1	US	14/060,119	10/22/2013	8787511	7/22/2014	In Force	Frequency And Q-Factor Tunable Filters Using Frequency Translatable
MP1679T	MP1679TW	TW	97102736	1/24/2008	1439059	5/21/2014	In Force	Frequency And Q-Factor Tunable Filters Using Frequency Translatable
MP1679T	MP1679TWD1	TW	97102738	1/24/2008	1469539	1/11/2015	In Force	FREQUENCY AND Q-FACTOR TUNABLE FILTERS USING FREQUENCY TR
MP1679W	MP1679WO	WO	PCT/US2008/00	1/24/2008			Pending	Frequency And Q-Factor Tunable Filters Using Frequency Translation
MP1680	MP1680	US	12/018,163	1/22/2008	8301189	10/30/2012	In Force	Method And Apparatus For Reducing Power Consumption In A Multi
MP1680	MP1680C1	US	13/661,577	10/26/2012	8588847	11/19/2013	In Force	Method And Apparatus For Reducing Power Consumption In A Multi
MP1681	MP1681CN	CN	200880062945J	1/22/2008			Pending	METHOD AND APPARATUS FOR LOW POWER REFRESH OF A DISPLAY
MP1681	MP1681CND1	CN	201110454970	1/22/2008			Pending	METHOD AND APPARATUS FOR LOW POWER REFRESH OF A DISPLAY
MP1681T	MP1681TW	TW	97102560	1/23/2008	1428871	3/1/2014	In Force	Method and Apparatus for Low Power Refresh of a Display Device
MP1684	MP1684	US	12/017,872	1/22/2008	7744184	6/29/2010	In Force	Mechanical Dithering Of Printing Mechanisms
MP1684W	MP1684WO	WO	PCT/US2008/00	1/23/2008			Pending	Mechanical Dithering Of Printing Mechanisms
MP1685	MP1685	US	12/018,752	1/23/2008	9114635	8/25/2015	In Force	DUAL PRINT HEAD ARRANGEMENT
MP1685W	MP1685WO	WO	PCT/US2008/00	1/23/2008			Pending	Dual Print Head Arrangement
MP1686	MP1686	US	11/971,612	1/9/2008	8345673	1/1/2013	In Force	Physical-Layer Device (PHY) Having A Serial Interface And A Magic Pa
MP1686	MP1686C1	US	13/730,085	12/28/2012	8837468	9/16/2014	In Force	Network Device And Method For Powering A Media Access Controlle
MP1688	MP1688	US	12/009,725	1/22/2008	8442011	5/14/2013	In Force	SUPPORT OF HIERARCHICAL NETWORK MOBILITY FOR PROXY MOBIL
MP1688	MP1688C1	US	13/893,849	5/14/2013	9119137	8/25/2015	In Force	Support Of Hierarchical Network Mobility For Proxy Mobile IP
MP1688	MP1688C1C1	US	14/833,828	8/24/2015	9344958	5/17/2016	In Force	System And Method For Providing Connectivity Between A Visited Ne
MP1689	MP1689	US	12/101,706	4/11/2008	8161482	4/17/2012	In Force	Power Optimization for Multi-Core Devices
MP1689	MP1689C1	US	13/495,264	3/30/2012	8566832	10/22/2013	In Force	POWER OPTIMIZATION FOR MULTI-CORE DEVICES HAVING A SHARE
MP1691	MP1691	US	12/020,327	1/25/2008	8360740	10/30/2012	In Force	METHOD AND SYSTEM FOR GENERATING INFORMATION FOR USE IN
MP1692	MP1692D1	US	14/331,624	7/15/2014	9252115	2/2/2016	In Force	METHOD FOR FORMING SEMICONDUCTOR LAYOUT
MP1693	MP1693C1	US	12/870,512	8/27/2010	8044693	10/25/2011	In Force	OUTPUT VOLTAGE SLEW RATE CONTROL IN HARD DISK MOTOR DRIV
MP1697	MP1697	US	12/025,581	2/4/2008	8111739	2/7/2012	In Force	DETECTOR FOR LOW FREQUENCY OFFSET DISTORTION
MP1698	MP1698	US	12/024,784	2/1/2008	7794875	4/27/2010	In Force	HIGH-DENSITY CONTACT HOLES
MP1698	MP1698C1	US	12/767,626	4/26/2010	8003523	8/23/2011	In Force	METHODS FOR FORMING A PLURALITY OF CONTACT HOLES IN A MIC
MP1698	MP1698C1a4	US	13/953,561	7/29/2013	8753976	6/17/2014	In Force	METHOD AND APPARATUS FOR ETCHING PHOTO-RESIST MATERIAL T
MP1698	MP1698C1C1	US	13/212,065	8/17/2011	8501619	8/6/2013	In Force	METHODS FOR FORMING A PLURALITY OF CONTACT HOLES IN A MIC
MP1699	MP1699	US	12/024,986	2/1/2008	7834341	11/16/2010	In Force	PHASE CHANGE MATERIAL (PCM) MEMORY DEVICES WITH BIPOLAR J
MP1699T	MP1699TW	TW	97104204	2/4/2008	1330672	3/21/2013	In Force	PHASE CHANGE MATERIAL (PCM) MEMORY DEVICES WITH BIPOLAR J
MP1699W	MP1699WO	WO	PCT/US2008/05	2/4/2008			Pending	PHASE CHANGE MATERIAL (PCM) MEMORY DEVICES WITH BIPOLAR J
MP1700	MP1700	US	12/026,429	2/5/2008	8161635	4/24/2012	In Force	METHODS FOR FORMING A SINGLE CAP VIA IN PAD OF SUBSTRATE
MP1700	MP1700D1	US	13/443,508	4/10/2012	8772647	7/8/2014	In Force	SINGLE-CAP VIA-IN-PAD AND METHODS FOR FORMING THEREOF

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MP1701	MP1701	US	12/025,600	2/4/2008	8112683	2/7/2012	In Force	SYSTEM AND APPLICATION DEBUGGING
MP1702	MP1702	US	12/029,853	2/12/2008	7873064	1/18/2011	In Force	Adaptive jitter Buffer-Packet loss Concealment
MP1702	MP1702C1	US	12/152,531	5/15/2008	8045571	10/25/2011	In Force	Adaptive jitter Buffer-Packet loss Concealment
MP1702	MP1702C2	US	12/152,532	5/15/2008	8045572	10/25/2011	In Force	Adaptive jitter Buffer-Packet loss Concealment
MP1703	MP1703	US	12/027,778	2/7/2008	8225185	7/17/2012	In Force	RS CODEC ARCHITECTURE THAT COMBINES A COMPACT ENCODER A
MP1704	MP1704	US	12/024,720	2/1/2008	8065598	11/22/2011	In Force	LOW LATENCY PROGRAMMABLE ENCODER WITH OUTER SYSTEMAT
MP1704	MP1704C1	US	13/290,319	11/7/2011	8276055	9/25/2012	In Force	LOW LATENCY PROGRAMMABLE ENCODER WITH OUTER SYSTEMAT
MP1705W	MP1705WO	WO	PCT/US2008/00	2/19/2008			Pending	METHODS AND SYSTEMS FOR IMPROVING LOW FRAME RATE VIDEO
MP1706	MP1706	US	12/025,371	2/4/2008	8019959	9/13/2011	In Force	Nonvolatile Memory System
MP1706	MP1706C1	US	13/230,624	9/12/2011	8219775	7/10/2012	In Force	NonVolatile Memory System
MP1706	MP1706C1aa	US	14/028,581	9/17/2013	8874874	10/28/2014	In Force	Method And Storage Drive For Writing Portions Of Blocks Of Data In
MP1706	MP1706C1C1	US	13/545,508	7/10/2012	8539195	9/17/2013	In Force	Non-Volatile Memory Device With Non-Evenly Distributable Data Acc
MP1706T	MP1706TW	TW	97104819	2/12/2008	1478171	3/21/2015	In Force	VOLATILE MEMORY SYSTEM
MP1706W	MP1706WO	WO	PCT/US2008/00	2/8/2008			Pending	Nonvolatile Memory System
MP1708	MP1708	US	12/029,327	2/11/2008	7872606	1/18/2011	In Force	COMPACT ULTRA WIDEBAND MICROSTRIP RESONATING ANTENNA
MP1709W	MP1709WO	WO	PCT/US2008/00	2/8/2008			Pending	Security For Codes Running In Non-Trusted Domains In A Processor C
MP1710	MP1710	US	12/021,951	1/29/2008	8176401	5/8/2012	In Force	DECODING METHODS AND APPARATUS FOR FRACTIONAL MULTIDIM
MP1710	MP1710C1	US	13/463,401	5/3/2012	8407570	3/26/2013	In Force	DECODING METHODS AND APPARATUS FOR FRACTIONAL MULTIDIM
MP1713	MP1713	US	12/029,908	2/12/2008	7796445	9/14/2010	In Force	STATE-RETENTIVE SCAN LATCH
MP1715	MP1715	US	12/029,134	2/11/2008	8316206	11/20/2012	In Force	Pilot Placement For Non-Volatile Memory
MP1715	MP1715C1	US	13/681,911	11/20/2012	8732432	5/20/2014	In Force	Pilot Placement For Non-Volatile Memory
MP1715	MP1715C1C1	US	14/279,829	5/16/2014	9534630	3/14/2017	In Force	System And Method For Writing Pilot Data Interspersed With User D
MP1715T	MP1715TW	TW	97104818	2/12/2008	1473109	2/11/2015	In Force	PILOT PLACEMENT FOR N/A-VOLATILE MEMORY
MP1715W	MP1715WO	WO	PCT/US2008/00	2/12/2008			Pending	Pilot Placement For Non-Volatile Memory
MP1715W	MP1715WOCN	CN	200880004767	2/12/2008	ZL2008800047	6/5/2013	In Force	PILOT PLACEMENT FOR NON-VOLATILE MEMORY
MP1717	MP1717C1	US	14/611,903	2/2/2015	9762429	9/12/2017	In Force	CONTROL PROTOCOL ENCAPSULATION
MP1717I	MP1717IL	IL	189513	2/13/2008	189513	9/28/2013	In Force	CONTROL PROTOCOL ENCAPSULATION
MP1719	MP1719	US	12/030,173	2/12/2008	8127089	2/28/2012	In Force	HARD DISK CONTROLLER WHICH COORDINATES TRANSMISSION OF B
MP1719	MP1719C1	US	13/401,179	2/21/2012	8412895	4/2/2013	In Force	HARD DISK CONTROLLER WHICH COORDINATES TRANSMISSION OF B
MP1721	MP1721	US	12/031,658	2/14/2008	9443114	9/13/2016	In Force	AUTO-LOGGING OF READ/WRITE COMMANDS IN A STORAGE NETW
MP1723	MP1723C1	US	12/901,823	10/11/2010	8031422	10/4/2011	In Force	ACCUMULATOR FOR NON-RETURN TO ZERO (NRZ) LINEAR FEEDBACK
MP1723	MP1723C1C1	US	13/246,960	9/28/2011	8270107	9/18/2012	In Force	ACCUMULATOR FOR NON-RETURN TO ZERO (NRZ) LINEAR FEEDBACK
MP1727	MP1727	US	12/070,237	2/14/2008	8040555	10/18/2011	In Force	METHOD AND APPARATUS FOR PROCESSING IMAGE DATA FOR AN IR
MP1727	MP1727C1	US	13/274,900	10/17/2011	8405880	3/26/2013	In Force	METHOD AND APPARATUS FOR PROCESSING IMAGE DATA FOR AN IR

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MP1731	MP1731C1	US	14/060,499	10/22/2013	9619358	4/11/2017	In Force	BUS TRAFFIC PROFILING
MP1734	MP1734CN	CN	200880005985	2/21/2008			Pending	SELF-PROPELLED IMAGE TRANSLATION DEVICE
MP1734E	MP1734EP	EP	8736404.4	2/21/2008			Pending	SELF-PROPELLED IMAGE TRANSLATION DEVICE
MP1734J	MP1734JP	JP	2009-551012	2/21/2008	5186713	2/1/2013	In Force	SELF-PROPELLED IMAGE TRANSLATION DEVICE
MP1734T	MP1734TW	TW	97106253	2/22/2008	1376936	11/11/2012	In Force	SELF-PROPELLED IMAGE TRANSLATION DEVICE
MP1734W	MP1734WO	WO	PCT/US2008/05	2/21/2008			Pending	Self-Propelled Image Translation Device
MP1737	MP1737	US	12/035,816	2/22/2008	7868249	10/5/2010	In Force	METHODS AND APPARATUS FOR MEASURING A LENGTH OF A CABLE
MP1737	MP1737C1	US	12/897,292	10/4/2010	7977951	7/12/2011	In Force	Methods And Apparatus For Measuring A Length Of A Cable
MP1738	MP1738	US	12/034,860	2/21/2008	7868247	10/5/2010	In Force	Fast Cable Tester
MP1738	MP1738C1	US	12/897,813	10/5/2010	7986147	7/26/2011	In Force	Fast Cable Tester
MP1739	MP1739	US	12/036,996	2/25/2008	8240801	8/14/2012	In Force	DETERMINING POSITIONING OF A HANDHELD IMAGE TRANSLATION
MP1739	MP1739CN	CN	200880013316	2/25/2008	Z12008800133	10/3/2012	In Force	DETERMINING POSITIONING OF A HANDHELD IMAGE TRANSLATION
MP1739E	MP1739EP	EP	8730675.9	2/25/2008			Pending	DETERMINING POSITIONING OF A HANDHELD IMAGE TRANSLATION
MP1739J	MP1739JP	JP	2009-551060	8/20/2009			Pending	DETERMINING POSITIONING OF A HANDHELD IMAGE TRANSLATION
MP1739W	MP1739WO	WO	PCT/US2008/05	2/25/2008			Pending	DETERMINING POSITIONING OF A HANDHELD IMAGE TRANSLATION
MP1739W	MP1739WOJP	JP	2009-551060	2/25/2008			Pending	DETERMINING POSITIONING OF A HANDHELD IMAGE TRANSLATION
MP1740	MP1740	US	12/037,029	2/25/2008	8223384	7/17/2012	In Force	DEFINING A PRINT IMAGE IN MEMORY FOR HANDHELD IMAGE TRAN
MP1742	MP1742	US	12/037,043	2/25/2008	8351062	1/8/2013	In Force	BIT SELECTION FROM PRINT IMAGE IN MEMORY OF HANDHELD IMA
MP1742	MP1742CN	CN	200880013455	2/26/2008	Z12008800134	11/13/2013	In Force	BIT SELECTION FROM A STORED PRINT IMAGE IN A PRINTER SCANNER
MP1742E	MP1742EP	EP	8730749.2	2/26/2008			Pending	BIT SELECTION FROM PRINT IMAGE IN IMAGE TRANSLATION DEVICE
MP1742J	MP1742JP	JP	2009-552008	2/26/2008			Pending	BIT SELECTION FROM PRINT IMAGE IN IMAGE TRANSLATION DEVICE
MP1742W	MP1742WO	WO	PCT/US2008/05	2/26/2008			Pending	Bit Selection from Print Image in Image Translation Device
MP1744	MP1744	US	12/036,137	2/22/2008	8233365	7/31/2012	In Force	METHODS AND APPARATUS FOR DETECTING A MODULATION TRA
MP1746	MP1746	US	12/038,582	2/27/2008	8381082	2/19/2013	In Force	POWER-SAVING AREA-EFFICIENT HYBRID 8CH CODING SYSTEM
MP1746	MP1746C1	US	13/758,664	2/4/2013	8578251	11/5/2013	In Force	POWER-SAVING AREA-EFFICIENT HYBRID 8CH CODING SYSTEM
MP1747	MP1747	US	12/039,491	2/28/2008	8128192	3/6/2012	In Force	CAP DESIGN FOR AN INKJET PRINT HEAD WITH HAND-HELD IMAGING
MP1747	MP1747C1	US	13/371,191	2/10/2012	8322816	12/4/2012	In Force	CAP DESIGN FOR AN INKJET PRINT HEAD WITH HAND-HELD IMAGING
MP1748	MP1748	US	12/038,660	2/27/2008	8107108	1/31/2012	In Force	PROVIDING USER FEEDBACK IN HANDHELD DEVICE
MP1749	MP1749	US	12/139,664	6/16/2008	7795910	9/14/2010	In Force	Field-Programmable Gate Array Using Charge-Based Nonvolatile Me
MP1751	MP1751	US	12/041,307	3/3/2008	8111809	2/21/2012	In Force	APPARATUS AND METHOD FOR DETERMINING THE POSITION OF A D
MP1751W	MP1751WO	WO	PCT/US2008/05	3/3/2008			Pending	APPARATUS AND METHOD FOR DETERMINING THE POSITION OF A D
MP1752	MP1752CN	CN	200880006958	3/3/2008	Z12008800069	9/5/2012	In Force	DEVICE AND METHOD FOR SERVICING AN INKJET PRINT HEAD ON A
MP1752	MP1752CND1	CN	201210159698	3/3/2008	Z12012101596	7/22/2015	In Force	DEVICE AND METHOD FOR SERVICING AN INKJET PRINT HEAD ON A
MP1752	MP1752D1	US	13/209,976	8/15/2011	8251488	8/28/2012	In Force	METHOD FOR SERVICING AN INKJET PRINT HEAD ON A HAND HELD P

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MP1752	MP1752D1C1	US	13/566,067	8/3/2012	8376510	2/19/2013	In Force	DEVICE FOR SERVICING AN INKJET PRINT HEAD ON A HAND HELD PRI
MP1752E	MP1752EP	EP	8731268.2	3/3/2008			Pending	DEVICE AND METHOD FOR SERVICING AN INKJET PRINT HEAD ON A
MP1752J	MP1752JP	JP	2009-551890	3/3/2008	JP5211350	3/8/2013	In Force	DEVICE AND METHOD FOR SERVICING AN INKJET PRINT HEAD ON A
MP1752W	MP1752WO	WO	PCT/US2008/05	3/3/2008			Pending	DEVICE AND METHOD FOR SERVICING AN INKJET PRINT HEAD ON A
MP1753	MP1753CN	CN	20088006948	3/3/2008	ZL200880069	10/3/2012	In Force	INK SUPPLY FOR A HAND-HELD INK JET PRINTER
MP1753J	MP1753JP	JP	2009-551889	3/3/2008			Pending	INK SUPPLY FOR A HAND-HELD INK JET PRINTER
MP1753W	MP1753WO	WO	PCT/US2008/05	3/3/2008			Pending	INK SUPPLY FOR A HAND-HELD INK JET PRINTER
MP1754	MP1754C1	US	12/961,130	12/6/2010	8100497	1/24/2012	In Force	HANDHELD IMAGE TRANSLATION DEVICE
MP1754W	MP1754WO	WO	PCT/US2008/05	3/3/2008			Pending	Handheld Image Translation Device
MP1755	MP1755	US	12/074,123	2/28/2008	8086713	1/17/2012	In Force	MANAGING PROJECT INFORMATION WITH A HAND-PROPELLED DEVI
MP1755	MP1755C1	US	13/349,356	1/12/2012	8297858	10/30/2012	In Force	MANAGING PROJECT INFORMATION WITH A HAND-PROPELLED DEVI
MP1755	MP1755C1C1	US	13/658,577	10/23/2012	8485743	7/16/2013	In Force	MANAGING PROJECT INFORMATION WITH A HAND-PROPELLED DEVI
MP1756	MP1756	US	12/041,535	3/3/2008	8339675	12/25/2012	In Force	DYNAMIC IMAGE DITHERING
MP1756	MP1756CN	CN	200880014418	3/3/2008	ZL2008800144	2/20/2013	In Force	DYNAMIC IMAGE DITHERING
MP1756W	MP1756WO	WO	PCT/US2008/05	3/3/2008			Pending	DYNAMIC IMAGE DITHERING
MP1756W	MP1756WOJP	JP	2009-551893	3/3/2008	JP5223122	3/22/2013	In Force	DYNAMIC IMAGE DITHERING
MP1757	MP1757	US	12/041,515	3/3/2008	9294649	3/22/2016	In Force	POSITION CORRECTION IN HANDHELD IMAGE TRANSLATION DEVICE
MP1757	MP1757CN	CN	200880014408	3/3/2008	ZL2008800144	6/5/2013	In Force	POSITION CORRECTION FOR HANDHELD PRINTER
MP1757E	MP1757EP	EP	8731276.5	3/3/2008			Pending	Position Correction for Handheld Printer
MP1757W	MP1757WO	WO	PCT/US2008/05	3/3/2008			Pending	Position Correction in Handheld Image Translation Device
MP1758	MP1758	US	12/074,018	2/28/2008	8079765	12/20/2011	In Force	HAND-PROPELLED LABELING PRINTER
MP1759W	MP1759WO	WO	PCT/US2008/05	3/3/2008			Pending	HAND-PROPELLED SCRAPBOOKING PRINTER
MP1760	MP1760	US	12/041,483	3/3/2008	8958290	2/17/2015	In Force	Multi-Level Multi-Pin Differential Interface
MP1761	MP1761	US	12/175,034	7/17/2008	8428535	4/23/2013	In Force	RECEIVER DYNAMIC POWER MANAGEMENT
MP1761	MP1761D1	US	13/867,478	4/22/2013	9571053	2/14/2017	In Force	RECEIVER DYNAMIC POWER MANAGEMENT
MP1768	MP1768	US	12/044,843	3/7/2008	7778257	8/17/2010	In Force	VIRTUAL ETHERNET STACK
MP1769	MP1769	US	12/044,652	3/7/2008	8351343	1/8/2013	In Force	EFFICIENT CHANGE RECOGNITION ALGORITHM
MP1769	MP1769C1	US	13/735,700	1/7/2013	9066246	6/23/2015	In Force	EFFICIENT CHANGE RECOGNITION ALGORITHM
MP1770	MP1770	US	12/044,117	3/7/2008	8442138	5/14/2013	In Force	CODEBOOK SELECTION FOR TRANSMIT BEAMFORMING
MP1770	MP1770C1	US	13/890,890	5/9/2013	8942306	1/27/2015	In Force	CODEBOOK SELECTION FOR TRANSMIT BEAMFORMING
MP1770T	MP1770TW	TW	97108156	3/7/2008	1442737	6/21/2014	In Force	CODEBOOK SELECTION FOR TRANSMIT BEAMFORMING
MP1770W	MP1770WO	WO	PCT/US2008/05	3/7/2008			Pending	CODEBOOK SELECTION FOR TRANSMIT BEAMFORMING
MP1771	MP1771	US	12/045,468	3/10/2008	7662495	4/6/2010	In Force	TUNABLE RF BANDPASS TRANSDUCUTANCE AMPLIFIER
MP1772	MP1772	US	12/044,762	3/7/2008	8325828	12/4/2012	In Force	METHOD AND APPARATUS FOR DETECTING IMPULSE NOISE

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MP1774	MP1774	US	12/044,517	3/7/2008	8125936	2/28/2012	In Force	LOW POWER DATA STREAMING
MP1775	MP1775	US	12/044,711	3/7/2008	8385294	2/26/2013	In Force	DYNAMICALLY RECONFIGURABLE RECEIVER
MP1775	MP1775C1	US	13/774,802	2/22/2013	8614992	12/24/2013	In Force	DYNAMICALLY RECONFIGURABLE RECEIVER
MP1775W	MP1775WO	WO	PCT/IB2008/002	3/7/2008			Pending	DYNAMICALLY RECONFIGURABLE RECEIVER
MP1775W	MP1775WOEP	EP	EP08828424.5	3/7/2008	2119040	12/27/2017	In Force	Dynamically Reconfigurable Receiver
MP1775W	MP1775WOEPDE	DE	EP08828424.5	3/7/2008	2119040	12/27/2017	In Force	Dynamically Reconfigurable Receiver
MP1775W	MP1775WOEPFR	FR	EP08828424.5	3/7/2008	2119040	12/27/2017	In Force	Dynamically Reconfigurable Receiver
MP1775W	MP1775WOEPGB	GB	EP08828424.5	3/7/2008	2119040	12/27/2017	In Force	Dynamically Reconfigurable Receiver
MP1776	MP1776	US	12/044,532	3/7/2008	8537952	9/17/2013	In Force	FRACTIONAL-N FREQUENCY SYNTHESIZER WITH SEPARATE PHASE AP
MP1777	MP1777	US	12/245,487	10/3/2008	7825715	11/2/2010	In Force	DIGITALLY TUNABLE CAPACITOR
MP1777	MP1777C1	US	12/905,657	10/15/2010	8044701	10/25/2011	In Force	DIGITALLY TUNABLE CAPACITOR
MP1779	MP1779	US	12/045,609	3/10/2008	8443030	5/14/2013	In Force	PROCESSING OF FLOATING POINT MULTIPLY-ACCUMULATE INSTRUCT
MP1782	MP1782	US	12/047,987	3/13/2008	8453966	7/23/2013	In Force	Optimized Packet Processing Architecture for Battery Powered Mobil
MP1782E	MP1782EP	EP	08835069.9	3/13/2008	2135409	5/16/2012	In Force	Optimized Packet Processing Architecture for Battery Powered Mobil
MP1782E	MP1782EPDE	DE	08835069.9	3/13/2008	2135409	5/16/2012	In Force	Optimized Packet Processing Architecture for Battery Powered Mobil
MP1782E	MP1782EPRR	FR	08835069.9	3/13/2008	2135409	5/16/2012	In Force	Optimized Packet Processing Architecture for Battery Powered Mobil
MP1782E	MP1782EPGB	GB	08835069.9	3/13/2008	2135409	5/16/2012	In Force	Optimized Packet Processing Architecture for Battery Powered Mobil
MP1782W	MP1782WOCN	CN	200880016075.1	3/13/2008	212008800160	10/3/2012	In Force	OPTIMIZED PACKET PROCESSING ARCHITECTURE FOR BATTERY POW
MP1784J	MP1784JP	JP	2009-530969	10/2/2007			Pending	FLASH MEMORY CONTROL INTERFACE
MP1784K	MP1784KR	KR	1020097006662	10/2/2007			Pending	Flash Memory Control Interface
MP1784T	MP1784TW	TW	96137216	10/4/2007			Pending	Flash Memory Control Interface
MP1784W	MP1784WO	WO	PCT/IB2007/004	10/2/2007			Pending	Flash Memory Control Interface
MP178T	MP178TW	TW	97109749	3/19/2008			Pending	IP MOBILITY MECHANISM SELECTION FOR MULTI-MODE TERMINALS
MP178W	MP178WO	WO	PCT/US2008/00	3/19/2008			Pending	IP Mobility Mechanism Selection For Multi-Mode Terminals With Tun
MP178W	MP178WOJP	JP	2009-554565	3/19/2008			Pending	IP MOBILITY MECHANISM SELECTION FOR TERMINALS
MP1791	MP1791	US	12/053,253	3/21/2008	7969022	6/28/2011	In Force	DIE-TO-DIE WIRE-BONDING
MP1792	MP1792	US	12/052,573	3/20/2008	8999786	4/7/2015	In Force	REDUCING SOURCE CONTACT TO GATE SPACING TO DECREASE TRAN
MP1792	MP1792D1	US	14/618,616	4/3/2015	9245961	1/26/2016	In Force	REDUCING SOURCE CONTACT TO GATE SPACING TO DECREASE TRAN
MP1793	MP1793	US	12/052,350	3/20/2008	7827450	11/2/2010	In Force	Defect Detection And Handling For Memory Based On Pilot Cells
MP1793	MP1793C1	US	12/917,375	11/1/2010	8010852	8/30/2011	In Force	Defect Detection And Handling For Memory Based On Pilot Cells
MP1794	MP1794	US	12/052,371	3/20/2008	8005178	8/23/2011	In Force	Synchronous Network Device
MP1794	MP1794C1	US	13/215,149	8/22/2011	8498699	7/16/2013	In Force	Synchronous Network Device
MP1794	MP1794C1C1	US	13/943,107	7/16/2013	9054823	6/9/2015	In Force	Synchronous Network Device
MP1794W	MP1794WD1	JP	2012-264557	12/3/2012	JP5626693	10/10/2014	In Force	SYNCHRONOUS NETWORK DEVICE

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP1794W	MP1794WO	WO	PCT/US2008/00	3/20/2008			Pending	Synchronous Network Device
MP1794W	MP1794WOCN	CN	20088009142.1	3/20/2008	ZL2008800091	6/5/2013	In Force	SYNCHRONOUS NETWORK DEVICE
MP1794W	MP1794WCEP	EP	EP20080742152	3/20/2008	2130316	5/23/2018	In Force	Synchronous Network Device
MP1794W	MP1794WCEPDE	DE	EP20080742152	3/20/2008	2130316	5/23/2018	In Force	Synchronous Network Device
MP1794W	MP1794WCEPFR	FR	EP20080742152	3/20/2008	2130316	5/23/2018	In Force	Synchronous Network Device
MP1794W	MP1794WCEPGB	GB	EP20080742152	3/20/2008	2130316	5/23/2018	In Force	Synchronous Network Device
MP1794W	MP1794WGPJP	JP	2009-554568	3/20/2008	JP5150652	12/7/2012	In Force	SYNCHRONOUS NETWORK DEVICE
MP1795	MP1795I1	US	13/397,851	2/16/2012	8779970	7/15/2014	In Force	Method and Apparatus for Determining Location Using a Hybrid Soft
MP1798	MP1798C1	US	12/953,117	11/23/2010	8149011	4/3/2012	In Force	STRESS PROGRAMMING OF TRANSISTORS
MP1799	MP1799	US	12/053,385	3/21/2008	8699316	4/15/2014	In Force	USF CODING
MP1802	MP1802	US	12/053,430	3/21/2008	8259835	9/4/2012	In Force	VARIABLE CODEBOOK FOR MIMO SYSTEM
MP1802	MP1802C1	US	13/523,018	6/14/2012	8781019	7/15/2014	In Force	VARIABLE SIZE CODEBOOK FOR MIMO SYSTEM
MP1802	MP1802C1C1	US	14/350,942	7/14/2014	9391679	7/12/2016	In Force	VARIABLE SIZE CODEBOOK FOR MIMO SYSTEM
MP1802T	MP1802TW	TW	97110430	3/24/2008	I474688	2/21/2015	In Force	VARIABLE CODEBOOK FOR MIMO SYSTEM
MP1802W	MP1802WO	WO	PCT/US2008/05	3/21/2008			Pending	VARIABLE CODEBOOK FOR MIMO SYSTEM
MP1802W	MP1802WOCN	CN	20088009398.1	3/21/2008	ZL2008800093	11/12/2014	In Force	VARIABLE CODEBOOK FOR MIMO SYSTEM
MP1802W	MP1802WCEP	EP	EP08744204.2	3/21/2008	2130308	12/19/2018	In Force	Variable Codebook for MIMO System
MP1802W	MP1802WCEPDI	EP	1819045.8	3/21/2008			Pending	Variable Codebook for MIMO System
MP1802W	MP1802WCEPDE	DE	EP08744204.2	3/21/2008	2130308	12/19/2018	In Force	Variable Codebook for MIMO System
MP1802W	MP1802WCEPFR	FR	EP08744204.2	3/21/2008	2130308	12/19/2018	In Force	Variable Codebook for MIMO System
MP1802W	MP1802WCEPGB	GB	EP08744204.2	3/21/2008	2130308	12/19/2018	In Force	Variable Codebook for MIMO System
MP1803W	MP1803WO	WO	PCT/US2008/06	5/29/2008			Pending	Memory Structure With Word Line Buffers
MP1803W	MP1803WGPJP	JP	2010-510482	5/29/2008	JP5240627	4/12/2013	In Force	MEMORY WITH WORD LINE BUFFERS AND METHOD FOR OPERATING
MP1803W	MP1803WOKR	KR	1020097027551	5/29/2008	101446726	9/25/2014	In Force	Memory Structure With Word Line Buffers
MP1809W	MP1809WO	WO	PCT/IB2008/00C	3/26/2008			Pending	Encoding And Decoding Systems With Header And Data Transmission
MP1810	MP1810	US	12/057,221	3/27/2008	8045636	10/25/2011	In Force	MAXIMUM-LIKELIHOOD FRAME SYNCHRONIZATION ALGORITHMS F
MP1811	MP1811	US	12/056,034	3/26/2008	8196004	6/5/2012	In Force	FAST ERASURE DECODING FOR PRODUCT CODE COLUMNS
MP1813	MP1813	US	12/056,913	3/27/2008	7978680	7/12/2011	In Force	System and Method For Synchronizing Clocks In A Wireless Local Are
MP1813	MP1813C1	US	13/180,040	7/11/2011	8306014	11/6/2012	In Force	System And Method For Synchronizing Clocks In A Wireless Local Are
MP1813	MP1813C1C1	US	13/669,799	11/6/2012	8831044	9/9/2014	In Force	System And Method For Synchronizing Clocks In A Wireless Local Are
MP1814	MP1814	US	12/056,836	3/27/2008	8185107	5/22/2012	In Force	IP Address Allocation In Evolved Wireless Networks
MP1814	MP1814C1	US	13/475,417	5/18/2012	9161211	10/13/2015	In Force	IP Address Allocation In Evolved Wireless Networks
MP1814W	MP1814WDI1	JP	2013-030162	2/19/2013	JP5453706	1/17/2014	In Force	IP ADDRESS ALLOCATION IN EVOLVED WIRELESS NETWORKS
MP1814W	MP1814WO	WO	PCT/US2008/00	3/28/2008			Pending	IP Address Allocation In Evolved Wireless Networks

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP1814W	MP1814WOEP	EP	EP08/42388.5	3/28/2008	2130389	2/21/2018	In Force	IP Address Allocation in Evolved Wireless Networks
MP1814W	MP1814WOEPDE	DE	EP08/42388.5	3/28/2008	2130389	2/21/2018	In Force	IP Address Allocation in Evolved Wireless Networks
MP1814W	MP1814WOEPFR	FR	EP08/42388.5	3/28/2008	2130389	2/21/2018	In Force	IP Address Allocation in Evolved Wireless Networks
MP1814W	MP1814WOEPGB	GB	EP08/42388.5	3/28/2008	2130389	2/21/2018	In Force	IP Address Allocation in Evolved Wireless Networks
MP1814W	MP1814WOJP	JP	2010-501017	3/28/2008	JP5218794	3/15/2013	In Force	IP ADDRESS ALLOCATION IN EVOLVED WIRELESS NETWORKS
MP1815	MP1815	US	12/059,739	3/31/2008	8278563	10/2/2012	In Force	VIA TO PLATING BUS
MP1817	MP1817	US	12/059,846	3/31/2008	8374273	2/12/2013	In Force	METHOD AND APPARATUS FOR TRANSMIT BEAMFORMING
MP1817	MP1817C1	US	13/615,544	9/13/2012	8565335	10/22/2013	In Force	METHOD AND APPARATUS FOR TRANSMIT BEAMFORMING
MP1817	MP1817C1C1	US	14/058,730	10/21/2013	8842713	9/23/2014	In Force	METHOD AND APPARATUS FOR TRANSMIT BEAMFORMING
MP1819	MP1819	US	12/059,945	3/31/2008	8498195	7/30/2013	In Force	HARQ RETRANSMISSION SCHEME FOR AT LEAST TWO TRANSMIT AN
MP1822	MP1822	US	12/062,036	4/3/2008	8238337	8/7/2012	In Force	Hybrid Multicast Switch Employing Network-Layer Routing
MP1825	MP1825	US	12/060,792	4/1/2008	7769835	5/4/2010	In Force	METHOD TO FORM HIGH EFFICIENCY GST CELL USING A DOUBLE HEA
MP1825T	MP1825TW	TW	97112165	4/3/2008	1419321	12/11/2013	In Force	METHODS TO FORM WIDE HEATER TRENCHES AND TO FORM MEMO
MP1825W	MP1825WO	WO	PCT/US2008/05	4/2/2008			Pending	METHODS TO FORM WIDE HEATER TRENCHES AND TO FORM MEMO
MP1828	MP1828	US	12/062,444	4/3/2008	7745809	6/29/2010	In Force	ULTRA HIGH DENSITY PHASE CHANGE MEMORY HAVING IMPROVED
MP1829	MP1829	US	12/060,613	4/1/2008	8005515	8/23/2011	In Force	BEACON MISS PREVENTION IN POWER SAVE MODES USING TIMING
MP1829	MP1829C1	US	13/214,958	8/22/2011	8315676	11/20/2012	In Force	BEACON MISS PREVENTION IN POWER SAVE MODES USING TIMING
MP1829	MP1829C1C1	US	13/682,185	11/20/2012	8888139	10/21/2014	In Force	Integrated Circuit And Method With Pre-Beacon Activation Time Adju
MP1829T	MP1829TW	TW	97112515	4/7/2008	1473513	2/11/2015	In Force	BEACON MISS PREVENTION IN POWER SAVE MODES USING TIMING
MP1829W	MP1829WO	WO	PCT/US2008/00	4/3/2008			Pending	Beacon Miss Prevention in Power Save Modes Using Timing Synchron
MP1830	MP1830	US	12/062,291	4/3/2008	8064710	1/10/2012	In Force	WIDE BAND NOISE EARLY DETECTION AND PROTECTION ARCHITECT
MP1833	MP1833	US	12/062,249	4/3/2008	8032724	10/4/2011	In Force	Demand-Driven Opportunistic Garbage Collection in Memory Compo
MP1834	MP1834	US	12/100,318	4/9/2008	8766032	4/22/2014	In Force	SYSTEMS AND METHODS FOR PROVIDING COLLABORATIVE COEXISTE
MP1834	MP1834C1	US	14/245,265	4/4/2014	9326310	4/26/2016	In Force	SYSTEMS AND METHODS FOR PROVIDING COLLABORATIVE COEXISTE
MP1834W	MP1834WO	WO	PCT/US2008/05	4/9/2008			Pending	SYSTEMS AND METHODS FOR PROVIDING COLLABORATIVE COEXISTE
MP1835W	MP1835WO	WO	PCT/US2008/05	4/9/2008			Pending	APPARATUSES, SYSTEMS, SOFTWARE AND METHODS FOR WIRELESS I
MP1835W	MP1835WOEP	EP	8745412	4/9/2008			Pending	APPARATUSES, SYSTEMS, SOFTWARE AND METHODS FOR WIRELESS I
MP1835W	MP1835WOJP	JP	2010-503188	4/9/2008			Pending	APPARATUSES, SYSTEMS, SOFTWARE AND METHODS FOR WIRELESS I
MP1839	MP1839C1	US	12/890,478	9/24/2010	8482319	7/9/2013	In Force	Current Switch For High Voltage Process
MP1842	MP1842	US	12/098,923	4/7/2008	8322817	12/4/2012	In Force	OPERATING MECHANISM FOR AN INKJET PRINTER
MP1842	MP1842C1	US	13/666,609	11/27/2012	9156268	10/13/2015	In Force	OPERATING MECHANISM FOR AN INKJET PRINTER
MP1842	MP1842CN	CN	200880019037	4/7/2008	212008800190	7/24/2013	In Force	OPERATING MECHANISM FOR AN INKJET PRINTER
MP1842W	MP1842WO	WO	PCT/US2608/05	4/7/2008			Pending	OPERATING MECHANISM FOR AN INKJET PRINTER
MP1843	MP1843	US	12/098,254	4/4/2008	8095816	1/10/2012	In Force	Processor Management Using A Buffer

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP1843	MP1843C1	US	13/333,551	12/21/2011	8327056	12/4/2012	In Force	PROCESSOR MANAGEMENT USING A BUFFER
MP1843	MP1843C1C1	US	13/588,282	8/29/2012	8843686	9/23/2014	In Force	Processor Management Using a Buffer
MP1845	MP1845	US	12/062,472	4/3/2008	9186686	11/10/2015	In Force	IMAGE TRANSLATION DEVICE PROVIDING NAVIGATIONAL DATA FEED
MP1846W	MP1846WO	WO	PCT/US2008/05	4/7/2008			Pending	INKJET PRINTER HAVING SWITCHED FIRING OF ADJACENT NOZZLES A
MP1847	MP1847	US	12/062,413	4/3/2008	8364918	1/29/2013	In Force	Sensed Opportunistic Garbage Collection in Memory Components
MP1847	MP1847C1	US	13/751,386	1/28/2013	8843712	9/23/2014	In Force	Sensed Opportunistic Garbage Collection in Memory Components
MP1849	MP1849	US	11/120,691	5/2/2005	7759719	7/20/2010	In Force	Electrically Alterable Memory Cell
MP1849	MP1849CN1	CN	2005100804278	7/1/2005	212605100804	12/24/2008	In Force	Electrically Alterable Memory Cell
MP1849	MP1849CN2	CN	2005100804259	7/1/2005	212605100804	12/24/2008	In Force	Method And Apparatus For Semiconductor Device And Semiconductor
MP1849	MP1849CN3	CN	2005100804314	7/1/2005	212605100804	6/11/2008	In Force	Method And Apparatus Transporting Charges In Semiconductor Devic
MP1849T	MP1849TW2	TW	94122116	6/30/2005	1257652	7/1/2006	In Force	Electrically Alterable Memory Cell
MP1849T	MP1849TW3	TW	94122357	7/1/2005	1287292	9/21/2007	In Force	Method And Apparatus Transporting Charges In Semiconductor Devic
MP1850	MP1850F3	US	11/879,090	7/16/2007	7745286	6/29/2010	In Force	Methods Of Forming Semiconductor Devices
MP1850	MP1850FFb	US	11/879,179	7/16/2007	7741177	6/22/2010	In Force	Method and Apparatus Transporting Charges In Semiconductor Devic
MP1850	MP1850FFa	US	11/978,875	10/30/2007	7547601	6/16/2009	In Force	Low Power Electrically Alterable Nonvolatile Memory Cells And Array
MP1850	MP1850FH1	US	11/234,646	9/23/2005	7411244	8/12/2008	In Force	Low Power Electrically Alterable Nonvolatile Memory Cells And Array
MP1850	MP1850H1	US	11/007,907	12/8/2004	7115942	10/3/2006	In Force	Method And Apparatus For Nonvolatile Memory
MP1850	MP1850H1i	US	11/055,427	2/9/2005	7297634	11/20/2007	In Force	Method And Apparatus For Semiconductor Device And Semiconduct
MP1850	MP1850H1a	US	11/980,154	10/30/2007	7824981	2/11/2010	In Force	METHOD AND APPARATUS FOR SEMICONDUCTOR DEVICE AND SEMI
MP1850	MP1850H11	US	11/169,399	6/28/2005	7550800	6/23/2009	In Force	Method And Apparatus Transporting Charges In Semiconductor Devic
MP1852	MP1852	US	10/457,249	6/6/2003	6958513	10/25/2005	In Force	FLOATING-GATE MEMORY CELL HAVING TRENCH STRUCTURE WITH
MP1852	MP1852CN	CN	2004100967406	12/3/2004	212604100967	3/5/2008	In Force	Electrically Erasable And Programmable Read Only Memory Device A
MP1852T	MP1852TW	TW	93136869	11/30/2004	1257149	6/21/2006	In Force	Electrically Erasable And Programmable Read Only Memory Device A
MP1853	MP1853	US	12/098,924	4/7/2008	8077785	12/13/2011	In Force	GENERALIZED CO-PHASING FOR MULTIPLE TRANSMITTING AND RECE
MP1853	MP1853C1	US	13/333,781	12/12/2011	8867635	10/21/2014	In Force	GENERALIZED CO-PHASING FOR MULTIPLE TRANSMITTING AND RECE
MP1854T	MP1854TW	TW	97116471	5/5/2008			Pending	Device With IC, SOC Or SIP Having One Or More Remotely Enabled M
MP1854W	MP1854WO	WO	PCT/US2008/06	5/2/2008			Pending	Device With IC, SOC Or SIP Having One Or More Remotely Enabled M
MP1854W	MP1854WOCN	CN	2008800234064	5/2/2008			Pending	Device With IC, SOC Or SIP Having One Or More Remotely Enabled M
MP1855T	MP1855TW	TW	97110431	3/24/2008			Pending	IP MOBILITY MECHANISM SELECTION FOR MULTI-MODE TERMINALS
MP1855W	MP1855WO	WO	PCT/US2008/00	3/21/2008			Pending	IP Mobility Mechanism Selection For Multi-Mode Terminals With Dir
MP1855W	MP1855WOCF	EP	87271011	3/21/2008			Pending	IP Mobility Mechanism Selection For Terminals
MP1855W	MP1855WOJP	JP	2009-554593	3/21/2008			Pending	IP MOBILITY MECHANISM SELECTION FOR TERMINALS
MP1856	MP1856	US	12/101,582	4/11/2008	8619910	12/31/2013	In Force	DECISION FEEDBACK EQUALIZATION FOR MAINO SYSTEMS WITH HYB
MP1857T	MP1857TW	TW	97113519	4/14/2008			Pending	PACKET DATA NETWORK CONNECTIVITY DOMAIN SELECTION AND B

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MP1857W	MP1857WO	WO	PCT/US2008/00	4/11/2008			Pending	Packet Data Network Connectivity Domain Selection And Bearer Setup
MP1858	MP1858	US	12/101,668	4/11/2008	8443187	5/14/2013	In Force	Authentication Of Computing Devices Using Augmented Credentials
MP1858	MP1858C1	US	13/874,201	4/30/2013	9253175	2/2/2016	In Force	Authentication of Computing Devices Using Augmented Credentials
MP1859	MP1859	US	12/124,115	5/20/2008	8099073	1/17/2012	In Force	NOISE REDUCTION IN AMPLIFIER CIRCUITRY USING SINGLE-SIDEBAND
MP1860	MP1860	US	12/110,028	4/25/2008	8004247	8/23/2011	In Force	Switchable Battery Charger Circuit
MP1861	MP1861	US	12/104,357	4/16/2008	8127100	2/28/2012	In Force	Unlimited Sub-Segment Support in a Buffer Manager
MP1864	MP1864	US	12/104,879	4/17/2008	8475581	7/2/2013	In Force	ASYMMETRY COMPENSATION SYSTEM
MP1866	MP1866	US	12/100,757	4/10/2008	8185151	5/22/2012	In Force	System And Process For Internet Protocol Multimedia Subsystem Call
MP1868	MP1868	US	12/100,107	4/9/2008	8332610	12/11/2012	In Force	System On Chip With Reconfigurable SRAM
MP1868	MP1868C1	US	13/709,980	12/10/2012	8516216	8/20/2013	In Force	System On Chip With Reconfigurable SRAM
MP1868T	MP1868TW	TW	97113980	4/17/2008	1470644	1/21/2015	In Force	SYSTEM ON CHIP WITH RECONFIGURABLE SRAM
MP1868W	MP1868WO	WO	PCT/US2008/00	4/17/2008			Pending	System On Chip With Reconfigurable SRAM
MP1869T	MP1869TW	TW	97113983	4/17/2008			Pending	MULTI-PORTED HOST ADAPTERS
MP1869W	MP1869WO	WO	PCT/US2008/00	4/17/2008			Pending	Multi-Ported Host Adapters
MP1873	MP1873	US	12/105,794	4/18/2008	8275946	9/25/2012	In Force	Channel Tags in Memory Components for Optimizing Logical to Physical
MP1874	MP1874	US	12/106,902	4/21/2008	7669189	10/27/2009	In Force	INTERFACE FOR HYBRID SIGMA-DELTA DATA CONVERTER
MP1875	MP1875	US	12/105,235	4/17/2008	7911053	3/22/2011	In Force	SEMICONDUCTOR PACKAGING WITH INTERNAL WIRING BUS
MP1875	MP1875C1	US	13/046,903	3/14/2011	8518742	8/27/2013	In Force	SEMICONDUCTOR PACKAGING WITH INTERNAL WIRING BUS
MP1875T	MP1875TW	TW	97114373	4/18/2008	1390702	3/21/2013	In Force	SEMICONDUCTOR PACKAGING WITH INTERNAL WIRING BUS
MP1875W	MP1875WO	WO	PCT/US2008/06	4/18/2008			Pending	SEMICONDUCTOR PACKAGING WITH INTERNAL WIRING BUS
MP1876T	MP1876TW	TW	97114369	4/18/2008	1449324	8/11/2014	In Force	IMPROVED COLPITTS OSCILLATOR
MP1876W	MP1876WO	WO	PCT/US2008/06	4/18/2008			Pending	IMPROVED COLPITTS OSCILLATOR
MP1878	MP1878	US	12/105,750	4/18/2008	7978784	7/12/2011	In Force	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING
MP1878	MP1878C1	US	13/178,312	7/7/2011	8213539	7/3/2012	In Force	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING
MP1878	MP1878C1C1	US	13/540,257	7/2/2012	8625701	1/7/2014	In Force	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING
MP1878E	MP1878EP	EP	EP08936199.5	4/18/2008	EP2140574	9/30/2015	In Force	Antenna Selection and Training Using a Spatial Spreading System for U
MP1878E	MP1878EPD1	EP	EP12003035.8	4/18/2008	EP2495409	8/3/2016	In Force	Antenna Selection and Training Using a Spatial Spreading Matrix for U
MP1878E	MP1878EPD1DE	DE	6020680045482.1	4/18/2008	2485409	8/3/2016	In Force	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING
MP1878E	MP1878EPD1FR	FR	12003035.8	4/18/2008	2485409	8/3/2016	In Force	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING
MP1878E	MP1878EPD1GB	GB	12003035.8	4/18/2008	2485409	8/3/2016	In Force	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING
MP1878E	MP1878EPD2	EP	16182468.5	8/2/2016			Pending	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING
MP1878E	MP1878EPDE	DE	6020680040440.4	4/18/2008	2140574	9/30/2015	In Force	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING S
MP1878E	MP1878EPFR	FR	08826199.5	4/18/2008	2140574	9/30/2015	In Force	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING S
MP1878E	MP1878EPGB	GB	08826199.5	4/18/2008	2140574	9/30/2015	In Force	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING S

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP1878J	MP1878JP	JP	2010-504294	4/18/2008	5345130	8/23/2013	In Force	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING
MP1878J	MP1878JP.D1	JP	2013-128850	6/19/2013			Pending	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING
MP1878T	MP1878TW	TW	97114542	4/21/2008	1474649	2/21/2015	In Force	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING
MP1878W	MP1878WO	WO	PCT/US2008/06	4/18/2008			Pending	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING
MP1878W	MP1878WOCN	CN	200880012771.1	4/18/2008	ZL2008800127	12/2/2013	In Force	ANTENNA SELECTION AND TRAINING USING A SPATIAL SPREADING
MP1880	MP1880	US	12/104,215	4/16/2008	8030128	10/4/2011	In Force	METHOD TO FORM HIGH DENSITY PHASE CHANGE MEMORY (PCM) T
MP1882	MP1882	US	12/108,910	4/24/2008	8560823	10/15/2013	In Force	TRUSTED MODULAR FIRMWARE UPDATE USING DIGITAL CERTIFICAT
MP1882	MP1882.D1	US	14/052,940	10/14/2013	9626513	4/18/2017	In Force	TRUSTED MODULAR FIRMWARE UPDATE USING DIGITAL CERTIFICAT
MP1883	MP1883	US	12/107,466	4/22/2008	8159241	4/17/2012	In Force	METHOD AND APPARATUS FOR ON-CHIP ADJUSTMENT OF CHIP CHA
MP1883	MP1883.D1	US	13/334,831	12/22/2011	9766966	9/19/2017	In Force	Method and Apparatus for On-Chip Adjustment of Chip Characteristic
MP1885	MP1885	US	12/109,094	4/24/2008	8396321	3/12/2013	In Force	METHOD AND APPARATUS FOR PROCESSING IMAGE DATA FROM A P
MP1885	MP1885.C1	US	13/792,858	11/3/2013	8625926	7/1/2014	In Force	METHOD AND APPARATUS FOR PROCESSING IMAGE DATA FROM A P
MP1886	MP1886	US	12/107,694	4/22/2008	7996642	8/9/2011	In Force	Digital Locked Loop on Channel Tagged Memory Requests for Memer
MP1886	MP1886.C1	US	13/196,122	8/2/2011	8289503	6/26/2012	In Force	Digital Locked Loop on Channel Tagged Memory Requests for Memer
MP1888	MP1888	US	12/109,257	4/24/2008	8165543	4/24/2012	In Force	POWER AMPLIFIER ADJUSTMENT FOR TRANSMIT BEAMFORMING IN
MP1888	MP1888.C1	US	13/452,475	4/20/2012	8380148	2/19/2013	In Force	POWER AMPLIFIER ADJUSTMENT FOR TRANSMIT BEAMFORMING IN
MP1888	MP1888.C1.AA	US	14/245,772	4/4/2014	9599046	11/29/2016	In Force	POWER AMPLIFIER ADJUSTMENT FOR TRANSMIT BEAMFORMING IN
MP1888	MP1888.C1.C1	US	13/769,567	2/18/2013	8653964	4/8/2014	In Force	POWER AMPLIFIER ADJUSTMENT FOR TRANSMIT BEAMFORMING IN
MP1888	MP1888.C1.C1.C1	US	15/361,907	11/28/2016	10141997	11/27/2018	In Force	POWER AMPLIFIER ADJUSTMENT FOR TRANSMIT BEAMFORMING IN
MP1888W	MP1888WO	WO	PCT/US2008/06	4/24/2008			Pending	POWER AMPLIFIER ADJUSTMENT FOR TRANSMIT BEAMFORMING IN
MP1888W	MP1888WCEP	EP	8746772.6	4/24/2008			Pending	POWER AMPLIFIER ADJUSTMENT FOR TRANSMIT BEAMFORMING IN
MP1895	MP1895	US	12/110,925	4/28/2008	8019016	9/13/2011	In Force	SYSTEM AND METHOD OF TRANSMIT BEAM SELECTION
MP1895	MP1895.C1	US	13/229,955	9/12/2011	8213540	7/3/2012	In Force	SYSTEM AND METHOD OF TRANSMIT BEAM SELECTION
MP1896	MP1896	US	12/109,320	4/24/2008	7961641	6/14/2011	In Force	INITIAL RANGING POWER CONTROL ALGORITHM FOR WIMAX MOBIL
MP1898	MP1898.C1	US	13/053,967	3/22/2011	8243569	8/14/2012	In Force	METHOD AND SYSTEM FOR RADIAL AND TANGENTIAL TILT CALIBRATI
MP1899	MP1899.C1	US	12/958,713	12/2/2010	8054207	11/8/2011	In Force	ENUMERATIVE DC-RLL CONSTRAINED CODING
MP1902	MP1902	US	12/110,717	4/28/2008	8286188	10/9/2012	In Force	METHOD AND APPARATUS FOR ADVANCED INTERPROCESS COMMUN
MP1905	MP1905	US	12/056,181	3/26/2008	7889728	2/15/2011	In Force	SYSTEM AND METHOD OF MODIFYING DATA PACKET TAGS
MP1908	MP1908	US	12/030,822	2/13/2008	7786594	9/14/2010	In Force	LOGICAL BRIDGING SYSTEM AND METHOD
MP1908	MP1908.C1	US	12/881,123	9/13/2010	8089963	1/3/2012	In Force	PACKET FORWARDING APPARATUS AND METHOD
MP1908	MP1908.C1.AA	US	14/188,484	2/24/2014	9203735	12/1/2015	In Force	PACKET FORWARDING APPARATUS AND METHOD
MP1908	MP1908.C1.C1	US	13/340,393	12/29/2011	8660120	2/25/2014	In Force	PACKET FORWARDING APPARATUS AND METHOD
MP1908I	MP1908IL	IL	189514	2/13/2008	189514	3/30/2012	In Force	LOGICAL BRIDGING SYSTEM AND METHOD

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MP19081	MP19081HD1	IL	216090	2/13/2008	216090	3/1/2013	In Force	LOGICAL BRIDGING SYSTEM AND METHOD
MP1909	MP1909	US	12/116,885	5/7/2008	8213556	7/3/2012	In Force	SIGNAL POWER ESTIMATION FOR CELLULAR OFDM SYSTEMS
MP1909	MP1909C1	US	13/464,043	5/30/2012	8374293	2/12/2013	In Force	SIGNAL POWER ESTIMATION FOR CELLULAR OFDM SYSTEMS
MP1909	MP1909C1C1	US	13/762,046	2/7/2013	8861658	10/14/2014	In Force	SIGNAL POWER ESTIMATION FOR CELLULAR OFDM SYSTEMS
MP1911	MP1911	US	12/116,640	5/7/2008	8467782	6/18/2013	In Force	System And Method For Multiple Packet Data Network Connectivity
MP1912	MP1912	US	12/116,589	5/7/2008	8301176	10/30/2012	In Force	INCREASING THE STAND-BY TIME OF WIRELESS DEVICES
MP1914	MP1914	US	11/952,849	12/7/2007	8083422	12/27/2011	In Force	HANDHELD TATTOO PRINTER
MP1916	MP1916	US	12/132,405	6/3/2008	8077350	12/13/2011	In Force	DEVICE AND METHOD FOR DISPENSING WHITE INK
MP1919	MP1919	US	12/116,658	5/7/2008	8225830	10/23/2012	In Force	System And Method For Enabling Authorization Of Local Breakout For
MP1922	MP1922	US	12/118,525	5/9/2008	8121085	2/21/2012	In Force	SLOT ALLOCATION METHOD AND APPARATUS FOR SLOT-BASED COM
MP1924	MP1924	US	12/118,566	5/9/2008	8041847	10/18/2011	In Force	PERIODIC AND CONDITIONAL EXECUTION OF DMA OPERATIONS
MP1924	MP1924C1	US	13/275,008	10/17/2011	8234418	7/31/2012	In Force	PERIODIC AND CONDITIONAL EXECUTION OF DMA OPERATIONS
MP1925T	MP1925TW	TW	97117233	5/9/2008			Pending	ELECTRICALLY ALTERABLE N/A-VOLATILE MEMORY AND ARRAY
MP1925W	MP1925WO	WO	PCT/US2008/06	5/9/2008			Pending	Electrically Alterable Non-Volatile Memory And Array
MP1928	MP1928	US	12/120,483	5/14/2008	8145976	3/27/2012	In Force	Error Correcting
MP1929	MP1929	US	12/152,675	5/14/2008	8081942	12/20/2011	In Force	METHOD AND APPARATUS FOR MUTEING BASED ON A CONSTANT M
MP1931	MP1931	US	12/122,097	5/16/2008	7835269	11/16/2010	In Force	Fast Link Down
MP1931	MP1931C1	US	12/945,262	11/12/2010	8589679	12/3/2013	In Force	Fast Link Down
MP1931	MP1931C1C1	US	14/095,433	12/3/2013	9154271	10/6/2015	In Force	Method And Apparatus For A Physical Layer Device (PHY) To Determini
MP1932	MP1932	US	12/125,874	5/22/2008	8022636	9/20/2011	In Force	CONTROL OF DELIVERY OF CURRENT THROUGH ONE OR MORE DISC
MP1932	MP1932C1	US	13/223,107	8/31/2011	8224378	10/23/2012	In Force	CONTROLLING A BRIGHTNESS OF A LAMP
MP1932	MP1932C1C1	US	13/649,999	10/11/2012	8536793	9/17/2013	In Force	CONTROLLING A CURRENT SUPPLIED TO A LOAD
MP1932	MP1932CN	CN	200880160094.1	5/22/2008	ZL2008801000	5/15/2013	In Force	CONTROL OF DELIVERY OF CURRENT THROUGH ONE OR MORE DISC
MP1932	MP1932D1	US	12/248,824	10/9/2008	7834558	11/16/2010	In Force	CONTROL OF DELIVERY OF CURRENT THROUGH ONE OR MORE DISC
MP1932	MP1932D1C1	US	12/946,798	11/15/2010	8222826	7/17/2012	In Force	CONTROL OF DELIVERY OF CURRENT THROUGH ONE OR MORE LAMP
MP1932	MP1932WOUS	US	12/302,012	5/22/2008	8116999	2/14/2012	In Force	CONTROL OF DELIVERY OF CURRENT THROUGH ONE OR MORE DISC
MP1932	MP1932WUSa	US	13/355,452	1/20/2012	8489349	7/16/2013	In Force	IDENTIFYING A CURRENT DRAWN BY A LOAD
MP1932W	MP1932WO	WO	PCT/US2008/06	5/22/2008			Pending	CONTROL OF DELIVERY OF CURRENT THROUGH ONE OR MORE DISC
MP1934	MP1934	US	12/121,656	5/15/2008	7965466	6/21/2011	In Force	EQUALIZATION AND DETECTION
MP1934	MP1934C1	US	13/162,011	6/16/2011	8174786	5/8/2012	In Force	EQUALIZATION AND DETECTION
MP1934	MP1934C1C1	US	13/453,567	4/23/2012	8416520	4/9/2013	In Force	EQUALIZATION AND DETECTION
MP1936	MP1936D1	US	12/854,737	8/11/2010	8046185	10/25/2011	In Force	OSCILLATOR CALIBRATION FOR MOTOR CONTROLLER
MP1937	MP1937	US	12/122,278	5/16/2008	8089939	1/3/2012	In Force	PREDICTIVE ROAMING BY A WIRELESS LAN CLIENT STATION
MP1938	MP1938	US	12/124,682	5/21/2008	815081	4/10/2012	In Force	SELF LEARNING ROAMING OPTIMIZATION

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MP1938	MP1938C1	US	13/428,057	3/23/2012	8630277	1/14/2014	In Force	SELF LEARNING ROAMING OPTIMIZATION
MP1938	MP1938C1C1	US	14/152,324	1/10/2014	8989147	3/24/2015	In Force	METHOD AND SYSTEM FOR SELECTING AN ACCESS POINT FOR A CLIE
MP1940	MP1940	US	12/125,670	5/22/2008	8423789	4/16/2013	In Force	Key Generation Techniques
MP1940	MP1940C1	US	13/883,122	4/15/2013	9037875	5/19/2015	In Force	Key Generation Techniques
MP1941	MP1941	US	12/123,222	5/19/2008	8130609	3/6/2012	In Force	System And Method Of Bias Control
MP1942	MP1942C1C1	US	13/099,792	5/3/2011	8154902	4/10/2012	In Force	Bit-line Decoder Architecture For NOR-Type Memory Array
MP1942W	MP1942WID1	JP	2013-161466	8/2/2013	5588803	8/22/2014	In Force	TREE TYPE BIT LINE DECODER ARCHITECTURE FOR NOR-TYPE MEMO
MP1942W	MP1942WOC	WO	PCT/US2008/06	5/27/2008			Pending	BIT LINE DECODER ARCHITECTURE FOR NOR-TYPE MEMORY ARRAY
MP1942W	MP1942WOCN	CN	200860017453.1	5/27/2008	ZL2008600174	8/21/2013	In Force	TREE TYPE BIT LINE DECODER ARCHITECTURE FOR NOR-TYPE MEMO
MP1942W	MP1942WQJP	JP	2010-509586	5/27/2008	5339541	8/16/2013	In Force	TREE TYPE BIT LINE DECODER ARCHITECTURE FOR NOR-TYPE MEMO
MP1943	MP1943	US	12/125,448	5/22/2008	8366245	11/6/2012	In Force	Multi-Mode Audio Amplifiers
MP1943T	MP1943TW	TW	97119156	5/23/2008	1454045	9/21/2014	In Force	Multi-Mode Audio Amplifiers
MP1943W	MP1943WO	WO	PCT/US2008/06	5/23/2008			Pending	Multi-Mode Audio Amplifiers
MP1948	MP1948	US	12/130,885	5/30/2008	8564121	8/6/2013	In Force	METHOD AND APPARATUS FOR REDUCING POWER CONSUMPTION I
MP1948	MP1948C1	US	13/960,433	8/6/2013	8649829	2/11/2014	In Force	METHOD AND APPARATUS FOR REDUCING POWER CONSUMPTION I
MP1948	MP1948C1C1	US	14/167,404	1/29/2014	8838188	9/16/2014	In Force	METHOD AND APPARATUS FOR REDUCING POWER CONSUMPTION I
MP1949	MP1949	US	12/129,473	5/29/2008	8130858	3/6/2012	In Force	METHOD AND APPARATUS FOR IMPLEMENTING TRANSMIT DIVERSIT
MP1949	MP1949C1	US	13/403,949	2/23/2012	8451931	5/28/2013	In Force	METHOD AND APPARATUS FOR IMPLEMENTING TRANSMIT DIVERSIT
MP1949	MP1949CIAA	US	14/309,657	6/19/2014	9215121	12/15/2015	In Force	METHOD AND APPARATUS FOR IMPLEMENTING TRANSMIT DIVERSIT
MP1949	MP1949C1C1	US	13/902,961	5/27/2013	8761302	6/24/2014	In Force	Method and Apparatus for implementing Transmit Diversity in OFDM
MP1950	MP1950	US	12/186,429	8/5/2008	8553561	10/8/2013	In Force	Quality Of Service For Mesh Networks
MP1950	MP1950C1	US	14/042,378	9/30/2013	9538417	1/3/2017	In Force	Quality Of Service For Mesh Networks
MP1952	MP1952	US	12/129,076	5/29/2008	8284940	10/23/2012	In Force	UPDATING DATA IN A ONE-TIME PROGRAMMABLE DATA STORAGE D
MP1952	MP1952C1	US	13/649,758	10/11/2012	8810846	8/19/2014	In Force	UPDATING DATA IN A ONE-TIME PROGRAMMABLE DATA STORAGE D
MP1954	MP1954	US	12/182,014	7/29/2008	7949817	5/24/2011	In Force	Adaptive Bus Profiler
MP1954	MP1954C1	US	13/092,734	4/22/2011	8285028	6/19/2012	In Force	Adaptive Bus Profiler
MP1956	MP1956	US	12/128,048	5/28/2008	8254911	8/28/2012	In Force	Estimation Of Direct Channel And Interference Channel
MP1956	MP1956C1	US	13/596,536	8/28/2012	8688637	7/30/2013	In Force	Estimation Of Direct Channel And Interference Channel
MP1956	MP1956C1ae	US	14/685,017	4/13/2015	9680592	6/13/2017	In Force	Method And Apparatus For Estimating A Gain Of A Channel In A Wirel
MP1956	MP1956C1C1	US	13/951,003	7/25/2013	9007950	4/14/2015	In Force	Estimation Of Direct Channel And Interference Channel
MP1960	MP1960	US	12/360,545	1/27/2009	8360651	10/30/2012	In Force	Channel Estimation With Co-Channel Pilots Suppression
MP1960	MP1960C1	US	13/662,877	10/29/2012	9729356	8/8/2017	In Force	Channel Estimation With Co-Channel Pilots Suppression
MP1961	MP1961	US	12/133,119	6/4/2008	7888973	2/15/2011	In Force	MATRIX TIME-TO-DIGITAL CONVERSION FREQUENCY SYNTHESIZER
MP1963	MP1963	US	12/131,730	6/2/2008	7852028	12/14/2010	In Force	VOICE COIL MOTOR CONTROL SYSTEM AND METHOD

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MP1963	MP1963C1	US	12/957,218	11/30/2010	8283882	10/9/2012	In Force	VOICE COIL MOTOR CONTROL SYSTEM AND METHOD
MP1965W	MP1965WO	WO	PCT/US2008/06	6/6/2008			Pending	Alignment Protection in Non-Volatile Memory and Array
MP1972	MP1972	US	12/135,712	6/9/2008	8133322	2/28/2012	In Force	MANUALLY OPERATED IMAGE TRANSLATION DEVICE
MP1977	MP1977	US	12/140,203	6/16/2008	7843931	11/30/2010	In Force	ISCSI SWITCHING METHOD AND APPARATUS
MP1978	MP1978	US	12/139,979	6/16/2008	8260246	9/4/2012	In Force	DATA RATE TRACKING FILTER
MP1978	MP1978C1	US	13/601,253	8/31/2012	8488606	7/30/2013	In Force	DATA RATE TRACKING FILTER
MP1979	MP1979	US	12/140,208	6/16/2008	8085440	12/27/2011	In Force	METHOD AND APPARATUS FOR SELECTING AN OPERATION TO BE PERFORMED
MP1979E	MP1979EP	EP	08771221.2	6/16/2008			Pending	Executing Operations in Connection with One or More Images with a
MP1979W	MP1979WOCN	CN	200860020301.1	6/16/2008	ZL2008600203	9/4/2013	In Force	EXECUTING OPERATIONS IN CONNECTION WITH ONE OR MORE IMAGES
MP1979W	MP1979WQJP	JP	2010-512434	6/16/2008	5327683	8/2/2013	In Force	EXECUTING OPERATIONS IN CONNECTION WITH ONE OR MORE IMAGES
MP1980	MP1980C1	US	12/811,257	8/30/2010	8226315	7/24/2012	In Force	ELECTRONIC PENS WITH DYNAMIC FEATURES
MP1981T	MP1981TW	TW	97123531	6/24/2008			Pending	SERVICE REQUEST DEVICE WIRELESS ACCESS DETACH AND BEARER D
MP1989W	MP1989WO	WO	PCT/US2008/06	6/24/2008			Pending	Service Request Device Wireless Access Detect and Bearer Deactivati
MP1990	MP1990	US	12/137,103	6/11/2008	8161199	4/17/2012	In Force	SMART PRINTER CARTRIDGE
MP1993J	MP1993JP	JP	2007-149135	6/5/2007			Pending	Disc Drive And Reproducing Head
MP1996	MP1996	US	12/145,965	6/25/2008	7701372	4/20/2010	In Force	Apparatus And Methods For Safe-Mode Delta-Sigma Modulators
MP1998	MP1998	US	12/163,405	6/27/2008	7994865	8/9/2011	In Force	Even Order Distortion Cancellation In Single-Ended Input, Differential
MP1998	MP1998D1	US	13/181,429	7/12/2011	8587381	11/19/2013	In Force	EVEN ORDER DISTORTION CANCELLATION IN SINGLE-ENDED INPUT,
MP1999	MP1999	US	12/163,801	6/27/2008	8234425	7/31/2012	In Force	Arbiter Module
MP2005	MP2005	US	12/147,255	6/26/2008	8004273	8/23/2011	In Force	SYSTEM FOR SIMPLIFIED CORRELATION OF INSTRUMENTATION PRO
MP2009	MP2009	US	12/048,957	3/14/2008	8164390	4/24/2012	In Force	REGULATING AN OPERATING CONDITION OF AN INTEGRATED CIRCUIT
MP2011	MP2011	US	12/166,880	7/2/2008	8085320	12/27/2011	In Force	Early Radial Distortion Correction
MP2011	MP2011C1	US	13/312,424	12/6/2011	8482628	7/9/2013	In Force	Early Radial Distortion Correction
MP2012	MP2012	US	12/167,804	7/3/2008	8628420	1/14/2014	In Force	Location Aware Ad-Hoc Gaming
MP2013	MP2013	US	12/167,442	7/3/2008	8018882	9/13/2011	In Force	Ad-Hoc Wireless Communication System With Variable ATIM Window
MP2013	MP2013C1	US	13/229,915	9/12/2011	8964550	2/24/2015	In Force	Ad-Hoc Wireless Communication System With Variable ATIM Window
MP2014	MP2014	US	12/167,911	7/3/2008	8077646	12/13/2011	In Force	EQUALIZATION IN A WIRELESS MULTIPLE ACCESS COMMUNICATION
MP2017	MP2017	US	12/168,594	7/7/2008	8107329	1/31/2012	In Force	OPTICAL STORAGE SYSTEM HAVING DIFFERENTIAL PHASE DETECTOR
MP2017	MP2017C1	US	12/436,291	5/6/2009	8089836	1/3/2012	In Force	DIFFERENTIAL PHASE DETECTOR
MP2017	MP2017C1C1	US	13/356,954	1/24/2012	8363517	1/29/2013	In Force	OPTICAL STORAGE SYSTEM HAVING DIFFERENTIAL PHASE DETECTOR
MP2018	MP2018	US	12/168,586	7/7/2008	8239731	8/7/2012	In Force	METHODS AND APPARATUS FOR PROVIDING MULTILEVEL COSET CO
MP2018	MP2018C1	US	13/548,409	7/13/2012	8402345	3/19/2013	In Force	METHODS AND APPARATUS FOR PROVIDING MULTILEVEL COSET CO
MP2024	MP2024	US	12/175,576	7/18/2008	7662500	4/6/2010	In Force	Apparatus And Methods For Mixed Analog-Digital Frequency Synthesi
MP2027	MP2027	US	12/169,044	7/8/2008	8085934	12/27/2011	In Force	Reverse Cryptographic Key Expansion

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MP2028	MP2028	US	12/170,649	7/10/2008	8125884	2/28/2012	In Force	Apparatus For Pre-Coding Using Multiple Codebooks And Associated
MP2028	MP2028C1	US	13/405,865	2/27/2012	8902816	12/2/2014	In Force	Apparatus For Pre-Coding Using Multiple Codebooks And Associated
MP2028	MP2028C1D1	US	14/556,626	12/1/2014	9313789	4/12/2016	In Force	Systems And Methods For Transmitting Using Pre-Coding With Multi
MP2030	MP2030	US	12/170,129	7/9/2008	8468428	6/18/2013	In Force	EARLY STOPPING RULES FOR NON-BINARY TURBO CODES
MP2033	MP2033	US	12/170,330	7/9/2008	8125490	2/28/2012	In Force	Systems and Methods for Reducing Display Under-Run and Conservin
MP2034	MP2034	US	12/171,873	7/11/2008	8379260	2/19/2013	In Force	LASER OSCILLATING MIRROR SUPPORT FOR COLOR PRINTER
MP2034	MP2034CN	CN	200880024618.4	7/11/2008	ZL2008800246	7/10/2013	In Force	LASER OSCILLATING MIRROR SUPPORT FOR COLOR PRINTER
MP2034E	MP2034EP	EP	8781714.4	7/11/2008			Pending	LASER OSCILLATING MIRROR SUPPORT FOR COLOR PRINTER
MP2034J	MP2034JP	JP	2010-516277	7/11/2008	5194282	2/15/2013	In Force	Method and apparatus related to laser oscillating mirror support for
MP2034W	MP2034WO	WO	PCT/US2008/06	7/11/2008			Pending	LASER OSCILLATING MIRROR SUPPORT FOR COLOR PRINTER
MP2036	MP2036	US	12/171,173	7/10/2008	8241993	8/14/2012	In Force	METHOD FOR SHALLOW TRENCH ISOLATION
MP2036T	MP2036TW	TW	97126647	7/14/2008	1400769	7/1/2013	In Force	METHOD FOR SHALLOW TRENCH ISOLATION
MP2036W	MP2036WO	WO	PCT/US2008/06	7/10/2008			Pending	METHOD FOR SHALLOW TRENCH ISOLATION
MP2037	MP2037	US	12/171,905	7/11/2008	8406552	3/26/2013	In Force	FAST IN-LOOP FILTERING IN VC-1
MP2040	MP2040	US	12/175,526	7/18/2008	8144647	3/27/2012	In Force	Access Point With Simultaneous Downlink Transmission Of Independ
MP2040	MP2040C1	US	13/430,170	3/26/2012	8588144	11/19/2013	In Force	Access Point With Simultaneous Downlink Transmission Of Independ
MP2040	MP2040C1aa	US	14/840,753	8/31/2015	9480064	10/25/2016	In Force	Method And Apparatus For Transmitting First Data Streams Via Resp
MP2040	MP2040C1C1	US	14/080,087	11/14/2013	9124402	9/1/2015	In Force	METHOD AND APPARATUS FOR TRANSMITTING FIRST DATA STREAM
MP2040W	MP2040W1AA	JP	2015-218535	11/6/2015	6061278	12/22/2016	In Force	ACCESS POINT WITH SIMULTANEOUS DOWNLINK TRANSMISSION OF
MP2040W	MP2040W1D1	JP	2013-171165	8/21/2013	6132431	4/28/2017	In Force	ACCESS POINT WITH SIMULTANEOUS DOWNLINK TRANSMISSION OF
MP2040W	MP2040WO	WO	PCT/US2008/07	7/18/2008			Pending	Access Point With Simultaneous Downlink Transmission Of Independ
MP2040W	MP2040WOCN	CN	200880024980	7/18/2008	ZL2008800249	8/7/2013	In Force	ACCESS POINT WITH SIMULTANEOUS DOWNLINK TRANSMISSION OF
MP2040W	MP2040WCEP	EP	EP08782051.0	7/18/2008	2171879	6/19/2019	In Force	Access Point With Simultaneous Downlink Transmission Of Independ
MP2040W	MP2040WCEPDE	DE	EP08782051.0	7/18/2008	602008060446	6/19/2019	In Force	Access Point With Simultaneous Downlink Transmission Of Independ
MP2040W	MP2040WCEPFR	FR	EP08782051.0	7/18/2008	2171879	6/19/2019	In Force	Access Point With Simultaneous Downlink Transmission Of Independ
MP2040W	MP2040WCEPGB	GB	EP08782051.0	7/18/2008	2171879	6/19/2019	In Force	Access Point With Simultaneous Downlink Transmission Of Independ
MP2040W	MP2040WCEJP	JP	2010-517184	7/18/2008	5350380	8/30/2013	In Force	ACCESS POINT WITH SIMULTANEOUS DOWNLINK TRANSMISSION OF
MP2040W	MP2040WUD1	EP	1300642.2	7/18/2008			Pending	Access Point with Simultaneous Downlink Transmission Of Independ
MP2041	MP2041	US	12/175,501	7/18/2008	8149811	4/3/2012	In Force	Wireless Network With Simultaneous Uplink Transmission Of Indepe
MP2041	MP2041aaa	US	15/053,586	2/25/2016	9628246	4/18/2017	In Force	Aggregating Acknowledgments Transmitted By An Access Point To A
MP2041	MP2041C1	US	13/428,282	3/23/2012	8958436	2/17/2015	In Force	Wireless Network With Simultaneous Uplink Transmission Of Indepe
MP2041	MP2041C1C1	US	14/619,652	2/11/2015	9294249	3/22/2016	In Force	Method And Apparatus For Aggregating Acknowledgments Transmitt
MP2041W	MP2041W1D1	JP	2012-165994	7/26/2012	5558800	8/22/2014	In Force	WIRELESS NETWORK WITH SIMULTANEOUS UPLINK TRANSMISSION
MP2041W	MP2041WO	WO	PCT/US2008/07	7/18/2008			Pending	Wireless Network With Simultaneous Uplink Transmission Of Indepe

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MP2041W	MP2041WOCN	CN	200880025080	7/18/2008	212008800250	9/28/2016	In Force	WIRELESS NETWORK AND CLIENT STATION
MP2041W	MP2041WOEP	EP	EP20080782054	7/18/2008	2183862	4/18/2018	In Force	Wireless Network with Simultaneous Uplink Transmission of Indepen
MP2041W	MP2041WOEPDE	DE	EP20080782054	7/18/2008	2183862	4/18/2018	In Force	Wireless Network with Simultaneous Uplink Transmission of Indepen
MP2041W	MP2041WOEPFR	FR	EP20080782054	7/18/2008	2183862	4/18/2018	In Force	Wireless Network with Simultaneous Uplink Transmission of Indepen
MP2041W	MP2041WOEPGB	GB	EP20080782054	7/18/2008	2183862	4/18/2018	In Force	Wireless Network with Simultaneous Uplink Transmission of Indepen
MP2041W	MP2041WOJP	JP	2010-517185	7/18/2008	5054193	8/3/2012	In Force	WIRELESS NETWORK WITH SIMULTANEOUS UPLINK TRANSMISSION
MP2042	MP2042	US	12/171,123	7/10/2008	8120632	2/21/2012	In Force	ALIGNMENT OF COLOR PLANES IN MULTI-BEAM MULTI-PASS PRINT
MP2042	MP2042C1	US	13/399,232	2/17/2012	8432424	4/30/2013	In Force	ALIGNMENT OF COLOR PLANES IN MULTI-BEAM MULTI-PASS PRINT
MP2043	MP2043	US	12/167,624	7/3/2008	8023492	9/20/2011	In Force	System And Method For Allocating An Anchoring Point For A Mobile
MP2043	MP2043C1	US	13/236,464	9/19/2011	8929265	1/6/2015	In Force	System And Method For Allocating An Anchoring Point For A Mobile
MP2043Q	MP2043QADE	DE	12007141.0	7/18/2008	2549779	3/14/2018	In Force	System for Allocating an Anchoring Point for a Mobile Terminal
MP2043Q	MP2043QAFR	FR	12007141.0	7/18/2008	2549779	3/14/2018	In Force	System for Allocating an Anchoring Point for a Mobile Terminal
MP2043Q	MP2043QAQB	GB	12007141.0	7/18/2008	2549779	3/14/2018	In Force	System for Allocating an Anchoring Point for a Mobile Terminal
MP2043T	MP2043TW	TW	97126649	7/14/2008	1439068	5/21/2014	In Force	SYSTEM AND METHOD FOR ALLOCATING AN ANCHORING POINT FOR
MP2043W	MP2043WO	WO	PCT/US2008/07	7/18/2008			Pending	System And Method For Allocating An Anchoring Point For A Mobile
MP2043W	MP2043WUD1	EP	12007141.0	7/18/2008	2549779	3/14/2018	In Force	System for Allocating an Anchoring Point for a Mobile Terminal
MP2044	MP2044	US	12/167,788	7/3/2008	7995528	8/9/2011	In Force	Precoding with Multi-user Codebooks
MP2044	MP2044C1	US	13/204,966	8/8/2011	8503384	8/6/2013	In Force	Precoding with Multi-user Codebooks
MP2044	MP20441	US	12/755,246	4/6/2010	8462716	6/11/2013	In Force	METHOD AND APPARATUS FOR USING MULTIPLE CODEBOOKS FOR
MP2044	MP20441C1	US	13/913,020	6/7/2013	9379795	6/28/2016	In Force	Method and Apparatus for Using Multiple Codebooks for Wireless Tr
MP2045	MP2045	US	12/175,265	7/17/2008	8548456	10/1/2013	In Force	ADAPTIVE MOBILITY MEASUREMENT FOR CONTINUOUS PACKET CO
MP2045	MP2045C1	US	14/041,726	9/30/2013	9374846	6/21/2016	In Force	ADAPTIVE MOBILITY MEASUREMENT FOR CONTINUOUS PACKET CO
MP2046	MP2046	US	12/175,730	7/18/2008	9042401	5/26/2015	In Force	Application-Layer Mechanism To Enable Power Enhancements In Mui
MP2051	MP2051	US	12/177,084	7/21/2008	8390226	3/5/2013	In Force	VOLTAGE MODE USING PSEUDO CURRENT LIMIT
MP2051	MP2051C1	US	13/783,569	3/4/2013	8765583	7/1/2014	In Force	VOLTAGE MODE USING PSEUDO CURRENT LIMIT
MP2054	MP2054	US	12/171,619	7/11/2008	8077686	12/13/2011	In Force	Multiple Packet Data Network Support Over Trusted Access
MP2054T	MP2054TW	TW	97127675	7/21/2008	1362201	4/11/2012	In Force	MULTIPLE PACKET DATA NETWORK SUPPORT OVER TRUSTED ACCES
MP2054W	MP2054WO	WO	PCT/US2008/07	7/18/2008			Pending	Multiple Packet Data Network Support Over Trusted Access
MP2055	MP2055	US	12/178,268	7/23/2008	8321706	11/27/2012	In Force	USB Self-Idling Techniques
MP2055	MP2055C1	US	13/683,056	11/21/2012	8839016	9/16/2014	In Force	USB Self-Idling Techniques
MP2055W	MP2055WO	WO	PCT/US2008/07	9/30/2008			Pending	USB Self-Idling Techniques
MP2056	MP2056	US	12/177,656	7/22/2008	8127159	2/28/2012	In Force	POWER MANAGEMENT IN A WIRELESS LOCAL AREA NETWORK
MP2056	MP2056C1	US	13/371,818	2/13/2012	8458502	6/4/2013	In Force	POWER MANAGEMENT IN A WIRELESS LOCAL AREA NETWORK
MP2056	MP2056C1AA	US	14/286,704	5/23/2014	9501126	11/22/2016	In Force	POWER MANAGEMENT IN A WIRELESS LOCAL AREA NETWORK

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MP2056	MP2056C1C1	US	13/908,302	6/3/2013	8738948	5/27/2014	In Force	POWER MANAGEMENT IN A WIRELESS LOCAL AREA NETWORK
MP2057	MP2057	US	12/181,172	7/28/2008	8214719	7/3/2012	In Force	LONG LATENCY PROTOCOL FOR HARD DISK CONTROLLER INTERFACE
MP2057	MP2057C1	US	13/540,313	7/12/2012	8489965	7/16/2013	In Force	LONG LATENCY PROTOCOL FOR HARD DISK CONTROLLER INTERFACE
MP2059	MP2059	US	12/182,833	7/30/2008	7865813	1/4/2011	In Force	RATE MATCHING FOR A WIRELESS COMMUNICATIONS SYSTEMS
MP2059	MP2059D1	US	12/962,493	12/7/2010	8392808	3/5/2013	In Force	RATE MATCHING FOR A WIRELESS COMMUNICATIONS SYSTEM
MP2059	MP2059D2	US	12/962,497	12/7/2010	8930801	1/6/2015	In Force	RATE MATCHING FOR A WIRELESS COMMUNICATIONS SYSTEM
MP2059	MP2059D2C1	US	13/830,020	3/14/2013	9088413	7/21/2015	In Force	RATE MATCHING FOR A WIRELESS COMMUNICATIONS SYSTEM
MP2060W	MP2060WO	WO	PCT/US2008/07	7/30/2008			Pending	System And Method For Establishing And Managing Multimedia Sessions
MP2061	MP2061	US	12/182,758	7/30/2008	8611317	12/17/2013	In Force	Simultaneously Maintaining Bluetooth and 802.11 Connections to In
MP2061	MP2061C1	US	14/107,857	12/16/2013	9538572	1/3/2017	In Force	SIMULTANEOUSLY MAINTAINING BLUETOOTH AND 802.11 CONNECT
MP2061E	MP2061EPD1	EP	EP11010015.3	7/30/2008	EP2434834	9/7/2016	In Force	Simultaneously Maintaining Bluetooth and 802.11 Connections to In
MP2061E	MP2061EPD1DE	DE	60 2008 046 231	7/30/2008	2434834	9/7/2016	In Force	SIMULTANEOUSLY MAINTAINING BLUETOOTH AND 802.11 CONNECT
MP2061E	MP2061EPD1FR	FR	11010015.3	7/30/2008	2434834	9/7/2016	In Force	SIMULTANEOUSLY MAINTAINING BLUETOOTH AND 802.11 CONNECT
MP2061E	MP2061EPD1GB	GB	11010015.3	7/30/2008	2434834	9/7/2016	In Force	SIMULTANEOUSLY MAINTAINING BLUETOOTH AND 802.11 CONNECT
MP2061W	MP2061WO	WO	PCT/US2008/07	7/30/2008			Pending	SIMULTANEOUSLY MAINTAINING BLUETOOTH AND 802.11 CONNECT
MP2061W	MP2061WOCN	CN	200880101439.1	7/30/2008	ZL2008801014	8/23/2013	In Force	SIMULTANEOUSLY MAINTAINING BLUETOOTH AND 802.11 CONNECT
MP2061W	MP2061WCEP	EP	08796877.2	7/30/2008	2186265	12/21/2011	In Force	SIMULTANEOUSLY MAINTAINING BLUETOOTH AND 802.11 CONNECT
MP2061W	MP2061WCEPDE	DE	8796877.2	7/30/2008	6020080	12/21/2011	In Force	SIMULTANEOUSLY MAINTAINING BLUETOOTH AND 802.11 CONNECT
MP2061W	MP2061WCEPFR	FR	8796877.2	7/30/2008	2186265	12/21/2011	In Force	SIMULTANEOUSLY MAINTAINING BLUETOOTH AND 802.11 CONNECT
MP2061W	MP2061WCEPGB	GB	8796877.2	7/30/2008	2186265	12/21/2011	In Force	SIMULTANEOUSLY MAINTAINING BLUETOOTH AND 802.11 CONNECT
MP2061W	MP2061WOCJP	JP	2010-520155	7/30/2008	5360029	6/28/2013	In Force	SIMULTANEOUSLY MAINTAINING BLUETOOTH AND 802.11 CONNECT
MP2062	MP2062	US	12/180,229	7/25/2008	8127209	2/28/2012	In Force	QC-LDPC DECODER WITH LIST-SYNDROME DECODING
MP2062	MP2062C1	US	13/397,479	2/15/2012	8301984	10/30/2012	In Force	QC-LDPC DECODER WITH LIST-SYNDROME DECODING
MP2062	MP2062C1C1	US	13/661,541	10/26/2012	9112532	8/18/2015	In Force	QC-LDPC DECODER WITH LIST-SYNDROME DECODING
MP2062	MP206211	US	13/493,760	6/11/2012	8799742	8/5/2014	In Force	QC-LDPC DECODER WITH LIST-SYNDROME DECODING
MP2063	MP2063	US	12/177,534	7/22/2008	8429729	4/23/2013	In Force	Secure Wireless Network Setup Using Multicast Packets
MP2063	MP2063C1	US	13/968,397	4/23/2013	9526000	12/20/2016	In Force	Secure Wireless Network Setup Using Multicast Packets
MP2068	MP2068	US	12/180,238	7/25/2008	8127104	2/28/2012	In Force	Alignment Matrix Memory Copy
MP2069	MP2069	US	12/186,898	8/6/2008	7683683	3/23/2010	In Force	Frequency Doubler With Duty-Cycle Correction And Associated Meth
MP2070	MP2070	US	12/186,016	8/5/2008	8160038	4/17/2012	In Force	Packet Data Network Specific Addressing Solutions With Network-Ba
MP2070	MP2070C1	US	12/221,989	8/8/2008	8161132	4/17/2012	In Force	Packet Data Network Specific Addressing Solutions With Network-Ba
MP2070	MP2070C1a	US	14/102,058	12/10/2013	8953609	2/10/2015	In Force	Packet Data Network-Specific Addressing Solutions For Clients Relyin
MP2070	MP2070C1C1	US	13/447,852	4/16/2012	8605572	12/10/2013	In Force	Packet Data Network Specific Addressing Solutions With Network-Ba
MP2073	MP2073	US	12/185,331	8/4/2008	8634344	1/21/2014	In Force	Dynamic Internet Protocol Addressing Solutions With Network-Based

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MP2073	MP2073C1	US	14/159,859	1/21/2014	8971255	3/3/2015	In Force	Method And Apparatus For Wireless Device With Multiple Wireless I
MP2073T	MP2073TW	TW	97129894	8/6/2008	1455548	10/1/2014	In Force	DYNAMIC INTERNET PROTOCOL ADDRESSING SOLUTIONS WITH NET
MP2073W	MP2073WO	WO	PCT/US2008/07	8/6/2008			Pending	Dynamic Internet Protocol Addressing Solutions With Network-Based
MP2073W	MP2073WOCN	CN	2008801023747	8/6/2008	ZL2008801023	5/21/2014	In Force	Dynamic Internet Protocol Addressing Solutions With Network-Based
MP2079	MP2079	US	12/188,056	8/7/2008	9555645	1/31/2017	In Force	CONTROLLING A PLURAUITY OF NOZZLES OF A HANDHELD PRINTER
MP2080	MP2080	US	12/188,107	8/7/2008	8335379	12/4/2012	In Force	POSITIONAL DATA ERROR CORRECTION
MP2080	MP2080C1	US	13/690,952	11/30/2012	8614826	12/24/2013	In Force	POSITIONAL DATA ERROR CORRECTION
MP2080J	MP2080JP	JP	2010-519282	8/7/2008			Pending	POSITIONAL DATA ERROR CORRECTION
MP2080W	MP2080WO	WO	PCT/US2008/07	8/7/2008			Pending	POSITIONAL DATA ERROR CORRECTION
MP2080W	MP2080WOCN	CN	2008801023121	8/7/2008	ZL2008801023	9/5/2012	In Force	POSITIONAL DATA ERROR CORRECTION
MP2080W	MP2080WOEP	EP	08797419.2	8/7/2008			Pending	Positional Data Error Correction
MP2082	MP2082	US	12/190,240	8/12/2008	8275314	9/25/2012	In Force	Bluetooth Scan Modes
MP2082	MP2082C1	US	13/604,563	9/5/2012	8649734	2/11/2014	In Force	Bluetooth Scan Modes
MP2083	MP2083	US	12/190,251	8/12/2008	8365782	2/5/2013	In Force	Bluetooth Wideband Scan Mode
MP2083	MP2083C1	US	13/757,276	2/1/2013	8897706	11/25/2014	In Force	Bluetooth Wideband Scan Mode
MP2084	MP2084	US	12/176,976	7/21/2008	7782751	8/24/2010	In Force	SYSTEMS AND METHODS FOR COMPUTING A RELATIVE PATH DELAY
MP2084	MP2084C1	US	12/835,904	7/14/2010	8379508	2/19/2013	In Force	SYSTEMS AND METHODS FOR COMPUTING A RELATIVE PATH DELAY
MP2084	MP2084C1C1	US	13/751,707	1/28/2013	8917584	12/23/2014	In Force	SYSTEMS AND METHODS FOR COMPUTING A RELATIVE PATH DELAY
MP2085	MP2085	US	12/187,160	8/6/2008	7782232	8/24/2010	In Force	ENCODING AND DECODING METHODS USING GENERALIZED CONCAT
MP2085	MP2085C1	US	12/861,693	8/23/2010	7978100	7/12/2011	In Force	ENCODING AND DECODING METHODS USING GENERALIZED CONCAT
MP2085	MP2085CN	CN	2008801023304	8/6/2008	ZL2008801023	3/12/2014	In Force	ENCODING AND DECODING USING GENERALIZED CONCATENATED C
MP2085T	MP2085TW	TW	97130354	8/8/2008	1375405	10/21/2012	In Force	ENCODING AND DECODING METHODS USING GENERALIZED CONCAT
MP2085W	MP2085WO	WO	PCT/US2008/07	8/5/2008			Pending	Encoding and Decoding Methods Using Generalized Concatenated Co
MP2086	MP2086	US	12/356,172	1/20/2009	8258766	9/4/2012	In Force	Power Management System With Digital Low Drop Out Regulator An
MP2086	MP2086C1	US	13/602,760	9/4/2012	8610417	12/17/2013	In Force	System With Device Startup Anticipated Voltage Supply For Voltage
MP2091	MP2091	US	12/186,262	8/5/2008	8254864	8/28/2012	In Force	DECREASING USER EQUIPMENT MEASUREMENT TIME
MP2092	MP2092	US	12/184,618	8/1/2008	7639165	12/29/2009	In Force	Calibrating Replica Digital-To-Analog Converters
MP2092	MP2092C1	US	12/645,712	12/23/2009	7852245	12/14/2010	In Force	Method And Apparatus For Calibrating A Replica Digital-To-Analog Co
MP2092T	MP2092TW	TW	97130587	8/11/2008	1462490	11/21/2014	In Force	Calibrating Replica Digital-To-Analog Converters
MP2092W	MP2092WO	WO	PCT/US2008/07	8/8/2008			Pending	CALIBRATING REPLICIA DIGITAL-TO-ANALOG CONVERTERS
MP2093	MP2093	US	12/188,841	8/8/2008	8255708	8/28/2012	In Force	APPARATUS AND METHODS FOR POWER SAVING IN USB DEVICES
MP2093	MP2093C1	US	13/595,433	8/27/2012	8762748	6/24/2014	In Force	APPARATUS AND METHODS FOR POWER SAVING IN USB DEVICES
MP2093	MP2093C1C1	US	14/312,359	6/23/2014	9665976	4/18/2017	In Force	APPARATUS AND METHODS FOR POWER SAVING IN USB DEVICES
MP2094	MP2094D1	US	14/223,942	3/24/2014	9240207	1/19/2016	In Force	MINIMIZATION OF VCM-RESOLUTION SWITCHING INDUCED POSITIO

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MP2095	MP2095	US	12/188,118	8/7/2008	7764457	7/27/2010	In Force	SPIRAL BAND END DETECTION
MP2095	MP2095C1	US	12/834,820	7/12/2010	8208216	6/26/2012	In Force	SPIRAL BAND END DETECTION
MP2095	MP2095C1C1	US	13/494,487	6/12/2012	8767340	7/1/2014	In Force	SPIRAL BAND END DETECTION
MP2095	MP2095C1D1	US	14/310,736	6/20/2014	9064517	6/23/2015	In Force	SPIRAL BAND END DETECTION
MP2098	MP2098C1	US	13/171,305	6/28/2011	8259542	4/9/2012	In Force	PLANT GAIN CALIBRATION FOR AN OPTICAL STORAGE SERVO SYSTEM
MP2099	MP2099	US	12/189,437	8/11/2008	8665694	3/4/2014	In Force	PILOT DESIGN FOR UNIVERSAL FREQUENCY REUSE IN CELLULAR ORT
MP2099	MP2099C1	US	14/174,486	2/6/2014	9184949	11/10/2015	In Force	PILOT DESIGN FOR UNIVERSAL FREQUENCY REUSE IN CELLULAR ORT
MP2099	MP2099CN	CN	200880103674.	8/12/2008	212008801036	2/4/2015	In Force	PILOT DESIGN FOR UNIVERSAL FREQUENCY REUSE IN CELLULAR ORT
MP2099E	MP2099EP	EP	08795273.5	8/12/2008	2188962	10/3/2012	In Force	Pilot Design for Universal Frequency Reuse in Cellular Orthogonal Fre
MP2099E	MP2099EPD1	EP	EP120050265	8/12/2008	2569267	2/11/2015	In Force	Pilot Design for Universal Frequency Reuse in Cellular Orthogonal Fre
MP2099E	MP2099EPD2	EP	EP130008543	8/12/2008	2567834	10/7/2015	In Force	Pilot Design for Universal Frequency Reuse in Cellular Orthogonal Fre
MP2099E	MP2099EPDE	DE	08795273.5	8/12/2008	2188962	10/3/2012	In Force	PILOT DESIGN FOR UNIVERSAL FREQUENCY REUSE IN CELLULAR ORT
MP2099E	MP2099EPR	FR	08795273.5	8/12/2008	2188962	10/3/2012	In Force	PILOT DESIGN FOR UNIVERSAL FREQUENCY REUSE IN CELLULAR ORT
MP2099E	MP2099EPGB	GB	08795273.5	8/12/2008	2188962	10/3/2012	In Force	PILOT DESIGN FOR UNIVERSAL FREQUENCY REUSE IN CELLULAR ORT
MP2099U	MP2099UADE	DE	12005026.5	8/12/2008	602008036654	2/11/2015	In Force	PILOT DESIGN FOR UNIVERSAL FREQUENCY REUSE IN CELLULAR ORT
MP2099U	MP2099UAFR	FR	12005026.5	8/12/2008	2569267	2/11/2015	In Force	PILOT DESIGN FOR UNIVERSAL FREQUENCY REUSE IN CELLULAR ORT
MP2099U	MP2099UAGB	GB	12005026.5	8/12/2008	2509267	2/11/2015	In Force	PILOT DESIGN FOR UNIVERSAL FREQUENCY REUSE IN CELLULAR ORT
MP2099U	MP2099UBDE	DE	13000854.3	8/12/2008	602008040596	10/7/2015	In Force	PILOT DESIGN FOR UNIVERSAL FREQUENCY REUSE IN CELLULAR ORT
MP2099U	MP2099UBFR	FR	13000854.3	8/12/2008	2567834	10/7/2015	In Force	PILOT DESIGN FOR UNIVERSAL FREQUENCY REUSE IN CELLULAR ORT
MP2099U	MP2099UBGB	GB	13000854.3	8/12/2008	2567834	10/7/2015	In Force	PILOT DESIGN FOR UNIVERSAL FREQUENCY REUSE IN CELLULAR ORT
MP2099W	MP2099WO	WO	PCT/US2008/00	8/12/2008			Pending	PILOT DESIGN FOR UNIVERSAL FREQUENCY REUSE IN CELLULAR ORT
MP2100	MP2100	US	12/128,135	7/23/2008	8077765	12/13/2011	In Force	Mixed Minimum Mean Square Error/Zero-Forcing FIR Filter Adaptatio
MP2101	MP2101	US	12/192,971	8/15/2008	8077422	12/13/2011	In Force	WRITING SPIRALS BASED ON SERVO TRACKS OF A DIFFERENT SAMPL
MP2101	MP2101C1	US	13/315,201	12/8/2011	8462458	6/11/2013	In Force	WRITING SPIRALS BASED ON SERVO TRACKS OF A DIFFERENT SAMPL
MP2102	MP2102	US	12/192,977	8/15/2008	8068304	11/29/2011	In Force	CALIBRATING SERVOS
MP2102	MP2102C1	US	13/280,448	11/7/2011	8184392	5/22/2012	In Force	CALIBRATING SERVOS
MP2102	MP2102C1C1	US	13/474,054	5/17/2012	8422158	4/16/2013	In Force	CALIBRATING SERVOS
MP2103	MP2103C1	US	12/838,016	7/16/2010	8194343	6/5/2012	In Force	CALIBRATING SERVOS
MP2103	MP2103C1C1	US	13/474,584	5/17/2012	8681449	3/25/2014	In Force	CALIBRATING SERVOS
MP2104	MP2104C1	US	12/972,798	12/20/2010	8085492	12/27/2011	In Force	HELICOID GROUP SWITCHING
MP2105	MP2105	US	12/192,901	8/15/2008	7999500	8/16/2011	In Force	CALIBRATING SPINDLE MOTOR CONTROLLERS
MP2105	MP2105C1	US	13/209,206	12/8/2011	8618764	12/31/2013	In Force	CALIBRATING SPINDLE MOTOR CONTROLLERS
MP2106	MP2106C1	US	13/195,442	8/1/2011	8395341	3/12/2013	In Force	REPETITIVE ERROR CORRECTION METHOD FOR DISK-DRIVE SPINDLE
MP2110	MP2110	US	12/187,858	8/7/2008	8186010	6/5/2012	In Force	GENERIC ENCODER FOR LOW-DENSITY PARITY-CHECK (LDPC) CODES

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MP2110	MP2110C1	US	13/482,489	5/29/2012	8595548	11/26/2013	In Force	GENERIC ENCODER FOR LOW-DENSITY PARITY-CHECK (LDPC) CODES
MP2110	MP2110C1aa	US	14/801,273	7/16/2015	9667272	5/30/2017	In Force	GENERIC ENCODER FOR LOW-DENSITY PARITY-CHECK (LDPC) CODES
MP2110	MP2110C1aaa	US	15/606,720	5/26/2017	10372530	6/8/2019	In Force	GENERIC ENCODER FOR LOW-DENSITY PARITY-CHECK (LDPC) CODES
MP2110	MP2110C1C1	US	14/084,944	11/20/2013	9088301	7/21/2015	In Force	GENERIC ENCODER FOR LOW-DENSITY PARITY-CHECK (LDPC) CODES
MP2111	MP2111	US	12/195,158	8/20/2008	8194768	6/5/2012	In Force	CHANNEL ESTIMATION USING LINEAR PHASE ESTIMATION
MP2111	MP2111C1	US	13/475,191	5/18/2012	8630371	1/14/2014	In Force	CHANNEL ESTIMATION USING LINEAR PHASE ESTIMATION
MP2112	MP2112C1	US	13/194,724	7/29/2011	8453684	7/23/2013	In Force	CALIBRATING VOICE COIL MOTORS
MP2113	MP2113	US	12/186,441	8/5/2008	7936530	5/3/2011	In Force	METHOD TO WRITE RAMP-TRACK
MP2114	MP2114	US	12/195,090	8/20/2008	8054574	1/10/2012	In Force	METHOD AND APPARATUS FOR WIRELESS RECEIVING
MP2114W	MP2114WO	WO	PCT/182008/003	8/20/2008			Pending	METHOD AND APPARATUS FOR WIRELESS RECEIVING
MP2115	MP2115	US	12/193,380	8/18/2008	7860951	9/21/2010	In Force	Threshold Voltage Digitizer For Array Of Programmable Threshold Tra
MP2115	MP2115C1	US	12/883,214	9/16/2010	8014206	9/6/2011	In Force	Threshold Voltage Digitizer For Array Of Programmable Threshold Tra
MP2115	MP2115C1aa	US	13/428,098	3/23/2012	8488398	7/16/2013	In Force	Threshold Voltage Digitizer For Array Of Programmable Threshold Tra
MP2115	MP2115C1C1	US	13/224,823	9/2/2011	8149626	4/3/2012	In Force	Threshold Voltage Digitizer For Array Of Programmable Threshold Tra
MP2115	MP2115C12a	US	13/943,080	7/16/2013	8717824	5/6/2014	In Force	Threshold Voltage Digitizer For Array Of Programmable Threshold Tra
MP2115T	MP2115TW	TW	97131835	8/20/2008	1473089	2/11/2015	In Force	THRESHOLD VOLTAGE DIGITIZER FOR ARRAY OF PROGRAMMABLE T
MP2115W	MP2115WID1	JP	2013-083911	4/12/2013	5737695	5/1/2015	In Force	THRESHOLD VOLTAGE DIGITIZER FOR ARRAY OF PROGRAMMABLE T
MP2115W	MP2115WO	WO	PCT/US2008/07	8/20/2008			Pending	Threshold Voltage Digitizer For Array Of Programmable Threshold Tra
MP2115W	MP2115WOJP	JP	2010-521997	8/20/2008	5252665	4/26/2013	In Force	THRESHOLD VOLTAGE DIGITIZER FOR ARRAY OF PROGRAMMABLE T
MP2115W	MP2115WOKR	KR	1020107003517	8/20/2008	101534788	7/1/2015	In Force	Threshold Voltage Digitizer For Array Of Programmable Threshold Tra
MP2116	MP2116CN	CN	2008102108461	8/20/2008	212008102108	6/26/2013	In Force	METHOD AND SYSTEM FOR OBJECT-ORIENTED DATA STORAGE
MP2116	MP2116I	US	12/964,971	12/10/2010	8583857	11/12/2013	In Force	METHOD AND SYSTEM FOR OBJECT-ORIENTED DATA STORAGE
MP2116I	MP2116ICN	CN	2010106083351	12/23/2010	212010106083	1/18/2017	In Force	METHOD AND SYSTEM FOR OBJECT-ORIENTED DATA STORAGE
MP2116I	MP2116IEP	EP	EP10196063.1	12/21/2010			Pending	Method and System for Object-Oriented Data Storage
MP2120	MP2120	US	12/197,527	8/25/2008	8164521	4/24/2012	In Force	PSEUDO-OMNI-DIRECTIONAL BEAMFORMING WITH MULTIPLE NARR
MP2120	MP2120C1	US	13/451,257	4/19/2012	8633855	1/21/2014	In Force	PSEUDO-OMNI-DIRECTIONAL BEAMFORMING WITH MULTIPLE NARR
MP2120	MP2120C1D1	US	14/159,185	1/20/2014	9749889	8/29/2017	In Force	PSEUDO-OMNI-DIRECTIONAL BEAMFORMING WITH MULTIPLE NARR
MP2120W	MP2120WO	WO	PCT/US2008/07	8/25/2008			Pending	PSEUDO-OMNI-DIRECTIONAL BEAMFORMING WITH MULTIPLE NARR
MP2120W	MP2120WCEP	EP	8827661.3	8/25/2008			Pending	PSEUDO-OMNI-DIRECTIONAL BEAMFORMING WITH MULTIPLE NARR
MP2121	MP2121	US	12/196,819	8/22/2008	8031526	10/4/2011	In Force	Write Pre-Compensation For Nonvolatile Memory
MP2121	MP2121C1	US	13/251,822	10/3/2011	8358541	1/22/2013	In Force	Write Pre-Compensation For Nonvolatile Memory
MP2121	MP2121C1aa	US	14/294,457	6/3/2014	9245632	1/26/2016	In Force	Write Pre-Compensation For Nonvolatile Memory
MP2121	MP2121C1C1	US	13/747,114	1/22/2013	8743616	6/3/2014	In Force	Write Pre-Compensation For Nonvolatile Memory
MP2123	MP2123	US	12/197,789	8/25/2008	8238968	8/7/2012	In Force	CAMERA SENSOR USAGE AS LUMINANCE METER FOR POWER SAVIN

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MP2128	MP2128	US	12/193,519	8/18/2008	7926360	11/2/2010	In Force	Adjusting Transmission Rates During Packet Expansion Using In Band
MP2128	MP2128C1	US	12/912,154	10/26/2010	841687	4/9/2013	In Force	Adjusting Transmission Rates During Packet Expansion Using In Band
MP2128	MP2128C1C1	US	13/858,773	4/8/2013	8897129	11/25/2014	In Force	Method and Apparatus for Implementing Flow Control in a Network
MP2129	MP2129C1	US	12/215,760	6/30/2008	9158355	10/13/2015	In Force	Dynamic Core Switching
MP2129T	MP2129TW	TW	97131838	8/20/2008	1484329	5/11/2015	In Force	DYNAMIC CORE SWITCHING
MP2129W	MP2129WO	WO	PCT/US2008/07	8/27/2008			Pending	Dynamic Core Switching
MP2129W	MP2129WOCN	CN	2008801048841	8/27/2008			Pending	DYNAMIC CORE SWITCHING
MP2129W	MP2129WCEP	EP	EP0828151.4	8/27/2008	2193419	10/31/2018	In Force	Dynamic Core Switching
MP2129W	MP2129WCEPDE	DE	EP0828151.4	8/27/2008	602008057721	10/31/2018	In Force	Dynamic Core Switching
MP2129W	MP2129WCEPFR	FR	EP0828151.4	8/27/2008	2193419	10/31/2018	In Force	Dynamic Core Switching
MP2129W	MP2129WCEPGB	GB	EP0828151.4	8/27/2008	2193419	10/31/2018	In Force	Dynamic Core Switching
MP2129W	MP2129WQJP	JP	2010-523107	8/27/2008	5257711	5/2/2013	In Force	DYNAMIC CORE SWITCHING
MP2130	MP2130	US	12/194,133	6/19/2008	8189381	5/29/2012	In Force	System And Method For Reading Flash Memory Cells
MP2130	MP2130D1	US	13/479,909	5/24/2012	8842470	9/23/2014	In Force	System And Method For Correcting Signals Read From A Memory Cell
MP2130	MP2130D1C1	US	14/492,418	9/22/2014	9196374	11/24/2015	In Force	System And Method For Detecting Data Stored in Multi-Bit Memory
MP2133	MP2133CN	CN	2008801049951	8/28/2008	212008301049	3/12/2014	In Force	DETERMINING POSITION AND VELOCITY OF A HANDHELD DEVICE
MP2133E	MP2133EP	EP	08798889.5	8/28/2008	2204040	11/9/2016	In Force	DETERMINING POSITION AND VELOCITY OF A HANDHELD DEVICE
MP2133W	MP2133WO	WO	PCT/US2008/07	8/28/2008			Pending	Systems and Methods for Determining Position and Velocity of a Han
MP2133W	MP2133WQJP	JP	2010-522107	8/28/2008	5449160	1/10/2014	In Force	DETERMINING POSITION AND VELOCITY OF A HANDHELD DEVICE
MP2133W	MP2133WUDE	DE	8798889.5	8/28/2008	2204040	11/9/2016	In Force	DETERMINING POSITION AND VELOCITY OF A HANDHELD DEVICE
MP2133W	MP2133WUFR	FR	8798889.5	8/28/2008	2204040	11/9/2016	In Force	DETERMINING POSITION AND VELOCITY OF A HANDHELD DEVICE
MP2133W	MP2133WUGB	GB	8798889.5	8/28/2008	2204040	11/9/2016	In Force	DETERMINING POSITION AND VELOCITY OF A HANDHELD DEVICE
MP2134	MP2134	US	12/191,616	6/14/2008	8085605	12/27/2011	In Force	Sequence Detection For Flash Memory With Inter-Cell Interference
MP2134	MP2134C1	US	13/335,037	12/22/2011	8339874	12/25/2012	In Force	Sequence Detection For Flash Memory With Inter-Cell Interference
MP2134	MP2134C1aa	US	14/257,529	4/21/2014	9208882	12/8/2015	In Force	System And Method For Reading Memory Cells By Accounting For Int
MP2134	MP2134C1C1	US	13/725,213	12/21/2012	8785285	4/22/2014	In Force	Sequence Detection For Flash Memory With Inter-Cell Interference
MP2134W	MP2134WO	WO	PCT/US2008/07	8/27/2008			Pending	SEQUENCE DETECTION FOR FLASH MEMORY WITH INTER-CELL INTER
MP2134W	MP2134WOCN	CN	2008801049401	8/27/2008	212008301049	9/3/2014	In Force	SEQUENCE DETECTION FOR FLASH MEMORY WITH INTER-CELL INTER
MP2135	MP2135	US	12/198,659	8/26/2008	8030098	10/4/2011	In Force	PRE-FORMED CONDUCTIVE BUMPS ON BONDING PADS
MP2135	MP2135D1	US	13/246,979	9/28/2011	8319353	11/27/2012	In Force	PRE-FORMED CONDUCTIVE BUMPS ON BONDING PADS
MP2136	MP2136	US	12/196,948	8/22/2008	8358013	1/22/2013	In Force	LEADLESS MULTI-CHIP MODULE STRUCTURE
MP2138	MP2138	US	12/204,316	9/4/2008	8816658	8/26/2014	In Force	Low-Dropout Converters With Feedback Compensation
MP2139	MP2139	US	12/198,527	8/26/2008	8843130	9/23/2014	In Force	DIRECTIONAL OVERLOAD COORDINATION FOR CELLULAR TELEPHON
MP2142	MP2142	US	12/569,089	9/29/2009	8255615	8/28/2012	In Force	FLEXIBLE SEQUENCE DESIGN ARCHITECTURE FOR SOLID STATE MEM

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MP2142	MP2142C1	US	13/595,616	8/27/2012	8626995	1/7/2014	In Force	FLEXIBLE SEQUENCE DESIGN ARCHITECTURE FOR SOLID STATE MEM
MP2142	MP2142C1C1	US	14/109,680	12/17/2013	9348536	5/24/2016	In Force	FLEXIBLE SEQUENCE DESIGN ARCHITECTURE FOR SOLID STATE MEM
MP2143	MP2143	US	12/203,691	9/3/2008	7911364	3/22/2011	In Force	INTERLEAVER FOR TURBO EQUALIZATION
MP2143	MP2143C1	US	13/038,141	3/1/2011	8004832	10/25/2011	In Force	INTERLEAVER FOR TURBO EQUALIZATION
MP2146	MP2146	US	12/204,706	9/4/2008	8908667	12/9/2014	In Force	METHOD AND APPARATUS FOR ANTENNA PATH SELECTION FOR MU
MP2146	MP2146C1	US	14/563,700	12/8/2014	9246522	1/26/2016	In Force	APPARATUS AND METHOD FOR SELECTING ANTENNA PATHS FOR MU
MP2147	MP2147	US	12/204,733	9/4/2008	8086213	12/27/2011	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS,
MP2147	MP2147C1C1	US	14/269,722	5/5/2014	9326235	4/26/2016	In Force	METHOD AND APPARATUS FOR WIRELESS ARBITER POWER SAVING
MP2149	MP2149	US	12/198,375	8/26/2008	7949812	5/24/2011	In Force	Priority Arbitration Of Coexisting Wireless Topologies
MP2149	MP2149C1	US	13/114,799	5/24/2011	8244944	8/14/2012	In Force	Priority Arbitration Of Coexisting Wireless Topologies
MP2149	MP2149C1C1	US	13/585,544	8/14/2012	8769176	7/1/2014	In Force	Priority Arbitration Of Coexisting Wireless Topologies
MP2150	MP2150	US	12/569,097	9/29/2009	8228084	7/24/2012	In Force	SYSTEMS AND METHODS FOR SELF-TESTING OF INTEGRATED DEVICE
MP2155	MP2155	US	12/207,350	9/9/2008	8155233	4/10/2012	In Force	MIMO DECODING IN THE PRESENCE OF VARIOUS INTERFERING SOUR
MP2155	MP2155C1	US	13/428,728	3/23/2012	8611452	12/17/2013	In Force	MIMO DECODING IN THE PRESENCE OF VARIOUS INTERFERING SOUR
MP2157	MP2157	US	12/197,764	8/25/2008	8090334	1/3/2012	In Force	HIGH ACCURACY PROGRAMMABLE GAIN AMPLIFIER
MP2157	MP2157D1	US	13/336,499	12/23/2011	8543072	9/24/2013	In Force	HIGH ACCURACY PROGRAMMABLE GAIN AMPLIFIER
MP2158	MP2158	US	12/206,978	9/9/2008	8599790	12/3/2013	In Force	Multiple Packet Data Network Support For Wireless Client Devices Wi
MP2158	MP2158C1	US	14/095,512	12/3/2013	9307477	4/5/2016	In Force	Apparatus And Method For Interfacing Wireless Client Device To Mul
MP2161	MP2161	US	12/209,577	9/12/2008	7724596	5/25/2010	In Force	Auto-Zero Current Sensing Amplifier
MP2161W	MP2161WO	WO	PCT/US2008/07	9/12/2008			Pending	Auto-Zero Current Sensing Amplifier
MP2162	MP2162C1	US	13/099,074	5/2/2011	8248120	8/21/2012	In Force	SYSTEMS, METHODS AND CIRCUITRY RELATING TO FREQUENCY DIVI
MP2164	MP2164	US	12/241,000	9/29/2008	8438356	5/7/2013	In Force	FLASH MEMORY CONTROLLER
MP2164	MP2164D1	US	13/874,218	4/30/2013	8645656	2/4/2014	In Force	FLASH MEMORY CONTROLLER
MP2164	MP2164D1C1	US	14/149,733	1/7/2014	9218284	12/22/2015	In Force	STORAGE DEVICE COMMAND INTERVAL CONTROLLER
MP2164T	MP2164TW	TW	97137662	10/1/2008	1500040	9/11/2015	In Force	FLASH MEMORY CONTROLLER
MP2164W	MP2164WO	WO	PCT/US2008/07	10/1/2008			Pending	FLASH MEMORY CONTROLLER
MP2167	MP2167	US	12/211,642	9/16/2008	8265583	9/11/2012	In Force	BLIND COMPENSATION FOR ARBITRARY RECEIVER NONLINEARITIES V
MP2168	MP2168C1	US	13/179,395	7/8/2011	8406105	3/26/2013	In Force	CALIBRATING OPTICAL DRIVE WRITE PARAMETERS DURING WRITING
MP2169	MP2169	US	12/235,333	9/22/2008	8139670	3/20/2012	In Force	MODULAR MIMO TRANSCIVER ARCHITECTURE
MP2171	MP2171	US	12/211,282	9/16/2008	8045569	10/25/2011	In Force	Mechanism To Verify Packet Data Network Support For Internet Prot
MP2171	MP2171C1	US	13/280,223	10/24/2011	8634394	1/21/2014	In Force	Mechanism To Verify Packet Data Network Support For Internet Prot
MP2172	MP2172	US	12/212,636	9/17/2008	8185713	5/22/2012	In Force	Flexible Sequencer Design Architecture For Solid State Memory Contr
MP2172T	MP2172TW	TW	97135781	9/18/2008	1444832	7/11/2014	In Force	Flexible Sequencer Design Architecture For Solid State Memory Contr
MP2172W	MP2172WO	KR	10-2010-760842	9/17/2008	10-1507975	3/26/2015	In Force	FLEXIBLE SEQUENCER DESIGN ARCHITECTURE FOR SOLID STATE ME

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MP2174	MP2174	US	12/235,418	9/22/2008	8340580	12/25/2012	In Force	METHOD AND APPARATUS FOR MANAGING COEXISTENCE INTERFER
MP2175	MP2175	US	12/235,169	9/22/2008	9107172	8/11/2015	In Force	METHOD AND APPARATUS FOR MANAGING COEXISTENCE INTERFER
MP2178	MP2178	US	12/209,794	9/12/2008	7746704	6/29/2010	In Force	Program-And-Erase Method For Multilevel Nonvolatile Memory
MP2181	MP2181	US	12/358,955	1/23/2009	8577305	11/5/2013	In Force	Circuits and Methods for Generating Oscillating Signals
MP2181	MP2181C1	US	14/071,171	11/4/2013	9401737	7/26/2016	In Force	Circuits and Methods for Generating Oscillating Signals
MP2187	MP2187	US	12/229,568	9/26/2008	7737793	6/15/2010	In Force	CALIBRATING CONTROL LOOPS
MP2187	MP2187C1	US	12/783,454	5/19/2010	7944313	5/17/2011	In Force	CALIBRATING CONTROL LOOPS
MP2193	MP2193	US	12/240,123	9/29/2008	8248848	8/21/2012	In Force	System And Methods For Multi-Level Nonvolatile Memory Read, Pro
MP2193	MP2193C1	US	13/590,230	8/21/2012	8792274	7/29/2014	In Force	Multi-Level Memory Cell Read, Program, And Erase Techniques
MP2199	MP2199	US	12/241,253	9/30/2008	8165176	4/24/2012	In Force	System And Method For Generating A Frequency Rich Spectrum In A
MP2203	MP2203	US	12/247,869	10/8/2008	7741928	6/22/2010	In Force	FREQUENCY MODULATION USING A DIGITAL FREQUENCY LOCKED LO
MP2204	MP2204	US	09/917,972	7/30/2001	7418642	8/26/2008	In Force	BUILT-IN-SELF-TEST USING EMBEDDED MEMORY AND PROCESSOR IN
MP2204	MP2204AAAA	US	13/683,760	11/21/2012	8566660	10/22/2013	In Force	BUILT-IN-SELF-TEST USING EMBEDDED MEMORY AND PROCESSOR IN
MP2204	MP2204C1	US	12/197,004	8/22/2008	7896828	2/15/2011	In Force	BUILT-IN-SELF-TEST USING EMBEDDED MEMORY AND PROCESSOR IN
MP2204	MP2204C1AA	US	13/277,740	10/20/2011	8321731	11/27/2012	In Force	BUILT-IN-SELF-TEST USING EMBEDDED MEMORY AND PROCESSOR IN
MP2204	MP2204C1C1	US	13/028,033	2/15/2011	8046652	10/25/2011	In Force	BUILT-IN-SELF-TEST USING EMBEDDED MEMORY AND PROCESSOR IN
MP2204	MP2204GB	GB	02003915.2	4/15/2002	1282041	6/14/2006	In Force	BUILT-IN-SELF-TEST USING EMBEDDED MEMORY AND PROCESSOR IN
MP2204	MP2204ZZC1	US	14/059,079	10/21/2013	0874983	10/28/2014	In Force	BUILT-IN-SELF-TEST USING EMBEDDED MEMORY AND PROCESSOR IN
MP2204D	MP2204DE	DE	02003915.2	4/15/2002	1282041	6/14/2006	In Force	BUILT-IN-SELF-TEST USING EMBEDDED MEMORY AND PROCESSOR IN
MP2204E	MP2204EP	EP	EP020039152	4/15/2002	EP1282041	6/14/2006	In Force	Built-In-Self-Test Using on Embedded Memory and a Processor in an
MP2204F	MP2204FR	FR	02003915.2	4/15/2002	1282041	6/14/2006	In Force	BUILT-IN-SELF-TEST USING EMBEDDED MEMORY AND PROCESSOR IN
MP2204K	MP2204KR	KR	4456112002	7/29/2002	932562	12/9/2009	In Force	BUILT-IN-SELF-TEST USING EMBEDDED MEMORY AND PROCESSOR IN
MP2204T	MP2204TW	TW	91103450	2/26/2002	1276107	3/11/2007	In Force	BUILT-IN-SELF-TEST USING EMBEDDED MEMORY AND PROCESSOR CI
MP2206	MP2206D1	US	12/845,675	7/28/2010	8098252	1/17/2012	In Force	PARALLEL VIDEO PROCESSING ARCHITECTURE
MP2206D	MP2206DE	DE	DE10200405135	10/21/2004			Pending	Parallele Videoverarbeitungsarchitektur
MP2207	MP2207	US	10/686,811	10/30/2003	7282967	10/16/2007	In Force	FIXED FREQUENCY CLOCK OUTPUT HAVING A VARIABLE HIGH FREQU
MP2207	MP2207C1	US	11/851,432	9/7/2007	7525351	4/28/2009	In Force	FIXED FREQUENCY CLOCK OUTPUT HAVING A VARIABLE HIGH FREQU
MP2207D	MP2207DE	DE	DE1020040331C	7/8/2004	1.02004E+11	7/16/2009	In Force	Fixed Frequency Clock Output Having a Variable High Frequency Inpu
MP2207J	MP2207JP	JP	2004-318340	11/1/2004	4494935	4/16/2010	In Force	FIXED FREQUENCY CLOCK OUTPUT HAVING A VARIABLE HIGH FREQU
MP2208D	MP2208DE	DE	DE10200501185	3/15/2005	DE1020050118	1/21/2010	In Force	Modulletter-Video-signalerzeugungsgenerator
MP2208J	MP2208JP	JP	2005-215634	7/26/2005	4527625	6/11/2010	In Force	A MODULATED VIDEO WAVEFORM GENERATOR
MP2209	MP2209	US	10/826,886	4/15/2004	7975094	7/5/2011	In Force	PROGRAMMABLE I/O INTERFACE
MP2209	MP2209C1	US	13/175,481	7/1/2011	8127070	2/28/2012	In Force	PROGRAMMABLE I/O INTERFACE
MP2209D	MP2209DE	DE	DE10200500442	1/31/2005	DE102005004	1/10/2013	In Force	Programmierbare I/O-Schnittstelle

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MP2209J	MP2209JP	JP	2005-108430	4/5/2005	5366288	9/20/2013	In Force	A PROGRAMMABLE I/O INTERFACE
MP2210	MP2210	US	10/994,011	11/19/2004	7380069	5/27/2008	In Force	METHOD AND APPARATUS FOR DMA-GENERATED MEMORY WRITE-
MP2211	MP2211	US	10/995,987	11/19/2004	7383363	6/3/2008	In Force	METHOD AND APPARATUS FOR INTERVAL DMA TRANSFER ACCESS
MP2211	MP2211C1	US	12/132,471	6/3/2008	7962673	6/14/2011	In Force	METHOD AND APPARATUS FOR ACCESSING A DATA BUS TO TRANSFER
MP2212	MP2212	US	10/938,098	9/10/2004	7340105	3/4/2008	In Force	METHOD AND APPARATUS FOR IMAGE PROCESSING
MP2214	MP2214	US	10/941,859	9/16/2004	7483595	1/27/2009	In Force	IMAGE PROCESSING METHOD AND DEVICE
MP2214	MP2214C1	US	12/350,771	1/8/2009	8135229	3/13/2012	In Force	IMAGE PROCESSING METHOD AND DEVICE
MP2215	MP2215	US	11/377,108	3/15/2006	7484188	1/27/2009	In Force	ON-CHIP TEST CIRCUIT AND METHOD FOR TESTING OF SYSTEM-ON-
MP2215	MP2215C1	US	12/350,802	1/8/2009	7941781	5/10/2011	In Force	ON-CHIP TEST CIRCUIT AND METHOD FOR TESTING OF SYSTEM-ON-
MP2216	MP2216	US	11/416,840	5/3/2006	7629675	12/8/2009	In Force	SYSTEM AND METHOD FOR ROUTING SIGNALS BETWEEN SIDE-BY-SI
MP2216	MP2216C1	US	12/621,377	11/18/2009	8039318	10/18/2011	In Force	SYSTEM AND METHOD FOR ROUTING SIGNALS BETWEEN SIDE-BY-SI
MP2217	MP2217	US	11/352,167	2/10/2006	7443011	10/28/2008	In Force	SYSTEM AND METHOD FOR ROUTING SUPPLY VOLTAGES OR OTHER
MP2217	MP2217D1	US	12/237,266	9/24/2008	7745263	6/29/2010	In Force	SYSTEM AND METHOD FOR ROUTING SUPPLY VOLTAGES OR OTHER
MP2217	MP2217D1C1	US	12/832,052	6/23/2010	8021928	9/20/2011	In Force	SYSTEM AND METHOD FOR ROUTING SUPPLY VOLTAGES OR OTHER
MP2218	MP2218	US	11/391,625	3/27/2006	7714450	5/11/2010	In Force	ON-DIE BOND WIRES SYSTEM AND METHOD FOR ENHANCING ROUT
MP2218	MP2218.D1	US	12/774,634	5/5/2010	8076164	12/13/2011	In Force	ON-DIE BOND WIRES SYSTEM AND METHOD FOR ENHANCING ROUT
MP2218	MP2218D1C1	US	13/323,729	12/12/2011	8592965	11/26/2013	In Force	ON-DIE BOND WIRES SYSTEM AND METHOD FOR ENHANCING ROUT
MP2222	MP2222	US	12/551,638	9/1/2009	8555421	10/22/2013	In Force	Block Cipher Improvements
MP2222	MP2222C1	US	14/060,066	10/22/2013	9112698	8/18/2015	In Force	Cryptographic Device And Method For Data Encryption With Per-Rou
MP2229	MP2229	US	12/249,248	10/10/2008	8164993	4/24/2012	In Force	METHOD AND APPARATUS FOR DETECTING LAND PRE-PTS
MP2229	MP2229C1	US	13/426,773	3/22/2012	8514678	8/20/2013	In Force	METHOD AND APPARATUS FOR DETECTING LAND PRE-PTS
MP2229	MP22291TW	TW	101112041	4/5/2012			Pending	METHOD AND APPARATUS FOR DETECTING LAND PRE-PTS
MP2229	MP22291W0	W0	PCT/US2012/03	4/4/2012			Pending	METHOD AND APPARATUS FOR DETECTING LAND PRE-PTS
MP2229W	MP2229W0	W0	PCT/US2008/01	10/10/2008			Pending	METHOD AND APPARATUS FOR DETECTING LAND PRE-PTS
MP2230	MP2230	US	12/184,196	7/31/2008	8125725	2/28/2012	In Force	RECORDING MEDIUM AND RECORDING APPARATUS FOR USING TEST
MP2230	MP2230D1	US	13/356,134	1/23/2012	8320068	11/27/2012	In Force	RECORDING MEDIUM AND RECORDING APPARATUS FOR USING TEST
MP2235	MP2235C1	US	13/113,303	5/23/2011	8270234	9/18/2012	In Force	Positive And Negative Voltage Level Shifter Circuit
MP2237	MP2237	US	12/251,367	10/14/2008	8225017	7/17/2012	In Force	Method and Apparatus for Reducing Power Supply Introduced Data
MP2238	MP2238	US	12/250,895	10/14/2008	8095717	1/10/2012	In Force	System And Method For Configuration Register Synchronization
MP2239	MP2239	US	12/285,987	10/17/2008	8281302	10/16/2012	In Force	STATE METRICS MEMORY REDUCTION IN A TURBO DECODER IMPL
MP2239	MP2239C1	US	13/651,853	10/15/2012	9077384	7/7/2015	In Force	STATE METRICS MEMORY REDUCTION IN A TURBO DECODER IMPL
MP2239W	MP2239W0	W0	PCT/IB2008/003	10/17/2008			Pending	STATE METRICS MEMORY REDUCTION IN A TURBO DECODER IMPL
MP2240	MP2240	US	12/285,992	10/17/2008	8214715	7/3/2012	In Force	HARDWARE IMPLEMENTATION OF QPP INTERLEAVER
MP2242	MP2242	US	12/252,965	10/16/2008	8331335	12/11/2012	In Force	LOCATION AWARE BACKGROUND ACCESS POINT SCANNING FOR WL

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MP2242	MP2242C1	US	13/709,709	12/10/2012	8611324	12/17/2013	In Force	LOCATION AWARE BACKGROUND ACCESS POINT SCANNING FOR WL
MP2242	MP2242C1C1	US	14/107,483	12/16/2013	8995414	3/31/2015	In Force	LOCATION AWARE BACKGROUND ACCESS POINT SCANNING FOR WL
MP2242W	MP2242WO	WO	PCT/US2008/08	10/16/2008			Pending	LOCATION AWARE BACKGROUND ACCESS POINT SCANNING FOR WL
MP2242W	MP2242WOCN	CN	200880112755	10/16/2008	ZL2008801127	4/30/2014	In Force	LOCATION AWARE BACKGROUND ACCESS POINT SCANNING FOR WL
MP2242W	MP2242WCEP	EP	8841773.8	10/16/2008			Pending	LOCATION AWARE BACKGROUND ACCESS POINT SCANNING FOR WL
MP2243	MP2243	US	12/285,993	10/17/2008	8107328	1/31/2012	In Force	METHOD AND APPARATUS FOR CALIBRATING OPTICAL RECORDING
MP2243	MP2243C1	US	13/358,970	1/26/2012	8385165	2/26/2013	In Force	METHOD AND APPARATUS FOR CALIBRATING OPTICAL RECORDING
MP2245	MP2245	US	12/356,391	1/20/2009	7948703	5/24/2011	In Force	ADAPTIVE TARGET OPTIMIZATION METHODS AND SYSTEMS FOR NO
MP2246	MP2246	US	12/254,482	10/20/2008	8458687	6/4/2013	In Force	Assisting a Basic Input/Output System
MP2246	MP2246C1	US	13/888,205	5/20/2013	9317300	4/19/2016	In Force	Assisting a Basic Input/Output System
MP2247	MP2247	US	12/254,506	10/20/2008	9354890	5/31/2016	In Force	Call Stack Structure for Enabling Execution of Code Outside of a Subr
MP2248	MP2248	US	12/258,351	10/24/2008	8094405	1/10/2012	In Force	REPETITIVE ERROR CORRECTION METHOD FOR DISK-DRIVE SERVO C
MP2249	MP2249	US	12/256,702	10/23/2008	8234320	7/31/2012	In Force	BITWISE COMPARATOR FOR SELECTING TWO SMALLEST NUMBERS F
MP2250	MP2250	US	12/259,130	10/27/2008	8054744	11/8/2011	In Force	METHODS AND APPARATUS FOR FLOW CLASSIFICATION AND FLOW
MP2250	MP2250C1	US	13/289,819	11/4/2011	8582428	11/12/2013	In Force	METHODS AND APPARATUS FOR FLOW CLASSIFICATION AND FLOW
MP2261	MP2261	US	12/169,986	7/9/2008	8411805	4/2/2013	In Force	Joint Estimation Of Channel And Preamble Sequence For Orthogonal
MP2261	MP2261C1	US	13/852,591	3/28/2013	9084133	7/14/2015	In Force	Joint Estimation Of Channel And Preamble Sequence For Orthogonal
MP2262	MP2262	US	12/245,981	10/6/2008	7839874	11/23/2010	In Force	System And Method For Reselection Of A Packet Data Network Gate
MP2262	MP2262C1	US	12/950,759	11/19/2010	8743747	6/3/2014	In Force	System And Method For Reselection Of A Packet Data Network Gate
MP2262	MP2262C1C1	US	14/293,513	6/2/2014	9042308	5/26/2015	In Force	System And Method For Connecting A Wireless Terminal To A Netwo
MP2262T	MP2262TW	TW	9713918	10/17/2008			Pending	A SYSTEM AND METHOD FOR RESELECTION OF A PACKET DATA NET
MP2262W	MP2262WO	WO	PCT/US2008/08	10/27/2008			Pending	A System And Method For Reselection Of A Packet Data Network Gat
MP2262W	MP2262WOCN	CN	2008801142248	10/27/2008	ZL2008801142	7/17/2013	In Force	A SYSTEM AND METHOD FOR RESELECTION OF A PACKET DATA NET
MP2262W	MP2262WCEP	EP	88450275	10/27/2008			Pending	A System And Method For Reselection Of A Packet Data Network Gat
MP2265	MP2265	US	12/262,787	10/31/2008	8768382	7/1/2014	In Force	Assisted GPS Data Management
MP2268	MP2268	US	12/258,087	10/24/2008	7756474	7/6/2010	In Force	METAL PROGRAMMABLE LOGIC AND MULTIPLE FUNCTION PIN INTE
MP2268	MP2268C1	US	12/929,290	7/1/2010	8295618	10/30/2012	In Force	METAL PROGRAMMABLE LOGIC AND MULTIPLE FUNCTION PIN INTE
MP2272	MP2272	US	12/260,956	10/29/2008	8331728	12/11/2012	In Force	METHODS AND APPARATUS FOR CORRELATING IMAGE FRAME STATI
MP2272	MP2272C1	US	13/708,631	12/7/2012	8483511	7/9/2013	In Force	METHODS AND APPARATUS FOR CORRELATING IMAGE FRAME STATI
MP2275	MP2275C1	US	13/190,333	7/25/2011	8259407	9/4/2012	In Force	Speed Control Systems And Methods For Decreasing Power Consump
MP2275	MP2275C1C1	US	13/602,879	9/4/2012	8638519	1/28/2014	In Force	Speed Control Systems And Methods For Decreasing Power Consump
MP2276	MP2276	US	12/255,468	10/21/2008	8154263	4/10/2012	In Force	CONSTANT GM CIRCUITS AND METHODS FOR REGULATING VOLTAGE
MP2277	MP2277H	US	13/528,703	6/20/2012	8727049	5/20/2014	In Force	EFFICIENT HYBRID VEHICLE
MP2278	MP2278	US	12/263,235	10/31/2008	8136016	3/13/2012	In Force	SPLIT SECTOR RECOVERY METHOD

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MP2278	MP2278C1	US	13/410,068	3/1/2012	8321763	11/27/2012	In Force	SPLIT SECTOR RECOVERY METHOD
MP2278	MP2278C1a8	US	14/137,671	12/20/2013	9143168	9/22/2015	In Force	METHOD AND SYSTEM FOR REMOVING INTERFERENCE CAUSED BY S
MP2280	MP2280	US	12/262,599	10/31/2008	8072023	12/6/2011	In Force	Isolation For Non-Volatile Memory Cell Array
MP2282	MP2282	US	12/260,527	10/29/2008	7936671	5/3/2011	In Force	Cable Far End Port Identification Using Repeating Link State Patterns
MP2282	MP2282C1	US	13/099,036	5/2/2011	8493864	7/23/2013	In Force	Cable Far End Port Identification Using Repeating Link State Patterns
MP2282	MP2282C1C1	US	13/945,224	7/18/2013	8934340	1/13/2015	In Force	Apparatus And Method For Identifying, Based On An Alternating Pat
MP2286	MP2286	US	12/266,351	11/6/2008	8045284	10/25/2011	In Force	Bit Patterned Magnetic Media Data Format
MP2286	MP2286D1	US	13/277,397	10/20/2011	8315093	11/20/2012	In Force	Bit Patterned Magnetic Media Data Format
MP2286W	MP2286WO	WO	PCT/US2008/08	11/6/2008			Pending	Bit Patterned Magnetic Media Data Format
MP2287	MP2287	US	12/269,744	11/12/2008	8542725	9/24/2013	In Force	Decision Feedback Equalization for Signals Having Unequally Distribut
MP2287	MP2287C1	US	13/966,753	8/14/2013	8968754	12/9/2014	In Force	Decision Feedback Equalization for Signals Having Unequally Distribut
MP2288	MP2288	US	12/270,982	11/14/2008	7869153	1/11/2011	In Force	Self Servo Write Tune Fixture For Preamps
MP2289	MP2289	US	12/265,434	11/5/2008	8077423	12/13/2011	In Force	FLY-HEIGHT TOUCHDOWN DETECTION CIRCUITS AND METHODS
MP2290	MP2290	US	12/269,312	11/12/2008	7964625	3/8/2011	In Force	Power Savings For Universal Serial Bus Devices
MP2291	MP2291	US	12/291,082	11/6/2008	8144606	3/27/2012	In Force	Interfacing Messages Between a Host and a Network
MP2292	MP2292C1	US	13/285,213	9/16/2011	8194344	6/5/2012	In Force	Position Gain Calibration In Disk Drives
MP2293	MP2293	US	12/236,075	9/23/2008	8175378	5/8/2012	In Force	Method and System for Noise Management for Spatial Processing in
MP2293W	MP2293WO	WO	PCT/US2008/07	9/23/2008			Pending	METHOD AND SYSTEM FOR NOISE MANAGEMENT FOR SPATIAL PRO
MP2294	MP2294	US	12/267,958	11/10/2008	8270369	9/18/2012	In Force	Service Data Unit Discard System For Radio Access Networks
MP2294	MP2294C1	US	13/613,913	9/13/2012	8767676	7/1/2014	In Force	Network Device With Handover Based Packet Management
MP2295	MP2295	US	12/271,761	11/14/2008	8171309	5/1/2012	In Force	Secure Memory Controlled Access
MP2296	MP2296	US	12/266,607	11/7/2008	8295163	10/23/2012	In Force	Reassigning signals to Cable Channels
MP2296	MP2296C1	US	13/654,826	10/18/2012	8717881	5/6/2014	In Force	Reassigning signals to Cable Channels
MP2298	MP2298	US	12/780,903	5/16/2010	8270543	9/18/2012	In Force	SCALING AND QUANTIZATION OF SOFT DECODING METRICS
MP2298	MP2298C1	US	13/572,703	8/13/2012	8472568	6/25/2013	In Force	SCALING AND QUANTIZATION OF SOFT DECODING METRICS
MP2301	MP2301	US	12/273,160	11/18/2008	8437410	5/7/2013	In Force	SYSTEM AND METHOD TO EXECUTE A CLIPPING INSTRUCTION
MP2301	MP2301C1	US	13/987,856	5/6/2013	8904850	8/12/2014	In Force	System and Method to Execute a Clipping Instruction
MP2302	MP2302C1	US	14/094,664	12/2/2013	9055019	6/9/2015	In Force	METHOD AND APPARATUS FOR MESSAGE MULTICASTING
MP2303	MP2303	US	12/271,818	11/14/2008	9229792	1/5/2016	In Force	METHOD AND APPARATUS FOR WEIGHTED MESSAGE PASSING
MP2308	MP2308	US	12/275,981	11/21/2008	8359462	1/22/2013	In Force	METHOD AND APPARATUS FOR PROGRAMMABLE COUPLING BETWE
MP2308	MP2308C1	US	13/759,512	1/11/2013	8756406	6/17/2014	In Force	METHOD AND APPARATUS FOR PROGRAMMABLE COUPLING BETWE
MP2309	MP2309	US	12/274,235	11/19/2008	8095775	1/10/2012	In Force	Instruction Pointers in Very Long Instruction Words
MP2311	MP2311	US	12/270,482	11/13/2008	8135853	3/13/2012	In Force	Streaming Data Engine
MP2313	MP2313	US	12/274,874	11/20/2008	8219887	7/10/2012	In Force	PARALLEL REED-SOLOMON RAID (RS-RAID) ARCHITECTURE, DEVICE,

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MP2313	MP2313C1	US	13/537,828	6/29/2012	8359524	1/22/2013	In Force	PARALLEL REED-SOLOMON RAID (RS-RAID) ARCHITECTURE, DEVICE,
MP2313	MP2313C1C1	US	13/746,779	1/22/2013	8645798	2/4/2014	In Force	PARALLEL REED-SOLOMON RAID (RS-RAID) ARCHITECTURE, DEVICE,
MP2313E	MP2313EP	EP	EP08854634.6	11/20/2008	2212796	2/27/2019	In Force	Parallel Reed-Solomon RAID (RS-RAID) Architecture, Device, and Met
MP2313E	MP2313EPD1	EP	EP13000066.4	11/20/2008			Pending	Parallel Reed-Solomon RAID (RS-RAID) Architecture, Device, and Met
MP2313E	MP2313EPDE	DE	EP08854634.6	11/20/2008	602008059166	2/27/2019	In Force	Parallel Reed-Solomon RAID (RS-RAID) Architecture, Device, and Met
MP2313E	MP2313EPRR	FR	EP08854634.6	11/20/2008	2212796	2/27/2019	In Force	Parallel Reed-Solomon RAID (RS-RAID) Architecture, Device, and Met
MP2313E	MP2313EPGB	GB	EP08854634.6	11/20/2008	2212796	2/27/2019	In Force	Parallel Reed-Solomon RAID (RS-RAID) Architecture, Device, and Me
MP2313J	MP2313JP	JP	2010-534958	11/20/2008	5288393	6/25/2013	In Force	PARALLEL REED-SOLOMON RAID (RS-RAID) ARCHITECTURE, DEVICE,
MP2313K	MP2313KR	KR	10-2010-701026	11/20/2008	10-1543369	8/4/2015	In Force	PARALLEL REED-SOLOMON RAID (RS-RAID) ARCHITECTURE, DEVICE,
MP2313W	MP2313WO	WO	PCT/US2008/01	11/20/2008			Pending	PARALLEL REED-SOLOMON RAID (RS-RAID) ARCHITECTURE, DEVICE,
MP2319	MP2319	US	12/275,024	11/20/2008	8042073	10/18/2011	In Force	SORTED DATA OUTLIER IDENTIFICATION
MP2319	MP2319D1	US	13/274,520	10/17/2011	8397202	3/12/2013	In Force	SORTED DATA OUTLIER IDENTIFICATION
MP2320	MP2320	US	12/333,247	11/25/2008	8049983	11/1/2011	In Force	METHODS AND APPARATUS FOR PERFORMING INTERPOLATED TIME
MP2320	MP2320C1	US	13/285,772	10/31/2011	8395858	3/12/2013	In Force	METHODS AND APPARATUS FOR PERFORMING INTERPOLATED TIME
MP2324	MP2324C1	US	12/848,839	8/2/2010	8114758	2/14/2012	In Force	METHOD FOR AVOIDING DIE CRACKING
MP2325	MP2325	US	12/277,118	11/24/2008	8219868	7/10/2012	In Force	QUASI-CYCLIC LOW-DENSITY PARITY-CHECK (QC-LDPC) ENCODER
MP2325	MP2325I1	US	12/553,584	9/3/2009	8473824	6/25/2013	In Force	QUASI-CYCLIC LOW-DENSITY PARITY-CHECK (QC-LDPC) ENCODER
MP2326	MP2326	US	12/277,058	11/24/2008	7911253	3/22/2011	In Force	HYBRID CLOCK NETWORK
MP2328	MP2328	US	12/323,995	11/26/2008	8181081	5/15/2012	In Force	System And Method For Decoding Correlated Data
MP2328	MP2328C1	US	13/470,495	5/14/2012	8321749	11/27/2012	In Force	System And Method For Decoding Correlated Data
MP2328	MP2328C1a8	US	14/065,751	10/29/2013	8806289	8/12/2014	In Force	Decoder And Decoding Method For A Communication System
MP2328	MP2328C1C1	US	13/684,895	11/26/2012	8572454	10/29/2013	In Force	System And Method For Decoding Correlated Data
MP2329	MP2329	US	12/327,627	12/3/2008	8219878	7/10/2012	In Force	POST-PROCESSING DECODER OF LDPC CODES FOR IMPROVED ERROR
MP2329	MP2329C1	US	13/494,747	6/12/2012	8484531	7/9/2013	In Force	POST-PROCESSING DECODER OF LDPC CODES FOR IMPROVED ERROR
MP2329	MP2329C1C1	US	13/915,146	6/11/2013	8760973	4/15/2014	In Force	POST-PROCESSING DECODER OF LDPC CODES FOR IMPROVED ERROR
MP2332	MP2332	US	12/417,187	4/2/2009	8433778	4/30/2013	In Force	Device Configuration
MP2333	MP2333	US	12/329,581	12/6/2008	8307268	11/6/2012	In Force	ITERATIVE DECODER SYSTEMS AND METHODS
MP2333	MP2333C1	US	13/648,790	10/10/2012	8661325	2/25/2014	In Force	ITERATIVE DECODER SYSTEMS AND METHODS
MP2333T	MP2333TW	TW	97147732	12/8/2008			Pending	ITERATIVE DECODER SYSTEMS AND METHODS
MP2333W	MP2333WO	WO	PCT/US2008/01	12/8/2008			Pending	ITERATIVE DECODER SYSTEMS AND METHODS
MP2335	MP2335C1	US	13/226,312	9/19/2011	8278689	10/2/2012	In Force	Memory And Interconnect Design In Fine Pitch
MP2335	MP2335C1a8	US	13/934,312	7/3/2013	8710553	4/29/2014	In Force	Methods And Apparatus For Connecting Signal Lines Of Multiple Layer
MP2335	MP2335C1C1	US	13/613,680	9/13/2012	8482039	7/9/2013	In Force	Memory And Interconnect Design In Fine Pitch
MP2336	MP2336	US	12/105,988	4/18/2008	8120088	2/21/2012	In Force	Non-Volatile Memory Cell And Array

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MP2337	MP2337C1	US	14/284,931	5/22/2014	9537392	1/3/2017	In Force	Circuits and Methods for Dynamic Voltage Management
MP2338	MP2338	US	12/330,823	12/9/2008	8243752	8/14/2012	In Force	LONG-REACH ETHERNET FOR 100GBASE-T AND 10GBASE-T
MP2338	MP2338C1	US	13/565,216	8/14/2012	8824502	9/2/2014	In Force	LONG-REACH ETHERNET FOR 100GBASE-T AND 10GBASE-T
MP2338T	MP2338TW	TW	97148224	12/11/2008	1462527	11/21/2014	In Force	LONG-REACH ETHERNET FOR 100GBASE-T AND 10GBASE-T
MP2338W	MP2338WO	WO	PCT/US2008/06	12/11/2008			Pending	Long-Reach Ethernet for 100GBase-T And 10GBase-T
MP2338W	MP2338WOBK	BR	P10821511-1	12/11/2008	N		Pending	Long-Reach Ethernet for 100GBase-T And 10GBase-T
MP2338W	MP2338WOCN	CN	2008801204225	12/11/2008	Z12008801204	7/23/2014	In Force	LONG-REACH ETHERNET FOR 100GBASE-T AND 10GBASE-T
MP2338W	MP2338WCEP	EP	EP088603758	12/11/2008			Pending	Long-Reach Ethernet for 100GBase-T and 10GBase-T
MP2338W	MP2338WGP	JP	2010-538154	12/11/2008	5250828	4/26/2013	In Force	LONG-REACH ETHERNET FOR 100GBASE-T AND 10GBASE-T
MP2344	MP2344CN	CN	2008801240884	12/17/2008	Z12008801240	1/2/2013	In Force	MOS DEVICE WITH LOW ON-RESISTANCE
MP2344W	MP2344WO	WO	PCT/US2008/08	12/17/2008			Pending	MOS DEVICE WITH LOW ON-RESISTANCE
MP2345	MP2345	US	12/331,283	12/9/2008	7859244	12/28/2010	In Force	SELF CONFIGURING VOLTAGE REGULATORS
MP2346	MP2346	US	12/330,751	12/9/2008	8513952	8/20/2013	In Force	SUB-SYMBOL RATE CABLE TESTER
MP2346	MP2346C1	US	13/958,988	8/5/2013	8665767	3/11/2014	In Force	SUB-SYMBOL RATE CABLE TESTER
MP2346	MP2346C1C1	US	14/200,707	3/7/2014	9267984	2/23/2016	In Force	SUB-SYMBOL RATE CABLE TESTER
MP2346T	MP2346TW	TW	97148226	12/11/2008			Pending	SUB-SYMBOL RATE CABLE TESTER
MP2346W	MP2346WO	WO	PCT/US2008/06	12/9/2008			Pending	SUB-SYMBOL RATE CABLE TESTER
MP2347	MP2347	US	12/333,292	11/25/2008	8588705	11/19/2013	In Force	SYSTEM AND METHOD OF DETERMINING POWER OVER ETHERNET I
MP2347	MP2347C1	US	14/082,981	11/18/2013	9148200	9/29/2015	In Force	Determining Power Over Ethernet Impairment
MP2349	MP2349	US	12/330,216	12/8/2008	8306163	11/6/2012	In Force	METHOD AND APPARATUS FOR AUTOMATIC GAIN CONTROL
MP2350	MP2350	US	12/334,160	12/12/2008	8429483	4/23/2013	In Force	EDGE-BASED DECODERS FOR LOW-DENSITY PARITY-CHECK CODES
MP2353	MP2353	US	12/180,930	7/28/2008	7788352	8/3/2010	In Force	HIGH-SPEED, MULTI-STAGE CLASS AB AMPLIFIERS
MP2353	MP2353C1	US	12/849,561	8/3/2010	7994859	8/9/2011	In Force	HIGH-SPEED, MULTI-STAGE CLASS AB AMPLIFIERS
MP2353	MP2353D1	US	12/849,238	8/3/2010	8022767	9/20/2011	In Force	HIGH-SPEED, MULTI-STAGE CLASS AB AMPLIFIERS
MP2353T	MP2353TW	TW	97147419	12/5/2008	1456691	10/11/2014	In Force	High-Speed, Multi-Stage Class AB Amplifiers
MP2353W	MP2353WO	WO	PCT/US2008/08	10/28/2008			Pending	High-Speed, Multi-Stage Class AB Amplifiers
MP2353W	MP2353WOCN	CN	2008801209651	10/28/2008	Z12008801209	9/11/2013	In Force	HIGH-SPEED, MULTI-STAGE CLASS AB AMPLIFIERS
MP2360	MP2360	US	12/336,308	12/16/2008	8164846	4/24/2012	In Force	HYBRID DEFECT DETECTION FOR RECORDING CHANNELS
MP2360	MP2360C1	US	13/419,070	3/13/2012	8339720	12/25/2012	In Force	HYBRID DEFECT DETECTION FOR RECORDING CHANNELS
MP2360	MP2360C1aa	US	14/067,381	10/30/2013	8824074	9/2/2014	In Force	HYBRID DEFECT DETECTION FOR RECORDING CHANNELS
MP2360	MP2360C1C1	US	13/719,312	12/19/2012	8533747	11/26/2013	In Force	HYBRID DEFECT DETECTION FOR RECORDING CHANNELS
MP2361	MP2361	US	12/331,050	12/9/2008	8050157	11/1/2011	In Force	TIMING RECOVERY FOR OPTICAL DISC DRIVE HIGH FREQUENCY MOD
MP2362	MP2362	US	12/333,267	11/25/2008	8156415	4/10/2012	In Force	METHOD AND SYSTEM FOR COMMAND QUEUING IN DISK DRIVES
MP2362	MP2362C1	US	13/441,492	4/6/2012	8566652	10/22/2013	In Force	COMMAND QUEUING IN DISK DRIVES

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MP2362	MP2362C1C1	US	14/046,696	10/4/2013	9021147	4/28/2015	In Force	Command Queuing in Disk Drives
MP2363	MP2363	US	12/463,473	5/11/2009	8331289	12/11/2012	In Force	BLUETOOTH / WI-FI COEXISTENCE
MP2363	MP2363C1	US	13/710,784	12/11/2012	8804624	8/12/2014	In Force	WIRELESS COMMUNICATION DEVICE WITH TRANSMISSION CHARACTERISTICS
MP2366	MP2366C1	US	12/945,112	11/12/2010	8045287	10/25/2011	In Force	Self-Servo-Write Method With Repeatable Runout Compensation
MP2367	MP2367	US	12/350,732	1/8/2009	8279727	10/2/2012	In Force	SYSTEMS AND METHODS FOR REDUCING SPINDLE RECTIFICATION S
MP2368	MP2368	US	12/333,854	12/12/2008	7870323	1/11/2011	In Force	BRIDGE CIRCUIT FOR INTERFACING PROCESSOR TO MAIN MEMORY A
MP2368T	MP2368TW	TW	97150740	12/25/2008	1451262	9/1/2014	In Force	Bridge Circuit
MP2368W	MP2368WO	WO	PCT/IB2008/003	12/23/2008			Pending	Bridge Circuit
MP2368W	MP2368WOCN	CN	2008601232032	12/23/2008	ZL2008601232	2/19/2014	In Force	BRIDGE CIRCUIT INTERFACING A PROCESSOR TO EXTERNAL DEVICES
MP2368W	MP2368WOJP	JP	2010-540184	12/23/2008	4972212	4/13/2012	In Force	BRIDGE CIRCUIT
MP2369	MP2369	US	12/337,706	12/18/2008	8972247	3/3/2015	In Force	SELECTION OF SPEECH ENCODING SCHEME IN WIRELESS COMMUNIC
MP2369	MP2369CN	CN	Z008301232041	12/21/2008	ZL2008301232	4/9/2014	In Force	SELECTION OF SPEECH ENCODING SCHEME IN WIRELESS COMMUNIC
MP2369E	MP2369EP	EP	08864909.0	12/21/2008	EP2232281	9/14/2016	In Force	Selection of Speech Encoding Scheme in Wireless Communication Ter
MP2369E	MP2369EPDE	DE	08864909.0	12/21/2008	602008046331	9/14/2016	In Force	SELECTION OF SPEECH ENCODING SCHEME IN WIRELESS COMMUNIC
MP2369E	MP2369EPFR	FR	08864909.0	12/21/2008	2232281	9/14/2016	In Force	SELECTION OF SPEECH ENCODING SCHEME IN WIRELESS COMMUNIC
MP2369E	MP2369EPGB	GB	08864909.0	12/21/2008	2232281	9/14/2016	In Force	SELECTION OF SPEECH ENCODING SCHEME IN WIRELESS COMMUNIC
MP2369J	MP2369JP	JP	2010-540217	12/21/2008			Pending	SELECTION OF SPEECH ENCODING SCHEME IN WIRELESS COMMUNIC
MP2370	MP2370C1	US	13/714,606	8/22/2011	8274750	9/25/2012	In Force	Predictive Methods And Systems For Sampling Data Read From A Rot
MP2370	MP2370C1C1	US	13/625,427	9/24/2012	8537483	9/17/2013	In Force	Method And Apparatus For Adjusting A Sampling Frequency Of A Dat
MP2371	MP2371	US	12/337,564	12/17/2008	8165247	4/24/2012	In Force	UNFOLDED DECISION-DIRECTED LOOP, ARCHITECTURES, APPARATUS
MP2371	MP2371C1	US	13/431,039	3/27/2012	8565353	10/22/2013	In Force	UNFOLDED DECISION-DIRECTED LOOP, ARCHITECTURES, APPARATUS
MP2374	MP2374	US	12/330,927	12/9/2008	8036170	10/11/2011	In Force	SYSTEMS AND METHODS FOR CONTROLLING THE TRANSMISSION AN
MP2374	MP2374C1	US	13/269,112	10/7/2011	8565198	10/22/2013	In Force	Systems and Methods for Controlling the Transmission and Receptio
MP2374	MP2374C1C1	US	14/035,028	9/24/2013	8998658	12/9/2014	In Force	Systems and Methods for Controlling the Transmission and Receptio
MP2375	MP2375	US	12/339,128	12/5/2008	8074112	12/6/2011	In Force	MEMORY BACKUP USED IN A RAID SYSTEM
MP2376	MP2376	US	12/336,280	12/16/2008	8312354	11/13/2012	In Force	METHOD AND APPARATUS FOR IMPROVED PERFORMANCE OF ITERA
MP2376	MP2376C1	US	13/674,767	11/12/2012	9438276	9/6/2016	In Force	METHOD AND APPARATUS FOR IMPROVED PERFORMANCE OF ITERA
MP2379	MP2379	US	12/338,561	12/4/2008	8739009	5/27/2014	In Force	METHODS AND APPARATUS FOR DEFECT DETECTION AND CORRECTI
MP2379	MP2379C1	US	14/262,300	4/25/2014	9098411	8/4/2015	In Force	METHODS AND APPARATUS FOR DEFECT DETECTION AND CORRECTI
MP2380	MP2380C1	US	13/012,725	1/24/2011	8477623	7/2/2013	In Force	Message Processing
MP2381	MP2381	US	12/334,796	12/15/2008	8341454	12/25/2012	In Force	RENDERING A VIDEO STREAM BASED ON A DIGITAL CLOCK GENERAT
MP2381	MP2381C1	US	13/721,690	12/20/2012	8595540	11/26/2013	In Force	RENDERING A CONTENT STREAM BASED ON A DIGITAL CLOCK GENER
MP2382	MP2382	US	12/340,911	12/22/2008	8116110	2/14/2012	In Force	Array Architecture Including Mirrored Segments For Nonvolatile Me
MP2383	MP2383	US	12/329,088	12/5/2008	9013999	4/21/2015	In Force	METHOD AND APPARATUS FOR EGRESS JITTER PACER

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MP2385	MP2385	US	12/344,218	12/12/2008	8175015	5/8/2012	In Force	WINNAY MAC
MP2385	MP2385D1	US	13/465,907	5/7/2012	8897293	11/25/2014	In Force	MAC PROCESSOR ARCHITECTURE
MP2386	MP2386	US	12/340,076	12/19/2008	7944277	5/17/2011	In Force	CIRCUIT AND METHODS OF ADAPTIVE CHARGE-PUMP REGULATION
MP2389	MP2389	US	12/350,052	1/7/2009	8231292	10/16/2012	In Force	OPTIMIZING ERROR FLOOR PERFORMANCE OF FINITE-PRECISION LAY
MP2389	MP2389D1	US	13/610,438	9/11/2012	8769382	7/1/2014	In Force	OPTIMIZING ERROR FLOOR PERFORMANCE OF FINITE-PRECISION LAY
MP2389	MP2389D1C1	US	14/725,718	5/21/2014	9256487	2/9/2016	In Force	OPTIMIZING ERROR FLOOR PERFORMANCE OF FINITE-PRECISION LAY
MP2390	MP2390	US	12/350,885	1/8/2009	8230312	7/24/2012	In Force	ITERATIVE DECODER MEMORY ARRANGEMENT
MP2390	MP2390C1	US	13/556,063	7/23/2012	8522123	8/27/2013	In Force	ITERATIVE DECODER MEMORY ARRANGEMENT
MP2390	MP2390C1C1	US	13/973,880	8/22/2013	8619530	8/26/2014	In Force	ITERATIVE DECODER MEMORY ARRANGEMENT
MP2393	MP2393	US	12/352,924	1/13/2009	8498186	7/30/2013	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP2393	MP2393CN	CN	2009801021824	1/13/2009	ZL2009801021	3/12/2014	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP2393J	MP2393JP	JP	2010-542426	1/13/2009	5582407	7/25/2014	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP2393T	MP2393TW	TW	98101152	1/14/2009	1436354	5/1/2014	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP2393W	MP2393WO	WO	PCT/US2009/03	1/13/2009			Pending	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP2396	MP2396	US	12/352,814	1/13/2009	8209356	6/26/2012	In Force	Direct Command Line Parser
MP2397	MP2397	US	12/353,391	1/14/2009	8279955	10/2/2012	In Force	Systems and Methods for Calibrating Digital Baseband DC Offset in a
MP2397	MP2397C1	US	13/613,212	9/13/2012	8592692	11/12/2013	In Force	Systems and Methods for Calibrating Digital Baseband DC Offset in a
MP2397	MP2397C1C1	US	14/050,893	10/10/2013	8781042	7/15/2014	In Force	Systems and Methods for Calibrating Digital Baseband DC Offset in a
MP2401	MP2401	US	12/352,439	1/12/2009	8330477	12/11/2012	In Force	TEST ENGINE FOR INTEGRATED CIRCUIT CHIP TESTING
MP2401	MP2401C1	US	13/709,851	12/10/2012	9244115	1/26/2016	In Force	TEST ENGINE FOR INTEGRATED CIRCUIT CHIP TESTING
MP2403	MP2403	US	12/357,200	1/21/2009	8745468	6/3/2014	In Force	ITERATIVE DECODING SYSTEMS USING NOISE-BIASING
MP2403	MP2403C1	US	14/279,217	5/15/2014	9256492	2/9/2016	In Force	ITERATIVE DECODING SYSTEMS USING NOISE-BIASING
MP2404	MP2404	US	12/356,761	1/21/2009	9442758	9/13/2016	In Force	Dynamic Processor Core Switching
MP2405	MP2405	US	12/353,441	1/14/2009	8077421	12/13/2011	In Force	Pole Mismatch Compensation Via Feedback Control Approach
MP2406	MP2406	US	12/349,715	1/7/2009	8045439	10/25/2011	In Force	System And Method For Storing Data To An Optical Recording Device
MP2412	MP2412	US	12/360,660	1/27/2009	8154968	4/10/2012	In Force	METHOD AND APPARATUS FOR OPTIMIZING OPTICAL RECORDING
MP2412	MP2412C1	US	13/440,471	4/5/2012	8345523	1/1/2013	In Force	METHOD AND APPARATUS FOR OPTIMIZING OPTICAL RECORDING
MP2415W	MP2415WO	WO	PCT/US2009/03	1/27/2009			Pending	Downlink Data Control System For Radio Access Networks
MP2419	MP2419	US	12/363,269	1/30/2009	8498362	7/30/2013	In Force	CHANNEL SOUNDING AND ESTIMATION STRATEGIES FOR ANTENNA S
MP2419	MP2419D1	US	13/953,355	7/29/2013	9178586	11/3/2015	In Force	CHANNEL SOUNDING AND ESTIMATION STRATEGIES FOR ANTENNA S
MP2419	MP2419D11	US	14/302,188	6/11/2014	9749022	8/29/2017	In Force	CHANNEL SOUNDING AND ESTIMATION STRATEGIES IN MIMO SYSTE
MP2419A	MP2419APWO	WO	PCT/US2014/04	6/11/2014			Pending	CHANNEL SOUNDING AND ESTIMATION STRATEGIES IN MIMO SYSTE
MP2419F	MP2419FWCN	CN	2014800421891	6/11/2014	ZL2014800421	3/12/2019	In Force	CHANNEL SOUNDING AND ESTIMATION STRATEGIES IN MIMO SYSTE
MP2419F	MP2419FWEP	EP	EP14738945.6	6/11/2014			Pending	Channel Sounding and Estimation Strategies in MIMO Systems

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MP2419F	MP2419FWJP	JP	2016-519623	6/11/2014			Pending	CHANNEL SOUNDING AND ESTIMATION STRATEGIES IN MIMO SYSTEM
MP2419W	MP2419W/D1	JP	2013-176706	8/28/2013	5813709	10/2/2015	In Force	METHODS, TRANSMITTER AND RECEIVER FOR CHANNEL SOUNDING
MP2419W	MP2419W/O	WO	PCT/US2009/03	1/30/2009			Pending	CHANNEL SOUNDING AND ESTIMATION STRATEGIES FOR ANTENNA S
MP2419W	MP2419W/CN	CN	2009801038914	1/30/2009	ZL2009801038	9/3/2014	In Force	METHODS, TRANSMITTER AND RECEIVER FOR CHANNEL SOUNDING
MP2419W	MP2419W/CEP	EP	EP09709317.3	1/30/2009	EP2238694	5/4/2016	In Force	Methods, Transmitter and Receiver for Channel Sounding and Estim
MP2419W	MP2419W/CEP/DE	DE	09709317.3	1/30/2009	2238694	5/4/2016	In Force	CHANNEL SOUNDING AND ESTIMATION STRATEGIES FOR ANTENNA S
MP2419W	MP2419W/CEP/FR	FR	09709317.3	1/30/2009	2238694	5/4/2016	In Force	CHANNEL SOUNDING AND ESTIMATION STRATEGIES FOR ANTENNA
MP2419W	MP2419W/CEP/GB	GB	09709317.3	1/30/2009	2238694	5/4/2016	In Force	CHANNEL SOUNDING AND ESTIMATION STRATEGIES FOR ANTENNA
MP2419W	MP2419W/CJP	JP	2010-545209	1/30/2009	5356415	9/6/2013	In Force	CHANNEL SOUNDING AND ESTIMATION STRATEGIES FOR ANTENNA
MP2421	MP2421	US	12/364,055	2/2/2009	8064926	8/23/2011	In Force	System And Method For Memory Array Decoding
MP2421	MP2421/aaa	US	14/223,335	3/24/2014	8881330	11/18/2014	In Force	Method And Apparatus For Simultaneously Accessing A Plurality Of
MP2421	MP2421/C1	US	13/214,543	8/22/2011	8203902	6/19/2012	In Force	System And Method For Memory Array Decoding
MP2421	MP2421/C1/aa	US	13/926,485	6/25/2013	8681553	3/25/2014	In Force	System And Method For Memory Array Decoding
MP2421	MP2421/C1/C1	US	13/537,119	6/19/2012	8472277	6/25/2013	In Force	System And Method For Memory Array Decoding
MP2421W	MP2421W/O	WO	PCT/US2009/03	2/4/2009			Pending	System And Method For Memory Array Decoding
MP2434	MP2434	US	12/359,159	1/23/2009	8181100	5/15/2012	In Force	MEMORY FAULT INJECTION
MP2438	MP2438	US	12/363,047	1/30/2009	8996066	3/31/2015	In Force	METHODS AND APPARATUS FOR DIRECTING A BEAM TOWARDS A DE
MP2438	MP2438/C1	US	14/671,661	3/27/2015	9450660	9/20/2016	In Force	METHODS AND APPARATUS FOR DIRECTING A BEAM TOWARDS A DE
MP2441	MP2441	US	12/367,630	2/9/2009	8290077	10/16/2012	In Force	COMMUNICATION WITH MULTIPLE USERS OVER A SINGLE TIME SLO
MP2443	MP2443	US	12/367,417	2/6/2009	8391115	3/5/2013	In Force	METHOD OF IMPROVING QUALITY OF OPTICAL RECORDING USING CI
MP2446	MP2446	US	12/372,293	2/17/2009	8406358	3/26/2013	In Force	Radio-Frequency Apparatus With Programmable Performance And As
MP2446	MP2446/C1/C1	US	14/380,538	6/10/2014	9166632	10/20/2015	In Force	Mixer Circuits With Programmable Characteristics
MP2448	MP2448	US	12/368,089	2/9/2009	8095667	1/10/2012	In Force	METHODS AND APPARATUS FOR REMOTELY WAKING UP A COMPUT
MP2448	MP2448/C1	US	13/330,191	12/19/2011	8219691	7/10/2012	In Force	METHODS AND APPARATUS FOR REMOTELY WAKING UP A COMPUT
MP2450	MP2450/C1	US	13/041,145	3/4/2011	8289095	10/16/2012	In Force	JITTER COMPENSATED NUMERICALLY CONTROLLED OSCILLATOR
MP2450	MP2450/C1/aa	US	14/216,395	3/17/2014	9182779	11/10/2015	In Force	JITTER COMPENSATED NUMERICALLY CONTROLLED OSCILLATOR
MP2452	MP2452	US	12/358,653	1/23/2009	8207696	6/26/2012	In Force	Transfer Characteristic Based Motor Speed Control
MP2453	MP2453	US	12/362,405	1/29/2009	8030215	10/4/2011	In Force	METHOD FOR CREATING ULTRA-HIGH-DENSITY HOLES AND METALL
MP2454	MP2454	US	12/361,327	1/28/2009	8266495	9/11/2012	In Force	SYSTEMS AND METHODS FOR PERFORMING CONCATENATED ERROR
MP2454	MP2454/C1	US	13/604,391	9/5/2012	8635508	1/21/2014	In Force	SYSTEMS AND METHODS FOR PERFORMING CONCATENATED ERROR
MP2454W	MP2454W/O	WO	PCT/US2009/00	1/30/2009			Pending	SYSTEMS AND METHODS FOR PERFORMING CONCATENATED ERROR
MP2454W	MP2454W/CN	CN	200980113875.3	1/30/2009	ZL2009801138	6/4/2014	In Force	SYSTEMS AND METHODS FOR PERFORMING CONCATENATED ERROR
MP2461	MP2461	US	12/372,215	2/17/2009	8165555	4/24/2012	In Force	METHOD AND APPARATUS FOR FILTERING
MP2461	MP2461/C1	US	13/434,920	3/30/2012	8380155	2/19/2013	In Force	METHOD AND APPARATUS FOR FILTERING

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MP2461	MP2461C1C1	US	13/758,512	2/4/2013	8626103	1/7/2014	In Force	METHOD AND APPARATUS FOR FILTERING
MP2466	MP2466C1	US	13/189,595	7/25/2011	8742863	6/3/2014	In Force	TEMPERATURE-CORRECTED FREQUENCY CONTROL WITH CRYSTAL O
MP2467	MP2467C1	US	13/073,439	3/28/2011	8207739	6/26/2012	In Force	IDENTIFICATION OF AN ABSENT LOAD FROM A CIRCUIT
MP2467	MP2467D1	US	12/229,647	10/10/2008	8193814	6/5/2012	In Force	IDENTIFYING A FLUORESCENT LAMP AMONG MULTIPLE FLUORESCEN
MP2471	MP2471	US	12/365,455	2/4/2009	9153337	10/6/2015	In Force	Fatigue Management System And Method For Hybrid Nonvolatile Soli
MP2471	MP2471C1	US	14/872,260	10/1/2015	9777455	8/8/2017	In Force	Method And Apparatus For Mapping A Logical Address Between Mem
MP2471	MP2471C1C1	US	15/670,339	8/7/2017	10067866	9/4/2018	In Force	Method And Apparatus For Mapping Logical Addresses Between Mem
MP2471T	MP2471TW	TW	98103888	2/25/2009	1490693	7/1/2015	In Force	FATIGUE MANAGEMENT SYSTEM AND METHOD FOR HYBRID N/AVOC
MP2471W	MP2471WO	WO	PCT/US2009/03	2/24/2009			Pending	Fatigue Management System And Method For Hybrid Nonvolatile S
MP2471W	MP2471WOCN	CN	2009801070441	2/24/2009	212009801070	11/19/2014	In Force	FATIGUE MANAGEMENT SYSTEM AND METHOD FOR HYBRID NONVO
MP2471W	MP2471WCEP	EP	EP09714131.1	2/24/2009	EP2248131	2/18/2015	In Force	Fatigue Management System and Method for Hybrid Nonvolatile Soli
MP2471W	MP2471WOKR	KR	1020107019163	2/24/2009	101576575	12/4/2015	In Force	Fatigue Management System And Method For Hybrid Nonvolatile Soli
MP2471W	MP2471WUDE	DE	09714131.1	2/24/2009	2248131	2/18/2015	In Force	Fatigue Management System And Method For Hybrid Nonvolatile Soli
MP2471W	MP2471WUFR	FR	09714131.1	2/24/2009	2248131	2/18/2015	In Force	Fatigue Management System And Method For Hybrid Nonvolatile Soli
MP2471W	MP2471WUGB	GB	09714131.1	2/24/2009	2248131	2/18/2015	In Force	Fatigue Management System And Method For Hybrid Nonvolatile Soli
MP2472	MP2472	US	12/393,639	2/26/2009	8535487	9/3/2013	In Force	CIRCUIT AND METHOD FOR REGULATING DISCHARGE OF A CAPACITO
MP2473	MP2473	US	12/393,853	2/26/2009	8345306	1/1/2013	In Force	HANDHELD IMAGE TRANSLATION DEVICE INCLUDING AN IMAGE CAP
MP2475	MP2475	US	12/397,366	3/4/2009	8099139	1/17/2012	In Force	POWER CONTROL USING FAST SIGNAL ENVELOPE DETECTION
MP2475	MP2475C1	US	13/330,715	12/20/2011	8660455	12/3/2013	In Force	POWER CONTROL USING FAST SIGNAL ENVELOPE DETECTION
MP2475	MP2475I1	US	13/351,273	1/17/2012	8781536	7/15/2014	In Force	POWER CONTROL USING FAST SIGNAL CORRELATION
MP2475	MP2475IHC1	US	14/306,281	6/17/2014	9265016	2/16/2016	In Force	POWER CONTROL USING FAST SIGNAL TIME ALIGNMENT
MP2482	MP2482	US	12/396,844	3/3/2009	8345607	1/1/2013	In Force	Coexistence And Collocation Of Remote Network And Local Network
MP2482	MP2482C1	US	13/730,022	12/28/2012	8565183	10/22/2013	In Force	Method And Apparatus For Preventing Interference Between Colloca
MP2482	MP2482C1C1	US	14/059,920	10/22/2013	8855079	10/7/2014	In Force	Method And Apparatus For, Based On Communication Of A First Phys
MP2482W	MP2482WO	WO	PCT/US2009/03	3/5/2009			Pending	Coexistence And Collocation Of Remote Network And Local Network
MP2483	MP2483	US	12/401,058	3/10/2009	8179719	5/15/2012	In Force	Systems And Methods For Improving Error Distributions In Multi-Lev
MP2483	MP2483C1	US	13/470,884	5/14/2012	8498154	7/30/2013	In Force	Systems And Methods For Improving Error Distributions In Multi-Lev
MP2483	MP2483C1aa	US	14/252,303	4/14/2014	9042168	5/26/2015	In Force	System And Method For Improving Error Distribution In Multi-Level
MP2483	MP2483C1C1	US	13/951,766	7/26/2013	8699269	4/15/2014	In Force	Systems And Methods For Improving Error Distributions In Multi-Lev
MP2489	MP2489	US	12/403,693	3/13/2009	8249505	8/21/2012	In Force	Method And Apparatus For Selecting A Mode In A Multi-Mode Comm
MP2494	MP2494	US	10/589,133	2/4/2005	7479834	1/20/2009	In Force	ANALOGUE SELF-CALIBRATION METHOD AND APPARATUS FOR LOW
MP2494	MP2494C1	US	12/355,948	1/19/2009	7902929	3/8/2011	In Force	Analogue Self-Calibration Method And Apparatus For Low Noise, Fast
MP2498	MP2498	US	12/405,594	3/17/2009	8165544	4/24/2012	In Force	BLUETOOTH AND WLAN COEXISTENCE ARCHITECTURE HAVING A SH
MP2498	MP2498C1	US	13/448,899	4/17/2012	8706065	4/22/2014	In Force	BLUETOOTH AND WLAN COEXISTENCE ARCHITECTURE HAVING A SH

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MP2498	MP2498C1C1	US	14/255,291	4/17/2014	9209855	12/8/2015	In Force	BLUETOOTH AND WLAN COEXISTENCE ARCHITECTURE HAVING A SH
MP2498W	MP2498WO	WO	PCT/US2009/03	3/17/2009			Pending	BLUETOOTH AND WLAN COEXISTENCE ARCHITECTURE HAVING A SH
MP2498W	MP2498WOCN	CN	200980109792.1	3/17/2009	Z12009801097	3/12/2014	In Force	BLUETOOTH AND WLAN COEXISTENCE ARCHITECTURE HAVING A SH
MP2498W	MP2498WCEP	EP	EP09722388.7	3/17/2009	2258050	9/19/2018	In Force	Bluetooth and WLAN Coexistence Architecture Having a Shared Low
MP2498W	MP2498WCEPDE	DE	EP09722388.7	3/17/2009	2258050	9/19/2018	In Force	Bluetooth and WLAN Coexistence Architecture Having a Shared Low
MP2498W	MP2498WCEPFR	FR	EP09722388.7	3/17/2009	2258050	9/19/2018	In Force	Bluetooth and WLAN Coexistence Architecture Having a Shared Low
MP2498W	MP2498WCEPGB	GB	EP09722388.7	3/17/2009	2258050	9/19/2018	In Force	Bluetooth and WLAN Coexistence Architecture Having a Shared Low
MP2498W	MP2498WCEJP	JP	2011-500910	3/17/2009	5585964	8/1/2014	In Force	BLUETOOTH AND WLAN COEXISTENCE ARCHITECTURE HAVING A SH
MP2498W	MP2498WCKR	KR	10-2010-702157	3/17/2009	10-1608023	3/31/2016	In Force	BLUETOOTH AND WLAN COEXISTENCE ARCHITECTURE HAVING A SH
MP2500	MP2500	US	12/386,323	3/2/2009	8311160	11/13/2012	In Force	EXPLICIT BEAMFORMEE METHOD AND APPARATUS FOR MIMO AND
MP2500	MP2500C1	US	13/674,752	11/12/2012	8494093	7/23/2013	In Force	METHOD AND APPARATUS FOR GENERATING BEAMFORMING FEEDS
MP2500	MP2500C1AA	US	14/153,896	1/13/2014	9112553	8/18/2015	In Force	METHOD AND APPARATUS FOR GENERATING BEAMFORMING FEEDS
MP2500	MP2500C1C1	US	13/947,985	7/22/2013	8630376	1/14/2014	In Force	METHOD AND APPARATUS FOR GENERATING BEAMFORMING FEEDS
MP2502	MP2502	US	12/388,085	3/4/2009	8246164	8/21/2012	In Force	HANDHELD MOBILE PRINTING DEVICE CAPABLE OF REAL-TIME IN-LIN
MP2502W	MP2502WO	WO	PCT/US2009/03	3/4/2009			Pending	HANDHELD MOBILE PRINTING DEVICE CAPABLE OF REAL-TIME IN-LI
MP2502W	MP2502WOCN	CN	200980112066.1	3/4/2009	Z12009801120	8/14/2013	In Force	HANDHELD MOBILE PRINTING DEVICE CAPABLE OF REAL-TIME IN-LIN
MP2502W	MP2502WCEP	EP	9723254	3/4/2009	2259928	12/25/2013	In Force	HANDHELD MOBILE PRINTING DEVICE CAPABLE OF REAL-TIME IN-LIN
MP2502W	MP2502WUDE	DE	9723254	3/4/2009	2259928	12/25/2013	In Force	HANDHELD MOBILE PRINTING DEVICE CAPABLE OF REAL-TIME IN-LIN
MP2502W	MP2502WUFR	FR	9723254	3/4/2009	2259928	12/25/2013	In Force	HANDHELD MOBILE PRINTING DEVICE CAPABLE OF REAL-TIME IN-LIN
MP2502W	MP2502WUGB	GB	9723254	3/4/2009	2259928	12/25/2013	In Force	HANDHELD MOBILE PRINTING DEVICE CAPABLE OF REAL-TIME IN-LIN
MP2503	MP2503	US	12/394,871	2/27/2009	8314942	11/20/2012	In Force	POSITIONING AND PRINTING OF A HANDHELD DEVICE
MP2504	MP2504	US	12/397,057	3/3/2009	8565325	10/22/2013	In Force	Wireless Device Communication in the 60GHz Band
MP2504	MP2504C1	US	14/058,879	10/21/2013	8953661	2/10/2015	In Force	Wireless Device Communication in the 60GHz Band
MP2510	MP2510	US	12/365,565	2/4/2009	8788918	7/22/2014	In Force	Block Encoding With A Variable Rate Block Code
MP2510I	MP2510IWO	WO	PCT/IB2009/005	3/26/2009			Pending	Block Encoding With A Variable Rate Block Code
MP2510W	MP2510WO	WO	PCT/IB2009/005	3/26/2009			Pending	Block Encoding With A Variable Rate Block Code
MP2510W	MP2510WOCN	CN	200980110744.1	3/26/2009	Z12009801107	4/16/2014	In Force	BLOCK ENCODING WITH A VARIABLE RATE BLOCK CODE
MP2510W	MP2510WOCIN	IN	6083CHENP2031	3/26/2009			Pending	Block Encoding With A Variable Rate Block Code
MP2510W	MP2510WQJP	JP	2011-501314	3/26/2009	5440887	12/27/2013	In Force	BLOCK ENCODING WITH A VARIABLE RATE BLOCK CODE
MP2510W	MP2510WOKR	KR	1020107021657	3/26/2009	101604407	3/11/2016	In Force	Block Encoding With A Variable Rate Block Code
MP2510W	MP2510WUDE	DE	09725578.0	3/26/2009	2274854	7/8/2015	In Force	Block Encoding With A Variable Rate Block Code
MP2510W	MP2510WUFR	FR	09725578.0	3/26/2009	2274854	7/8/2015	In Force	Block Encoding With A Variable Rate Block Code
MP2510W	MP2510WUGB	GB	09725578.0	3/26/2009	2274854	7/8/2015	In Force	Block Encoding With A Variable Rate Block Code
MP2511	MP2511	US	12/402,438	3/11/2009	8423870	4/16/2013	In Force	Method And Apparatus For Determining The Start Of A Data Field In

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MP2511	MP2511C1	US	13/863,811	4/16/2013	8762817	6/24/2014	In Force	Method And Apparatus For Determining The Start Of A Data Field In
MP2512	MP2512	US	12/401,308	3/10/2009	8355380	1/15/2013	In Force	MESH POWER CONSERVATION
MP2513	MP2513	US	12/410,021	3/24/2009	8055230	11/8/2011	In Force	LOW NOISE AMPLIFIER GAIN ADAPTION BASED ON A RECEIVED SIGN
MP2513	MP2513AAAA	US	14/320,509	6/30/2014	9319017	4/19/2016	In Force	LNA GAIN ADAPTION BASED ON RECEIVED SIGNAL STRENGTH OF SIG
MP2513	MP2513C1	US	13/285,978	10/31/2011	8254866	8/28/2012	In Force	LOW NOISE AMPLIFIER GAIN ADAPTION BASED ON A RECEIVED SIGN
MP2513	MP2513C1AA	US	13/846,475	3/18/2013	8768276	7/1/2014	In Force	LNA GAIN ADAPTION BASED ON RECEIVED SIGNAL STRENGTH OF SIG
MP2513	MP2513C1C1	US	13/593,955	8/24/2012	8401505	3/19/2013	In Force	LNA GAIN ADAPTION BASED ON RECEIVED SIGNAL STRENGTH OF SIG
MP2514	MP2514	US	12/406,465	3/18/2009	8214066	7/3/2012	In Force	System And Method For Controlling Noise In Real-Time Audio Signal
MP2516	MP2516	US	12/389,118	2/19/2009	8045285	10/25/2011	In Force	Data Synchronization for Bit Insertion or Deletion
MP2516	MP2516C1	US	13/277,474	10/20/2011	8360344	10/30/2012	In Force	Data Synchronization for Bit Insertion or Deletion
MP2517	MP2517	US	12/406,587	3/18/2009	8331475	12/11/2012	In Force	Low Complexity Technique for Log-Likelihood Ratio Computation
MP2517	MP2517C1	US	13/672,141	11/8/2012	8774301	7/8/2014	In Force	Low Complexity Technique for Log-Likelihood Ratio Computation
MP2517	MP2517C1C1	US	14/291,473	5/30/2014	9031145	5/12/2015	In Force	Low Complexity Technique for Log-Likelihood Ratio Computation
MP2520	MP2520	US	12/388,302	2/18/2009	8238367	8/7/2012	In Force	SLOT ALLOCATION, USER GROUPING, AND FRAME PARTITION METH
MP2521	MP2521	US	12/388,831	2/19/2009	8134988	3/13/2012	In Force	Coexistence Mechanism For WiMax And IEEE 802.11
MP2521	MP2521C1	US	13/418,029	3/12/2012	8638770	1/28/2014	In Force	Coexistence Mechanism For WiMAX And IEEE 802.11
MP2521	MP2521C1aa	US	14/679,601	4/6/2015	9247558	1/26/2016	In Force	System And Method For Reducing Interference Between Collocated T
MP2521	MP2521C1C1	US	14/166,256	1/28/2014	9001808	4/7/2015	In Force	System And Method For Reducing Interference Between Collocated T
MP2521W	MP2521WO	WO	PCT/US2009/03	3/5/2009			Pending	Coexistence Mechanism For WiMAX And IEEE 802.11
MP2523	MP2523	US	12/404,570	3/16/2009	8144510	3/27/2012	In Force	METHOD AND SYSTEM FOR PROGRAMMING MULTI-STATE MEMORY
MP2525	MP2525	US	12/411,742	3/26/2009	8233417	7/31/2012	In Force	Femtocell Architecture Using Internet Protocol Multimedia Subsystem
MP2527	MP2527	US	12/410,931	3/25/2009	8412271	4/2/2013	In Force	Boosted, Dedicated Reference Signal
MP2527	MP2527C1	US	13/850,856	3/26/2013	9781611	10/3/2017	In Force	Boosted, Dedicated Reference Signal
MP2527W	MP2527WO	WO	PCT/IB2009/005	3/27/2009			Pending	Boosted, Dedicated Reference Signal
MP2527W	MP2527WOCN	CN	2009801195790	3/27/2009	ZL2009801195	3/19/2014	In Force	BOOSTED, DEDICATED REFERENCE SIGNAL
MP2527W	MP2527WCEP	EP	09724906.4	3/27/2009	EP2258130	8/13/2014	In Force	Boosted, Dedicated Reference Signal
MP2527W	MP2527WJJP	JP	2011-501317	3/27/2009	5282321	6/7/2013	In Force	BOOSTED, DEDICATED REFERENCE SIGNAL
MP2527W	MP2527WUDE	DE	09724906.4	3/27/2009	2258130	8/13/2014	In Force	BOOSTED, DEDICATED REFERENCE SIGNAL
MP2527W	MP2527WUFR	FR	09724906.4	3/27/2009	2258130	8/13/2014	In Force	BOOSTED, DEDICATED REFERENCE SIGNAL
MP2527W	MP2527WUGB	GB	09724906.4	3/27/2009	2258130	8/13/2014	In Force	BOOSTED, DEDICATED REFERENCE SIGNAL
MP2529	MP2529	US	12/414,124	3/30/2009	8359505	1/22/2013	In Force	METHOD AND APPARATUS FOR DECODING
MP2529	MP2529C1	US	13/745,466	1/18/2013	8954814	10/2/2015	In Force	METHOD AND APPARATUS FOR DECODING
MP2529W	MP2529WOCN	CN	200980112065.1	3/30/2009	ZL2009801120	11/9/2016	In Force	METHOD AND APPARATUS FOR SELECTIVE COMBINING AND DECODING
MP2529W	MP2529WCEP	EP	EP09726943.5	3/30/2009	EP2269338	9/16/2015	In Force	Method and Apparatus for Decoding

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MP2529W	MP2529WUDE	DE	09726943.5	3/30/2009	2269338	9/16/2015	In Force	METHOD AND APPARATUS FOR SELECTIVE COMBINING AND DECODI
MP2529W	MP2529WUFR	FR	09726943.5	3/30/2009	2269338	9/16/2015	In Force	METHOD AND APPARATUS FOR SELECTIVE COMBINING AND DECODI
MP2529W	MP2529WUGB	GB	09726943.5	3/30/2009	2269338	9/16/2015	In Force	METHOD AND APPARATUS FOR SELECTIVE COMBINING AND DECODI
MP2531	MP2531	US	12/493,112	3/12/2009	8111235	2/21/2012	In Force	DIMENSION REDUCTION FOR CODEBOOK SEARCH
MP2531	MP2531C1	US	13/399,553	2/17/2012	8565360	10/22/2013	In Force	DIMENSION REDUCTION FOR CODEBOOK SEARCH
MP2531	MP2531C1C1	US	14/052,356	10/11/2013	9025707	5/5/2015	In Force	DIMENSION REDUCTION FOR CODEBOOK SEARCH
MP2532	MP2532	US	12/410,031	3/24/2009	8311144	11/13/2012	In Force	SYSTEMS AND METHODS FOR EFFICIENT CODEBOOK SEARCHES
MP2532	MP2532C1	US	13/672,574	11/8/2012	8564222	11/26/2013	In Force	SYSTEMS AND METHODS FOR EFFICIENT CODEBOOK SEARCHES
MP2535	MP2535	US	12/415,705	3/31/2009	8670335	3/11/2014	In Force	Reduced Power Transmission
MP2535T	MP2535TW	TW	98110958	4/2/2009	1527461	3/21/2016	In Force	NETWORK DEVICE, SYSTEM AND METHOD FOR REDUCING POWER C
MP2535W	MP2535WO	WO	PCT/US2009/03	3/31/2009			Pending	Reduced Power Transmission
MP2536	MP2536	US	12/415,207	3/31/2009	8412998	4/2/2013	In Force	Restart Operation with Logical Blocks in Queued Commands
MP2536	MP2536C1	US	13/849,739	3/25/2013	8760966	4/15/2014	In Force	Restart Operation with Logical Blocks in Queued Commands
MP2536	MP2536C1C1	US	14/247,341	4/8/2014	9547556	1/17/2017	In Force	Restart Operation with Logical Blocks in Queued Commands
MP2541	MP2541	US	12/403,243	3/12/2009	8370717	2/5/2013	In Force	Method and Apparatus for Flexible Buffers in an XOR Engine
MP2541	MP2541C1	US	13/748,078	1/23/2013	8935593	1/13/2015	In Force	Method and Apparatus for Flexible Buffers in an XOR Engine
MP2541	MP2541C1C1	US	14/584,970	12/29/2014	9244762	1/26/2016	In Force	Method and Apparatus for Flexible Buffers in an XOR Engine
MP2542	MP2542	US	12/400,442	3/9/2009	8897394	11/25/2014	In Force	METHODS AND APPARATUS FOR ADAPTIVELY SELECTING A COMMUN
MP2542	MP2542C1	US	14/550,511	11/21/2014	9420594	8/16/2016	In Force	METHODS AND APPARATUS FOR ADAPTIVELY SELECTING A COMMUN
MP2543	MP2543	US	12/410,883	3/25/2009	8358668	1/22/2013	In Force	PHYSICAL LAYER FRAME FORMAT DESIGN FOR WIDEBAND WIRELESS
MP2543	MP25431	US	12/489,865	6/23/2009	8441968	5/14/2013	In Force	Physical Layer Frame Format Design for Wideband Wireless Commun
MP2543	MP25431aa	US	14/077,624	11/12/2013	8902798	12/2/2014	In Force	Physical Layer Frame Format Design for Wideband Wireless Commun
MP2543	MP254311C1	US	13/847,649	3/20/2013	8665635	12/10/2013	In Force	Physical Layer Frame Format Design for Wideband Wireless Commun
MP2543I	MP254311WEP	EP	9789891	6/23/2009			Pending	Physical Layer Frame Format Design for Wideband Wireless Commun
MP2543I	MP254311WO	WO	PCT/US2009/04	6/23/2009			Pending	Physical Layer Frame Format Design for Wideband Wireless Commun
MP2543I	MP25431WCN	CN	200980126648.6	6/23/2009	212009801266	5/25/2016	In Force	PHYSICAL LAYER FRAME FORMAT DESIGN FOR WIDEBAND WIRELESS
MP2543I	MP25431WJP	JP	2011-517458	6/23/2009	5315546	7/19/2013	In Force	Physical Layer Frame Format Design for Wideband Wireless Commun
MP2543W	MP2543WO	WO	PCT/US2009/03	3/25/2009			Pending	PHYSICAL LAYER FRAME FORMAT DESIGN FOR WIDEBAND WIRELESS
MP2543W	MP2543WOCN	CN	200980121372.1	3/25/2009	212009801213	12/25/2013	In Force	PHYSICAL LAYER FRAME FORMAT FOR WIDEBAND WIRELESS COMM
MP2543W	MP2543WCEP	EP	09730549.4	3/25/2009	2294772	1/6/2016	In Force	Physical Layer Frame Format Design for Wideband Wireless Commun
MP2543W	MP2543WCEPDE	DE	09730549.4	3/25/2009	2294772	1/6/2016	In Force	Physical Layer Frame Format Design for Wideband Wireless Commun
MP2543W	MP2543WCEPFR	FR	09730549.4	3/25/2009	2294772	1/6/2016	In Force	Physical Layer Frame Format Design for Wideband Wireless Commun
MP2543W	MP2543WCEPGB	GB	09730549.4	3/25/2009	2294772	1/6/2016	In Force	Physical Layer Frame Format Design for Wideband Wireless Commun
MP2544	MP2544	US	12/404,495	3/16/2009	8381053	2/19/2013	In Force	Generating a Moving Average

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MP2547	MP2547	US	12/386,250	3/2/2009	8131915	3/6/2012	In Force	Modifying or Overwriting Data Stored in Flash Memory
MP2547	MP2547C1	US	13/406,756	2/28/2012	8533386	9/10/2013	In Force	Modifying Data Stored in Flash Memory
MP2547	MP2547C1C1	US	14/021,752	9/9/2013	9070451	6/30/2015	In Force	Modifying Data Stored in a Multiple-Write Flash Memory Cell
MP2550	MP2550	US	12/407,345	3/19/2009	8638875	1/28/2014	In Force	Transmit Beamforming Systems And Methods
MP2550	MP25501	US	14/164,734	1/27/2014	9300371	3/29/2016	In Force	Beamforming Systems And Methods
MP2550	MP25501C1	US	15/052,206	2/24/2016	9673875	6/6/2017	In Force	Beamforming Systems And Methods
MP2551	MP2551	US	12/400,604	3/9/2009	8281160	10/2/2012	In Force	METHOD AND SYSTEM FOR SELECTING AN OPERATING FREQUENCY
MP2551	MP25511C1	US	14/260,728	4/24/2014	9355001	5/31/2016	In Force	METHOD AND APPARATUS FOR SELECTING AN OPERATING FREQUENCY
MP2552	MP2552	US	12/401,509	3/10/2009	8360868	3/5/2013	In Force	SYSTEMS AND METHODS FOR MEMORY REDUCTION FOR PRINT HEAD
MP2555	MP2555	US	12/402,739	3/12/2009	8761069	6/24/2014	In Force	Multicast To Unicast Conversion System
MP2555	MP2555C1	US	14/312,878	6/24/2014	9313125	4/12/2016	In Force	Method And Apparatus For Disabling Transmission Of A Packet With
MP2555W	MP2555WO	WO	PCT/US2009/04	4/16/2009			Pending	Multicast To Unicast Conversion System
MP2555W	MP2555WOCN	CN	2009801135319	4/16/2009	ZL2009801135	1/28/2015	In Force	MULTICAST TO UNICAST CONVERSION SYSTEM
MP2555W	MP2555WCEP	EP	09733331.4	4/16/2009	2272205	8/22/2018	In Force	Multicast To Unicast Conversion System
MP2555W	MP2555WCEPDE	DE	09733331.4	4/16/2009	2272205	8/22/2018	In Force	Multicast To Unicast Conversion System
MP2555W	MP2555WCEPFR	FR	09733331.4	4/16/2009	2272205	8/22/2018	In Force	Multicast To Unicast Conversion System
MP2555W	MP2555WCEPGB	GB	09733331.4	4/16/2009	2272205	8/22/2018	In Force	Multicast To Unicast Conversion System
MP2555W	MP2555WCOJP	JP	2011-505192	4/16/2009	5339221	8/16/2013	In Force	MULTICAST TO UNICAST CONVERSION SYSTEM
MP2555W	MP2555WOKR	KR	1020107023295	4/16/2009	101502105	3/6/2015	In Force	Multicast To Unicast Conversion System
MP2559	MP2559	US	12/414,046	3/30/2009	8351367	1/8/2013	In Force	Signaling Of Unused Resources
MP2559W	MP2559WO	WO	PCT/IB2009/005	4/24/2009			Pending	Signaling Of Unused Resources
MP2559W	MP2559WOCN	CN	2009801142391	4/24/2009	ZL2009801142	8/6/2014	In Force	SIGNALING OF UNUSED RESOURCES
MP2559W	MP2559WCOJP	JP	2011-505613	4/24/2009	5145457	11/30/2012	In Force	SIGNALING OF UNUSED RESOURCES
MP2563	MP2563	US	12/416,359	4/1/2009	8135916	3/13/2012	In Force	Method and Apparatus for Hardware-Configurable Multi-Policy Cohe
MP2574	MP2574C1	US	13/175,312	7/1/2011	8199626	6/12/2012	In Force	RF Interpolated Timing Recovery Using Clock Synchronized to Wobbl
MP2574	MP2574C1C1	US	13/482,975	6/11/2012	8477582	7/2/2013	In Force	RF INTERPOLATED TIME RECOVERY USING CLOCK SYNCHRONIZED TO
MP2578	MP2578	US	12/422,160	4/10/2009	8754506	6/17/2014	In Force	Through Via Semiconductor Die with Backside Redistribution Layer
MP2580	MP2580	US	12/435,830	5/5/2009	8116242	5/8/2012	In Force	APPARATUS AND METHOD FOR IMPROVING CAM USAGE
MP2582	MP2582	US	12/435,292	5/4/2009	8441910	5/14/2013	In Force	System and Method of Adjusting Gain and Offset Loops in Data Stora
MP2583	MP2583	US	12/434,000	5/1/2009	8683085	3/25/2014	In Force	USB interface Configurable for Host or Device Mode
MP2583	MP2583C1	US	14/111,491	2/3/2014	8924598	12/30/2014	In Force	USB interface Configurable for Host or Device Mode
MP2584	MP2584	US	12/431,584	4/28/2009	7872828	1/18/2011	In Force	FLYING HEIGHT MEASUREMENT
MP2584	MP2584C1	US	13/007,315	1/14/2011	8088452	1/17/2012	In Force	FLYING HEIGHT MEASUREMENT
MP2584	MP2584C1AA	US	13/677,184	11/14/2012	8593755	11/26/2013	In Force	FLYING HEIGHT MEASUREMENT

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MP2584	MP2584C1C1	US	13/349,840	1/13/2012	8315007	11/20/2012	In Force	FLYING HEIGHT MEASUREMENT
MP2585	MP2585	US	12/507,548	7/22/2009	7948798	5/24/2011	In Force	MIXED MULTI-LEVEL CELL AND SINGLE LEVEL CELL STORAGE DEVICE
MP2585	MP2585.C6	US	15/897,000	2/14/2018	10369902	2/19/2019	In Force	METHOD AND APPARATUS FOR SELECTING A MEMORY BLOCK FOR
MP2585	MP2585a8aaa	US	15/400,154	1/6/2017	9888212	2/20/2018	In Force	METHOD AND APPARATUS FOR SELECTING A MEMORY BLOCK FOR
MP2585	MP2585C1	US	13/113,236	5/23/2011	8135913	3/13/2012	In Force	MIXED MULTI-LEVEL CELL AND SINGLE LEVEL CELL STORAGE DEVICE
MP2585	MP2585C1aa	US	13/945,700	7/18/2013	9244834	1/26/2016	In Force	METHOD AND APPARATUS FOR SELECTING A MEMORY BLOCK FOR
MP2585	MP2585C1C1	US	13/416,678	3/9/2012	8495320	7/23/2013	In Force	METHOD AND APPARATUS FOR STORING DATA IN A FLASH MEMOR
MP2595	MP2595	US	12/425,638	4/17/2009	8270467	9/18/2012	In Force	Encoding Image Data
MP2597	MP2597	US	12/436,577	5/6/2009	8066023	8/23/2011	In Force	DRIVER BASED WIRELESS USB
MP2599	MP2599	US	12/463,930	5/11/2009	8072980	12/6/2011	In Force	PREDICTION OF FUTURE FRAMES IN A WIRELESS NETWORK
MP2599	MP2599C1	US	13/310,524	12/2/2011	8467391	6/18/2013	In Force	PREDICTION OF FUTURE FRAMES IN A WIRELESS NETWORK
MP2599	MP2599C1C1	US	13/918,015	6/14/2013	9326216	4/26/2016	In Force	METHOD AND APPARATUS FOR PREDICTING A PHYSICAL LAYER PARA
MP2602	MP2602	US	12/392,872	2/25/2009	8120871	2/21/2012	In Force	Method And Apparatus For Reducing Repeatable Runout In Storage S
MP2602	MP2602C1	US	13/400,386	2/20/2012	8366041	2/5/2013	In Force	METHOD AND APPARATUS FOR REDUCING REPEATABLE RUNOUT IN
MP2602	MP2602C1C1	US	13/759,326	2/5/2013	8665550	3/4/2014	In Force	Method And System For Reducing A Disturbance Associated With Re
MP2605	MP2605	US	12/434,544	5/1/2009	8031521	10/4/2011	In Force	REPROGRAMMING NON-VOLATILE MEMORY DEVICES FOR READ DIS
MP2605	MP2605D1	US	13/249,080	9/29/2011	8264880	9/11/2012	In Force	REPROGRAMMING NON-VOLATILE MEMORY DEVICES FOR READ DIS
MP2606	MP2606	US	12/434,155	5/1/2009	8886181	8/12/2014	In Force	Dynamic Pipeline Reconfiguration Including Changing A Number Of St
MP2609	MP2609	US	12/435,166	5/4/2009	8611477	12/17/2013	In Force	ESTIMATING AND COMPENSATING FOR NOISE ON ANTENNAS OF A
MP2609	MP2609C1	US	14/107,904	12/16/2013	9166828	10/20/2015	In Force	ESTIMATING AND COMPENSATING FOR NOISE IN A MULTI-ANTENNA
MP2612	MP2612	US	12/431,195	4/28/2009	7940129	5/10/2011	In Force	LOW KVCO PHASE-LOCKED LOOP WITH LARGE FREQUENCY DRIFT HA
MP2612	MP2612C1	US	13/088,645	4/18/2011	8149065	4/3/2012	In Force	LOW KVCO PHASE-LOCKED LOOP WITH LARGE FREQUENCY DRIFT HA
MP2613	MP2613	US	12/436,017	5/5/2009	9395940	7/19/2016	In Force	MODIFYING A DEVICE DRIVER OF A DEVICE TO INCLUDE A NETWORK
MP2616	MP2616	US	12/463,687	5/11/2009	8151037	4/3/2012	In Force	INTERFACE FOR SOLID-STATE MEMORY
MP2616	MP2616C1	US	13/431,059	3/27/2012	8533387	9/10/2013	In Force	Interface for Solid-State Memory
MP2617	MP2617	US	12/433,697	4/30/2009	8266978	9/4/2012	In Force	INSTALLATION OF A DEVICE DRIVER
MP2617	MP2617C1	US	13/601,611	8/31/2012	8468277	6/18/2013	In Force	INSTALLATION OF A DEVICE DRIVER
MP2619	MP2619	US	12/436,460	5/6/2009	8253538	8/28/2012	In Force	Asset Management Using Mobile Radio-Frequency Identification (RFI
MP2621	MP2621	US	12/433,574	4/30/2009	8031020	10/4/2011	In Force	BIAS CIRCUIT TO REDUCE FLICKER NOISE IN TUNABLE LC OSCILLATOR
MP2624	MP2624	US	12/467,885	5/18/2009	8027114	9/27/2011	In Force	SPIRAL SERVO DETECTION WITH PHASE-LOCKED LOOP (PLL)
MP2626	MP2626	US	12/417,268	4/2/2009	8645793	2/4/2014	In Force	Statistical Tracking For Flash Memory
MP2626	MP2626C1	US	14/172,199	2/4/2014	9318223	4/19/2016	In Force	Programming Nonvolatile Memory Based On Statistical Analysis Of C
MP2626	MP2626CN	CN	200910142622.1	6/2/2009	Z12009101426	11/19/2014	In Force	STATISTICAL TRACKING FOR FLASH MEMORY
MP2626T	MP2626TW	TW	98117907	6/1/2009	1534820	5/21/2016	In Force	STATISTICAL TRACKING FOR FLASH MEMORY

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MP2631	MP2631	US	12/479,549	6/5/2009	8244894	8/14/2012	In Force	DETACH MECHANISMS FOR HOST-BASED MOBILITY PROTOCOLS
MP2634	MP2634	US	12/471,085	5/22/2009	8291283	10/16/2012	In Force	LAYERED QUASI-CYCLIC LDPC DECODER WITH REDUCED-COMPLEXITY
MP2634	MP2634C1	US	13/615,411	9/13/2012	8473806	6/25/2013	In Force	LAYERED QUASI-CYCLIC LDPC DECODER WITH REDUCED-COMPLEXITY
MP2635	MP2635	US	12/480,520	6/8/2009	8788826	7/22/2014	In Force	METHOD AND APPARATUS FOR DYNAMICALLY ALLOCATING A MOBILE
MP2639	MP2639	US	12/480,977	6/9/2009	7994961	8/9/2011	In Force	Systems and Methods for Providing an Analog-to-Digital Converter in
MP2646	MP2646	US	12/469,382	5/20/2009	8401127	3/19/2013	In Force	MULTI-STREAM SOFT DEMODULATION USING HARD-DECISION SCHEMES
MP2646	MP2646C1	US	13/846,361	3/18/2013	9054063	7/28/2015	In Force	MULTI-STREAM SOFT DEMODULATION USING HARD-DECISION SCHEMES
MP2647	MP2647	US	12/478,446	6/4/2009	8315564	11/20/2012	In Force	Short-Range Wireless Communication
MP2647	MP2647C1	US	13/656,482	10/19/2012	8655279	2/18/2014	In Force	Short-Range Wireless Communication
MP2647	MP2647C2	US	13/656,502	10/19/2012	8571479	10/29/2013	In Force	Short-Range Wireless Communication
MP2647	MP2647C2C1	US	14/063,972	10/25/2013	8989669	3/24/2015	In Force	Short-Range Wireless Communication
MP2647Q	MP2647QADE	DE	13169350.9	6/4/2009	2635077	11/23/2016	In Force	SHORT-RANGE WIRELESS COMMUNICATION
MP2647Q	MP2647QAFR	FR	13169350.9	6/4/2009	2635077	11/23/2016	In Force	SHORT-RANGE WIRELESS COMMUNICATION
MP2647Q	MP2647QAGB	GB	13169350.9	6/4/2009	2635077	11/23/2016	In Force	SHORT-RANGE WIRELESS COMMUNICATION
MP2647W	MP2647WQ	WO	PCT/US2009/04	6/4/2009			Pending	Short-Range Wireless Communication
MP2647W	MP2647WOCN	CN	2009801225874	6/4/2009	212009801225	11/9/2016	In Force	SHORT-RANGE WIRELESS COMMUNICATION
MP2647W	MP2647WCEP	EP	09789754.0	6/4/2009	EP2289274	11/23/2016	In Force	Short-Range Wireless Communication
MP2647W	MP2647WQJP	JP	2011-513586	6/4/2009	5383276	8/23/2013	In Force	SHORT-RANGE WIRELESS COMMUNICATION
MP2647W	MP2647WOKR	KR	1020107027891	6/4/2009	101650109	8/16/2016	In Force	Short-Range Wireless Communication
MP2647W	MP2647WUD1	EP	13169350.9	6/4/2009	EP2635077	11/23/2016	In Force	Short-Range Wireless Communication
MP2647W	MP2647WUDE	DE	09789754.0	6/4/2009	2289274	11/23/2016	In Force	SHORT-RANGE WIRELESS COMMUNICATION
MP2647W	MP2647WUFR	FR	09789754.0	6/4/2009	2289274	11/23/2016	In Force	SHORT-RANGE WIRELESS COMMUNICATION
MP2647W	MP2647WUGB	GB	09789754.0	6/4/2009	2289274	11/23/2016	In Force	SHORT-RANGE WIRELESS COMMUNICATION
MP2648	MP2648	US	12/481,884	6/10/2009	8195112	6/5/2012	In Force	TIME-REVERSAL-BASED COMMUNICATION
MP2651	MP2651	US	12/401,807	3/11/2009	8719485	5/6/2014	In Force	Solid-State Disk With Wireless Functionality
MP2651	MP2651C1	US	14/266,083	4/30/2014	8935464	1/13/2015	In Force	Solid-State Disk With Wireless Functionality
MP2651T	MP2651TW	TW	98121484	6/26/2009	1460593	11/11/2014	In Force	SOLID-STATE DISK WITH WIRELESS FUNCTIONALITY
MP2651W	MP2651WO	WO	PCT/US2009/04	5/26/2009			Pending	Solid-State Disk With Wireless Functionality
MP2651W	MP2651WOCN	CN	200980124150	5/26/2009	212009801241	11/25/2015	In Force	SOLID-STATE DISK WITH WIRELESS FUNCTIONALITY
MP2651W	MP2651WOJP	JP	2011-516381	5/26/2009	5179660	1/18/2013	In Force	SOLID-STATE DISK WITH WIRELESS FUNCTIONALITY
MP2651W	MP2651WOKR	KR	1020107028376	5/26/2009	101580658	12/21/2015	In Force	Solid-State Disk With Wireless Functionality
MP2652	MP2652	US	12/485,076	6/16/2009	8243733	8/14/2012	In Force	Cascaded Memory Tables for Searching
MP2652	MP2652C1	US	13/570,785	8/9/2012	8837487	9/16/2014	In Force	Cascaded Memory Tables for Searching
MP2652T	MP2652TW	TW	98120737	6/19/2009	1491206	7/1/2015	In Force	CASCADED MEMORY TABLES FOR SEARCHING

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MP2652W	MP2652WO	WO	PCT/US2009/04	6/16/2009			Pending	Cascaded Memory Tables for Searching
MP2652W	MP2652WOCN	CN	2009801230169	6/16/2009	ZL2009801230	1/15/2014	In Force	CASCADED MEMORY TABLES FOR SEARCHING
MP2652W	MP2652WOEP	EP	EP09767563.1	6/16/2009	2297905	8/22/2018	In Force	Cascaded Memory Tables for Searching
MP2652W	MP2652WOEPDE	DE	EP09767563.1	6/16/2009	2297905	8/22/2018	In Force	Cascaded Memory Tables for Searching
MP2652W	MP2652WOEPFR	FR	EP09767563.1	6/16/2009	2297905	8/22/2018	In Force	Cascaded Memory Tables for Searching
MP2652W	MP2652WOEPGB	GB	EP09767563.1	6/16/2009	2297905	8/22/2018	In Force	Cascaded Memory Tables for Searching
MP2652W	MP2652WOJP	JP	2011-513755	6/16/2009	5245169	4/19/2013	In Force	INTEGRATED CHIP, SEMICONDUCTOR CHIP AND METHOD
MP2653	MP2653	US	12/348,003	1/1/2009	8018897	9/13/2011	In Force	BALANCING CAPACITY BETWEEN LINK DIRECTIONS USING VARIABLE
MP2653	MP2653C1	US	13/204,685	8/7/2011	8644236	2/4/2014	In Force	BALANCING CAPACITY BETWEEN LINK DIRECTIONS USING VARIABLE
MP2653W	MP2653WOCN	CN	200880129728.1	12/21/2008	ZL2008801297	8/27/2014	In Force	BALANCING CAPACITY BETWEEN LINK DIRECTIONS USING VARIABLE
MP2653W	MP2653WOEP	EP	8874702.7	12/21/2008			Pending	BALANCING CAPACITY BETWEEN LINK DIRECTIONS USING VARIABLE
MP2655	MP2655	US	12/476,683	6/2/2009	8407559	3/26/2013	In Force	Monitoring Memory
MP2655	MP2655CN	CN	200910149910.1	6/22/2009	ZL2009101499	6/11/2014	In Force	MONITORING MEMORY
MP2657	MP2657	US	12/481,974	6/10/2009	8471921	6/25/2013	In Force	REDUCING OPTICAL CROSSALK AND RADIAL FALL-OFF IN IMAGING S
MP2661	MP2661	US	12/487,994	6/19/2009	8436659	5/7/2013	In Force	CIRCUITS AND METHODS FOR REDUCING ELECTRICAL STRESS ON A T
MP2666	MP2666	US	12/403,192	3/12/2009	9286202	3/15/2016	In Force	OPERATION AWARE STRIPING IN PARALLEL DRIVES
MP2666	MP2666C1	US	15/068,500	3/11/2016	9952928	4/24/2018	In Force	OPERATION AWARE STRIPING IN PARALLEL DRIVES
MP2667	MP2667	US	12/487,425	6/18/2009	8660324	12/3/2013	In Force	Circuit and Method for Adjusting a Digitally Controlled Oscillator
MP2667	MP2667C1	US	14/089,515	11/25/2013	8923788	12/30/2014	In Force	Circuit and Method for Adjusting a Digitally Controlled Oscillator
MP2670	MP2670	US	12/481,453	6/9/2009	8379988	2/19/2013	In Force	IMAGE PROCESSING USING STABILITY CRITERIA FOR PIXEL WINDOWS
MP2671	MP2671	US	12/484,284	6/15/2009	8762654	6/24/2014	In Force	SELECTIVELY SCHEDULING MEMORY ACCESSSES IN PARALLEL BASED O
MP2671	MP2671C1	US	14/311,743	6/23/2014	9547444	1/17/2017	In Force	Selectively Scheduling Memory Accesses in Parallel Based on Access S
MP2672	MP2672	US	12/488,240	6/19/2009	8428161	4/23/2013	In Force	SYMBOL VECTOR MAPPING FOR CHASE-COMBINING HARQ
MP2672	MP2672C1	US	13/867,487	4/22/2013	9054868	6/9/2015	In Force	SYMBOL VECTOR MAPPING FOR CHASE-COMBINING HARQ
MP2672W	MP2672WO	WO	PCT/US2009/04	6/19/2009			Pending	SYMBOL VECTOR MAPPING FOR CHASE-COMBINING HARQ
MP2673	MP2673	US	12/480,188	6/23/2009	8427706	4/23/2013	In Force	ERROR DIFFUSION HALFTONING TECHNIQUES AND CONFIGURATION
MP2675	MP2675	US	12/501,728	7/13/2009	8412878	4/2/2013	In Force	Combined Mobile Device And Solid State Disk With A Shared Memor
MP2675	MP2675C1	US	13/885,594	4/2/2013	8769187	7/1/2014	In Force	Combined Mobile Device And Solid State Disk With A Shared Memor
MP2675T	MP2675TW	TW	98123817	7/14/2009	1593362	8/11/2017	In Force	Combined Mobile Device And Solid State Disk With A Shared Memor
MP2675W	MP2675WO	WO	PCT/US2009/05	7/14/2009			Pending	Combined Mobile Device And Solid State Disk With A Shared Memor
MP2675W	MP2675WOCN	CN	2009801268698	7/14/2009			Pending	COMBINED MOBILE DEVICE AND SOLID STATE DISK WITH A SHARED
MP2680	MP2680	US	12/482,545	6/11/2009	8185152	5/22/2012	In Force	Access Network Discovery And Selection And Internet Protocol Multi
MP2682	MP2682D1	US	14/251,395	4/11/2014	9202797	12/1/2015	In Force	Lead Frame Apparatus and Method for Improved Wire Bonding
MP2684	MP2684	US	12/491,167	6/24/2009	8570925	10/29/2013	In Force	Systems And Methods For Reducing Power Consumption In Wireless

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MP2684	MP2684C1	US	14/061,836	10/24/2013	9210655	12/8/2015	In Force	Systems And Methods For Reducing Power Consumption In Wireless
MP2684	MP2684C1C1	US	14/938,179	11/11/2015	9955420	4/24/2018	In Force	Systems And Methods For Reducing Power Consumption In Wireless
MP2684W	MP2684WO	WO	PCT/US2009/04	6/24/2009			Pending	Systems And Methods For Reducing Power Consumption In Wireless
MP2684W	MP2684WOCN	CN	2009801266495	6/24/2009	ZL2009801266	4/22/2015	In Force	SYSTEMS AND METHODS FOR REDUCING POWER CONSUMPTION IN
MP2684W	MP2684WOEP	EP	EP097949606	6/24/2009	2304845	8/22/2018	In Force	Systems and Methods for Reducing Power Consumption In Wireless
MP2684W	MP2684WOEPDE	DE	EP097949606	6/24/2009	2304845	8/22/2018	In Force	Systems and Methods for Reducing Power Consumption In Wireless
MP2684W	MP2684WOEPFR	FR	EP097949606	6/24/2009	2304845	8/22/2018	In Force	Systems and Methods for Reducing Power Consumption In Wireless
MP2684W	MP2684WOEPGB	GB	EP097949606	6/24/2009	2304845	8/22/2018	In Force	Systems and Methods for Reducing Power Consumption In Wireless
MP2684W	MP2684WOGJP	JP	2011-517462	6/24/2009	5087296	9/28/2012	In Force	SYSTEMS AND METHODS FOR REDUCING POWER CONSUMPTION IN
MP2687	MP2687C1	US	14/032,610	9/20/2013	8917645	12/23/2014	In Force	Power Save Mode For Access Points
MP2687D	MP2687D1KR	KR	1020167005657	7/9/2009	101650118	8/16/2016	In Force	Power Save Mode For Access Points
MP2687W	MP2687WO	WO	PCT/US2009/05	7/9/2009			Pending	Power Save Mode For Access Points
MP2687W	MP2687WOCN	CN	2009801268531	7/9/2009			Pending	POWER SAVE MODE FOR ACCESS POINTS
MP2687W	MP2687WODE	DE	09790210.0	7/9/2009	6.02009E+12	1/23/2013	In Force	Power Save Mode For Access Points
MP2687W	MP2687WOEP	EP	09790210.0	7/9/2009	2304992	1/23/2013	In Force	Power Save Mode for Access Points
MP2687W	MP2687WOFR	FR	09790210.0	7/9/2009	2304992	1/23/2013	In Force	Power Save Mode For Access Points
MP2687W	MP2687WOGGB	GB	09790210.0	7/9/2009	2304992	1/23/2013	In Force	Power Save Mode For Access Points
MP2687W	MP2687WOGJP	JP	2011-517609	7/9/2009	5303752	7/5/2013	In Force	POWER SAVE MODE FOR ACCESS POINTS
MP2687W	MP2687WOKR	KR	1020107029045	7/9/2009			Pending	Power Save Mode For Access Points
MP2688	MP2688	US	12/499,390	7/8/2009	8576759	11/5/2013	In Force	Partial Power Save Mode For Access Points During Device Discovery
MP2688	MP2688C1	US	14/068,787	10/31/2013	9007975	4/14/2015	In Force	Power Saving Mode For Access Point
MP2688W	MP2688WO	WO	PCT/US2009/05	7/9/2009			Pending	Partial Power Save Mode For Access Points During Device Discovery
MP2688W	MP2688WOCN	CN	200980126865X	7/9/2009	ZL2009801268	10/16/2013	In Force	PARTIAL POWER SAVE MODE FOR ACCESS POINTS DURING DEVICE DI
MP2688W	MP2688WOEP	EP	09790204.3	7/9/2009	2301283	3/7/2012	In Force	Partial Power Save Mode for Access Points During Device Discovery
MP2688W	MP2688WOEPDE	DE	09790204.3	7/9/2009	602009005814	3/7/2012	In Force	Partial Power Save Mode For Access Points During Device Discovery
MP2688W	MP2688WOEPFR	FR	09790204.3	7/9/2009	2301283	3/7/2012	In Force	Partial Power Save Mode For Access Points During Device Discovery
MP2688W	MP2688WOEPGB	GB	09790204.3	7/9/2009	2301283	3/7/2012	In Force	Partial Power Save Mode For Access Points During Device Discovery
MP2688W	MP2688WOGJP	JP	2011-517604	7/9/2009	5445873	1/10/2014	In Force	PARTIAL POWER SAVE MODE FOR ACCESS POINTS DURING DEVICE DI
MP2688W	MP2688WOKR	KR	1020107029044	7/9/2009	101608907	3/29/2016	In Force	Partial Power Save Mode For Access Points During Device Discovery
MP2689	MP2689	US	12/499,937	7/9/2009	8468374	6/18/2013	In Force	ACCESS POINT ROTATION FOR SHARING POWER LOAD
MP2689	MP2689C1	US	13/919,546	6/17/2013	8762754	6/24/2014	In Force	ACCESS POINT ROTATION FOR SHARING POWER LOAD
MP2689	MP2689C1C1	US	14/311,632	6/23/2014	9201468	12/1/2015	In Force	ACCESS POINT ROTATION FOR SHARING POWER LOAD
MP2689W	MP2689WO	WO	PCT/US2609/05	7/9/2009			Pending	ACCESS POINT ROTATION FOR SHARING POWER LOAD
MP2690	MP2690	US	12/499,885	7/9/2009	8116233	2/14/2012	In Force	IP ASSIGNMENT SCHEME FOR DYNAMIC PEER-TO-PEER NETWORKS

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MP2690W	MP2690WO	WO	PCT/US2009/05	7/9/2009			Pending	IP ASSIGNMENT SCHEME FOR DYNAMIC PEER-TO-PEER NETWORKS
MP2691	MP2691	US	12/499,931	7/9/2009	8719384	5/6/2014	In Force	SERVICE DISCOVERY METHODS
MP2691W	MP2691WO	WO	PCT/US2009/05	7/9/2009			Pending	SERVICE DISCOVERY METHODS
MP2691W	MP2691WOCN	CN	200980126854	7/9/2009	ZL2009801268	9/16/2015	In Force	SERVICE DISCOVERY METHODS
MP2691W	MP2691WOEP	EP	EP09790178.9	7/9/2009	2319226	4/11/2018	In Force	Service Discovery Methods
MP2691W	MP2691WOEPDE	DE	EP09790178.9	7/9/2009	2319226	4/11/2018	In Force	Service Discovery Methods
MP2691W	MP2691WOEPFR	FR	EP09790178.9	7/9/2009	2319226	4/11/2018	In Force	Service Discovery Methods
MP2691W	MP2691WOEPGB	GB	EP09790178.9	7/9/2009	2319226	4/11/2018	In Force	Service Discovery Methods
MP2691W	MP2691WOJP	JP	2011-517588	7/9/2009			Pending	SERVICE DISCOVERY METHODS
MP2694	MP2694	US	12/501,380	7/10/2009	8485454	7/23/2013	In Force	METHODS, APPARATUSES, SYSTEMS, AND ARCHITECTURES FOR QUICK
MP2694	MP2694CN	CN	200910159482	7/14/2009	ZL2009101594	10/22/2014	In Force	METHODS, APPARATUS, SYSTEMS, AND ARCHITECTURES FOR ENCODING
MP2695	MP2695CN	CN	200910140318	7/15/2009	ZL2009101403	7/12/2014	In Force	WINDOW ACTIVITY DETECTING METHOD AND APPARATUS OF DOWNLINK
MP2696	MP2696CN	CN	200910140071	7/16/2009	ZL2009101400	12/10/2014	In Force	CODE ACTIVITY DETECTING METHOD AND APPARATUS OF DOWNLINK
MP2696	MP2696CND1	CN	201410357080	7/16/2009	ZL2014103570	7/13/2016	In Force	CODE ACTIVITY DETECTING METHOD AND APPARATUS OF DOWNLINK
MP2698	MP2698CN	CN	200910140317	7/15/2009	ZL2009101403	5/21/2014	In Force	MULTIPATH TRACKING METHOD AND MULTIPATH TRACKING APPARATUS
MP2699	MP2699CN	CN	200910151245	6/29/2009	ZL2009101512	1/28/2015	In Force	A METHOD AND APPARATUS FOR MEASURING SIGNAL TO NOISE RATIO
MP2700	MP2700CN	CN	200910140323	7/15/2009	ZL2009101403	8/26/2015	In Force	A METHOD AND APPARATUS FOR JOINT DETECTION USED IN A COMMUNICATIONS
MP2702	MP2702CN	CN	200910151064	7/9/2009	ZL2009101510	6/18/2014	In Force	A METHOD AND APPARATUS FOR JOINT DETECTION USED IN A COMMUNICATIONS
MP2705	MP2705CN	CN	200910151067	7/9/2009	ZL2009101510	6/25/2014	In Force	A METHOD AND APPARATUS FOR JOINT DETECTION USED IN A COMMUNICATIONS
MP2706	MP2706CN	CN	200910140321	7/15/2009	ZL2009101403	1/14/2015	In Force	METHOD AND APPARATUS FOR SYNCHRONIZATION CORRELATION IN COMMUNICATIONS
MP2707	MP2707CN	CN	200910159483	7/14/2009	ZL2009101594	11/6/2013	In Force	METHOD AND APPARATUS FOR BCH TTI DETECTION AND SYNCHRONIZATION
MP2709	MP2709CN	CN	200910140069	7/16/2009	ZL2009101400	8/13/2014	In Force	COI DERIVATION METHOD AND APPARATUS
MP2710	MP2710CN	CN	200910140315	7/15/2009			Pending	AFC DEVICE AND METHOD, AND COMMUNICATION APPARATUS
MP2711	MP2711CN	CN	200910151065	7/9/2009	ZL2009101510	11/12/2014	In Force	DEVICE AND METHOD FOR ESTIMATING FREQUENCY OFFSET, AND COMMUNICATIONS
MP2712	MP2712CN	CN	200910139464	6/18/2009	ZL2009101394	7/12/2014	In Force	A METHOD AND APPARATUS FOR FREQUENCY OFFSET ESTIMATION, AND COMMUNICATIONS
MP2713	MP2713CN	CN	200910140316	7/15/2009	ZL2009101403	4/16/2014	In Force	A METHOD AND APPARATUS FOR ESTIMATING FREQUENCY OFFSET
MP2714	MP2714CN	CN	200910140322	7/15/2009	ZL2009101403	3/12/2014	In Force	A CHANNEL ESTIMATING METHOD AND APPARATUS
MP2720	MP2720CN	CN	200910151066	7/9/2009	ZL2009101510	11/12/2014	In Force	CHARACTERISTIC WINDOW DETECTION APPARATUS AND METHOD OF DOWNLINK
MP2722	MP2722CN	CN	200910140314	7/15/2009	ZL2009101403	1/22/2014	In Force	CORRELATION METHOD AND APPARATUS FOR DWPTS SEARCHING
MP2723	MP2723CN	CN	200910140072	7/16/2009	ZL2009101400	8/6/2014	In Force	CARRIER SORTING METHOD AND APPARATUS
MP2728	MP2728	US	12/417,680	4/3/2009	8143825	3/27/2012	In Force	Determining Stuck Conditions For Electric Motors Using Inductive Sensing
MP2728	MP2728C1	US	13/426,056	3/21/2012	8405330	3/26/2013	In Force	Determining Stuck Conditions For Electric Motors Using Inductive Sensing
MP2728	MP2728CN	CN	200910179649	10/26/2009	ZL2009101796	2/19/2014	In Force	DETERMINING STUCK CONDITIONS FOR ELECTRIC MOTORS USING INDUCTIVE SENSING
MP2729	MP2729	US	12/348,375	1/5/2009	8150478	4/3/2012	In Force	UPLINK POWER CONTROL IN AGGREGATED SPECTRUM SYSTEMS

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MP2729	MP2729C1	US	13/400,078	2/19/2012	8320977	11/27/2012	In Force	UPLINK POWER CONTROL IN AGGREGATED SPECTRUM SYSTEMS
MP2729	MP2729C1C1	US	13/669,480	11/6/2012	8831690	9/9/2014	In Force	UPLINK POWER CONTROL IN SUBCARRIER-BASED AGGREGATED SPECTRUM SYSTEMS
MP2729W	MP2729WOCN	CN	2009801266471	7/9/2009	212009801266	11/19/2014	In Force	UPLINK POWER CONTROL IN AGGREGATED SPECTRUM SYSTEMS
MP2729W	MP2729WCEP	EP	09797612.0	7/9/2009	EP3001159	6/26/2013	In Force	Uplink Power Control in Aggregated Spectrum Systems
MP2729W	MP2729WQJP	JP	2011-518043	7/9/2009	5257909	5/12/2013	In Force	UPLINK POWER CONTROL IN AGGREGATED SPECTRUM SYSTEMS
MP2729W	MP2729WOKR	KR	10-2011-700105	7/9/2009	10-1578495	12/11/2015	In Force	UPLINK POWER CONTROL IN AGGREGATED SPECTRUM SYSTEMS
MP2729W	MP2729WUDE	DE	09797612.0	7/9/2009	2301159	6/26/2013	In Force	UPLINK POWER CONTROL IN AGGREGATED SPECTRUM SYSTEMS
MP2729W	MP2729WUFR	FR	09797612.0	7/9/2009	2301159	6/26/2013	In Force	UPLINK POWER CONTROL IN AGGREGATED SPECTRUM SYSTEMS
MP2729W	MP2729WUGB	GB	09797612.0	7/9/2009	2301159	6/26/2013	In Force	UPLINK POWER CONTROL IN AGGREGATED SPECTRUM SYSTEMS
MP2730	MP2730	US	12/494,004	6/29/2009	8458536	6/4/2013	In Force	DATA RECOVERY IN SOLID STATE MEMORY DEVICES
MP2730	MP2730C1	US	13/908,291	6/3/2013	9032263	5/12/2015	In Force	DATA RECOVERY IN SOLID STATE MEMORY DEVICES
MP2730	MP2730CN	CN	2009101514285	7/17/2009	212009101514	9/24/2014	In Force	DATA RECOVERY IN SOLID STATE MEMORY DEVICES
MP2730J	MP2730JP	JP	2009-165075	7/13/2009	5272212	5/24/2013	In Force	DATA RECOVERY IN SOLID STATE MEMORY DEVICES
MP2730J	MP2730JPD1	JP	2013-058608	3/21/2013	5582372	7/25/2014	In Force	DATA RECOVERY IN SOLID STATE MEMORY DEVICES
MP2731	MP2731	US	12/493,349	6/29/2009	8706951	4/22/2014	In Force	Selectively Accessing Faster or Slower Multi-Level Cell Memory
MP2731	MP2731C1	US	14/257,248	4/21/2014	9542312	1/10/2017	In Force	Selectively Programming Data in Multi-Level Cell Memory
MP2731J	MP2731JP	JP	2009-166080	7/14/2009	5436960	12/20/2013	In Force	Selectively accessing Memory
MP2734	MP2734	US	12/501,270	7/10/2009	9354898	5/31/2016	In Force	DETECTION OF A USB OS DESCRIPTOR REQUEST TO FACILITATE INST
MP2735	MP2735	US	12/501,277	7/10/2009	8341306	12/25/2012	In Force	INSTALLATION OF A DEVICE DRIVER AND A FILTER DRIVER
MP2741	MP2741	US	12/504,080	7/16/2009	8275978	9/25/2012	In Force	Execution of Conditional Branch Instruction Specifying Branch Point
MP2741	MP2741C1	US	13/603,958	9/5/2012	9135006	9/15/2015	In Force	EARLY EXECUTION OF CONDITIONAL BRANCH INSTRUCTION WITH PC
MP2750	MP2750	US	12/509,800	7/27/2009	8127058	2/28/2012	In Force	System and Method of Video Decoding Using Hybrid Buffer
MP2750	MP2750C1	US	13/396,981	2/15/2012	8327046	12/4/2012	In Force	System and Method of Video Decoding Using Hybrid Buffer
MP2768	MP2768	US	12/533,308	7/31/2009	8321869	11/27/2012	In Force	Synchronization Using Agent-Based Semaphores
MP2782	MP2782	US	12/499,807	7/9/2009	8537802	9/17/2013	In Force	CHANNEL MEASUREMENTS IN AGGREGATED-SPECTRUM WIRELESS S
MP2782	MP2782WCEPDE	DE	EP09800142.3	7/12/2009	60200960601	10/16/2019	In Force	Channel Measurements in Aggregated-Spectrum Wireless Systems
MP2782	MP2782WCEPFR	FR	EP09800142.3	7/12/2009	2304986	10/16/2019	In Force	Channel Measurements in Aggregated-Spectrum Wireless Systems
MP2782	MP2782WCEPGB	GB	EP09800142.3	7/12/2009	2304986	10/16/2019	In Force	Channel Measurements in Aggregated-Spectrum Wireless Systems
MP2782W	MP2782WOCN	CN	2009801287371	7/12/2009	212009801287	5/6/2015	In Force	CHANNEL MEASUREMENTS IN AGGREGATED-SPECTRUM WIRELESS S
MP2782W	MP2782WCEP	EP	EP09800142.3	7/12/2009			Pending	Channel Measurements in Aggregated-Spectrum Wireless Systems
MP2782W	MP2782WQJP	JP	2011-519272	7/12/2009	5548929	5/30/2014	In Force	CHANNEL MEASUREMENTS IN AGGREGATED-SPECTRUM WIRELESS S
MP2787	MP2787	US	12/498,979	7/7/2009	8731506	5/20/2014	In Force	Complementary Low Noise Transducer with Active Single Ended to D
MP2787	MP2787C1	US	14/249,143	4/9/2014	9954500	4/24/2018	In Force	COMPLEMENTARY LOW NOISE TRANSDUCTOR WITH ACTIVE SINGLE
MP2787T	MP2787TW	TW	98125350	7/28/2009	1517558	1/11/2016	In Force	COMPLEMENTARY LOW NOISE TRANSDUCTOR WITH ACTIVE SINGLE

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MP2789	MP2789	US	12/646,802	12/23/2009	9288764	3/15/2016	In Force	Discovery-Phase Power Conservation
MP2789	MP2789C1	US	15/066,836	3/10/2016	9656041	5/16/2017	In Force	Discovery-Phase Power Conservation
MP2794	MP2794	US	12/511,881	7/29/2009	8498268	7/30/2013	In Force	METHOD AND APPARATUS FOR RE-ESTABLISHING NETWORK CONNE
MP2801	MP2801C1	US	14/148,219	1/6/2014	8888466	11/25/2014	In Force	SECURE BLOCK ACKNOWLEDGEMENT MECHANISM FOR USE IN COM
MP2802	MP2802	US	12/534,633	8/3/2009	8468326	6/18/2013	In Force	METHOD AND APPARATUS FOR ACCELERATING EXECUTION OF LOGI
MP2808	MP2808	US	12/579,987	10/15/2009	8335993	12/18/2012	In Force	Enhanced Touch Sensitive Interface and Methods and Software for M
MP2810	MP2810	US	12/536,962	8/6/2009	8661739	3/25/2014	In Force	METHOD AND APPARATUS FOR SUPPORTING MULTIPLE CONNECTIO
MP2810	MP2810C1	US	14/071,258	11/4/2013	9388812	5/10/2016	In Force	METHOD AND APPARATUS FOR SUPPORTING MULTIPLE CONNECTIO
MP2810	MP2810C1C1	US	15/145,223	5/3/2016	9750071	8/29/2017	In Force	METHOD AND APPARATUS FOR SUPPORTING MULTIPLE CONNECTIO
MP2810	MP2810C1C1C1	US	15/688,564	8/28/2017			Pending	METHOD AND APPARATUS FOR SUPPORTING MULTIPLE CONNECTIO
MP2811	MP2811	US	12/534,609	8/3/2009	8044733	10/25/2011	In Force	STRESS TOLERANT DIFFERENTIAL COLPITTS VOLTAGE CONTROLLED O
MP2812	MP2812	US	12/535,987	8/5/2009	8406048	3/26/2013	In Force	Accessing Memory Using Fractional Reference Voltages
MP2812	MP2812C1	US	13/848,959	3/22/2013	8867268	10/21/2014	In Force	ACCESSING MEMORY USING REFERENCE VOLTAGES
MP2812	MP2812CN	CN	200910159261.1	8/10/2009	212009101592	10/29/2014	In Force	ACCESSING MEMORY USING FRACTIONAL REFERENCE VOLTAGES
MP2812J	MP2812JP	JP	2009-184665	8/7/2009	5519209	4/11/2014	In Force	Accessing Memory Using Fractional Reference Voltages
MP2812J	MP2812JP01	JP	2014-076702	4/3/2014			Pending	Accessing Memory Using Fractional Reference Voltages
MP2813	MP2813	US	12/192,984	8/15/2008	8023216	9/20/2011	In Force	CALIBRATING SERVOS
MP2817	MP2817	US	12/534,361	8/3/2009	8472968	6/25/2013	In Force	Location-Based Detection of Interference in Cellular Communications
MP2817	MP2817C1	US	13/911,979	6/6/2013	9055460	6/9/2015	In Force	Location-Based Detection of Interference in Cellular Communications
MP2820	MP2820	US	12/539,473	8/11/2009	8363261	1/29/2013	In Force	METHODS, SOFTWARE, CIRCUITS AND APPARATUSES FOR DETECTIN
MP2822	MP2822	US	12/540,995	8/13/2009	8181084	5/15/2012	In Force	DETECTING INSERTION/DELETION USING LDPC CODE
MP2825	MP2825C1	US	14/605,230	1/26/2015	9892051	2/13/2018	In Force	METHOD AND APPARATUS FOR USE OF A PRELOAD INSTRUCTION TO
MP2827	MP2827	US	12/540,788	8/13/2009	8458404	6/4/2013	In Force	PROGRAMMABLE CACHE ACCESS PROTOCOL TO OPTIMIZE POWER C
MP2827	MP2827C1	US	13/909,552	6/4/2013	8769204	7/1/2014	In Force	PROGRAMMABLE CACHE ACCESS PROTOCOL TO OPTIMIZE POWER C
MP2830	MP2830	US	12/537,857	8/7/2009	8296525	10/23/2012	In Force	Method and Apparatus for Data-less Bus Query
MP2834	MP2834	US	12/540,758	8/13/2009	8804691	8/12/2014	In Force	Phase Based Preamble Detection
MP2835	MP2835	US	12/540,753	8/13/2009	8345533	1/1/2013	In Force	Frame Synchronization Techniques
MP2836	MP2836	US	12/539,474	8/11/2009	8284510	10/9/2012	In Force	DETECTING AND RECOVERING TIMING SLIPS IN A SELF-SERVO WRITE
MP2837	MP283711	US	12/619,353	11/16/2009	8495458	7/23/2013	In Force	SYSTEMS AND METHODS FOR MULTISTAGE ERROR CORRECTION
MP2838	MP2838	US	12/539,064	8/11/2009	7944378	5/17/2011	In Force	CIRCUITS AND METHODS FOR CALIBRATING ANALOG AND DIGITAL CI
MP2841W	MP2841WO	WO	PCT/US2009/05	8/18/2009			Pending	MULTI-FUNCTION DEVICE ARCHITECTURE
MP2841W	MP2841W0CN	CN	200980131095.1	8/18/2009	212009801310	8/20/2014	In Force	MULTI-FUNCTION DEVICE ARCHITECTURE
MP2841W	MP2841W0EP	EP	EP09791620.9	8/18/2009			Pending	Multi-Function Device Architecture
MP2841W	MP2841W0JP	JP	2011-522308	8/18/2009	5881056	2/12/2016	In Force	MULTI-FUNCTION DEVICE ARCHITECTURE

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MP2844	MP2844	US	12/542,504	8/17/2009	7932844	4/26/2011	In Force	CIRCUITS AND METHODS FOR CALIBRATING A FREQUENCY RESPON
MP2846	MP2846	US	12/541,731	8/14/2009	8510560	8/13/2013	In Force	Efficient Key Establishment for Wireless Networks
MP2846	MP2846C1	US	13/958,101	8/2/2013	9769653	9/19/2017	In Force	Efficient Key Establishment for Wireless Networks
MP2855	MP2855	US	12/544,014	8/19/2009	8868221	10/21/2014	In Force	ADAPTIVE NEURAL NET FEED FORWARD SYSTEM AND METHOD FOR
MP2856	MP2856	US	12/543,335	8/18/2009	8140724	3/20/2012	In Force	SATA PASS THROUGH PORT
MP2856	MP2856C1	US	13/419,040	3/13/2012	8266487	10/23/2012	In Force	SATA PASS THROUGH PORT
MP2858	MP2858	US	12/545,522	8/21/2009	8417934	4/9/2013	In Force	METHOD AND APPARATUS FOR INTEGRATING PRECISE TIME PROTO
MP2858	MP2858C1	US	13/858,689	4/8/2013	8990552	3/24/2015	In Force	METHOD AND APPARATUS FOR INTEGRATING PRECISE TIME PROTO
MP2858W	MP2858WO	WO	PCT/US2009/05	8/21/2009			Pending	Method And Apparatus For Integrating Precise Time Protocol And Me
MP2858W	MP2858WOCN	CN	2009801326848	8/21/2009	212009801336	9/24/2014	In Force	METHOD AND APPARATUS FOR INTEGRATING PRECISE TIME PROTO
MP2858W	MP2858WOEP	EP	EP09/91789.2	8/21/2009	2316196	2/27/2019	In Force	Method and Apparatus for Integrating Precise Time Protocol and Me
MP2858W	MP2858WOEPDE	DE	EP09/91789.2	8/21/2009	602009057223	2/27/2019	In Force	Method and Apparatus for Integrating Precise Time Protocol and Me
MP2858W	MP2858WOEPFR	FR	EP09/91789.2	8/21/2009	2316196	2/27/2019	In Force	Method and Apparatus for Integrating Precise Time Protocol and Me
MP2858W	MP2858WOEPGB	GB	EP09/91789.2	8/21/2009	2316196	2/27/2019	In Force	Method and Apparatus for Integrating Precise Time Protocol and Me
MP2858W	MP2858WOJP	JP	2011-524029	8/21/2009	5129887	11/9/2012	In Force	SYSTEM FOR INTEGRATING PRECISE TIME PROTOCOL AND MEDIA AC
MP2858W	MP2858WOKR	KR	1020117003358	8/21/2009	101610270	4/1/2016	In Force	Method And Apparatus For Integrating Precise Time Protocol And Me
MP2860	MP2860C1	US	14/200,091	3/7/2014	9262249	2/16/2016	In Force	ADJUSTING A DEFECT THRESHOLD
MP2862	MP2862	US	12/541,688	8/14/2009	8297509	10/30/2012	In Force	USER INTERFACE FEEDBACK USING SCANNER LIGHT SOURCE
MP2863	MP2863	US	12/546,164	8/24/2009	8027117	9/27/2011	In Force	Zone Servo Writing Using Self Servo Writing
MP2863	MP2863C1	US	13/245,323	9/26/2011	8169735	5/1/2012	In Force	ZONE SERVO WRITING USING SELF SERVO WRITING
MP2864	MP2864	US	12/548,205	8/26/2009	8477813	7/2/2013	In Force	PHYSICAL LAYER DATA UNIT FORMAT
MP2864	MP2864C1	US	13/888,223	5/6/2013	8857569	10/21/2014	In Force	PHYSICAL LAYER DATA UNIT FORMAT
MP2864E	MP2864EPD1	EP	13000191.0	8/26/2009	2554750	8/8/2018	In Force	PHYSICAL LAYER DATA FORMAT
MP2864J	MP2864JPD1	JP	2012-267625	12/6/2012	5464507	1/31/2014	In Force	PHYSICAL LAYER DATA UNIT FORMAT
MP2864J	MP2864JUAE	DE	13000191.0	8/26/2009	602009053821	8/8/2018	In Force	PHYSICAL LAYER DATA FORMAT
MP2864J	MP2864JUAFR	FR	13000191.0	8/26/2009	2584750	8/8/2018	In Force	PHYSICAL LAYER DATA FORMAT
MP2864J	MP2864JUAGB	GB	13000191.0	8/26/2009	2584750	8/8/2018	In Force	PHYSICAL LAYER DATA FORMAT
MP2864W	MP2864WO	WO	PCT/US2009/05	8/26/2009			Pending	PHYSICAL LAYER DATA UNIT FORMAAT
MP2864W	MP2864WOCN	CN	2009801335361	8/26/2009	212009801335	9/3/2014	In Force	PHYSICAL LAYER DATA UNIT FORMAT
MP2864W	MP2864WOEP	EP	EP09/91953.4	8/26/2009	EP342877	3/27/2013	In Force	Physical Layer Data Unit Format
MP2864W	MP2864WOJP	JP	2011-525173	8/26/2009	5153944	12/14/2012	In Force	PHYSICAL LAYER DATA UNIT FORMAAT
MP2864W	MP2864WUDE	DE	09791953.4	8/26/2009	602009014146	3/27/2013	In Force	PHYSICAL LAYER DATA UNIT FORMAAT
MP2864W	MP2864WUFR	FR	09791953.4	8/26/2009	2382877	3/27/2013	In Force	PHYSICAL LAYER DATA UNIT FORMAAT
MP2864W	MP2864WUGB	GB	09791953.4	8/26/2009	2382877	3/27/2013	In Force	PHYSICAL LAYER DATA UNIT FORMAAT

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MP2865	MP2865	US	12/548,393	8/26/2009	8766039	4/22/2014	In Force	BEAMFORMING BY SECTOR SWEEPING
MP2865	MP2865C1	US	14/222,569	3/21/2014	8886139	11/11/2014	In Force	BEAMFORMING BY SECTOR SWEEPING
MP2865Q	MP2865QaDE	DE	13003687.4	8/26/2009	603029048741	10/4/2017	In Force	BEAMFORMING BY SECTOR SWEEPING
MP2865Q	MP2865QaFR	FR	13003687.4	8/26/2009	2658144	10/4/2017	In Force	BEAMFORMING BY SECTOR SWEEPING
MP2865Q	MP2865QaGB	GB	13003687.4	8/26/2009	2658144	10/4/2017	In Force	BEAMFORMING BY SECTOR SWEEPING
MP2865W	MP2865W/D1	JP	2013-208243	10/3/2013	5961874	7/8/2016	In Force	BEAMFORMING BY SECTOR SWEEPING
MP2865W	MP2865W/O	WO	PCT/US2009/05	8/26/2009			Pending	BEAMFORMING BY SECTOR SWEEPING
MP2865W	MP2865W/CN	CN	200980133117.4	8/26/2009	Z12009801331	12/10/2014	In Force	BEAMFORMING BY SECTOR SWEEPING
MP2865W	MP2865W/CEP	EP	EP09/191954.2	8/26/2009	EP2319194	11/16/2016	In Force	Beamforming by Sector Sweeping
MP2865W	MP2865W/OJP	JP	2011-525174	8/26/2009	5385392	10/11/2013	In Force	BEAMFORMING BY SECTOR SWEEPING
MP2865W	MP2865W/UD1	EP	EP13003687.4	8/26/2009	EP2658144	10/4/2017	In Force	Beamforming by Sector Sweeping
MP2865W	MP2865W/UDE	DE	602009042488.1	8/26/2009	2319194	11/16/2016	In Force	BEAMFORMING BY SECTOR SWEEPING
MP2865W	MP2865W/JFR	FR	09791954.2	8/26/2009	2319194	11/16/2016	In Force	BEAMFORMING BY SECTOR SWEEPING
MP2865W	MP2865W/JGB	GB	09791954.2	8/26/2009	2319194	11/16/2016	In Force	BEAMFORMING BY SECTOR SWEEPING
MP2866	MP2866	US	12/547,727	8/26/2009	7975195	7/5/2011	In Force	SCAN ARCHITECTURE FOR FULL CUSTOM BLOCKS WITH IMPROVED S
MP2866	MP2866C1	US	13/111,281	5/19/2011	8276031	9/25/2012	In Force	SCAN ARCHITECTURE FOR FULL CUSTOM BLOCKS WITH IMPROVED S
MP2866	MP2866C1C1	US	13/617,586	9/14/2012	8464113	6/11/2013	In Force	SCAN ARCHITECTURE FOR FULL CUSTOM BLOCKS WITH IMPROVED S
MP2869	MP2869CN	CN	200910171528.4	8/26/2009	Z12009101715	12/10/2014	In Force	LIGHT-EMITTING DIODE(LED) DRIVER AND CONTROLLER
MP2873	MP2873	US	12/544,648	8/20/2009	8315187	11/20/2012	In Force	DYNAMIC HEAD OF LINE ALLOCATION
MP2874	MP2874	US	12/547,285	8/25/2009	8238488	8/7/2012	In Force	MULTI-STREAM MAXIMUM-LIKELIHOOD DEMODULATION BASED ON
MP2874	MP2874C1	US	13/567,960	8/6/2012	8437434	5/7/2013	In Force	MULTI-STREAM MAXIMUM-LIKELIHOOD DEMODULATION BASED ON
MP2876	MP2876	US	12/552,027	9/1/2009	9773557	9/26/2017	In Force	MULTI-PLANE DATA ORDER
MP2876T	MP2876TW	TW	98129668	9/3/2009	1607310	12/1/2017	In Force	MULTI-PLANE DATA ORDER
MP2876T	MP2876TW/D1	TW	104115180	9/3/2009	1607311	12/1/2017	In Force	MULTI-PLANE DATA ORDER
MP2876W	MP2876W/O	WO	PCT/US2009/05	9/1/2009			Pending	MULTI-PLANE DATA ORDER
MP2876W	MP2876W/CEP	EP	EP09/192140.7	9/1/2009	2347417	4/18/2018	In Force	Multi-Plane Data Order
MP2876W	MP2876W/OJP	JP	2011-525287	9/1/2009	5360214	9/13/2013	In Force	PROGRAMMING DATA INTO A MULTI-PLANE FLASH MEMORY
MP2876W	MP2876W/OKR	KR	10-2011-700776	9/1/2009	10-1684583	12/2/2016	In Force	PROGRAMMING DATA INTO A MULTI-PLANE FLASH MEMORY (as am
MP2876W	MP2876W/UDE	DE	EP09/192140.7	9/1/2009	602009051876	4/18/2018	In Force	Multi-Plane Data Order
MP2876W	MP2876W/JFR	FR	EP09/192140.7	9/1/2009	2347417	4/18/2018	In Force	Multi-Plane Data Order
MP2876W	MP2876W/JGB	GB	EP09/192140.7	9/1/2009	2347417	4/18/2018	In Force	Multi-Plane Data Order
MP2880	MP2880	US	12/551,758	9/1/2009	8332931	12/11/2012	In Force	PROCESSING COMMANDS ACCORDING TO AUTHORIZATION
MP2880	MP2880C1	US	13/709,204	12/10/2012	8776211	7/8/2014	In Force	PROCESSING COMMANDS ACCORDING TO AUTHORIZATION
MP2885	MP2885	US	12/552,925	9/2/2009	8077520	12/13/2011	In Force	DETERMINING THRESHOLD VOLTAGE DISTRIBUTION IN FLASH MEM

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MP2885	MP2885D1	US	13/323,787	12/12/2011	8331169	12/11/2012	In Force	DETERMINING THRESHOLD VOLTAGE DISTRIBUTION IN FLASH MEM
MP2885	MP2885D1C1	US	13/680,371	11/30/2012	8625369	1/7/2014	In Force	DETERMINING THRESHOLD VOLTAGE DISTRIBUTION IN FLASH MEM
MP2885	MP2885D1C2	US	14/139,552	12/23/2013	8929147	1/6/2015	In Force	Determining Threshold Voltage Distribution in Flash Memory
MP2889	MP2889C1	US	13/184,222	7/15/2011	8289642	10/16/2012	In Force	DIGITAL TECHNIQUES TO DECODE SIGNALS EXTRACTED FROM COMP
MP2890	MP2890	US	12/554,787	9/4/2009	8154822	4/10/2012	In Force	ANALOG TECHNIQUES TO DECODE SIGNALS EXTRACTED FROM COM
MP2890	MP2890C1	US	13/353,193	1/18/2012	8405928	3/26/2013	In Force	ANALOG TECHNIQUES TO DECODE SIGNALS EXTRACTED FROM COM
MP2892	MP2892	US	12/555,346	9/8/2009	8688144	4/1/2014	In Force	METHOD AND APPARATUS TO CONFIGURE MOBILE COMMUNICATION
MP2894	MP2894	US	12/545,398	8/21/2009	8385640	3/12/2013	In Force	USING AMBIENT LIGHT TO DETERMINE AN OUTPUT COLORMAP
MP2896	MP2896	US	12/556,483	9/9/2009	7987396	7/26/2011	In Force	Reducing Bit-Error Rate Using Adaptive Decision Feedback Equalizat
MP2898	MP2898C1	US	13/248,659	9/29/2011	8354865	1/15/2013	In Force	METHOD AND APPARATUS FOR SAMPLING
MP2898	MP2898C1C1	US	13/740,576	1/14/2013	8581635	11/12/2013	In Force	METHOD AND APPARATUS FOR SAMPLING
MP2899	MP2899	US	12/552,543	9/2/2009	8183890	5/22/2012	In Force	METHOD AND APPARATUS FOR SAMPLING
MP2899	MP2899C1	US	13/473,086	5/16/2012	8638128	1/28/2014	In Force	METHOD AND APPARATUS FOR SAMPLING
MP2901	MP2901	US	12/556,859	9/10/2009	8344709	1/1/2013	In Force	Intelligent Switching Controller And Power Conversion Circuits And M
MP2901	MP2901C1	US	13/724,652	12/21/2012	8749208	6/10/2014	In Force	Intelligent Switching Controller And Power Conversion Circuits And M
MP2901	MP2901C1C1	US	14/299,619	6/9/2014	9552005	1/24/2017	In Force	Systems And Methods For Estimating An Output Current Of A Power
MP2901W	MP2901WO	WO	PCT/US2009/05	9/10/2009			Pending	Intelligent Switching Controller And Power Conversion Circuits And M
MP2901W	MP2901WOCN	CN	2009801355569	9/10/2009	212009801355	12/25/2013	In Force	INTELLIGENT SWITCHING CONTROLLER AND POWER CONVERSION CI
MP2901W	MP2901WCEP	EP	EP098135791	9/10/2009			Pending	Intelligent Switching Controller and Power Conversion Circuits and M
MP2904	MP2904	US	12/557,311	9/10/2009	8351290	1/8/2013	In Force	ERASED PAGE DETECTION
MP2904	MP2904C1	US	13/722,840	12/20/2012	8656231	2/18/2014	In Force	METHOD AND APPARATUS FOR DETERMINING WHETHER A PAGE IN
MP2909	MP2909	US	12/549,464	8/28/2009	8238315	8/7/2012	In Force	RAPID LOCAL ADDRESS ASSIGNMENT FOR WIRELESS COMMUNICATI
MP2909	MP2909C1	US	13/568,641	8/7/2012	8446843	5/21/2013	In Force	Rapid Local Address Assignment For Wireless Communication Netwo
MP2909	MP2909C1C1	US	13/898,819	5/21/2013	9055430	6/9/2015	In Force	Rapid Local Address Assignment For Wireless Communication Netwo
MP2909I	MP2909WO	WO	PCT/US2009/05	9/2/2009			Pending	Rapid Local Address Assignment For Wireless Communication Netwo
MP2909W	MP2909WO	WO	PCT/US2009/05	9/1/2009			Pending	Rapid Local Address Assignment For Wireless Communication Netwo
MP2909W	MP2909WOP	JP	2011-526912	9/2/2009	5272182	5/24/2013	In Force	RAPID LOCAL ADDRESS ASSIGNMENT FOR WIRELESS COMMUNICATI
MP2910	MP2910	US	12/557,590	9/11/2009	8213452	7/17/2012	In Force	WRITING AND READING USER DATA USING SERVO SPIRALS
MP2911	MP2911	US	12/559,351	9/14/2009	8291285	10/16/2012	In Force	CIRCLANT PROCESSING SCHEDULER FOR LAYERED LDPC DECODER
MP2911	MP2911C1	US	13/651,949	10/15/2012	8495453	7/23/2013	In Force	CIRCLANT PROCESSING SCHEDULER FOR LAYERED LDPC DECODER
MP2912	MP2912	US	12/559,987	9/15/2009	8296555	10/23/2012	In Force	PRELOADER
MP2912	MP2912C1	US	13/657,511	10/22/2012	8688968	4/1/2014	In Force	PRELOADING AN APPLICATION WHILE AN OPERATING SYSTEM LOAD
MP2912	MP2912C1C1	US	14/205,196	3/11/2014	9652249	5/16/2017	In Force	Preloading an Application while an Operating System Loads
MP2912W	MP2912WO	WO	PCT/US2009/05	9/15/2009			Pending	PRELOADER

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MP2912W	MP2912WOCN	CN	2009801368499	9/15/2009			Pending	PRELOADING APPLICATIONS ONTO MEMORY AT LEAST PARTIALLY D
MP2912W	MP2912WOP	JP	2011-527899	9/15/2009	5488182	3/7/2014	In Force	PRELOADING APPLICATIONS ONTO MEMORY AT LEAST PARTIALLY D
MP2912W	MP2912WOKR	KR	1020117008760	9/15/2009	101595043	2/11/2016	In Force	Preloading Applications Onto Memory at Least Partially During Boot
MP2914	MP2914	US	12/563,964	9/21/2009	8286637	10/23/2012	In Force	Channel Quality Monitoring and Method for Qualifying a Storage Cha
MP2914	MP2914C1	US	13/650,446	10/12/2012	8543894	9/24/2013	In Force	Channel Quality Monitoring and Method for Qualifying a Storage Cha
MP2914	MP2914C1aa	US	14/511,695	10/10/2014	9189315	11/17/2015	In Force	CHANNEL QUALITY MONITORING AND METHOD FOR QUALIFYING A
MP2914	MP2914C1C1	US	13/974,293	8/23/2013	8887033	11/11/2014	In Force	Channel Quality Monitoring and Method for Qualifying a Storage Ch
MP2916	MP2916	US	12/563,239	9/21/2009	8417928	4/9/2013	In Force	TURBO BOOT SYSTEMS AND METHODS FOR SUBSEQUENT BOOTING
MP2916	MP2916C1	US	13/857,216	4/5/2013	9195472	11/24/2015	In Force	SYSTEM AND METHOD FOR BOOTING UP A COMPUTER BASED ON D
MP2916W	MP2916WO	WO	PCT/US2009/05	9/24/2009			Pending	Turbo Boot Systems And Methods
MP2916W	MP2916WOCN	CN	2009801374574	9/24/2009	ZL2009801374	4/1/2015	In Force	TURBO BOOT SYSTEMS AND METHODS
MP2916W	MP2916WCEP	EP	EP09792927.7	9/24/2009	2329365	3/14/2018	In Force	Turbo Boot Systems and Methods
MP2916W	MP2916WOP	JP	2011-528097	9/24/2009	5234473	4/5/2013	In Force	TURBO BOOT SYSTEMS AND METHODS
MP2916W	MP2916WUDE	DE	EP09792927.7	9/24/2009	602009051261	3/14/2018	In Force	Turbo Boot Systems and Methods
MP2916W	MP2916WUGB	GB	EP09792927.7	9/24/2009	2329365	3/14/2018	In Force	Turbo Boot Systems and Methods
MP2917	MP2917	US	12/565,430	9/23/2009	8253231	8/28/2012	In Force	Stacked Integrated Circuit Package Using a Window Substrate
MP2918	MP2918	US	12/563,390	9/21/2009	8428079	4/23/2013	In Force	SYSTEMS AND METHODS FOR DISCOVERING A WIRELESS NETWORK I
MP2918	MP2918C1	US	13/867,368	4/22/2013	9332485	5/3/2016	In Force	METHOD AND INTEGRATED CIRCUIT FOR DISCOVERING PEERS IN A P
MP2919	MP2919	US	12/562,782	9/18/2009	8184052	5/22/2012	In Force	DIGITAL BEAMFORMING SCHEME FOR PHASED-ARRAY ANTENNAS
MP2919	MP2919C1	US	13/474,691	5/17/2012	8446317	5/21/2013	In Force	BEAMFORMING SCHEME FOR PHASED-ARRAY ANTENNAS
MP2919	MP2919C1AA	US	14/451,248	8/4/2014	9991941	6/5/2018	In Force	BEAMFORMING SCHEME FOR PHASED-ARRAY ANTENNAS
MP2919	MP2919C1C1	US	13/898,247	5/20/2013	8797212	8/5/2014	In Force	BEAMFORMING SCHEME FOR PHASED-ARRAY ANTENNAS
MP2924	MP2924	US	12/724,836	3/16/2010	8279018	10/2/2012	In Force	TRIFILA BALUN FOR WIRELESS TRANSCIVER
MP2928	MP2928	US	12/562,687	9/18/2009	8351549	1/8/2013	In Force	MULTI-STREAM DEMODULATION SCHEME USING MULTIPLE DETECT
MP2928	MP2928C1	US	13/735,781	1/7/2013	8638887	1/28/2014	In Force	MULTI-STREAM DEMODULATION SCHEME USING MULTIPLE DETECT
MP2928	MP2928C1C1	US	14/165,335	1/27/2014	8995584	3/31/2015	In Force	MULTI-STREAM DEMODULATION SCHEME USING MULTIPLE DETECT
MP2929	MP2929	US	12/567,870	9/28/2009	8072862	12/6/2011	In Force	Adaptive Distortion Corrector
MP2932	MP2932	US	12/571,939	10/1/2009	8347023	1/1/2013	In Force	COMPRESSION BASED WEAR LEVELING FOR NON-VOLATILE MEMOR
MP2932	MP2932CN	CN	200910246805.1	10/9/2009	ZL2009102468	4/9/2014	In Force	COMPRESSION BASED WEAR LEVELING FOR NON-VOLATILE MEMOR
MP2934	MP2934	US	12/574,790	10/7/2009	8310570	11/13/2012	In Force	Repairing Defective Pixels
MP2937	MP2937	US	12/572,329	10/2/2009	8321771	11/27/2012	In Force	MODIFIED TRACE-BACK USING SOFT OUTPUT VITERBI ALGORITHM (S
MP2937	MP2937C1	US	13/669,846	11/6/2012	8578254	11/5/2013	In Force	MODIFIED TRACE-BACK USING SOFT OUTPUT VITERBI ALGORITHM (S
MP2939	MP2939C1	US	14/027,949	9/16/2013	8767087	7/1/2014	In Force	PREVENTING COLOR ARTIFACTS IN OVEREXPOSED REGIONS AND PRE

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MP2940	MP2940	US	12/573,473	10/5/2009	8705460	4/22/2014	In Force	AUTONOMOUS ADAPTATION OF MODULATION AND CODING SCHEM
MP2940	MP2940C1	US	14/206,550	3/12/2014	8982828	3/17/2015	In Force	AUTONOMOUS ADAPTATION OF MODULATION AND CODING SCHEM
MP2943	MP2943	US	12/579,678	10/15/2009	8706926	4/22/2014	In Force	Architecture For Data Storage Systems
MP2943	MP2943C1	US	14/237,410	4/21/2014	9639324	5/2/2017	In Force	Architecture For Writing And Reading Data In A Data Storage System
MP2943W	MP2943WO	WO	PCT/US2009/06	10/15/2009			Pending	Architecture For Data Storage Systems
MP2943W	MP2943WOJP	JP	2011-532246	10/15/2009	5354816	9/6/2013	In Force	Architecture For Data Storage Systems
MP2943W	MP2943WOKR	KR	1020117008008	10/15/2009	101534790	7/1/2015	In Force	Architecture For Data Storage Systems
MP2945	MP2945C1	US	13/196,368	8/2/2011	8593058	12/3/2013	In Force	DATA CONVERTER WITH REDUNDANCY FOR ERROR CORRECTION IN
MP2946	MP2946	US	12/578,536	10/13/2009	8089318	1/3/2012	In Force	Methods, Algorithms, Circuits, and Systems for Determining a Refere
MP2946	MP2946C1	US	13/332,833	12/21/2011	8830002	9/9/2014	In Force	Methods, Algorithms, Circuits, and Systems for Determining a Refere
MP2946W	MP2946WOCN	CN	200980141141.1	10/14/2009	ZL2009801411	8/6/2014	In Force	METHODS, ALGORITHMS, CIRCUITS, AND SYSTEMS FOR DETERMININ
MP2946W	MP2946WCEP	EP	EP09021190.7	10/14/2009	EP2351223	3/25/2015	In Force	Methods, Algorithms, Circuits, and Systems for Determining a Refere
MP2946W	MP2946WJJP	JP	2011-532213	10/14/2009	5245183	4/19/2013	In Force	Methods, Algorithms, Circuits, and Systems for Determining a Refere
MP2946W	MP2946WJDE	DE	09821190.7	10/14/2009	60200930254	3/25/2015	In Force	Methods, Algorithms, Circuits, and Systems for Determining a Refere
MP2946W	MP2946WJFR	FR	09821190.7	10/14/2009	2351223	3/25/2015	In Force	Methods, Algorithms, Circuits, and Systems for Determining a Refere
MP2946W	MP2946WJGB	GB	09821190.7	10/14/2009	2351223	3/25/2015	In Force	Methods, Algorithms, Circuits, and Systems for Determining a Refere
MP2951	MP2951	US	12/580,809	10/16/2009	8369471	2/5/2013	In Force	METHOD AND APPARATUS FOR IMPROVING CHANNEL ESTIMATION
MP2951	MP2951C1	US	13/758,733	2/4/2013	8649472	2/1/2014	In Force	METHOD AND APPARATUS FOR IMPROVING CHANNEL ESTIMATION
MP2952	MP2952	US	12/603,422	10/21/2009	8761007	6/24/2014	In Force	METHOD AND APPARATUS FOR PREVENTING A MOBILE DEVICE FRO
MP2953	MP2953	US	12/625,279	11/24/2009	8982908	3/17/2015	In Force	EXTENSION OF PATH REPLY MESSAGE TO ENCODE MULTIPLE ROUTE
MP2955	MP2955C1	US	14/095,220	12/3/2013	9252777	2/2/2016	In Force	Method And Apparatus For Multiplexing Pins Of An Integrated Circuit
MP2955W	MP2955WO	WO	PCT/US2009/06	10/23/2009			Pending	Switch Pin Multiplexing
MP2955W	MP2955WOCN	CN	200980142208.1	10/23/2009	ZL2009801422	5/28/2014	In Force	SWITCH PIN MULTIPLEXING
MP2956	MP2956	US	12/581,068	10/16/2009	8236421	10/23/2012	In Force	METHODS, ALGORITHMS, CIRCUITS, AND SYSTEMS FOR CONTROLLIN
MP2956	MP2956I1	US	13/657,539	10/22/2012	9100325	8/4/2015	In Force	Controlling a Network Connection Status Indicator
MP2957	MP2957	US	12/606,167	10/26/2009	8570898	10/29/2013	In Force	METHOD FOR DISCOVERING DEVICES IN A WIRELESS NETWORK
MP2957	MP2957C1	US	14/045,488	10/3/2013	9226337	12/29/2015	In Force	METHOD AND APPARATUS FOR ESTABLISHING CONNECTIONS IN A W
MP2957	MP2957C1C1	US	14/974,134	12/18/2015	9820333	11/14/2017	In Force	METHOD AND APPARATUS FOR NEGOTIATING DEVICE ROLES IN A WI
MP2958	MP2958	US	12/605,983	10/26/2009	8995390	3/31/2015	In Force	METHOD AND APPARATUS FOR INCREASING THE SPEED OF HANDOV
MP2966	MP2966	US	12/607,668	10/28/2009	8514777	8/20/2013	In Force	METHOD AND APPARATUS FOR PROTECTING LOCATION PRIVACY OF
MP2969	MP2969	US	12/605,978	10/26/2009	8630588	1/14/2014	In Force	EFFICIENT AND FLEXIBLE TRANSMIT BEAMFORMING SECTOR SWEEP
MP2969	MP2969D1	US	14/140,120	12/24/2013	9413437	8/9/2016	In Force	EFFICIENT AND FLEXIBLE TRANSMIT BEAMFORMING SECTOR SWEEP
MP2969W	MP2969WD1	JP	2013-035685	2/26/2013	5473174	2/14/2014	In Force	EFFICIENT AND FLEXIBLE TRANSMIT BEAMFORMING SECTOR SWEEP
MP2969W	MP2969WO	WO	PCT/US2009/06	10/26/2009			Pending	EFFICIENT AND FLEXIBLE TRANSMIT BEAMFORMING SECTOR SWEEP

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MP2969W	MP2969W/CN	CN	200980140469.1	10/26/2009	ZL2009801404	4/22/2015	In Force	METHOD AND APPARATUS FOR EFFICIENT AND FLEXIBLE TRANSMIT
MP2969W	MP2969W/EP	EP	EP09744290.9	10/26/2009			Pending	Efficient and Flexible Transmit Beamforming Sector Sweep in a Multi-
MP2969W	MP2969W/JP	JP	2011-534661	10/26/2009	5214033	3/8/2013	In Force	EFFICIENT AND FLEXIBLE TRANSMIT BEAMFORMING SECTOR SWEEP
MP2970	MP2970	US	12/581,262	10/19/2009	7986487	7/26/2011	In Force	HARMONIC SENSOR
MP2970	MP2970/C1	US	13/190,036	7/25/2011	8154820	4/10/2012	In Force	Harmonic Sensor
MP2970	MP2970/C1aa	US	13/883,830	4/16/2013	8711510	4/29/2014	In Force	Harmonic Sensor
MP2970	MP2970/C1C1	US	13/442,033	4/9/2012	8422160	4/16/2013	In Force	Harmonic Sensor
MP2971	MP2971	US	12/607,841	10/28/2009	8341476	12/25/2012	In Force	IR VOLTAGE DROP SCREENING WHEN EXECUTING A MEMORY BUILT
MP2971	MP2971/C1	US	13/721,397	12/20/2012	8667110	12/10/2013	In Force	IR VOLTAGE DROP SCREENING WHEN EXECUTING A MEMORY BUILT
MP2987	MP2987	US	12/612,615	11/4/2009	8386738	2/26/2013	In Force	OFF-CHIP NON-VOLATILE MEMORY ACCESS
MP2987	MP2987/C1	US	13/772,185	2/20/2013	9292429	3/22/2016	In Force	OFF-CHIP NON-VOLATILE MEMORY ACCESS
MP3003	MP3003	US	12/604,558	10/23/2009	8321769	11/27/2012	In Force	MULTI-PARITY TENSOR-PRODUCT CODE FOR DATA CHANNEL
MP3003	MP3003/C1	US	13/674,512	11/12/2012	8635515	1/21/2014	In Force	MULTI-PARITY TENSOR-PRODUCT CODE FOR DATA CHANNEL
MP3004	MP3004	US	12/610,106	10/30/2009	8213228	7/3/2012	In Force	Flash Memory Read Performance
MP3004	MP3004/1	US	13/268,183	10/7/2011	8611151	12/17/2013	In Force	Flash Memory Read Performance
MP3004	MP3004/2	US	13/558,771	6/29/2012	8947929	2/3/2015	In Force	Flash-Based Soft Information Generation
MP3006	MP3006	US	12/579,928	10/15/2009	8520415	8/27/2013	In Force	MULTI-FUNCTION FEEDBACK USING AN OPTOCOUPLER
MP3007	MP3007	US	12/480,259	6/8/2009	8164285	4/24/2012	In Force	External Disturbance Detection System And Method For Two-Phase
MP3007	MP3007/C1	US	13/453,661	4/23/2012	8278861	10/2/2012	In Force	EXTERNAL DISTURBANCE DETECTION SYSTEM AND METHOD FOR TW
MP3007	MP3007/CN	CN	200910206454.1	11/13/2009	ZL2009102064	9/24/2014	In Force	EXTERNAL DISTURBANCE DETECTION SYSTEM AND METHOD FOR TW
MP3008	MP3008	US	12/469,196	5/20/2009	8277198	10/2/2012	In Force	Fan Motor Control Systems
MP3008	MP3008/CN	CN	200910206456.1	11/13/2009	ZL2009102064	4/2/2014	In Force	MOTOR CONTROL SYSTEM
MP3008	MP3008/1	US	13/233,593	9/15/2011	8867956	8/19/2014	In Force	Apparatus And Method For Controlling Speed Of A Fan Via A First Co
MP3008	MP3008/CN	CN	201110291642.1	9/23/2011	ZL2011102916	3/2/2016	In Force	FAN MOTOR CONTROL SYSTEM
MP3010	MP3010	US	12/576,501	10/9/2009	8417046	4/9/2013	In Force	SHADOW AND HIGHLIGHT IMAGE ENHANCEMENT
MP3015	MP3015	US	12/611,837	11/3/2009	8223545	7/17/2012	In Force	SYSTEMS AND METHODS FOR DATA PAGE MANAGEMENT OF NAND F
MP3016	MP3016	US	12/607,129	10/28/2009	8321752	11/27/2012	In Force	Integrated 2-Level Low Density Parity Check (LDPC) Codes
MP3016	MP3016/C1	US	13/686,365	11/27/2012	8555139	10/8/2013	In Force	Integrated 2-Level Low Density Parity Check (LDPC) Codes
MP3021	MP3021	US	12/620,367	11/17/2009	8339722	12/25/2012	In Force	METHODS OF DEMODULATING A SERVO POSITION ERROR SIGNAL ON
MP3021	MP3021/C1	US	13/715,980	12/14/2012	9583137	2/28/2017	In Force	METHODS OF DEMODULATING A SERVO POSITION ERROR SIGNAL ON
MP3023	MP3023	US	12/621,268	11/18/2009	8401043	3/19/2013	In Force	HARDWARE INTERFACE UTILIZING ALIGNMENT SYMBOLS FOR DEMU
MP3026	MP3026	US	12/614,043	11/6/2009	8191033	5/29/2012	In Force	IN SITU CLOCK JITTER MEASUREMENT
MP3028	MP3028	US	12/480,161	6/8/2009	8269440	9/18/2012	In Force	Motor Speed Control System And Method Without Pulse-Width Mod
MP3028	MP3028/CN	CN	200910246078	12/1/2009	ZL2009102460	7/2/2014	In Force	MOTOR SPEED CONTROL SYSTEM AND METHOD WITHOUT PULSE-W

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MP3029	MP3029	US	12/612,692	11/5/2009	8301989	10/30/2012	In Force	CALCULATION OF SOFT DECODING METRICS
MP3029	MP3029C1	US	13/662,485	10/28/2012	8645808	2/4/2014	In Force	CALCULATION OF SOFT DECODING METRICS
MP3030	MP3030	US	12/612,694	11/5/2009	8088717	1/17/2012	In Force	HYBRID EQUALIZER
MP3031	MP3031	US	12/612,695	11/5/2009	8175135	5/8/2012	In Force	EQUALIZER WITH ADAPTIVE NOISE LOADING
MP3035	MP3035	US	12/624,353	11/23/2009	8331494	12/11/2012	In Force	Combined Digital Down Conversion (DDC) and Decimation Filter
MP3041	MP3041	US	12/628,038	11/30/2009	8360328	9/4/2012	In Force	CORRECTION OF TIMING ERRORS TO ENABLE LONG SLEEP TIMES
MP3041	MP3041C1	US	13/601,699	8/31/2012	8564079	8/6/2013	In Force	CORRECTION OF TIMING ERRORS TO ENABLE LONG SLEEP TIMES
MP3043	MP3043	US	12/624,101	11/23/2009	8879516	11/4/2014	In Force	EFFICIENT FORMATS OF BEACON, ANNOUNCEMENT, AND BEAMFORM
MP3043	MP3043C1	US	14/531,709	11/3/2014	9992772	6/5/2018	In Force	BEACON FRAMES THAT CAN BE USED FOR BEAMFORMING TRAINING
MP3043W	MP3043WO	WO	PCT/US2009/06	11/23/2009			Pending	EFFICIENT FORMATS OF BEACON, ANNOUNCEMENT, AND BEAMFOR
MP3047	MP3047	US	12/628,059	11/30/2009	8368214	2/5/2013	In Force	ALPHA SHIELDING TECHNIQUES AND CONFIGURATIONS
MP3047	MP3047CN	CN	200910253962.1	12/9/2009	ZL2009102539	3/13/2013	In Force	ALPHA SHIELDING TECHNIQUES AND CONFIGURATIONS
MP3047	MP3047D1	US	13/758,650	2/4/2013	8663861	12/10/2013	In Force	ALPHA SHIELDING TECHNIQUES AND CONFIGURATIONS
MP3048	MP3048	US	12/627,452	11/30/2009	8198925	6/12/2012	In Force	Digital Power On Reset
MP3048	MP3048C1	US	13/490,731	6/7/2012	8810289	8/19/2014	In Force	Digital Power On Reset
MP3050	MP3050	US	12/614,843	11/9/2009	8605478	3/26/2013	In Force	LOW LOSS MAGNETIC CORE
MP3050	MP3050C1	US	13/850,080	3/25/2013	8760249	6/24/2014	In Force	METHOD AND APPARATUS FOR INCREASING ENERGY EFFECENCY OF
MP3051	MP3051	US	12/629,575	12/2/2009	8258729	9/4/2012	In Force	DOWN SAMPLING METHOD FOR HARD DISK SPEED CONTROL LOOP
MP3051	MP3051C1	US	13/598,541	8/29/2012	8614556	12/24/2013	In Force	DOWN SAMPLING METHOD FOR HARD DISK SPEED CONTROL LOOP
MP3052	MP3052	US	12/640,460	12/17/2009	8566455	10/22/2013	In Force	METHOD AND APPARATUS FOR SUPPORTING MULTIPLE CONNECTIO
MP3052	MP3052C1	US	14/056,778	10/17/2013	9277579	3/1/2016	In Force	METHOD AND APPARATUS FOR SUPPORTING MULTIPLE CONNECTIO
MP3052	MP3052C1C1	US	15/047,202	2/18/2016	9844082	12/12/2017	In Force	METHOD AND APPARATUS FOR SUPPORTING MULTIPLE CONNECTIO
MP3056	MP3056	US	12/579,260	10/14/2009	8452311	5/28/2013	In Force	SLEEP ESTIMATOR FOR CELLULAR TELEPHONES
MP3058	MP3058	US	12/636,558	12/11/2009	8443211	5/14/2013	In Force	HIBERNATION OR SUSPEND USING A NON-VOLATILE-MEMORY DEVIC
MP3058W	MP3058WO	WO	PCT/US2009/06	12/11/2009			Pending	HIBERNATION OR SUSPEND USING A NON-VOLATILE-MEMORY DEVIC
MP3058W	MP3058W/CN	CN	200908153758.1	12/11/2009	ZL2009081537	9/10/2014	In Force	METHOD AND SYSTEM FOR HIBERNATION OR SUSPEND USING A NO
MP3058W	MP3058W/CEP	EP	EP09803951.4	12/11/2009	EP2390081	11/6/2013	In Force	Method and System for Hibernation or Suspend Using a Non-Volatile-
MP3058W	MP3058W/JP	JP	2011-544456	12/11/2009	5555778	6/27/2014	In Force	METHOD AND SYSTEM FOR HIBERNATION OR SUSPEND USING A NO
MP3058W	MP3058W/DE	DE	09803951.4	12/11/2009	602009019992	11/6/2013	In Force	METHOD AND SYSTEM FOR HIBERNATION OR SUSPEND USING A NO
MP3058W	MP3058W/FR	FR	09803951.4	12/11/2009	2380081	11/6/2013	In Force	METHOD AND SYSTEM FOR HIBERNATION OR SUSPEND USING A NO
MP3058W	MP3058W/GB	GB	09803951.4	12/11/2009	2380081	11/6/2013	In Force	METHOD AND SYSTEM FOR HIBERNATION OR SUSPEND USING A NO
MP3060	MP3060	US	12/652,044	1/5/2010	8391392	3/5/2013	In Force	PRECODING CODEBOOKS FOR MIMO COMMUNICATION SYSTEMS
MP3060	MP3060C1	US	13/760,088	2/6/2013	8711970	4/29/2014	In Force	PRECODING CODEBOOKS FOR MIMO COMMUNICATION SYSTEMS
MP3060	MP3060C1C1	US	14/215,093	3/17/2014	9031150	5/12/2015	In Force	PRECODING CODEBOOKS FOR 4TX AND 8TX MIMO COMMUNICATIO

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MP3060W	MP3060W/1a	JP	2014-255857	12/18/2014	5907631	4/1/2016	In Force	PRECODING CODEBOOKS FOR MIMO COMMUNICATION SYSTEMS
MP3060W	MP3060W/1D1	JP	2013-146952	7/12/2013	5671104	12/26/2014	In Force	PRECODING CODEBOOKS FOR MIMO COMMUNICATION SYSTEMS
MP3060W	MP3060W/CN	CN	201090003252	1/5/2010	212010860032	10/8/2014	In Force	PRECODING CODEBOOKS FOR MIMO COMMUNICATION SYSTEMS
MP3060W	MP3060W/CEP	EP	EP10726796.5	1/5/2010	2374221	9/11/2019	In Force	Precoding Codebooks for MIMO Communication Systems
MP3060W	MP3060W/CEPDE	DE	EP10726796.5	1/5/2010	602010060985	9/11/2019	In Force	Precoding Codebooks for MIMO Communication Systems
MP3060W	MP3060W/CEPFR	FR	EP10726796.5	1/5/2010	2374221	9/11/2019	In Force	Precoding Codebooks for MIMO Communication Systems
MP3060W	MP3060W/CEPGB	GB	EP10726796.5	1/5/2010	2374221	9/11/2019	In Force	Precoding Codebooks for MIMO Communication Systems
MP3060W	MP3060W/CJP	JP	2011-544111	1/5/2010	5322327	7/26/2013	In Force	PRECODING CODEBOOKS FOR MIMO COMMUNICATION SYSTEMS
MP3060W	MP3060W/CKR	KR	10-2011-701594	1/5/2010	1673497	11/1/2016	In Force	PRECODING CODEBOOKS FOR MIMO COMMUNICATION SYSTEMS
MP3061	MP3061D	US	29/330,340	1/5/2009	D626549	11/2/2010	In Force	PORTABLE COMPUTING DEVICE
MP3064	MP3064	US	12/652,223	1/5/2010	8138822	3/20/2012	In Force	CIRCUITS AND METHODS FOR CALIBRATING SWITCHING CURRENT S
MP3065	MP3065	US	12/477,152	6/3/2009	8385441	2/26/2013	In Force	EFFICIENT MIMO TRANSMISSION SCHEMES
MP3065	MP3065C1	US	13/766,790	2/14/2013	8670499	3/11/2014	In Force	EFFICIENT MIMO TRANSMISSION SCHEMES
MP3065W	MP3065W/CN	CN	200980150266	6/25/2009	212009801502	3/26/2014	In Force	EFFICIENT MIMO TRANSMISSION SCHEMES
MP3065W	MP3065W/CEP	EP	EP09837397	6/25/2009	2374252	3/28/2018	In Force	Efficient MIMO Transmission Schemes
MP3065W	MP3065W/CEPDE	DE	EP09837397	6/25/2009	2374252	3/28/2018	In Force	Efficient MIMO Transmission Schemes
MP3065W	MP3065W/CEPFR	FR	EP09837397	6/25/2009	2374252	3/28/2018	In Force	Efficient MIMO Transmission Schemes
MP3065W	MP3065W/CEPGB	GB	EP09837397	6/25/2009	2374252	3/28/2018	In Force	Efficient MIMO Transmission Schemes
MP3066	MP3066	US	12/663,348	1/6/2010	8270978	9/18/2012	In Force	METHOD AND APPARATUS FOR PERFORMING A HANDOVER BETWEE
MP3066	MP3066C1	US	13/618,575	9/14/2012	8666419	3/4/2014	In Force	METHOD AND APPARATUS FOR PERFORMING A HANDOVER BETWEE
MP3067	MP3067	US	12/360,587	1/27/2009	8159919	4/17/2012	In Force	METHOD AND APPARATUS FOR OPTIMIZING OPTICAL RECORDING
MP3067	MP3067C1	US	13/444,448	4/11/2012	8462601	6/11/2013	In Force	METHOD AND APPARATUS FOR OPTIMIZING OPTICAL RECORDING
MP3068	MP3068	US	12/636,923	12/14/2009	8521976	8/27/2013	In Force	METHOD AND SYSTEM FOR IMPROVING DISK DRIVE PERFORMANCE
MP3068	MP3068C1C1	US	14/261,706	4/25/2014	9081505	7/14/2015	In Force	METHOD AND SYSTEM FOR IMPROVING DISK DRIVE PERFORMANCE
MP3071	MP3071	US	12/683,963	1/7/2010	8553582	10/8/2013	In Force	TRAFFIC SPRAYING IN A CHASSIS-BASED NETWORK SWITCH
MP3071	MP3071C1	US	14/047,865	10/7/2013	9165916	10/20/2015	In Force	TRAFFIC SPRAYING IN A CHASSIS-BASED NETWORK SWITCH
MP3072	MP3072	US	12/683,971	1/7/2010	8565234	10/22/2013	In Force	MULTICAST QUEUEING IN A SWITCH
MP3072	MP3072C1	US	14/047,871	10/7/2013	9137030	9/15/2015	In Force	MULTICAST QUEUEING IN A NETWORK SWITCH
MP3073	MP3073	US	12/663,878	1/7/2010	8250399	8/21/2012	In Force	METHOD AND APPARATUS FOR CLOCK WANDER COMPENSATION
MP3074	MP3074	US	12/495,707	5/5/2009	8442016	5/14/2013	In Force	CO-EXISTANCE OF BLUETOOTH AND WIRELESS LOCAL AREA NETWORK
MP3074	MP3074C1	US	13/892,944	5/13/2013	9398603	7/19/2016	In Force	APPARATUS TO FACILITATE CO-EXISTENCE OF BLUETOOTH AND WIR
MP3075	MP3075	US	12/616,454	11/11/2009	8340034	12/25/2012	In Force	Bluetooth and Wireless LAN Arbitration
MP3075	MP3075C1	US	13/722,354	12/20/2012	8861469	10/14/2014	In Force	Bluetooth and Wireless LAN Arbitration
MP3076	MP3076	US	12/370,953	2/13/2009	8346171	1/1/2013	In Force	Reducing Interference Between Wireless Networks

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MP3076	MP3076C1	US	13/726,682	12/26/2012	8498575	7/30/2013	In Force	Reducing Interference Between Wireless Networks
MP3077	MP3077	US	12/684,659	1/8/2010	7995301	8/9/2011	In Force	METHOD AND APPARATUS FOR DETERMINING A LOCATION OF A DEF
MP3077	MP3077C1	US	13/189,111	7/22/2011	8305703	11/6/2012	In Force	METHOD AND APPARATUS FOR DETERMINING A LOCATION OF A DEF
MP3077	MP3077C1A4	US	14/094,710	12/2/2013	8902722	12/2/2014	In Force	METHOD AND APPARATUS FOR DETERMINING A LOCATION OF A FEA
MP3077W	MP3077WO	WO	PCT/US2010/02	1/8/2010			Pending	METHOD AND APPARATUS FOR DETERMINING A LOCATION OF A DEF
MP3077W	MP3077WOCN	CN	201080004501.1	1/8/2010	212010800045	4/22/2015	In Force	METHOD AND APPARATUS FOR DETERMINING A LOCATION OF A DEF
MP3077W	MP3077WCEP	EP	EP10700900.3	1/8/2010	2377125	3/7/2018	In Force	Method and Apparatus for Determining a Location of a Defect on a S
MP3077W	MP3077WGP	JP	2011-545464	1/8/2010	5725370	4/10/2015	In Force	METHOD AND APPARATUS FOR DETERMINING A LOCATION OF A DEF
MP3077W	MP3077WOKR	KR	10-2011-701518	1/8/2010	10-1608024	4/11/2016	In Force	METHOD AND APPARATUS FOR DETERMINING A LOCATION OF A DEF
MP3077W	MP3077WUDE	DE	EP10700900.3	1/8/2010	2377125	3/7/2018	In Force	Method and Apparatus for Determining a Location of a Defect on a St
MP3077W	MP3077WUFR	FR	EP10700900.3	1/8/2010	2377125	3/7/2018	In Force	Method and Apparatus for Determining a Location of a Defect on a St
MP3077W	MP3077WUGB	GB	EP10700900.3	1/8/2010	2377125	3/7/2018	In Force	Method and Apparatus for Determining a Location of a Defect on a St
MP3078	MP3078CN	CN	201010004034X	1/18/2010			Pending	Caching Systems And Methods Using A Solid State Disk
MP3080	MP3080	US	12/684,735	1/8/2010	8077069	12/13/2011	In Force	Two-Step Subranging ADC Architecture
MP3080	MP3080C1	US	13/323,527	12/12/2011	8742969	6/3/2014	In Force	Two-Step Subranging ADC Architecture
MP3080	MP3080C1C1	US	14/291,204	5/30/2014	9077364	7/7/2015	In Force	Method And Apparatus For Converting An Analog Signal To A Digital S
MP3080T	MP3080TW	TW	99101532	1/20/2010	1506961	11/1/2015	In Force	TWO-STEP SUBRANGING ADC ARCHITECTURE
MP3082	MP3082	US	12/684,760	1/8/2010	8098652	11/1/2011	In Force	REFERENCE PRE-CHARGING FOR TWO-STEP SUBRANGING ADC ARCH
MP3084	MP3084	US	12/684,773	1/8/2010	7978105	7/12/2011	In Force	CURRENT SENSING AND BACKGROUND CALIBRATION TO MATCH TW
MP3086	MP3086	US	12/690,829	1/20/2010	8276052	9/25/2012	In Force	Iterative PRBS Seed Recovery Using Soft Decisions
MP3086	MP3086C1	US	13/614,397	9/13/2012	8489970	7/16/2013	In Force	Iterative PRBS Seed Recovery Using Soft Decisions
MP3089	MP3089	US	12/684,424	1/8/2010	8788850	7/22/2014	In Force	Systems And Methods For Using A Security Circuit To Monitor A Volta
MP3090	MP3090	US	12/689,611	1/19/2010	8347195	1/1/2013	In Force	SYSTEMS AND METHODS FOR NEAR-CODEWORD DETECTION AND C
MP3090	MP3090C1	US	13/728,524	12/27/2012	8601328	12/3/2013	In Force	SYSTEMS AND METHODS FOR NEAR-CODEWORD DETECTION AND C
MP3090	MP3090C1C1	US	14/079,487	11/13/2013	9160368	10/13/2015	In Force	SYSTEMS AND METHODS FOR NEAR-CODEWORD DETECTION AND C
MP3092	MP3092	US	09/131,766	8/10/1998	6222695	4/24/2001	In Force	SYSTEM AND METHOD FOR A PREAMPLIFIER WRITE CIRCUIT WITH R
MP3092D	MP3092DE	DE	99113536.9	7/6/1999	980065	2/8/2006	In Force	SYSTEM AND METHOD FOR A PREAMPLIFIER WRITE CIRCUIT WITH R
MP3092E	MP3092EP	EP	99113536.9	7/6/1999	980065	2/8/2006	In Force	SYSTEM AND METHOD FOR A PREAMPLIFIER WRITE CIRCUIT WITH R
MP3092F	MP3092FR	FR	99113536.9	7/6/1999	980065	2/8/2006	In Force	SYSTEM AND METHOD FOR A PREAMPLIFIER WRITE CIRCUIT WITH R
MP3092I	MP3092IT	IT	99113536.9	7/6/1999	980065	2/8/2006	In Force	SYSTEM AND METHOD FOR A PREAMPLIFIER WRITE CIRCUIT WITH R
MP3093	MP3093	US	09/407,017	9/28/1999	6661863	12/9/2003	In Force	PHASE MIXER
MP3094	MP3094	US	09/550,440	4/17/2000	6636372	10/21/2003	In Force	ACCUMULATING READ CHANNEL PERFORMANCE DATA
MP3095	MP3095	US	09/445,333	11/5/1999	6657802	12/2/2003	In Force	PHASE ASSISTED SYNCHRONIZATION DETECTOR
MP3095E	MP3095EP	EP	EP00118360.7	8/24/2000	EP1081705	7/20/2011	In Force	A phase assisted synchronization detector

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MP3095E	MP3095EPOE	DE	00118360.7	8/24/2000	1081705	7/20/2011	In Force	PHASE ASSISTED SYNCHRONIZATION DETECTOR
MP3095E	MP3095EPFR	FR	00118360.7	8/24/2000	1081705	7/20/2011	In Force	PHASE ASSISTED SYNCHRONIZATION DETECTOR
MP3095E	MP3095EPGB	GB	00118360.7	8/24/2000	1081705	7/20/2011	In Force	PHASE ASSISTED SYNCHRONIZATION DETECTOR
MP3095S	MP3095SG	SG	200009398-1	9/1/2000	92737	5/31/2006	In Force	A phase assisted synchronization detector
MP3096	MP3096	US	09/480,875	1/10/2000	6662303	12/9/2003	In Force	WRITE PRECOMPENSATION CIRCUIT AND READ CHANNEL WITH WRI
MP3097	MP3097	US	09/497,301	2/2/2000	6493403	12/10/2002	In Force	ASYNCHRONOUS TIMING FOR INTERPOLATED TIMING RECOVERY
MP3097	MP3097C1	US	10/293,090	11/12/2002	6714603	3/30/2004	In Force	ASYNCHRONOUS TIMING FOR INTERPOLATED TIMING RECOVERY
MP3098	MP3098	US	09/496,617	2/2/2000	6660615	7/29/2003	In Force	SYNCHRONOUS TIMING FOR INTERPOLATED TIMING RECOVERY
MP3099	MP3099	US	10/025,001	12/18/2001	6554094	7/15/2003	In Force	READ/WRITE CHANNEL
MP3100	MP3100	US	09/882,084	6/13/2001	6816328	11/9/2004	In Force	PSEUDO-SYNCHRONOUS INTERPOLATED TIMING RECOVERY FOR A S
MP3101	MP3101	US	09/865,757	5/25/2001	6519103	2/11/2003	In Force	VIEW DAC FEEDBACK INSIDE ANALOG FRONT CIRCUIT
MP3101	MP3101CN	CN	2810656.3	5/22/2002	Z12002028106	1/20/2010	In Force	DAC FEEDBACK INSIDE ANALOG FRONT CIRCUIT
MP3101	MP3101GB	GB	02737117.8	5/22/2002	1390949	10/4/2006	In Force	View DAC Feedback inside Analog Front Circuit
MP3101D	MP3101DE	DE	02737117.8	5/22/2002	1390949	10/4/2006	In Force	View DAC Feedback inside Analog Front Circuit
MP3101E	MP3101EP	EP	EP02737117.8	5/22/2002	EP1390949	10/4/2006	In Force	DAC Feedback inside Analog Front Circuit
MP3102	MP3102	US	09/865,790	5/25/2001	6633447	10/14/2003	In Force	METHOD AND APPARATUS FOR COMPENSATION OF SECOND ORDER
MP3102	MP3102CN	CN	02810655.5	5/22/2002	Z12002028106	12/5/2007	In Force	METHOD AND APPARATUS FOR COMPENSATION OF SECOND ORDER
MP3102	MP3102GB	GB	02737118.6	5/22/2002	1393437	8/19/2009	In Force	METHOD AND APPARATUS FOR COMPENSATION OF SECOND ORDER
MP3102D	MP3102DE	DE	02737118.6	5/22/2002	1393437	8/19/2009	In Force	METHOD AND APPARATUS FOR COMPENSATION OF SECOND ORDER
MP3102E	MP3102EP	EP	EP02737118.6	5/22/2002	EP1393437	8/19/2009	In Force	Method and Apparatus for Compensation of Second Order Distortio
MP3102F	MP3102FR	FR	02737118.6	5/22/2002	1393437	8/19/2009	In Force	METHOD AND APPARATUS FOR COMPENSATION OF SECOND ORDER
MP3103	MP3103	US	09/865,861	5/25/2001	6697204	2/24/2004	In Force	METHOD AND APPARATUS FOR OPERATING A CONTINUOUS TIME FIL
MP3103	MP3103CN	CN	2810655.2	5/22/2002	Z12002028106	3/14/2007	In Force	METHOD AND APPARATUS FOR OPERATING A CONTINUOUS TIME FIL
MP3103	MP3103GB	GB	02737115.2	5/22/2002	1412940	3/7/2007	In Force	Method and Apparatus for Operating a Continuous Time Filter of a R
MP3103D	MP3103DE	DE	02737115.2	5/22/2002	60218692.7	3/14/2007	In Force	Method and Apparatus for Operating a Continuous Time Filter of a R
MP3103E	MP3103EP	EP	EP02737115.2	5/22/2002	EP1412940	3/7/2007	In Force	Method and Apparatus for Operating a Continuous Time Filter of a Re
MP3104	MP3104	US	09/865,651	5/25/2001	6552865	4/22/2003	In Force	DIAGNOSTIC SYSTEM FOR A READ/WRITE CHANNEL IN A DISK DRIVE
MP3104	MP3104CN	CN	2810657.1	5/24/2002	Z12002028106	9/2/2009	In Force	DIAGNOSTIC SYSTEM FOR A READ/WRITE CHANNEL IN A DISK DRIVE
MP3104	MP3104GB	GB	02739417.0	5/24/2002	1390950	10/20/2010	In Force	DIAGNOSTIC SYSTEM FOR A READ/WRITE CHANNEL IN A DISK DRIVE
MP3104D	MP3104DE	DE	02739417.0	5/24/2002	1390950	10/20/2010	In Force	DIAGNOSTIC SYSTEM FOR A READ/WRITE CHANNEL IN A DISK DRIVE
MP3104E	MP3104EP	EP	02739417.0	5/24/2002	EP1390950	10/20/2010	In Force	Diagnostic System for a Read/Write Channel in a Disk Drive
MP3104F	MP3104FR	FR	02739417.0	5/24/2002	1390950	10/20/2010	In Force	DIAGNOSTIC SYSTEM FOR A READ/WRITE CHANNEL IN A DISK DRIVE
MP3104N	MP3104NL	NL	02739417.0	5/24/2002	1390950	10/20/2010	In Force	DIAGNOSTIC SYSTEM FOR A READ/WRITE CHANNEL IN A DISK DRIVE
MP3105	MP3105	US	09/865,611	5/25/2001	6628467	9/30/2003	In Force	INPUT/OUTPUT MULTIPLEX SYSTEM FOR A READ/WRITE CHANNEL I

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MP3106	MP3106	US	09/865,860	5/25/2001	6661590	12/9/2003	In Force	EFFICIENT ANALOG FRONT END FOR A READ/WRITE CHANNEL OF A H
MP3106E	MP3106EP	EP	EP02808174.3	11/22/2002	1563500	4/26/2017	In Force	An Efficient Analog Front End for a Read/Write Channel of a Hard Dis
MP3106E	MP3106EPDE	DE	EP02808174.3	11/22/2002	60248843.5	4/26/2017	In Force	An Efficient Analog Front End for a Read/Write Channel of a Hard Dis
MP3106E	MP3106EPPR	FR	EP02808174.3	11/22/2002	1563500	4/26/2017	In Force	An Efficient Analog Front End for a Read/Write Channel of a Hard Dis
MP3106E	MP3106EPGB	GB	EP02808174.3	11/22/2002	1563500	4/26/2017	In Force	An Efficient Analog Front End for a Read/Write Channel of a Hard Dis
MP3107	MP3107	US	10/402,033	3/28/2003	6889154	5/3/2005	In Force	METHOD AND APPARATUS FOR CALIBRATING DATA-DEPENDENT NOI
MP3107	MP3107C1	US	11/109,207	4/18/2005	7165000	1/16/2007	In Force	METHOD AND APPARATUS FOR CALIBRATING DATA-DEPENDENT NOI
MP3107	MP3107C1D1	US	11/490,981	7/20/2006	7191083	3/13/2007	In Force	METHOD AND APPARATUS FOR CALIBRATING DATA-DEPENDENT NOI
MP3107E	MP3107EP	EP	EP03722512.5	4/18/2003	1485469	1/2/2019	In Force	Method and Apparatus for Calibrating Data-Dependent Noise Predicti
MP3107E	MP3107EPDE	DE	EP03722512.5	4/18/2003	1485469	1/2/2019	In Force	Method and Apparatus for Calibrating Data-Dependent Noise Predicti
MP3107E	MP3107EPPR	FR	EP03722512.5	4/18/2003	1485469	1/2/2019	In Force	Method and Apparatus for Calibrating Data-Dependent Noise Predicti
MP3107E	MP3107EPGB	GB	EP03722512.5	4/18/2003	1485469	1/2/2019	In Force	Method and Apparatus for Calibrating Data-Dependent Noise Predicti
MP3107T	MP3107TW	TW	92109156	4/18/2003	1267734	12/1/2006	In Force	METHOD AND APPARATUS FOR CALIBRATING DATA-DEPENDENT NOI
MP3108	MP3108	US	10/402,654	3/28/2003	752678	4/21/2009	In Force	METHOD AND APPARATUS FOR A DATA-DEPENDENT NOISE PREDICTI
MP3108	MP3108C1	US	11/607,492	12/1/2006	7743314	6/22/2010	In Force	METHOD AND APPARATUS FOR A DATA-DEPENDENT NOISE PREDICTI
MP3108E	MP3108EP	EP	03720464.1	4/14/2003	1485468	1/16/2013	In Force	METHOD AND APPARATUS FOR A DATA-DEPENDENT NOISE PREDICTI
MP3108E	MP3108EPDE	DE	03720464.1	4/14/2003	1485468	1/16/2013	In Force	METHOD AND APPARATUS FOR A DATA-DEPENDENT NOISE PREDICTI
MP3108E	MP3108EPPR	FR	03720464.1	4/14/2003	1485468	1/16/2013	In Force	METHOD AND APPARATUS FOR A DATA-DEPENDENT NOISE PREDICTI
MP3108E	MP3108EPGB	GB	03720464.1	4/14/2003	1485468	1/16/2013	In Force	METHOD AND APPARATUS FOR A DATA-DEPENDENT NOISE PREDICTI
MP3108T	MP3108TW	TW	92108588	4/14/2003	1251814	3/21/2006	In Force	METHOD AND APPARATUS FOR A DATA-DEPENDENT NOISE PREDICTI
MP3112	MP3112C1	US	14/032,266	9/20/2013	9304692	4/5/2016	In Force	Solid-State Drive Command Grouping
MP3112	MP3112CN	CN	201010111351.1	2/2/2010	212010101113	3/23/2016	In Force	SOLID-STATE DRIVE COMMAND GROUPING
MP3113	MP3113	US	12/698,810	2/2/2010	8335167	12/18/2012	In Force	REFINING BEAMFORMING TECHNIQUES FOR PHASED-ARRAY ANTEN
MP3113	MP3113C1	US	13/716,928	12/17/2012	8644184	2/4/2014	In Force	BEAMFORMING TRAINING TECHNIQUES FOR MIMO SYSTEMS
MP3113	MP3113C1C1	US	14/171,773	2/3/2014	9160074	8/4/2015	In Force	BEAMFORMING TRAINING TECHNIQUES FOR MIMO SYSTEMS
MP3114	MP3114	US	12/606,746	10/27/2009	7994847	8/9/2011	In Force	REDUCED GAIN VARIATION BIASING FOR SHORT CHANNEL DEVICES
MP3114	MP3114C1	US	13/193,998	7/29/2011	8174307	5/8/2012	In Force	REDUCED GAIN VARIATION BIASING FOR SHORT CHANNEL DEVICES
MP3114	MP3114C1C1	US	13/462,968	5/3/2012	8362826	1/29/2013	In Force	REDUCED GAIN VARIATION BIASING FOR SHORT CHANNEL DEVICES
MP3114	MP3114Zaaa	US	14/268,108	5/12/2014	9106186	8/11/2015	In Force	REDUCED GAIN VARIATION BIASING FOR SHORT CHANNEL DEVICES
MP3115	MP3115	US	12/699,702	2/3/2010	8830917	9/9/2014	In Force	SHORT PREAMBLE IN A PHYSICAL LAYER DATA UNIT
MP3125	MP3125	US	12/699,277	2/3/2010	8478554	7/12/2013	In Force	Reducing Eye Monitor Data Samplers in a Receiver
MP3126	MP3126	US	12/643,834	12/11/2009	8397139	3/12/2013	In Force	METHOD AND APPARATUS FOR PROCESSING NUISANCE DATA
MP3127	MP3127	US	12/700,561	2/4/2010	8782325	7/15/2014	In Force	DATA TYPE BASED ALIGNMENT OF DATA WRITTEN TO NON-VOLATIL
MP3131	MP3131	US	12/702,186	2/8/2010	8402351	3/19/2013	In Force	ERROR CORRECTION CODING WITH NUISANCE DATA

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MP3131	MP3131C1	US	13/842,221	3/15/2013	8739003	5/27/2014	In Force	Method and apparatus for decoding a data sector including user data
MP3132	MP3132	US	12/701,895	2/8/2010	8418031	4/9/2013	In Force	Systems and Methods for Encoding Data to Meet an Output Constraint
MP3133	MP3133CN	CN	201010114340.1	2/9/2010	ZL2010101143	3/18/2015	In Force	NON-VOLATILE MEMORY DEVICES WITH INTERNAL PROGRAM DATA
MP3134	MP3134	US	12/707,910	2/18/2010	8330586	12/11/2012	In Force	SYSTEMS AND METHODS FOR PROGRAMMING OF A COOLING FAN VIA
MP3134	MP3134C1	US	13/708,800	12/7/2012	9334870	5/10/2016	In Force	SYSTEMS AND METHODS FOR PROGRAMMING OF A COOLING FAN VIA
MP3134	MP3134CN	CN	201010120463.1	2/24/2010	ZL2010101204	4/24/2013	In Force	SERIAL PORT COMMUNICATION MODE PROGRAMMING SYSTEM FOR
MP3135	MP3135	US	12/695,352	1/28/2010	8340619	12/25/2012	In Force	PHASE SYNCHRONIZATION OF PHASED ARRAY OR MULTIPLE RECEIVER
MP3137W	MP3137WO	WO	PCT/US2010/02	2/18/2010			Pending	Method And System For Performing I/O Operations On Disk Arrays
MP3137W	MP3137WCEP	EP	10/7073934	2/18/2010			Pending	Method And System For Performing I/O Operations On Disk Arrays
MP3137W	MP3137WOJP	JP	2011-550325	2/18/2010	5360666	9/13/2013	In Force	METHOD AND SYSTEM FOR PERFORMING I/O OPERATIONS ON DISK
MP3138	MP3138	US	12/702,193	2/8/2010	8371669	2/12/2013	In Force	FIRE TIMING CONTROL IN PRINTING DEVICES
MP3140	MP3140	US	12/705,444	2/12/2010	8405332	3/26/2013	In Force	METHOD TO ELIMINATE CURRENT SURGE DURING SPINDLE SPIN UP
MP3140	MP3140C1	US	13/744,043	1/17/2013	9543863	1/10/2017	In Force	METHOD TO ELIMINATE CURRENT SURGE DURING SPINDLE SPIN UP
MP3143	MP3143	US	12/704,388	2/11/2010	8107187	1/31/2012	In Force	TRANSITIONING BETWEEN MODES OF CONTROL FOR MOTOR OF REC
MP3143	MP3143C1	US	13/349,507	1/12/2012	8339727	12/25/2012	In Force	TRANSITIONING BETWEEN MODES OF CONTROL FOR MOTOR OF REC
MP3145	MP3145	US	12/709,115	2/19/2010	8509130	8/13/2013	In Force	TECHNIQUES FOR FLEXIBLE AND EFFICIENT BEAMFORMING
MP3145	MP3145C1	US	13/964,953	8/12/2013	9160428	10/13/2015	In Force	TECHNIQUES FOR FLEXIBLE AND EFFICIENT BEAMFORMING
MP3145W	MP3145WO	WO	PCT/US2010/02	2/19/2010			Pending	TECHNIQUES FOR FLEXIBLE AND EFFICIENT BEAMFORMING
MP3147W	MP3147WO	WO	PCT/US2010/02	2/19/2010			Pending	Method and Apparatus for Spatial Noise Adaptive Filtering for Digital
MP3148	MP3148	US	12/713,064	2/25/2010	8611447	12/17/2013	In Force	FEEDBACK AND USER SCHEDULING FOR MULTI - USER MULTIPLE INP
MP3148	MP3148C1	US	14/107,284	12/16/2013	8811523	8/19/2014	In Force	FEEDBACK AND USER SCHEDULING FOR MULTI - USER MULTIPLE INP
MP3149	MP3149	US	12/493,252	6/29/2009	8238483	8/7/2012	In Force	SIGNALING OF DEDICATED REFERENCE SIGNAL (DRS) PRECODING GR
MP3149	MP3149C1	US	13/543,835	7/8/2012	8609633	4/15/2014	In Force	SYSTEMS AND METHODS FOR COMMUNICATION USING DEDICATED
MP3149	MP314911	US	12/983,898	1/4/2011	8609528	4/15/2014	In Force	SYSTEMS AND METHODS FOR COMMUNICATION USING DEDICATED
MP3149F	MP3149FWCN	CN	201180005108.4	1/4/2011	ZL2011800051	4/15/2015	In Force	SIGNALING OF DEDICATED REFERENCE SIGNAL (DRS) PRECODING GR
MP3149F	MP3149FWEP	EP	EP11731717.2	1/4/2011			Pending	Signaling of Dedicated Reference Signal (DRS) Precoding Granularity
MP3149F	MP3149FWJP	JP	2012-547574	1/4/2011	6012472	9/30/2016	In Force	SIGNALING OF DEDICATED REFERENCE SIGNAL (DRS) PRECODING GR
MP3149F	MP3149FWUN	DE	11731717.2	1/4/2011	2011111016	5/2/2018	In Force	SIGNALING OF DEDICATED REFERENCE SIGNAL (DRS) PRECODING GR
MP3149W	MP3149WOCN	CN	20108004856.1	2/24/2010	ZL201080048	10/29/2014	In Force	SIGNALING OF DEDICATED REFERENCE SIGNAL (DRS) PRECODING GR
MP3149W	MP3149WCEP	EP	EP10745882	2/24/2010	2401821	4/18/2018	In Force	Signaling of Dedicated Reference Signal (DRS) Precoding Granularity
MP3149W	MP3149WCEPDE	DE	EP10745882	2/24/2010	2401821	4/18/2018	In Force	Signaling of Dedicated Reference Signal (DRS) Precoding Granularity
MP3149W	MP3149WCEPFR	FR	EP10745882	2/24/2010	2401821	4/18/2018	In Force	Signaling of Dedicated Reference Signal (DRS) Precoding Granularity
MP3149W	MP3149WCEPGB	GB	EP10745882	2/24/2010	2401821	4/18/2018	In Force	Signaling of Dedicated Reference Signal (DRS) Precoding Granularity
MP3149W	MP3149WCOJP	JP	2011-551560	2/24/2010	5548930	5/30/2014	In Force	SIGNALING OF DEDICATED REFERENCE SIGNAL (DRS) PRECODING GR

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MP3150	MP3150	US	12/715,184	3/1/2010	9081963	7/14/2015	In Force	PROTECTING AGAINST USE OF UNAUTHORIZED ELECTRONIC HARDW
MP3152	MP3152	US	12/715,612	3/2/2010	8448001	5/21/2013	In Force	A SYSTEM HAVING A FIRST DEVICE AND SECOND DEVICE IN WHICH T
MP3152	MP3152C1	US	13/895,775	5/16/2013	8826047	9/2/2014	In Force	SELF GOVERNING POWER MANAGEMENT ARCHITECTURE THAT ALLO
MP3153	MP3153	US	12/712,686	2/25/2010	8271720	9/18/2012	In Force	ADAPTIVE PHYSICAL ALLOCATION IN SOLID-STATE DRIVES
MP3153	MP3153C1	US	13/606,226	9/7/2012	8612672	12/17/2013	In Force	ADAPTIVE PHYSICAL ALLOCATION IN SOLID-STATE DRIVES
MP3155	MP3155	US	12/713,069	2/25/2010	8322846	7/31/2012	In Force	RESISTOR CAPACITOR (RC) OSCILLATOR
MP3156	MP3156	US	12/713,297	2/26/2010	8358779	1/22/2013	In Force	Prime Field Elliptic Curve Cryptography Processor
MP3156	MP3156C1C1	US	14/679,565	4/6/2015	9449198	9/20/2016	In Force	Prime Field Elliptic Cryptography Processor
MP3158	MP3158	US	12/713,692	2/26/2010	8044829	10/25/2011	In Force	MULTI-MODE ENCODING FOR DATA COMPRESSION
MP3158	MP3158CN	CN	201010143498.1	3/5/2010	ZL2010101434	9/9/2015	In Force	MULTI-MODE ENCODING FOR DATA COMPRESSION
MP3159	MP3159	US	12/716,300	3/3/2010	8611288	12/17/2013	In Force	Systems And Methods For Link Adaptation In Wireless Communicatio
MP3159	MP3159C1	US	14/104,168	12/12/2013	8913582	12/16/2014	In Force	Systems And Methods For Selecting A Modulation And Coding Schem
MP3159	MP3159I1	US	12/940,413	11/5/2010	8982803	3/17/2015	In Force	Systems And Methods For Link Adaptation In Wireless Communication
MP3161	MP3161	US	12/713,865	2/26/2010	8072854	12/6/2011	In Force	METHOD AND APPARATUS FOR DETECTING WOBBLE DEFECTS IN OP
MP3161	MP3161C1	US	13/311,353	5/12/2011	8315136	11/20/2012	In Force	METHOD AND APPARATUS FOR DETECTING WOBBLE DEFECTS IN OP
MP3167	MP3167CN	CN	201010134076.1	3/12/2010	ZL2010101340	1/7/2015	In Force	APPARATUS AND METHOD TO PROTECT METADATA AGAINST UNEXP
MP3169	MP3169	US	12/725,222	3/16/2010	8599976	12/3/2013	In Force	MULTI - USER MULTIPLE INPUT MULTIPLE OUTPUT (MU - MIMO) RE
MP3169W	MP3169WO	WO	PCT/US2010/02	3/16/2010			Pending	MULTI-USER MULTIPLE INPUT MULTIPLE OUTPUT (MU-MIMO) RECEI
MP3169W	MP3169WOCN	CN	201080012222.1	3/16/2010	ZL2010800122	6/3/2015	In Force	MULTI-USER MULTIPLE INPUT MULTIPLE OUTPUT (MU-MIMO) RECEI
MP3169W	MP3169WQIP	JP	2012-500895	3/16/2010	5509474	4/4/2014	In Force	MULTI-USER MULTIPLE INPUT MULTIPLE OUTPUT (MU-MIMO) RECEI
MP3170	MP3170	US	12/729,781	3/23/2010	8154889	4/10/2012	In Force	OPERATING MODE DETECTION IN A FLYPACK CONVERTER
MP3172	MP3172	US	12/722,654	3/12/2010	8976200	3/10/2015	In Force	DISPLAY CONTROLLER FOR ROTATION OF IMAGE DATA
MP3173	MP3173	US	12/716,569	3/3/2010	8254296	8/28/2012	In Force	Peer-to-Peer Frequency Band Negotiation
MP3175	MP3175	US	12/723,194	3/12/2010	8352837	1/8/2013	In Force	SYSTEM AND METHODS FOR STORING DATA ENCODED WITH ERROR
MP3175	MP3175C1	US	13/717,231	12/17/2012	8935596	1/13/2015	In Force	SYSTEM AND METHODS FOR STORING DATA ENCODED WITH ERROR
MP3177	MP3177	US	12/728,726	3/22/2010	8106799	1/31/2012	In Force	DATA COMPRESSION AND DECOMPRESSION USING PARALLEL PROCE
MP3179	MP3179	US	12/726,310	3/17/2010	8423710	4/16/2013	In Force	Sequential Writes to Flash Memory
MP3179	MP3179C1	US	13/880,394	4/10/2013	8874833	10/28/2014	In Force	Sequential Writes to Flash Memory
MP3180	MP3180	US	12/722,970	3/12/2010	8793511	7/29/2014	In Force	POWER MANAGEMENT FOR POWER OVER ETHERNET (POE) SYSTEM
MP3182	MP3182	US	12/731,007	3/24/2010	8773969	7/8/2014	In Force	MULTI-RADIO DEVICE FOR WLAN
MP3182	MP3182C1	US	14/325,254	7/7/2014	9941988	4/10/2018	In Force	MULTI-RADIO DEVICE FOR WLAN
MP3182	MP3182C1C1	US	15/940,602	3/29/2018	10419143	9/17/2019	In Force	MULTI-RADIO DEVICE FOR WLAN
MP3182	MP3182I1	US	13/948,994	7/23/2013	9226270	12/29/2015	In Force	MULTI-RADIO DEVICE FOR WLAN
MP3182	MP3182I1C1	US	14/981,603	12/28/2015	10411846	9/10/2019	In Force	MULTI-RADIO DEVICE FOR WLAN

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MP3182	MP318211C1C1	US	16/546,961	8/21/2019			Pending	MULTI-RADIO DEVICE FOR WLAN
MP3183	MP3183	US	12/754,353	4/5/2010	8324870	12/4/2012	In Force	Adaptive Current Limit For Power Factor Correction
MP3183	MP3183C1	US	13/690,581	11/30/2012	8755208	6/17/2014	In Force	ADAPTIVE CURRENT LIMIT FOR POWER FACTOR CORRECTION
MP3186	MP3186C1	US	14/146,862	3/1/2014	8873468	10/28/2014	In Force	EMBEDDED ACCESS POINT
MP3188	MP3188	US	12/725,924	3/17/2010	8848676	9/30/2014	In Force	APPARATUS AND METHOD FOR COEXISTENT WIRELESS AND BLUETO
MP3190	MP3190	US	12/750,636	3/30/2010	8270909	9/18/2012	In Force	SOUNDING AND STEERING PROTOCOLS FOR WIRELESS COMMUNICA
MP3190	MP3190C1	US	13/610,654	9/11/2012	8526892	9/3/2013	In Force	SOUNDING AND STEERING PROTOCOLS FOR WIRELESS COMMUNICA
MP3190	MP3190C1aa	US	15/048,867	2/19/2016	10481881	1/15/2019	In Force	SOUNDING AND STEERING PROTOCOLS FOR WIRELESS COMMUNICA
MP3190	MP3190C1C1	US	14/015,722	8/30/2013	9270431	2/23/2016	In Force	SOUNDING AND STEERING PROTOCOLS FOR WIRELESS COMMUNICA
MP3190W	MP3190W/CNA	CN	201410219896.1	3/30/2010	ZL2014102198	9/29/2017	In Force	SOUNDING AND STEERING PROTOCOLS FOR WIRELESS COMMUNICA
MP3190W	MP3190W/O	WO	PCT/US2010/02	3/30/2010			Pending	SOUNDING AND STEERING PROTOCOLS FOR WIRELESS COMMUNICA
MP3190W	MP3190W/CN	CN	201080014699.1	3/30/2010	ZL2010800146	7/23/2014	In Force	SOUNDING AND STEERING PROTOCOLS FOR WIRELESS COMMUNICA
MP3190W	MP3190W/OEP	EP	EP10730596.3	3/30/2010	2415182	11/28/2018	In Force	Sounding and Steering Protocols for Wireless Communications
MP3190W	MP3190W/OEPD1	EP	18208710.6	3/30/2010			Pending	Method and Apparatus With Sounding and Steering Protocols for Wir
MP3190W	MP3190W/OEPDE	DE	EP10730596.3	3/30/2010	2415182	11/28/2018	In Force	Sounding and Steering Protocols for Wireless Communications
MP3190W	MP3190W/OEPFR	FR	EP10730596.3	3/30/2010	2415182	11/28/2018	In Force	Sounding and Steering Protocols for Wireless Communications
MP3190W	MP3190W/OEPGB	GB	EP10730596.3	3/30/2010	2415182	11/28/2018	In Force	Sounding and Steering Protocols for Wireless Communications
MP3190W	MP3190W/OJP	JP	2012-503630	3/30/2010	5618245	9/26/2014	In Force	SOUNDING AND STEERING PROTOCOLS FOR WIRELESS COMMUNICA
MP3190W	MP3190W/OKR	KR	10-2011-702286	3/30/2010			Pending	SOUNDING AND STEERING PROTOCOLS FOR WIRELESS COMMUNICA
MP3191	MP3191	US	12/749,228	3/29/2010	8555082	10/8/2013	In Force	SECURING EXTERNAL MEMORY DATA
MP3191	MP3191C1	US	14/029,506	9/17/2013	9235712	1/12/2016	In Force	SECURING EXTERNAL MEMORY DATA
MP3193	MP3193	US	12/725,683	3/17/2010	8493028	7/23/2013	In Force	Power Management Circuit For Rechargeable Battery Stack
MP3193	MP3193C1	US	13/947,711	7/22/2013	8736231	5/27/2014	In Force	Power Management Circuit For Rechargeable Battery Stack
MP3193W	MP3193W/O	WO	PCT/US2010/02	3/30/2010			Pending	Power Management Circuit For Rechargeable Battery Stack
MP3193W	MP3193W/CN	CN	201080020482.1	3/30/2010	ZL2010800204	10/22/2014	In Force	POWER MANAGEMENT CIRCUIT FOR RECHARGEABLE BATTERY STAC
MP3193W	MP3193W/OEP	DE	DE11201000150	3/30/2010			Pending	Power Management Circuit For Rechargeable Battery Stack
MP3193W	MP3193W/IN	IN	7366CHENP2011	3/30/2010			Pending	Power Management Circuit For Rechargeable Battery Stack
MP3193W	MP3193W/OJP	JP	2012-503581	3/30/2010	5455124	12/20/2013	In Force	POWER MANAGEMENT CIRCUIT FOR RECHARGEABLE BATTERY STAC
MP3193W	MP3193W/OKR	KR	1020117025986	3/30/2010	101677679	11/14/2016	In Force	Power Management Circuit For Rechargeable Battery Stack
MP3196	MP3196	US	12/732,036	3/25/2010	8472427	6/25/2013	In Force	Packet Exchange Arbitration for Coexisting Radios
MP3196	MP3196C1	US	13/923,949	6/21/2013	8804690	8/12/2014	In Force	Packet Exchange Arbitration for Coexisting Radios
MP3196	MP3196C1C1	US	14/456,460	8/11/2014	9131520	9/8/2015	In Force	Packet Exchange Arbitration for Coexisting Radios
MP3199	MP3199	US	12/723,645	3/14/2010	8457236	6/4/2013	In Force	FEEDBACK STRATEGIES FOR MULTI-USER MIMO COMMUNICATION S
MP3199W	MP3199W/EDI	EP	EP19160934.6	3/6/2019			Pending	Improved Feedback Strategies for Multi-User MIMO Communication

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MP3199W	MP3199WOCN	CN	201080012027.1	3/14/2010	ZL2010800120	5/28/2014	In Force	IMPROVED FEEDBACK STRATEGIES FOR MULTI-USER MIMO COMMUN
MP3199W	MP3199WCEP	EP	EP10761252.5	3/14/2010	2417780	5/8/2019	In Force	Improved Feedback Strategies for Multi-User MIMO Communication
MP3199W	MP3199WCEPDE	DE	EP10761252.5	3/14/2010	603010058756	5/8/2019	In Force	Improved Feedback Strategies for Multi-User MIMO Communication
MP3199W	MP3199WCEPFR	FR	EP10761252.5	3/14/2010	2417780	5/8/2019	In Force	Improved Feedback Strategies for Multi-User MIMO Communication
MP3199W	MP3199WCEPGB	GB	EP10761252.5	3/14/2010	2417780	5/8/2019	In Force	Improved Feedback Strategies for Multi-User MIMO Communication
MP3200	MP3200	US	12/751,484	3/31/2010	8442141	5/14/2013	In Force	OPPORTUNISTIC BEAMFORMING IN A WIRELESS COMMUNICATION S
MP3200	MP3200C1	US	13/893,104	5/13/2013	8995587	3/31/2015	In Force	OPPORTUNISTIC BEAMFORMING IN A WIRELESS COMMUNICATION
MP3202	MP3202	US	12/728,757	3/22/2010	9164914	10/20/2015	In Force	MULTIPLE PORT ROUTING CIRCUITRY FOR FLASH MEMORY STORAGE
MP3203W	MP3203WOCN	WO	PCT/US2010/02	4/1/2010			Pending	Transistor Gate Driver For Short Circuit Protection
MP3203W	MP3203WOCN	CN	201080022545.1	4/1/2010			Pending	TRANSISTOR GATE DRIVER FOR SHORT CIRCUIT PROTECTION
MP3204	MP3204	US	12/731,782	3/25/2010	8383962	2/26/2013	In Force	Exposed Die Pad Package With Power Ring
MP3204	MP3204C1	US	13/777,757	2/26/2013	9117815	8/25/2015	In Force	Method Of Fabricating A Packaged Semiconductor
MP3204	MP3204CN	CN	201010149857.1	4/8/2010	ZL2010101498	10/29/2014	In Force	EXPOSED DIE PAD PACKAGE WITH POWER RING
MP3205	MP3205	US	12/572,503	10/2/2009	8205144	6/19/2012	In Force	ERROR EVENT PROCESSING METHODS AND SYSTEMS
MP3205	MP3205C1	US	13/481,979	5/29/2012	8407569	3/26/2013	In Force	ERROR EVENT PROCESSING METHODS AND SYSTEMS
MP3205	MP3205C1C1	US	13/776,848	2/26/2013	8977940	3/10/2015	In Force	ERROR EVENT PROCESSING METHODS AND SYSTEMS
MP3209	MP3209	CN	201010149844.1	4/8/2010			Pending	A METHOD FOR IMPLEMENTING HIGH SENSITIVITY AND FAST FIRST
MP3210	MP3210	US	12/758,709	4/12/2010	8842614	9/23/2014	In Force	SIGNALING FOR MULTI-DIMENSION WIRELESS RESOURCE ALLOCATIO
MP3210	MP3210C1	US	14/491,931	9/19/2014	10070418	9/4/2018	In Force	SIGNALING FOR MULTI-DIMENSION WIRELESS RESOURCE ALLOCATIO
MP3210W	MP3210WOCN	WO	PCT/US2010/03	4/9/2010			Pending	SIGNALING FOR MULTI-DIMENSION WIRELESS RESOURCE ALLOCATIO
MP3211	MP3211	US	12/758,603	4/12/2010	9655002	5/16/2017	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP3211	MP3211C1	US	15/595,116	5/15/2017			Pending	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP3211W	MP3211WOCN	WO	PCT/US2010/03	4/12/2010			Pending	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP3211W	MP3211WOCN	CN	201080017083.1	4/12/2010	ZL2010800170	12/10/2014	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP3211W	MP3211WCEPDE	DE	10716939.3	4/12/2010	2420023	8/27/2014	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP3211W	MP3211WCEPFR	FR	10716939.3	4/12/2010	2420023	8/27/2014	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP3211W	MP3211WCEPGB	GB	10716939.3	4/12/2010	2420023	8/27/2014	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP3211W	MP3211WCEPJP	JP	2012-504920	4/12/2010	5649011	11/21/2014	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP3211W	MP3211WOKR	KR	10-2011-702402	4/12/2010	10-1646721	8/12/2016	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP3211W	MP3211WUD1	EP	EP14174964.8	4/12/2010	EP2811717	8/3/2016	In Force	Physical Layer Frame Format for WLAN
MP3211W	MP3211WUD1DE	DE	14174964.8	4/12/2010	2811717	8/3/2016	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP3211W	MP3211WUD1FR	FR	14174964.8	4/12/2010	2811717	8/3/2016	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP3211W	MP3211WUD1GB	GB	14174964.8	4/12/2010	2811717	8/3/2016	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP3212	MP3212	US	12/754,376	4/5/2010	8270194	9/18/2012	In Force	DISTRIBUTED FLASH MEMORY STORAGE MANAGER SYSTEMS

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MP3212	MP3212C1	US	13/603,994	9/5/2012	8422264	4/16/2013	In Force	DISTRIBUTED FLASH MEMORY STORAGE MANAGER SYSTEMS
MP3212E	MP3212EPDE	DE	10159218.6	4/7/2010	60 2010 034 6	7/13/2016	In Force	DISTRIBUTED FLASH MEMORY STORAGE MANAGER SYSTEMS
MP3212E	MP3212EPFR	FR	10159218.6	4/7/2010	2239665	6/13/2016	In Force	DISTRIBUTED FLASH MEMORY STORAGE MANAGER SYSTEMS
MP3212E	MP3212EPGB	GB	10159218.6	4/7/2010	2239665	6/13/2016	In Force	DISTRIBUTED FLASH MEMORY STORAGE MANAGER SYSTEMS
MP3213	MP3213	US	12/686,773	1/13/2010	8217727	7/10/2012	In Force	Slew Rate Edge Enhancer
MP3213	MP3213C1C1	US	14/159,881	1/21/2014	9007135	4/14/2015	In Force	Slew Rate Edge Enhancer
MP3215	MP3215	US	12/751,305	3/31/2010	8885531	11/11/2014	In Force	Power Save Schemes For Access Point Solutions
MP3215	MP3215C1	US	14/537,333	11/10/2014	9386519	7/5/2016	In Force	METHOD AND APPARATUS FOR TRANSITIONING A FIRST PROCESSOR
MP3216	MP3216	US	12/753,774	4/2/2010	7982985	7/19/2011	In Force	Method And Apparatus For Adapting A Finite Impulse Response Equ
MP3217	MP3217	US	12/762,029	4/16/2010	8767683	7/1/2014	In Force	SEGMENTED BEAMFORMING
MP3217	MP3217C1	US	14/320,306	6/30/2014	9370002	6/14/2016	In Force	SEGMENTED BEAMFORMING
MP3217W	MP3217WO	WO	PCT/US2010/03	4/16/2010			Pending	SEGMENTED BEAMFORMING
MP3217W	MP3217WOCN	CN	201080016968.1	4/16/2010	ZL2010800169	11/25/2015	In Force	SEGMENTED BEAMFORMING
MP3217W	MP3217WODE	DE	10714814.0	4/16/2010	2420007	9/25/2013	In Force	SEGMENTED BEAMFORMING
MP3217W	MP3217WCEPFR	FR	10714814.0	4/16/2010	2420007	9/25/2013	In Force	SEGMENTED BEAMFORMING
MP3217W	MP3217WCEPGB	GB	10714814.0	4/16/2010	2420007	9/25/2013	In Force	SEGMENTED BEAMFORMING
MP3217W	MP3217WQJP	JP	2012-505971	4/16/2010	5558771	8/22/2014	In Force	SEGMENTED BEAMFORMING
MP3217W	MP3217WOKR	KR	10-2011-702402	4/16/2010	10-1721615	3/30/2017	In Force	SEGMENTED BEAMFORMING
MP3220W	MP3220WO	WO	PCT/US2010/03	4/16/2010			Pending	LED LIGHTING DEVICE
MP3222	MP3222	US	12/791,238	6/1/2010	8088106	1/17/2012	In Force	LOW NOISE VOLTAGE CONTROLLED OSCILLATOR (VCO)
MP3224	MP3224	US	12/723,647	3/14/2010	8543063	9/24/2013	In Force	MULTI-POINT OPTORTUNISTIC BEAMFORMING WITH SELECTIVE BEA
MP3224	MP3224C1	US	14/028,558	9/17/2013	8670719	3/11/2014	In Force	MULTI-POINT OPTORTUNISTIC BEAMFORMING WITH SELECTIVE BEA
MP3224W	MP3224WOCN	CN	201080017650.1	3/14/2010	ZL2010800176	4/29/2015	In Force	METHOD, DEVICE AND SYSTEM OF BEAMFORMING WITH SELECTIVE
MP3224W	MP3224WOJP	JP	2012-506601	3/14/2010	5607143	9/5/2014	In Force	MULTI-POINT OPTORTUNISTIC BEAMFORMING WITH SELECTIVE BEA
MP3225	MP3225	US	12/762,150	4/16/2010	8213236	7/3/2012	In Force	Flash Memory
MP3225	MP3225C1	US	13/538,827	6/29/2012	8638613	1/28/2014	In Force	FLASH MEMORY
MP3225	MP3225C1C1	US	14/162,395	1/23/2014	9070454	6/30/2015	In Force	Flash Memory
MP3226	MP3226	US	12/757,222	4/9/2010	8565918	10/15/2013	In Force	METHOD AND APPARATUS FOR DYNAMICALLY SELECTING AN ERROR
MP3226	MP3226C1	US	14/053,064	10/14/2013	9128859	9/8/2015	In Force	METHOD AND APPARATUS FOR DYNAMICALLY SELECTING AN ERROR
MP3229	MP3229	US	12/763,446	4/20/2010	8457088	6/4/2013	In Force	Multi-Level Piconet Data Aggregation
MP3229	MP3229C1	US	13/908,546	6/3/2013	9445219	9/13/2016	In Force	Multi-Level Piconet Data Aggregation
MP3230	MP3230	US	12/759,336	4/13/2010	8532041	9/10/2013	In Force	Methods for Transmitting Information in a Regulated Spectrum and N
MP3230	MP3230C1	US	14/018,232	9/4/2013	8982826	3/17/2015	In Force	Methods for Transmitting Information in a Regulated Spectrum and N
MP3231	MP3231	US	12/767,937	4/27/2010	8190718	5/29/2012	In Force	Model Driven Command Language Translator

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MP3231	MP3231D1	US	13/480,587	5/25/2012	8478580	7/2/2013	In Force	Model Driven Command Language Translator
MP3231	MP3231D1C1	US	13/929,865	6/28/2013	9262374	2/16/2016	In Force	Model Driven Command Language Translator
MP3234	MP3234	US	12/767,922	4/27/2010	8526771	8/27/2013	In Force	WCDMA Modulation
MP3236	MP3236C1	US	13/246,132	9/27/2011	8339917	12/25/2012	In Force	SUPPLEMENTARY TIMING RECOVERY
MP3238T	MP3238TW	TW	99114554	5/6/2010			Pending	Die Exposed Chip Package
MP3238W	MP3238WO	WO	PCT/US2010/03	5/3/2010			Pending	Die Exposed Chip Package
MP3241	MP3241	US	12/765,106	4/22/2010	8481155	7/9/2013	In Force	Using Television Whitespace Spectrum For Wireless Local Area Netw
MP3241	MP3241C1	US	13/933,800	7/2/2013	9204311	12/1/2015	In Force	Using Television Whitespace Spectrum For Wireless Local Area Netw
MP3245	MP3245	US	12/772,852	5/3/2010	8400774	3/19/2013	In Force	PACKAGING TECHNIQUES AND CONFIGURATIONS
MP3245W	MP3245WO	WO	PCT/US2010/03	5/4/2010			Pending	Packaging Techniques and Configurations
MP3245W	MP3245WOCN	CN	2010800199361	5/4/2010	ZL2010800199	11/16/2016	In Force	PACKAGING TECHNIQUES AND CONFIGURATIONS
MP3250	MP3250	US	12/779,743	5/13/2010	8358117	1/22/2013	In Force	HYSTERETIC REGULATOR WITH OUTPUT SLOPE DETECTION
MP3250	MP3250C1	US	13/745,435	1/18/2013	8797012	8/5/2014	In Force	HYSTERETIC REGULATOR WITH OUTPUT SLOPE DETECTION
MP3251	MP3251	US	12/772,654	5/3/2010	8346179	1/1/2013	In Force	Push-Pull Low-Noise Amplifier With Area-Efficient Implementation
MP3251	MP3251C1	US	13/730,439	12/28/2012	8688055	4/1/2014	In Force	Push-Pull Low-Noise Amplifier With Area-Efficient Implementation
MP3251W	MP3251WO	WO	PCT/US2010/03	5/10/2010			Pending	Push-Pull Low-Noise Amplifier With Area-Efficient Implementation
MP3251W	MP3251WOCN	CN	2010800210821	5/10/2010	ZL2010800210	4/22/2015	In Force	PUSH-PULL LOW-NOISE AMPLIFIER WITH AREA-EFFICIENT IMPLME
MP3253	MP3253	US	12/778,236	5/12/2010	8442470	5/14/2013	In Force	Harmonic-Reject FTT Filter
MP3254	MP3254	US	12/776,860	5/10/2010	7915921	3/29/2011	In Force	CIRCUITS AND METHODS FOR LEVEL SHIFTING A SIGNAL
MP3254	MP3254C1	US	13/053,933	3/22/2011	8253441	8/28/2012	In Force	CIRCUITS AND METHODS FOR LEVEL SHIFTING A SIGNAL
MP3255	MP3255	US	12/778,248	5/12/2010	8354874	1/15/2013	In Force	Kickback Cancellation In Class B Type Circuits Using AC Coupling
MP3257	MP3257	US	12/783,017	5/19/2010	8534594	11/26/2013	In Force	Circuits And Methods For Combining Signal Power
MP3257	MP3257C1	US	14/090,283	11/26/2013	9362857	6/14/2016	In Force	Combining Signal Power Using Magnetic Coupling Between Conducto
MP3257T	MP3257TW	TW	99116054	5/19/2010	1458174	10/21/2014	In Force	Circuits And Methods Combining Signal Power
MP3257W	MP3257WO	WO	PCT/US2010/03	5/19/2010			Pending	CIRCUITS AND METHODS COMBINING SIGNAL POWER
MP3257W	MP3257WOCN	CN	2010800220611	5/19/2010	ZL2010800220	8/27/2014	In Force	CIRCUITS AND METHODS COMBINING SIGNAL POWER
MP3257W	MP3257WCEP	EP	EP10731793.5	5/19/2010	EP2493333	7/12/2017	In Force	Circuits and Methods Combining Signal Power
MP3257W	MP3257WCEPDE	DE	10731793.5	5/19/2010	603010043566	7/12/2017	In Force	Circuits And Methods Combining Signal Power
MP3257W	MP3257WCEPFR	FR	10731793.5	5/19/2010	2433333	7/12/2017	In Force	Circuits And Methods Combining Signal Power
MP3257W	MP3257WCEPGB	GB	10731793.5	5/19/2010	2433333	7/12/2017	In Force	Circuits And Methods Combining Signal Power
MP3257W	MP3257WGP	JP	2012-511366	5/19/2010	5748149	5/22/2015	In Force	Circuits and Methods Combining Signal Power
MP3257W	MP3257WOKR	KR	1020117027331	5/19/2010	101715831	3/7/2017	In Force	Circuits And Methods Combining Signal Power
MP3257W	MP3257WUD1	EP	EP16164953.8	5/19/2010			Pending	Circuits and Methods Combining Signal Power
MP3262	MP3262	US	12/782,848	5/19/2010	8868015	10/21/2014	In Force	TRANSMIT ARCHITECTURE FOR WIRELESS MULTI-MODE APPLICATIO

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MP3262	MP3262C1	US	14/518,722	10/20/2014	9258018	2/9/2016	In Force	TRANSMIT ARCHITECTURE FOR WIRELESS MULTI-MODE APPLICATIONS
MP3262T	MP3262TW	TW	9911603	5/19/2010	1488447	6/11/2015	In Force	TRANSMIT ARCHITECTURE FOR WIRELESS MULTI-MODE APPLICATIONS
MP3262T	MP3262TW.D1	TW	104109077	5/19/2010	1551063	9/21/2016	In Force	WIRELESS TRANSMITTER AND METHOD FOR TRANSMITTING A SIGNAL
MP3262W	MP3262WCNA	CN	201510109459.3	5/19/2010	212015101094	12/15/2017	In Force	TRANSMIT ARCHITECTURE FOR WIRELESS MULTI-MODE APPLICATIONS
MP3262W	MP3262WOCN	CN	201080022054.1	5/19/2010	212010800220	4/15/2015	In Force	TRANSMIT ARCHITECTURE FOR WIRELESS MULTI-MODE APPLICATIONS
MP3262W	MP3262WCEP	EP	10778306.0	5/19/2010	2433366	12/19/2018	In Force	Transmit Architecture for Wireless Multi-Mode Applications
MP3262W	MP3262WCEPDE	DE	10778306.0	5/19/2010	2433366	12/19/2018	In Force	Transmit Architecture for Wireless Multi-Mode Applications
MP3262W	MP3262WCEPFR	FR	10778306.0	5/19/2010	2433366	12/19/2018	In Force	Transmit Architecture for Wireless Multi-Mode Applications
MP3262W	MP3262WCEPGB	GB	10778306.0	5/19/2010	2433366	12/19/2018	In Force	Transmit Architecture for Wireless Multi-Mode Applications
MP3262W	MP3262WOKR	KR	10-2011-702760	5/19/2010	10-1699913	1/19/2017	In Force	TRANSMIT ARCHITECTURE FOR WIRELESS MULTI-MODE APPLICATIONS
MP3265	MP3265	US	12/776,007	5/7/2010	8521921	8/27/2013	In Force	Automatic Direct Memory Access (DMA)
MP3265	MP3265C1	US	13/969,807	8/19/2013	9032112	5/12/2015	In Force	AUTOMATIC DIRECT MEMORY ACCESS (DMA)
MP3266	MP3266	US	12/785,822	5/24/2010	9201148	12/1/2015	In Force	TIMING ERROR DETECTOR FOR ADVERSE CHANNEL CONDITIONS
MP3267	MP3267CN	CN	201010228952.1	5/26/2010	212010102289	12/12/2012	In Force	WIREBOND STRUCTURES
MP3267T	MP3267TW	TW	99116677	5/25/2010	1512925	12/11/2015	In Force	WIREBOND STRUCTURES
MP3268	MP3268	US	12/786,242	5/24/2010	8477444	7/2/2013	In Force	CLOCK FREQUENCY SYNCHRONIZATION FOR MAGNETIC RECORDING
MP3269	MP3269	US	12/790,158	5/28/2010	8437440	5/7/2013	In Force	PHY FRAME FORMATS IN A SYSTEM WITH MORE THAN FOUR SPACE-TIME STREAMS
MP3269	MP3269C1	US	13/888,206	5/6/2013	9247579	1/26/2016	In Force	PHY FRAME FORMATS IN A SYSTEM WITH MORE THAN FOUR SPACE-TIME STREAMS
MP3269	MP3269C1C1	US	15/005,150	1/25/2016	9614707	4/4/2017	In Force	PHY FRAME FORMATS IN A SYSTEM WITH MORE THAN FOUR SPACE-TIME STREAMS
MP3270	MP3270	US	12/789,325	5/27/2010	8379757	2/19/2013	In Force	NARROW-BAND OFDM MODE FOR WLAN
MP3270	MP3270C1	US	13/769,134	2/15/2013	8917795	12/23/2014	In Force	NARROW-BAND OFDM MODE FOR WLAN
MP3271	MP3271	US	12/793,309	6/3/2010	8081039	12/20/2011	In Force	HIGH EFFICIENCY HARMONIC VOLTAGE CONTROLLED OSCILLATOR (HVCO)
MP3273	MP3273	US	12/775,850	5/7/2010	8750357	6/10/2014	In Force	SYSTEMS AND METHODS FOR ESTIMATING SIGNAL TO INTERFERENCE RATIO
MP3274	MP3274	US	12/791,430	6/1/2010	8159881	4/17/2012	In Force	Reference Voltage Optimization For Flash Memory
MP3274	MP3274aaaa	US	14/329,277	7/11/2014	8958250	2/17/2015	In Force	METHOD AND APPARATUS FOR OPTIMIZING REFERENCE VOLTAGES IN A FLASH MEMORY
MP3274	MP3274C1	US	13/447,789	4/16/2012	8416623	4/9/2013	In Force	Reference Voltage Optimization For Flash Memory
MP3274	MP3274C1aa	US	14/154,477	1/14/2014	8780637	7/15/2014	In Force	Updating Reference Voltages To Compensate For Changes In Threshold Voltage
MP3274	MP3274C1C1	US	13/858,634	4/8/2013	8630121	1/14/2014	In Force	Reference Voltage Optimization For Flash Memory
MP3274W	MP3274WO	WO	PCT/US2010/03	6/3/2010			Pending	Reference Voltage Optimization For Flash Memory
MP3274W	MP3274WOCN	CN	201080001353.1	6/3/2010	212010800013	8/3/2016	In Force	REFERENCE VOLTAGE OPTIMIZATION FOR FLASH MEMORY
MP3277	MP3277	US	12/793,404	6/3/2010	8467694	6/18/2013	In Force	METHOD AND APPARATUS FOR CONTROLLING A FUSER OF A PRINTING DEVICE
MP3283	MP3283D1	US	14/746,543	6/22/2015	9289589	3/29/2016	In Force	BALL GRID ARRAY PACKAGE WITH LASER VIAS AND METHODS FOR MANUFACTURING THE SAME
MP3284	MP3284	US	12/793,827	6/4/2010	8391260	3/5/2013	In Force	Power Management Systems And Methods For Peer-To-Peer Network
MP3284	MP3284C1	US	13/782,252	3/1/2013	8711825	4/29/2014	In Force	Client Network Device And Method For Adjusting Parameters Of Client Network

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MP3284	MP3284C1C1	US	14/258,322	4/22/2014	9215657	12/15/2015	In Force	Method And Apparatus For Adjusting Windows During Which Reques
MP3285	MP3285	US	12/793,850	6/4/2010	8892722	11/18/2014	In Force	Peer-To-Peer Discovery Systems And Methods
MP3286	MP3286	US	12/822,957	6/24/2010	8886833	11/11/2014	In Force	METHOD AND APPARATUS FOR PEER-TO-PEER NETWORKING
MP3288	MP3288	US	12/795,994	6/8/2010	8813201	8/19/2014	In Force	Generating Security Material
MP3288W	MP3288WO	WO	PCT/US2010/03	6/8/2010			Pending	Generating Security Material
MP3289	MP3289	US	12/821,595	6/23/2010	8301098	10/30/2012	In Force	System And Transceiver Clocking To Minimize Required Number Of R
MP3289	MP3289C1	US	13/664,309	10/30/2012	8532600	9/10/2013	In Force	System And Transceiver Clocking To Minimize Required Number Of
MP3289	MP3289C1aa	US	14/263,492	4/28/2014	9054677	6/9/2015	In Force	Clock Multiplexing For Baseband Automatic Frequency Correction
MP3289	MP3289C1C1	US	14/021,339	9/9/2013	8712360	4/29/2014	In Force	System And Transceiver Clocking To Minimize Required Number Of
MP3289	MP3289C12a	US	14/705,320	5/6/2015	9214923	12/15/2015	In Force	Wireless Communications System Including Dual-Purpose Clock Refer
MP3289W	MP3289WO	WO	PCT/US2010/03	6/24/2010			Pending	System And Transceiver Clocking To Minimize Required Number Of R
MP3289W	MP3289WOCN	CN	201080284520	6/24/2010	ZL2010800284	6/22/2016	In Force	SYSTEM AND TRANSCIVER CLOCKING TO MINIMIZE REQUIRED NUM
MP3289W	MP3289WCEP	EP	EP10729992.7	6/24/2010	EP2446543	11/16/2016	In Force	System and Transceiver Clocking To Minimize Required Number of Re
MP3289W	MP3289WCEPDE	DE	10729992.7	6/24/2010	602010038023	11/16/2016	In Force	System And Transceiver Clocking To Minimize Required Number Of R
MP3289W	MP3289WCEPFR	FR	10729992.7	6/24/2010	2446543	11/16/2016	In Force	System And Transceiver Clocking To Minimize Required Number Of R
MP3289W	MP3289WCEPGB	GB	10729992.7	6/24/2010	2446543	11/16/2016	In Force	System And Transceiver Clocking To Minimize Required Number Of R
MP3289W	MP3289WQJP	JP	2012-517714	6/24/2010	5704465	3/6/2015	In Force	SYSTEM AND TRANSCIVER CLOCKING TO MINIMIZE REQUIRED NUM
MP3289W	MP3289WOKR	KR	1020127001844	6/24/2010	101686154	12/7/2016	In Force	System And Transceiver Clocking To Minimize Required Number Of R
MP3290	MP3290	US	12/792,082	6/2/2010	8411694	4/2/2013	In Force	Congestion Avoidance for Network Traffic
MP3290	MP3290C1	US	13/852,592	3/28/2013	9350659	5/24/2016	In Force	Congestion Avoidance for Network Traffic
MP3292	MP3292	US	12/822,664	6/24/2010	8549384	10/1/2013	In Force	Method And Apparatus For Determining, Based On An Error Correcti
MP3292	MP3292C1	US	14/043,260	10/1/2013	8738996	5/27/2014	In Force	METHOD AND SYSTEM FOR CEASING ACCESS TO MEMORY CELLS OF
MP3292	MP3292C1C1	US	14/284,845	5/22/2014	9015560	4/21/2015	In Force	METHOD AND APPARATUS FOR CEASING ACCESS TO A PORTION OF
MP3293	MP3293D1	US	15/202,210	7/5/2016	10298386	5/21/2019	In Force	METHOD AND APPARATUS FOR SECURE COMMUNICATIONS IN NET
MP3295	MP3295	US	12/822,574	6/24/2010	8688404	4/1/2014	In Force	METHOD AND APPARATUS OF COMMON TIME-STAMPING
MP3296	MP3296	US	12/817,944	6/17/2010	8564091	10/22/2013	In Force	DIE-TO-DIE ELECTRICAL ISOLATION IN A SEMICONDUCTOR PACKAGE
MP3296	MP3296CN	CN	201010224714.1	7/6/2010	ZL2010102247	5/8/2013	In Force	DIE-TO-DIE ELECTRICAL ISOLATION IN A SEMICONDUCTOR PACKAGE
MP3301	MP3301	US	12/823,898	6/25/2010	8340231	12/25/2012	In Force	OPTIMAL SYMBOL DETECTION IN THE PRESENCE OF NON-GAUSSIAN
MP3301	MP3301C1	US	13/712,701	12/12/2012	9008245	4/14/2015	In Force	OPTIMAL SYMBOL DETECTION IN THE PRESENCE OF NON-GAUSSIAN
MP3302	MP3302	US	12/836,146	7/14/2010	8125272	2/28/2012	In Force	LOW POWER WIDE-BAND AMPLIFIER WITH REUSED CURRENT
MP3302	MP3302C1	US	13/369,175	2/8/2012	8339205	12/25/2012	In Force	LOW POWER WIDE-BAND AMPLIFIER WITH REUSED CURRENT
MP3302	MP3302C1C1	US	13/716,853	12/17/2012	8766362	5/27/2014	In Force	LOW POWER WIDE-BAND AMPLIFIER WITH REUSED CURRENT
MP3304	MP3304	US	12/820,266	6/22/2010	8437193	5/7/2013	In Force	Flash Memory Data Recovery
MP3308	MP3308CN	CN	201010230399.4	7/15/2010	ZL2010102303	4/13/2016	In Force	MULTI-CONNECT LEAD

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MP3308T	MP3308TW	TW	99123210	7/14/2010	1571995	2/21/2017	In Force	MULTI-CONNECT LEAD
MP3311	MP3311	US	12/883,054	9/15/2010	9066369	6/23/2015	In Force	Coexisting Radio Communication
MP3314	MP3314	US	12/841,772	7/22/2010	8462863	6/11/2013	In Force	MIDAMBLE FOR WLAN PHY FRAMES
MP3314	MP3314I1	US	13/669,183	11/29/2012	9706599	7/11/2017	In Force	LONG WIRELESS LOCAL AREA NETWORK (WLAN) PACKETS WITH MID
MP3314	MP3314I1C1	US	15/645,335	7/10/2017	10070482	9/4/2018	In Force	MIDAMBLE FOR WLAN PHY FRAMES
MP3314	MP3314I1C1C1	US	16/120,393	9/3/2018	10368391	7/30/2019	In Force	MIDAMBLE FOR WLAN PHY PACKETS
MP3314	MP3314I1C1C1C1	US	16/525,420	7/29/2019			Pending	MIDAMBLE FOR WLAN PHY PACKETS
MP3314	MP3314I1D1	US	13/689,223	11/29/2012	9586715	3/14/2017	In Force	LONG WIRELESS LOCAL AREA NETWORK (WLAN) PACKETS WITH MID
MP3314	MP3314I1D2	US	13/689,244	11/29/2012	9351333	5/24/2016	In Force	LONG WIRELESS LOCAL AREA NETWORK (WLAN) PACKETS WITH MID
MP3317	MP3317	US	12/832,443	6/24/2010	8212706	7/3/2012	In Force	Threshold Driven Dynamic Comparator
MP3319W	MP3319WO	WO	PCT/US2010/03	5/26/2010			Pending	METADATA MANAGEMENT FOR VIRTUAL VOLUMES
MP3320	MP3320	US	12/846,681	7/29/2010	8724720	5/13/2014	In Force	METHODS AND APPARATUS FOR WLAN TRANSMISSION
MP3320	MP3320D1	US	14/221,820	3/21/2014	9118530	8/25/2015	In Force	METHODS AND APPARATUS FOR WLAN TRANSMISSION
MP3320	MP3320D1C1	US	14/834,117	8/24/2015	9413576	8/9/2016	In Force	METHODS AND APPARATUS FOR WLAN TRANSMISSION
MP3320	MP3320D1Z	US	15/230,879	8/8/2016	10093432	6/19/2018	In Force	METHODS AND APPARATUS FOR WLAN TRANSMISSION
MP3320W	MP3320WCD1	CN	2015101414181	7/29/2010	ZL2015101414	4/13/2018	In Force	METHODS AND APPARATUS FOR WLAN TRANSMISSION
MP3320W	MP3320WED1	EP	EP17153789.7	7/29/2010	3182629	9/4/2019	In Force	Methods and Apparatus for WLAN Transmission
MP3320W	MP3320WED1D1	EP	19192232.7	7/29/2010			Pending	Methods and Apparatus for WLAN Transmission
MP3320W	MP3320WED1DE	DE	EP17153789.7	7/29/2010	602010060955	9/4/2019	In Force	Methods and Apparatus for WLAN Transmission
MP3320W	MP3320WED1FR	FR	EP17153789.7	7/29/2010	3182629	9/4/2019	In Force	Methods and Apparatus for WLAN Transmission
MP3320W	MP3320WED1GB	GB	EP17153789.7	7/29/2010	3182629	9/4/2019	In Force	Methods and Apparatus for WLAN Transmission
MP3320W	MP3320W1D1	JP	2014-225344	11/5/2014	6062976	9/16/2016	In Force	METHODS AND APPARATUS FOR WLAN TRANSMISSION
MP3320W	MP3320WO	WO	PCT/US2010/04	7/29/2010			Pending	METHOD AND APPARATUS FOR WLAN TRANSMISSION
MP3320W	MP3320WOCN	CN	2010800334541	7/29/2010	ZL2010800334	4/29/2015	In Force	METHODS AND APPARATUS FOR WLAN TRANSMISSION
MP3320W	MP3320WOEP	EP	10740806.4	7/29/2010			Pending	METHODS AND APPARATUS FOR WLAN TRANSMISSION
MP3320W	MP3320WOP	JP	2012-523051	7/29/2010	5646624	12/14/2014	In Force	METHODS AND APPARATUS FOR WLAN TRANSMISSION
MP3320W	MP3320WOKR	KR	10-2012-700285	7/29/2010	10-1657255	9/13/2016	In Force	METHODS AND APPARATUS FOR WLAN TRANSMISSION
MP3321	MP3321	US	12/842,724	7/23/2010	8345477	1/1/2013	In Force	NON-VOLATILE MEMORY DEVICES HAVING UNIFORM ERROR DISTRIB
MP3321	MP3321C1	US	13/724,275	12/21/2012	8638604	1/28/2014	In Force	NON-VOLATILE MEMORY DEVICES HAVING UNIFORM ERROR DISTRIB
MP3321	MP3321C1C1	US	14/153,987	1/13/2014	8780627	7/15/2014	In Force	NON-VOLATILE MEMORY DEVICES HAVING UNIFORM ERROR DISTRIB
MP3322	MP3322	US	12/840,801	7/21/2010	8242847	8/14/2012	In Force	METHOD AND APPARATUS FOR IMPROVING AMPLIFIER LINEARITY
MP3322	MP3322C1	US	13/546,263	7/11/2012	8456241	6/4/2013	In Force	METHOD AND APPARATUS FOR IMPROVING AMPLIFIER LINEARITY
MP3325	MP3325	US	12/831,835	7/7/2010	8493316	4/23/2013	In Force	SWITCH LOW POWER STATE IN A BLADE SERVER SYSTEM
MP3325	MP3325C1	US	13/867,714	4/22/2013	8738825	5/27/2014	In Force	SWITCH LOW POWER STATE IN A BLADE SERVER SYSTEM

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MP33325	MP33325C1C1	US	14/275,508	5/12/2014	8996764	3/31/2015	In Force	METHOD AND APPARATUS FOR CONTROLLING TRANSMISSION OF D
MP33326	MP33326	US	12/898,420	10/5/2010	8566520	10/22/2013	In Force	STORAGE SPACE ALLOCATION FOR LOGICAL DISK CREATION
MP33326	MP33326C1aa	US	15/147,486	5/5/2016	9946022	4/10/2018	In Force	STORAGE SPACE ALLOCATION FOR LOGICAL DISK CREATION
MP33326	MP33326C1C1	US	14/707,862	5/8/2015	9335927	5/10/2016	In Force	STORAGE SPACE ALLOCATION FOR LOGICAL DISK CREATION
MP33326	MP3326C1C1C1C1	US	15/944,599	4/3/2018	10387038	8/20/2019	In Force	STORAGE SPACE ALLOCATION FOR LOGICAL DISK CREATION
MP33328	MP33328	US	12/852,307	8/6/2010	8954502	2/10/2015	In Force	INFRASTRUCTURE DEVICES IN PEER-TO-PEER ENVIRONMENTS
MP33331	MP33331	US	12/862,714	7/23/2010	8650438	2/11/2014	In Force	CONTROLLER FOR READING DATA FROM NON-VOLATILE MEMORY
MP33331	MP33331C1	US	14/167,873	1/29/2014	8892940	11/18/2014	In Force	CONTROLLER FOR READING DATA FROM NON-VOLATILE MEMORY
MP33331W	MP33331WO	WO	PCT/US2010/04	7/23/2010			Pending	CONTROLLER FOR READING DATA FROM NON-VOLATILE MEMORY
MP33331W	MP33331WOCN	CN	201080035769.1	7/23/2010	212010800357	1/20/2016	In Force	CONTROLLER FOR READING DATA FROM NON-VOLATILE MEMORY
MP33331W	MP33331WOEP	EP	EP10742629.8	7/23/2010	2465027	3/20/2019	In Force	Controller for Reading Data from Non-Volatile Memory
MP33331W	MP33331WOEPDE	DE	EP10742629.8	7/23/2010	602010057676	3/20/2019	In Force	Controller for Reading Data from Non-Volatile Memory
MP33331W	MP33331WOEPFR	FR	EP10742629.8	7/23/2010	2465027	3/20/2019	In Force	Controller for Reading Data from Non-Volatile Memory
MP33331W	MP33331WOEPGB	GB	EP10742629.8	7/23/2010	2465027	3/20/2019	In Force	Controller for Reading Data from Non-Volatile Memory
MP33331W	MP33331WOJP	JP	2012-524720	7/23/2010	5732708	4/24/2015	In Force	CONTROLLER FOR READING DATA FROM NON-VOLATILE MEMORY
MP33331W	MP33331WOKR	KR	10-2012-700415	7/23/2010	10-1608910	3/29/2016	In Force	CONTROLLER FOR READING DATA FROM NON-VOLATILE MEMORY
MP33335	MP33335	US	12/850,529	8/4/2010	8923217	12/30/2014	In Force	SDMA MULTI-DEVICE WIRELESS COMMUNICATIONS
MP33335	MP33335C1	US	14/585,000	12/29/2014	9516667	12/6/2016	In Force	SDMA MULTI-DEVICE WIRELESS COMMUNICATIONS
MP33335W	MP33335WO	WO	PCT/US2010/04	8/4/2010			Pending	SDMA MULTI-DEVICE WIRELESS COMMUNICATIONS
MP33335W	MP33335WOCN	CN	201080040641.1	8/4/2010	212010800406	9/9/2015	In Force	SDMA MULTI-DEVICE WIRELESS COMMUNICATIONS
MP33335W	MP33335WOEP	EP	EP10743297.3	8/4/2010	EP2465227	7/26/2017	In Force	SDMA Multi-Device Wireless Communications
MP33335W	MP33335WOJP	JP	2012-524749	8/4/2010	5633048	10/24/2014	In Force	SDMA MULTI-DEVICE WIRELESS COMMUNICATIONS
MP33335W	MP33335WOKR	KR	10-2012-700646	8/4/2010	10-1714687	3/3/2017	In Force	SDMA MULTI-DEVICE WIRELESS COMMUNICATIONS
MP33335W	MP33335WUDE	DE	602010043880.1	8/4/2010	2465227	7/26/2017	In Force	SDMA MULTI-DEVICE WIRELESS COMMUNICATIONS
MP33335W	MP33335WUFR	FR	10743297.3	8/4/2010	2465227	7/26/2017	In Force	SDMA MULTI-DEVICE WIRELESS COMMUNICATIONS
MP33335W	MP33335WUGB	GB	10743297.3	8/4/2010	2465227	7/26/2017	In Force	SDMA MULTI-DEVICE WIRELESS COMMUNICATIONS
MP33336	MP33336	US	12/818,430	6/18/2010	8503291	8/6/2013	In Force	SYSTEMS AND METHODS FOR DIRECTING A BEAM TOWARDS A DEVI
MP33336	MP33336C1	US	13/942,156	7/15/2013	8958287	2/17/2015	In Force	SYSTEMS AND METHODS FOR DIRECTING A BEAM TOWARDS A DEVI
MP33337	MP33337	US	12/814,957	6/14/2010	8374004	2/12/2013	In Force	Isolated AC-DC Converter With Master Controller On Secondary Side
MP33337	MP33337CN	CN	201010256387.1	8/16/2010	212010102563	9/16/2015	In Force	ISOLATED AC-DC CONVERTER WITH MASTER CONTROLLER ON SECO
MP33337E	MP33337EP	EP	EP10172392.2	8/10/2010			Pending	Isolated AC-DC Converter with Master Controller on Secondary Side a
MP33339	MP33339	US	12/856,421	8/13/2010	8472361	6/25/2013	In Force	METHODS AND APPARATUS FOR ANTENNA SPOOFING
MP33339	MP33339C1	US	13/925,206	6/24/2013	9166662	10/20/2015	In Force	METHODS AND APPARATUS FOR ANTENNA SPOOFING
MP33340	MP33340	US	12/898,483	10/5/2010	9003159	4/7/2015	In Force	DATA CACHING IN NON-VOLATILE MEMORY

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MP3340W	MP3340WO	WO	PCT/US2010/05	10/5/2010			Pending	DATA CACHING IN NON-VOLATILE MEMORY
MP3340W	MP3340WOCN	CN	2010800447141	10/5/2010	ZL2010800447	1/13/2016	In Force	DATA CACHING IN NON-VOLATILE MEMORY
MP3340W	MP3340WOCIN	IN	959/MUMNP/21	10/5/2010			Pending	DATA CACHING IN NON-VOLATILE MEMORY
MP3341	MP3341	US	12/855,521	8/12/2010	8660497	2/25/2014	In Force	Beamsteering in a Spatial Division Multiple Access (SDMA) System
MP3341	MP3341C1	US	14/188,363	2/24/2014	9319122	4/19/2016	In Force	BEAMSTEERING IN A SPATIAL DIVISION MULTIPLE ACCESS (SDMA) SY
MP3341	MP3341C1C1	US	15/131,536	4/10/2016			Pending	BEAMSTEERING IN A SPATIAL DIVISION MULTIPLE ACCESS (SDMA) SY
MP3350E	MP3350EP	EP	10173832.6	8/24/2010			Pending	METHOD AND APPARATUS FOR IMPROVED VAMOS TRANSMISSION
MP3352	MP3352	US	12/858,446	8/18/2010	8416740	4/9/2013	In Force	GROUPING OF USERS ONTO TRAFFIC CHANNELS IN MULTIPLE-USER
MP3352	MP3352CN	CN	2010102659954	8/25/2010	ZL2010102659	11/25/2015	In Force	GROUPING OF USERS ONTO TRAFFIC CHANNELS IN MULTIPLE-USER
MP3353	MP3353	US	12/832,352	7/8/2010	8253614	8/28/2012	In Force	ANALOG-TO-DIGITAL CONVERSION METHODS AND SYSTEMS
MP3354	MP3354	US	12/831,282	7/7/2010	8526552	9/3/2013	In Force	NOISE ESTIMATION IN COMMUNICATION RECEIVERS
MP3356	MP3356	US	12/868,414	8/25/2010	8532200	9/10/2013	In Force	System And Method For Side Band Communication In SERDES Trans
MP3356	MP3356C1	US	14/020,263	9/6/2013	8938014	1/20/2015	In Force	System And Method For Side Band Communication In SERDES Trans
MP3359	MP3359	US	12/855,598	8/12/2010	935951	5/31/2016	In Force	INTERCONNECT LAYOUTS FOR ELECTRONIC ASSEMBLIES
MP3359	MP3359CN	CN	2010102695431	8/30/2010	ZL2010102695	11/5/2014	In Force	INTERCONNECT LAYOUTS FOR ELECTRONIC ASSEMBLIES
MP3360	MP3360	US	12/872,455	8/31/2010	8407562	3/26/2013	In Force	Systems And Methods For Compressing Data In Non-Volatile Semicon
MP3360	MP3360C1	US	13/849,882	3/25/2013	8806299	8/12/2014	In Force	Method And Apparatus For Compressing Data Sectors In Storage Driv
MP3360W	MP3360WO	WO	PCT/US2010/04	9/1/2010			Pending	Systems And Methods For Compressing Data In Non-Volatile Semicon
MP3360W	MP3360WOCN	CN	2010800373021	9/1/2010	ZL2010800373	8/26/2015	In Force	SYSTEMS AND METHODS FOR COMPRESSING DATA IN NON-VOLATIL
MP3363	MP3363	US	12/875,890	9/3/2010	8417202	4/9/2013	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS,
MP3363	MP3363C1	US	13/858,821	4/8/2013	8742850	6/3/2014	In Force	METHOD AND APPARATUS FOR RESPECTIVELY CALIBRATING A GAIN
MP3366	MP3366	US	12/878,703	9/9/2010	8605534	12/10/2013	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS,
MP3366	MP3366C1	US	14/077,634	11/12/2013	9218856	12/22/2015	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS,
MP3366	MP3366C1C1	US	14/957,029	12/2/2015	9570118	2/14/2017	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS,
MP3366W	MP3366WO	WO	PCT/US2010/04	9/9/2010			Pending	Circuits, Architectures, Apparatuses, Systems, Algorithms and Metho
MP3366W	MP3366WOCN	CN	2010800468311	9/9/2010	ZL2010800468	3/25/2015	In Force	Circuits, Architectures, Apparatuses, Systems, Algorithms and Metho
MP3366W	MP3366WCEP	EP	10816098.7	9/9/2010			Pending	Circuits, Architectures, Apparatuses, Systems, Algorithms and Metho
MP3366W	MP3366WOKR	KR	10-2012-700757	9/9/2010	10-1737020	5/11/2017	In Force	Circuits, Architectures, Apparatuses, Systems, Algorithms and Metho
MP3369	MP3369C1	US	14/076,026	11/8/2013	8837658	9/16/2014	In Force	METHOD AND APPARATUS FOR SAMPLING A SERIAL DATA STREAM U
MP3369W	MP3369WO	WO	PCT/US2010/04	9/8/2010			Pending	PROGRAMMABLE UNIVERSAL ASYNCHRONOUS RECEIVER/TRANSMIT
MP3370	MP3370	US	12/884,255	9/17/2010	8525434	9/3/2013	In Force	METHOD AND APPARATUS FOR POWER DRIVING
MP3370	MP3370C1	US	13/901,186	5/23/2013	8907586	12/9/2014	In Force	METHOD AND APPARATUS FOR POWER DRIVING
MP3370	MP3370C1C1	US	14/547,863	11/19/2014	9293995	3/22/2016	In Force	METHOD AND APPARATUS FOR POWER DRIVING
MP3370W	MP3370WO	WO	PCT/US2010/04	9/17/2010			Pending	METHOD AND APPARATUS FOR POWER DRIVING

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MP3370W	MP3370W/OCN	CN	201080045378.1	9/17/2010	ZL2010800453	3/6/2018	In Force	METHOD AND APPARATUS FOR POWER DRIVING
MP3373	MP3373	US	12/873,908	9/1/2010	9157645	10/13/2015	In Force	APPARATUS, METHOD, AND SYSTEM FOR CONTROLLING FLOW OF A
MP3377	MP3377	US	12/870,089	8/27/2010	8489804	7/16/2013	In Force	SYSTEM FOR WRITING TO MEMORY
MP3377	MP3377C1C1	US	14/187,933	2/24/2014	8882816	11/18/2014	In Force	System And Method For Writing Data To A Memory
MP3378	MP3378	US	12/881,127	9/13/2010	8228728	7/24/2012	In Force	PROGRAMMING METHOD FOR MULTI-LEVEL CELL FLASH FOR MINIM
MP3378	MP3378C1	US	13/543,634	7/6/2012	0817535	8/26/2014	In Force	PROGRAMMING METHOD FOR MULTI-LEVEL CELL FLASH FOR MINIM
MP3379	MP3379	US	12/881,881	9/14/2010	8402217	3/19/2013	In Force	IMPLEMENTING RAID IN SOLID STATE MEMORY
MP3379	MP3379aall	US	14/642,526	3/9/2015	9201731	12/1/2015	In Force	ERROR CORRECTION FOR STORAGE DEVICES
MP3379	MP3379C1	US	13/844,302	3/15/2013	8725944	5/13/2014	In Force	IMPLEMENTING RAID IN SOLID STATE MEMORY
MP3379	MP3379C1C1	US	14/260,237	4/23/2014	8977813	3/10/2015	In Force	IMPLEMENTING RAID IN SOLID STATE MEMORY
MP3379	MP3379CN	CN	201010285233.1	9/15/2010	ZL2010102852	1/20/2016	In Force	IMPLEMENTING RAID IN SOLID STATE MEMORY
MP3379	MP3379ZF3	US	14/947,833	11/20/2015	9922678	3/20/2018	In Force	ERROR CORRECTION FOR STORAGE DEVICES
MP3379	MP3379ZFZ	US	15/911,610	3/14/2018	10186296	1/22/2019	In Force	ERROR CORRECTION FOR STORAGE DEVICES
MP3379	MP3379ZFC1	US	16/253,027	1/21/2019			Pending	ERROR CORRECTION FOR STORAGE DEVICES
MP3382	MP3382	US	12/881,459	9/14/2010	8473680	6/25/2013	In Force	Hotspot Detection And Caching For Storage Devices
MP3382	MP3382C1	US	13/921,966	6/19/2013	9112607	9/1/2015	In Force	Hotspot Detection And Caching For Storage Devices
MP3383	MP3383	US	12/706,704	2/16/2010	8358651	1/22/2013	In Force	SWITCH DEVICE HAVING A PLURALITY OF PROCESSING CORES
MP3383	MP3383C1	US	13/745,493	1/18/2013	9185052	11/10/2015	In Force	SWITCH DEVICE HAVING A PLURALITY OF PROCESSING CORES
MP3383	MP3383C1C1	US	14/936,271	11/9/2015	9509639	11/29/2016	In Force	SWITCH DEVICE HAVING A PLURALITY OF PROCESSING CORES
MP3384	MP3384JP	JP	2010-212505	9/22/2010	5729662	4/17/2015	In Force	Spread-Spectrum Clock Acquisition And Tracking
MP3385	MP3385	US	12/888,310	9/22/2010	8644368	2/4/2014	In Force	TRANSPARENT IMPLICIT BEAMFORMING IN A COMMUNICATION SYS
MP3385	MP3385C1	US	14/171,769	2/3/2014	9203488	12/1/2015	In Force	TRANSPARENT IMPLICIT BEAMFORMING IN A COMMUNICATION SYS
MP3385	MP3385C1C1	US	14/954,640	11/30/2015	9425872	8/23/2016	In Force	TRANSPARENT IMPLICIT BEAMFORMING IN A COMMUNICATION SYS
MP3388	MP3388	US	12/883,208	9/16/2010	8644861	2/4/2014	In Force	EFFICIENT ACQUISITION OF SYSTEM INFORMATION IN MOBILE COM
MP3388	MP3388C1	US	14/138,071	12/22/2013	8934926	1/13/2015	In Force	EFFICIENT ACQUISITION OF SYSTEM INFORMATION IN MOBILE COM
MP3389	MP3389	US	12/888,744	9/23/2010	8331050	12/11/2012	In Force	Patterned Magnetic Media Synchronization Systems
MP3389	MP3389C1	US	13/708,497	12/7/2012	8755139	6/17/2014	In Force	System With Servo Clock And Write Clock Based Phase Estimation Fo
MP3390	MP3390	US	12/882,496	9/15/2010	8646760	5/7/2013	In Force	LOW POWER CURRENT-VOLTAGE MIXED ADC ARCHITECTURE
MP3391	MP3391	US	12/896,586	10/1/2010	8164383	4/24/2012	In Force	CIRCUITS AND METHODS FOR AMPLIFYING SIGNALS
MP3393	MP3393	US	12/882,802	9/15/2010	8306339	10/30/2012	In Force	METHOD AND SYSTEM FOR COMPENSATING FOR ADJACENT TRACKS
MP3393	MP3393C1	US	13/610,468	9/11/2012	8441750	5/14/2013	In Force	METHOD AND SYSTEM FOR COMPENSATING FOR ADJACENT TRACKS
MP3393	MP3393C1C1	US	13/864,454	4/17/2013	0693122	4/8/2014	In Force	METHOD AND SYSTEM FOR COMPENSATING FOR ADJACENT TRACKS
MP3394	MP3394	US	11/252,085	10/17/2005	7490828	2/17/2009	In Force	IMAGE-BASED EDGE DETECTION OF STACKED SHEET MEDIA
MP3395	MP3395	US	12/883,430	9/16/2010	8407407	3/26/2013	In Force	Solid State Drive Access Control System With Equalized Access Timing

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MP3395	MP3395C1	US	13/849,794	3/25/2013	9396105	7/19/2016	In Force	Method And Apparatus For Controlling Execution Of Commands To T
MP3396	MP3396	US	12/887,154	9/21/2010	8436883	5/7/2013	In Force	SYSTEMS AND METHODS FOR LASER SPACING COMPENSATION IN LA
MP3398	MP3398	US	12/891,377	9/27/2010	8559543	10/15/2013	In Force	SOFT SPHERE DECODER FOR MIMO MAXIMUM LIKELIHOOD DEMOD
MP3398	MP3398C1	US	14/050,115	10/9/2013	9143210	9/22/2015	In Force	SOFT SPHERE DECODER FOR MIMO MAXIMUM LIKELIHOOD DEMOD
MP3399	MP3399	US	12/897,210	10/4/2010	8295002	10/23/2012	In Force	NULL SERVO DEMODULATION FOR SHORT SERVO WEDGE IN DISK ST
MP3399	MP3399C1	US	13/628,913	9/27/2012	8462454	6/11/2013	In Force	NULL SERVO DEMODULATION FOR SHORT SERVO WEDGE IN DISK ST
MP3399	MP3399C1C1	US	13/893,593	5/14/2013	8765191	4/22/2014	In Force	NULL SERVO DEMODULATION FOR SHORT SERVO WEDGE IN DISK ST
MP3400	MP3400	US	12/892,183	9/28/2010	8438461	5/7/2013	In Force	POWER CONSUMPTION IN LDPC DECODER FOR LOW-POWER APPLIC
MP3400	MP3400CN	CN	201010566571.4	10/12/2010	ZL2010105065	11/25/2015	In Force	IMPROVING POWER CONSUMPTION IN LDPC DECODER FOR LOW-PO
MP3400	MP3400D1	US	13/888,705	4/23/2013	9083267	4/7/2015	In Force	POWER CONSUMPTION IN LDPC DECODER FOR LOW-POWER APPLIC
MP3402	MP3402	US	12/902,168	10/12/2010	8675794	3/18/2014	In Force	EFFICIENT ESTIMATION OF FEEDBACK FOR MODULATION AND CODI
MP3402	MP3402C1	US	14/179,593	2/13/2014	8861662	10/14/2014	In Force	EFFICIENT ESTIMATION OF CHANNEL STATE INFORMATION (CSI) FEE
MP3405	MP3405	US	12/901,287	10/8/2010	8891592	11/18/2014	In Force	CONTROL PHYSICAL LAYER (PHY) DATA UNIT
MP3405	MP3405C1	US	14/543,103	11/17/2014	9628260	4/18/2017	In Force	CONTROL PHYSICAL LAYER (PHY) DATA UNIT
MP3406	MP3406	US	12/901,357	10/8/2010	8392809	3/5/2013	In Force	LOG-LIKELIHOOD-RATIO (LLR) TABLE CALIBRATION
MP3406	MP3406C1	US	13/781,359	2/28/2013	8611334	12/31/2013	In Force	LOG-LIKELIHOOD-RATIO (LLR) TABLE CALIBRATION
MP3406	MP3406C1C1	US	14/133,042	12/18/2013	8898553	11/25/2014	In Force	LOG-LIKELIHOOD-RATIO (LLR) TABLE CALIBRATION
MP3407	MP3407	US	12/899,813	10/7/2010	8378738	2/19/2013	In Force	Adaptive Voltage Scaling Using A Delay Line
MP3407	MP3407C1	US	13/744,462	1/18/2013	8519781	8/27/2013	In Force	Adaptive Voltage Scaling Using A Delay Line
MP3407	MP3407C1aa	US	14/242,174	4/1/2014	9264030	2/16/2016	In Force	Adaptive Voltage Scaling Using A Delay Line
MP3408	MP3408	US	12/903,055	10/12/2010	8462249	3/19/2013	In Force	System And Method For Mixed-Mode Saram Address Mapping
MP3408	MP3408C1	US	13/826,665	3/14/2013	8621181	12/31/2013	In Force	System And Method For Accessing Distinct Regions Of Memory Using
MP3408	MP3408C1C1	US	14/141,817	12/27/2013	8996844	3/31/2015	In Force	Apparatus And Method For Accessing Non-Overlapping Portions Of
MP3409	MP3409	US	12/903,406	10/13/2010	8509125	8/13/2013	In Force	Method and Apparatus for Reducing Nonlinear Echo Distortion in a C
MP3409	MP3409C1	US	13/961,130	8/7/2013	9077529	7/7/2015	In Force	Method and Apparatus for Reducing Nonlinear Echo Distortion in a C
MP3411	MP3411	US	12/903,237	10/13/2010	8917796	12/23/2014	In Force	TRANSMISSION-MODE-AWARE RATE MATCHING IN MIMO SIGNAL G
MP3411	MP3411C1	US	14/571,314	12/16/2014	9596017	3/14/2017	In Force	TRANSMISSION-MODE-AWARE RATE MATCHING IN MIMO SIGNAL G
MP3416	MP3416	US	12/899,087	10/6/2010	8717838	5/6/2014	In Force	METHOD AND APPARATUS FOR MEMORY REDUNDANCY
MP3417	MP3417	US	12/772,212	5/2/2010	8260226	9/4/2012	In Force	HIGH-ACCURACY TRANSMIT POWER CONTROL WITH HIGH-EFFICIEN
MP3417	MP3417C1	US	13/587,231	8/16/2012	8457680	6/4/2013	In Force	HIGH-ACCURACY TRANSMIT POWER CONTROL WITH HIGH-EFFICIEN
MP3419	MP3419	US	12/814,456	6/13/2010	8417198	4/9/2013	In Force	SELECTION OF CLOSED-LOOP/OPEN-LOOP POWER CONTROL IN USER
MP3421	MP3421	US	12/938,116	11/2/2010	8625594	1/7/2014	In Force	Switching Apparatus and Method Based on Virtual Interfaces
MP3421	MP3421C1	US	14/148,073	1/6/2014	9065775	6/23/2015	In Force	SWITCHING APPARATUS AND METHOD BASED ON VIRTUAL INTERFA
MP3421W	MP3421WO	WO	PCT/US2010/05	11/2/2010			Pending	SWITCHING APPARATUS AND METHOD BASED ON VIRTUAL INTERFA

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MP3421W	MP3421WOCN	CN	201080025581	11/2/2010	ZL2010800255	5/4/2016	In Force	SWITCHING APPARATUS AND METHOD BASED ON VIRTUAL INTERFA
MP3421W	MP3421WOEP	EP	EP10791005.1	11/2/2010	2497234	9/19/2018	In Force	Switching Apparatus and Method Based on Virtual Interfaces
MP3421W	MP3421WOEPD1	EP	EP18167245.8	11/2/2010			Pending	Switching Apparatus and Method Based on Virtual Interfaces
MP3421W	MP3421WOEPDE	DE	EP10791005.1	11/2/2010	2497234	9/19/2018	In Force	Switching Apparatus and Method Based on Virtual Interfaces
MP3421W	MP3421WOEPFR	FR	EP10791005.1	11/2/2010	2497234	9/19/2018	In Force	Switching Apparatus and Method Based on Virtual Interfaces
MP3421W	MP3421WOEPGB	GB	EP10791005.1	11/2/2010	2497234	9/19/2018	In Force	Switching Apparatus and Method Based on Virtual Interfaces
MP3422	MP3422	US	12/916,968	11/1/2010	9229518	1/5/2016	In Force	Wake-on-Frame for Frame Processing Devices
MP3422	MP3422C1	US	14/981,992	12/29/2015			Pending	Wake-on-Frame for Frame Processing Devices
MP3425	MP3425	US	12/917,252	11/1/2010	8565682	10/22/2013	In Force	FEEDBACK REDUCTION SCHEMES FOR COORDINATED TRANSMISSION
MP3425	MP3425C1	US	14/056,813	10/17/2013	9064753	6/9/2015	In Force	REDUCTION OF FEEDBACK INFORMATION RECEIVED BY A BASE STATION
MP3426	MP3426	US	12/909,675	10/21/2010	8385016	2/26/2013	In Force	ASYMMETRIC CORRECTION CIRCUIT
MP3429	MP3429	US	12/940,778	11/5/2010	9184877	11/10/2015	In Force	METHOD AND APPARATUS FOR DECODING INDEPENDENTLY ENCODE
MP3430	MP3430	US	12/917,591	11/2/2010	8531982	9/10/2013	In Force	Quality of Service Aware Channel Quality Indicator
MP3430	MP3430C1	US	14/014,869	8/30/2013	8923156	12/30/2014	In Force	Quality of Service Aware Channel Quality Indicator
MP3433	MP3433	US	12/838,509	7/19/2010	8325860	12/4/2012	In Force	ASYMMETRICAL FEEDBACK FOR COORDINATED TRANSMISSION SYST
MP3433	MP3433C1	US	13/669,481	11/6/2012	8933455	12/30/2014	In Force	ASYMMETRICAL FEEDBACK FOR COORDINATED TRANSMISSION SYST
MP3433W	MP3433WIPD	JP	2014-086094	4/18/2014	6065288	1/6/2017	In Force	ASYMMETRICAL FEEDBACK FOR COORDINATED TRANSMISSION SYST
MP3433W	MP3433WOCN	CN	2010800424873	7/19/2010	ZL2010800424	10/21/2015	In Force	ASYMMETRICAL FEEDBACK FOR COORDINATED TRANSMISSION SYST
MP3433W	MP3433WOEP	EP	EP10827994.4	7/19/2010	2499862	9/5/2018	In Force	Asymmetrical Feedback for Coordinated Transmission Systems
MP3433W	MP3433WOEPFR	FR	EP10827994.4	7/19/2010	2499862	9/5/2018	In Force	Asymmetrical Feedback for Coordinated Transmission Systems
MP3433W	MP3433WOEPGB	GB	EP10827994.4	7/19/2010	2499862	9/5/2018	In Force	Asymmetrical Feedback for Coordinated Transmission Systems
MP3433W	MP3433WOJP	JP	2012-537460	7/19/2010	5669854	12/26/2014	In Force	ASYMMETRICAL FEEDBACK FOR COORDINATED TRANSMISSION SYST
MP3434	MP3434C1	US	14/021,460	9/9/2013	8924747	12/30/2014	In Force	Power Management Of Iterative Data Processing Systems
MP3434	MP3434C1C1	US	14/577,545	12/19/2014	9535477	1/3/2017	In Force	Power Management Of Iterative Data Processing Systems
MP3435	MP3435	US	12/945,453	11/12/2010	8872812	10/28/2014	In Force	POWER SAVING IN MOBILE DEVICES BY OPTIMIZING FRAME RATE O
MP3435	MP3435CN	CN	201010556995.1	11/12/2010			Pending	POWER SAVING IN MOBILE DEVICES BY OPTIMIZING FRAME RATE O
MP3441	MP3441	US	12/945,805	11/12/2010	8385997	3/12/2013	In Force	MULTI-CHANNEL WIRELESS COMMUNICATIONS
MP3441	MP3441C1	US	13/794,158	3/11/2013	8743851	6/3/2014	In Force	MULTI-CHANNEL WIRELESS COMMUNICATIONS
MP3441W	MP3441WO	WO	PCT/US2010/05	11/12/2010			Pending	MULTI-CHANNEL WIRELESS COMMUNICATIONS
MP3441W	MP3441WOCN	CN	201080057913.1	11/12/2010	ZL2010800579	3/25/2015	In Force	MULTI-CHANNEL WIRELESS COMMUNICATIONS
MP3441W	MP3441WOEP	EP	EP10785256.8	11/12/2010	2499872	1/14/2015	In Force	MULTI-CHANNEL WIRELESS COMMUNICATIONS
MP3441W	MP3441WOJP	JP	2012-539041	11/12/2010	5668267	12/5/2014	In Force	MULTI-CHANNEL WIRELESS COMMUNICATIONS
MP3441W	MP3441WUDE	DE	6020100218504	11/12/2010	2499872	1/14/2015	In Force	MULTI-CHANNEL WIRELESS COMMUNICATIONS

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MP3441W	MP3441WU/FR	FR	10785256.8	11/12/2010	2499872	1/14/2015	In Force	MULTI-CHANNEL WIRELESS COMMUNICATIONS
MP3441W	MP3441WU/GB	GB	10785256.8	11/12/2010	2499872	1/14/2015	In Force	MULTI-CHANNEL WIRELESS COMMUNICATIONS
MP3442	MP3442	US	12/940,377	11/5/2010	8886885	11/11/2014	In Force	Systems And Methods For Operating A Plurality Of Flash Modules In
MP3442	MP3442C1	US	14/552,108	11/4/2014	9043552	5/26/2015	In Force	Systems And Methods For Operating A Flash Memory File System
MP3442W	MP3442WO	WO	PCT/US2010/05	11/12/2010			Pending	Flash Memory File System
MP3442W	MP3442WOCN	CN	2010800513680	11/12/2010	ZL2010800513	12/16/2015	In Force	FLASH MEMORY FILE SYSTEM
MP3442W	MP3442WCEP	EP	EP10784905.1	11/12/2010			Pending	Flash Memory File System
MP3443	MP3443	US	12/941,534	11/8/2010	8775854	7/8/2014	In Force	Clock Turn-On Strategy For Power Management
MP3443W	MP3443WO	WO	PCT/US2010/05	11/12/2010			Pending	Clock Turn-On Strategy For Power Management
MP3443W	MP3443WOCN	CN	2010800513861	11/12/2010	ZL2010800513	9/23/2015	In Force	CLOCK TURN-ON STRATEGY FOR POWER MANAGEMENT
MP3443W	MP3443WCEP	EP	EP10788434.8	11/12/2010	EP2499549	4/1/2015	In Force	Clock Turn-On Strategy for Power Management
MP3443W	MP3443WQIN	IN	3646CHENP201	11/12/2010			Pending	Clock Turn-On Strategy For Power Management
MP3443W	MP3443WQJP	JP	2012-539013	11/12/2010	5776124	7/17/2015	In Force	CLOCK TURN-ON STRATEGY FOR POWER MANAGEMENT
MP3443W	MP3443WQKR	KR	1020127012017	11/12/2010	101699916	1/19/2017	In Force	Clock Turn-On Strategy For Power Management
MP3443W	MP3443WUDE	DE	10788434.8	11/12/2010	2499549	4/1/2015	In Force	CLOCK TURN-ON STRATEGY FOR POWER MANAGEMENT
MP3443W	MP3443WU/FR	FR	10788434.8	11/12/2010	2499549	4/1/2015	In Force	CLOCK TURN-ON STRATEGY FOR POWER MANAGEMENT
MP3443W	MP3443WU/GB	GB	10788434.8	11/12/2010	2499549	4/1/2015	In Force	CLOCK TURN-ON STRATEGY FOR POWER MANAGEMENT
MP3444W	MP3444W/D1	JP	2015-149511	11/16/2010			Pending	ITERATIVE PARSING AND CLASSIFICATION
MP3444W	MP3444WO	WO	PCT/US2010/05	11/16/2010			Pending	Iterative Parsing and Classification
MP3444W	MP3444WOCN	CN	2010800456541	11/16/2010	ZL2010800456	6/3/2015	In Force	ITERATIVE PARSING AND CLASSIFICATION
MP3444W	MP3444WQJP	JP	2012-539074	11/16/2010			Pending	ITERATIVE PARSING AND CLASSIFICATION
MP3445W	MP3445WO	WO	PCT/US2010/05	11/10/2010			Pending	Ground Shield Capacitor
MP3445W	MP3445WOCN	CN	201080046212	11/10/2010	ZL2010800462	1/20/2016	In Force	GROUND SHIELD CAPACITOR
MP3447	MP3447	US	12/917,117	11/1/2010	8618425	8/26/2014	In Force	Synchronization Of Paging And Sleep Cycles In Wireless Communicati
MP3448	MP3448	US	12/944,031	11/11/2010	8570887	10/29/2013	In Force	Analog Bias Control For Packet Communication Systems
MP3448W	MP3448WO	WO	PCT/US2010/05	11/18/2010			Pending	Analog Bias Control For Packet Communication Systems
MP3448W	MP3448WOCN	CN	2010800525061	11/18/2010	ZL2010800525	8/3/2016	In Force	METHOD AND APPARATUS FOR ANALOG BIAS CONTROL OF PACKET
MP3449	MP3449	US	12/948,784	11/18/2010	8693963	7/23/2013	In Force	MULTIPLE TIME ACCURATE STROBE (TAS) MESSAGING
MP3450	MP3450	US	12/887,514	9/22/2010	8331885	12/11/2012	In Force	PREDICTIVE WAKE-UP OF BASEBAND CIRCUITRY
MP3454	MP3454	US	12/917,748	11/12/2010	8562443	11/12/2013	In Force	Method and Apparatus for Virtual Cable Test Using Echo Cancellation Co
MP3454	MP3454C1	US	14/074,849	11/8/2013	9246604	1/26/2016	In Force	Method and Apparatus for Virtual Cable Test Using Echo Cancellation Co
MP3455	MP3455	US	12/917,989	11/12/2010	8510077	8/13/2013	In Force	METHODS AND APPARATUS FOR SELF-ASSISTING POSITIONING SYSTE
MP3455	MP3455D1	US	13/964,991	8/12/2013	9448306	9/20/2016	In Force	METHODS AND APPARATUS FOR SELF-ASSISTING POSITIONING SYST
MP3456	MP3456	US	12/951,923	11/22/2010	8531792	9/10/2013	In Force	Self-Servo Write Process for Discrete Track Media

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MP3456	MP3456C1	US	14/020,717	9/6/2013	8786979	7/22/2014	In Force	SELF SERVO WRITE PROCESS FOR DISCRETE TRACK MEDIA
MP3457	MP3457TW	TW	99140338	11/23/2010	1458028	10/21/2014	In Force	EMBEDDED CHIP PACKAGES
MP3457W	MP3457WO	WO	PCT/US2010/05	11/10/2010			Pending	EMBEDDED CHIP PACKAGES
MP3457W	MP3457WOCN	CN	2010800605703	11/10/2010	ZL2010800605	1/20/2016	In Force	EMBEDDED CHIP PACKAGES
MP3457W	MP3457WOKR	KR	10-2012-701580	11/10/2010	101801307	11/24/2017	In Force	Embedded Chip Packages
MP3459	MP3459C1	US	14/049,769	10/9/2013	8786554	7/1/2014	In Force	METHOD AND APPARATUS FOR DIMMING
MP3461	MP3461	US	12/877,106	9/8/2010	8442129	5/14/2013	In Force	DIRECT GENERATION OF DISCRETE FOURIER TRANSFORMS OF TIME
MP3461	MP3461CA	CA	2716273	10/1/2010			Pending	DIRECT GENERATION OF DISCRETE FOURIER TRANSFORMS OF TIME
MP3462	MP3462	US	12/956,641	11/30/2010	8259862	10/30/2012	In Force	Tuning Circuit For Inductor Capacitor (LC) Tank Digitally Controlled Co
MP3463	MP3463	US	12/959,079	12/2/2010	8885620	11/11/2014	In Force	METHOD AND APPARATUS FOR SOUNDING MULTIPLE STATIONS
MP3463	MP3463C1	US	12/959,145	12/2/2010	8873525	10/28/2014	In Force	METHOD AND APPARATUS FOR SOUNDING MULTIPLE STATIONS
MP3463W	MP3463WO	WO	PCT/US2010/05	12/2/2010			Pending	METHOD AND APPARATUS FOR SOUNDING MULTIPLE STATIONS
MP3463W	MP3463WOCN	CN	2010800545733	12/2/2010	ZL2010800545	1/20/2016	In Force	METHOD AND APPARATUS FOR SOUNDING MULTIPLE STATIONS
MP3463W	MP3463WOEPR	EP	EP10796206	12/2/2010			Pending	Method and Apparatus for Sounding Multiple Stations
MP3463W	MP3463WOJP	JP	2012-542191	12/2/2010	5663811	12/19/2014	In Force	METHOD AND APPARATUS FOR SOUNDING MULTIPLE STATIONS
MP3463W	MP3463WOKR	KR	10-2012-701305	12/2/2010	10-1746733	6/13/2017	In Force	METHOD AND APPARATUS FOR SOUNDING MULTIPLE STATIONS
MP3465	MP3465	US	12/853,339	8/10/2010	8238929	8/7/2012	In Force	RSSI ESTIMATION IN MULTI-TECHNOLOGY COMMUNICATION TERMI
MP3465	MP3465C1	US	13/547,071	7/12/2012	8588768	11/19/2013	In Force	RSSI ESTIMATION IN MULTI-TECHNOLOGY COMMUNICATION TERMI
MP3465	MP3465CN	CN	2010105798713	12/2/2010	ZL2010105798	11/25/2015	In Force	RSSI ESTIMATION IN MULTI-TECHNOLOGY COMMUNICATION TERMI
MP3465E	MP3465EP	EP	EP10190183.3	11/5/2010	EP2330761	2/18/2015	In Force	RSSI Estimation in Multi-Technology Communication Terminals
MP3465E	MP3465EPDE	DE	10190183.3	11/5/2010	602010022285	2/18/2015	In Force	RSSI ESTIMATION IN MULTI-TECHNOLOGY COMMUNICATION TERMI
MP3465E	MP3465EPRR	FR	10190183.3	11/5/2010	2330761	2/18/2015	In Force	RSSI ESTIMATION IN MULTI-TECHNOLOGY COMMUNICATION TERMI
MP3465E	MP3465EPPG8	G8	10190183.3	11/5/2010	2330761	2/18/2015	In Force	RSSI ESTIMATION IN MULTI-TECHNOLOGY COMMUNICATION TERMI
MP3467	MP3467	US	12/950,733	11/19/2010	8789241	7/1/2014	In Force	VIRTUALIZATION OF NON-VOLATILE MEMORY AND HARD DISK DRIVE
MP3467	MP3467C1	US	14/314,446	6/25/2014	9164895	10/20/2015	In Force	VIRTUALIZATION OF SOLID STATE DRIVE AND MASS STORAGE DRIVE
MP3467T	MP3467TW	TW	99142053	12/3/2010	1507869	11/11/2015	In Force	SYSTEM, APPARATUS, AND METHOD FOR VIRTUALIZING STORAGE DE
MP3467W	MP3467W/D1	JP	2015-143318	7/17/2015	6253614	12/8/2017	In Force	VIRTUALIZATION OF STORAGE DEVICES
MP3467W	MP3467WO	WO	PCT/US2010/05	11/19/2010			Pending	VIRTUALIZATION OF STORAGE DEVICES
MP3467W	MP3467WOJP	JP	2012-542078	11/19/2010	5984118	8/12/2016	In Force	VIRTUALIZATION OF STORAGE DEVICES
MP3470	MP3470	US	12/963,347	12/8/2010	8264392	9/11/2012	In Force	COMPACT HIGH-SPEED ANALOG-TO-DIGITAL CONVERTER FOR BOTH
MP3470	MP3470D1	US	13/608,832	9/10/2012	8994571	3/31/2015	In Force	COMPACT HIGH-SPEED ANALOG-TO-DIGITAL CONVERTER FOR BOTH
MP3471	MP3471	US	12/964,584	12/9/2010	8400968	3/19/2013	In Force	FRAME PADDING FOR WIRELESS COMMUNICATIONS
MP3471	MP3471C1	US	13/830,672	3/14/2013	9237597	1/12/2016	In Force	FRAME PADDING FOR WIRELESS COMMUNICATIONS
MP3471	MP3471C1C1	US	14/989,754	1/6/2016	9521581	12/13/2016	In Force	FRAME PADDING FOR WIRELESS COMMUNICATIONS

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MP3471W	MP3471WO	WO	PCT/US2010/05	12/9/2010			Pending	FRAME PADDING FOR WIRELESS COMMUNICATIONS
MP3471W	MP3471WOCN	CN	201080056043	12/9/2010	ZL2010800560	11/25/2015	In Force	FRAME PADDING FOR WIRELESS COMMUNICATIONS
MP3471W	MP3471WOEP	EP	EP10836712.9	12/9/2010	2510660	5/8/2019	In Force	Frame Padding for Wireless Communications
MP3471W	MP3471WOEPDE	DE	EP10836712.9	12/9/2010	602010058832	5/8/2019	In Force	Frame Padding for Wireless Communications
MP3471W	MP3471WOEPFR	FR	EP10836712.9	12/9/2010	2510660	5/8/2019	In Force	Frame Padding for Wireless Communications
MP3471W	MP3471WOEPGB	GB	EP10836712.9	12/9/2010	2510660	5/8/2019	In Force	Frame Padding for Wireless Communications
MP3471W	MP3471WQJP	JP	2012-543291	12/9/2010	5577548	7/13/2014	In Force	FRAME PADDING FOR WIRELESS COMMUNICATIONS
MP3471W	MP3471WOKR	KR	10-2012-701495	12/9/2010	10-1837706	3/13/2018	In Force	FRAME PADDING FOR WIRELESS COMMUNICATIONS
MP3472	MP3472	US	12/963,053	12/8/2010	8886755	11/11/2014	In Force	METHOD AND APPARATUS FOR FACILITATING SIMULTANEOUS TRAN
MP3472	MP3472C1	US	14/537,388	11/10/2014	9226294	12/29/2015	In Force	METHOD AND APPARATUS FOR FACILITATING SIMULTANEOUS TRAN
MP3472	MP3472C1C1	US	14/981,289	12/28/2015	9456446	9/27/2016	In Force	METHOD AND APPARATUS FOR FACILITATING SIMULTANEOUS TRAN
MP3472	MP3472C1C1C1	US	15/275,952	9/26/2016	9844076	12/12/2017	In Force	METHOD AND APPARATUS FOR FACILITATING SIMULTANEOUS TRAN
MP3473	MP3473	US	12/961,422	12/6/2010	8339195	12/25/2012	In Force	CIRCUITS AND METHODS FOR CALIBRATING OFFSET IN AN AMPLIFIER
MP3473	MP3473C1	US	13/723,401	12/21/2012	8933749	1/13/2015	In Force	CIRCUITS AND METHODS FOR CALIBRATING OFFSET IN AN AMPLIFIER
MP3473W	MP3473WO	WO	PCT/US2010/05	12/6/2010			Pending	CIRCUITS AND METHODS FOR CALIBRATING OFFSET IN AN AMPLIFIER
MP3473W	MP3473WOCN	CN	2010800456527	12/6/2010	ZL2010800456	11/30/2016	In Force	CIRCUITS AND METHODS FOR CALIBRATING OFFSET IN AN AMPLIFIER
MP3474W	MP3474WO	WO	PCT/US2010/05	12/6/2010			Pending	CIRCUITS AND METHODS FOR CALIBRATING OFFSET IN AN AMPLIFIER
MP3475	MP3475	US	12/959,891	12/3/2010	8539385	10/1/2013	In Force	SOFT DECODING FOR QUANTIZED CHANNEL
MP3475	MP3475C1	US	14/033,843	9/23/2013	8910023	12/9/2014	In Force	SOFT DECODING FOR QUANTIZED CHANNEL
MP3475	MP3475CN	CN	2010105937506	12/15/2010	ZL2010105937	9/24/2014	In Force	SOFT DECODING FOR QUANTIZED CHANNEL
MP3479	MP3479	US	12/964,241	12/9/2010	8416659	4/9/2013	In Force	METHOD AND APPARATUS TO CORRECT DISTORTION
MP3480	MP3480	US	13/109,315	5/17/2011	8354887	1/15/2013	In Force	Charge Compensation for Operational Transconductance Amplifier B
MP3481	MP3481	US	13/047,313	3/14/2011	8471625	6/25/2013	In Force	Beta Enhanced Voltage Reference Circuit
MP3482	MP3482	US	13/109,320	5/17/2011	8451160	5/28/2013	In Force	Low Power High Speed Pipeline ADC
MP3483	MP3483	US	12/965,878	12/12/2010	8761289	6/24/2014	In Force	MIMO FEEDBACK SCHEMES FOR CROSS-POLARIZED ANTENNAS
MP3483W	MP3483WOCN	CN	2010800576523	12/12/2010	ZL2010800576	7/1/2015	In Force	MIMO FEEDBACK SCHEMES FOR CROSS-POLARIZED ANTENNAS
MP3483W	MP3483WOEP	EP	EP10837147.7	12/12/2010	2514181	10/3/2018	In Force	MIMO Feedback Schemes for Cross-Polarized Antennas
MP3483W	MP3483WOEPDE	DE	EP10837147.7	12/12/2010	602010054115	10/3/2018	In Force	MIMO Feedback Schemes for Cross-Polarized Antennas
MP3483W	MP3483WOEPFR	FR	EP10837147.7	12/12/2010	2514181	10/3/2018	In Force	MIMO Feedback Schemes for Cross-Polarized Antennas
MP3483W	MP3483WOEPGB	GB	EP10837147.7	12/12/2010	2514181	10/3/2018	In Force	MIMO Feedback Schemes for Cross-Polarized Antennas
MP3483W	MP3483WQJP	JP	2012-543960	12/12/2010	5637486	10/31/2014	In Force	MIMO FEEDBACK SCHEMES FOR CROSS-POLARIZED ANTENNAS
MP3485	MP3485	US	12/970,555	12/16/2010	8618785	12/31/2013	In Force	Start-Up Supply
MP3485	MP3485C1	US	14/143,502	12/30/2013	9287707	3/15/2016	In Force	System And Method For Providing Power To Circuits Until Power Sup
MP3485T	MP3485TW	TW	99145324	12/22/2010	1505613	10/21/2015	In Force	START-UP SUPPLY

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MP3485W	MP3485WO	WO	PCT/JP2010/003	12/16/2010			Pending	Start-Up Supply
MP3485W	MP3485WOCN	CN	2010800499842	12/16/2010	ZL2010800499	12/2/2015	In Force	START-UP SUPPLY
MP3485W	MP3485WCEP	EP	10838782.0	12/16/2010	2517341	6/26/2019	In Force	START-UP SUPPLY FOR A SWITCH MODE POWER SUPPLY
MP3485W	MP3485WCEPDE	DE	10838782.0	12/16/2010	602010059752	6/26/2019	In Force	START-UP SUPPLY FOR A SWITCH MODE POWER SUPPLY
MP3485W	MP3485WCEPFR	FR	10838782.0	12/16/2010	2517341	6/26/2019	In Force	START-UP SUPPLY FOR A SWITCH MODE POWER SUPPLY
MP3485W	MP3485WCEPGB	GB	10838782.0	12/16/2010	2517341	6/26/2019	In Force	START-UP SUPPLY FOR A SWITCH MODE POWER SUPPLY
MP3485W	MP3485WCEPROR	WO	PCT/US2010/06	12/16/2010			Pending	START-UP SUPPLY
MP3485W	MP3485WOCJP	JP	2012-545472	12/16/2010	5822209	10/16/2015	In Force	START-UP SUPPLY
MP3486	MP3486	US	12/993,731	1/3/2011	8568294	8/13/2013	In Force	POWER AMPLIFIER WITH FEEDBACK IMPEDANCE FOR STABLE OUTPUT
MP3486	MP3486I	US	13/946,912	7/19/2013	9041467	5/26/2015	In Force	POWER AMPLIFIER WITH FEEDBACK IMPEDANCE FOR STABLE OUTPUT
MP3486F	MP3486FWCN	CN	2013800752558	7/19/2013	ZL2013800752	1/11/2019	In Force	POWER AMPLIFIER WITH FEEDBACK IMPEDANCE FOR STABLE OUTPUT
MP3486F	MP3486FWEP	EP	13844567.1	7/19/2013			Pending	Power Amplifier with Feedback Impedance for Stable Output
MP3486I	MP3486IITW	TW	102127344	7/30/2013	1601374	10/1/2017	In Force	POWER AMPLIFIER WITH FEEDBACK IMPEDANCE FOR STABLE OUTPUT
MP3486I	MP3486IWO	WO	PCT/US2013/05	7/19/2013			Pending	POWER AMPLIFIER WITH FEEDBACK IMPEDANCE FOR STABLE OUTPUT
MP3486W	MP3486WO	WO	PCT/US2011/02	1/3/2011			Pending	POWER AMPLIFIER WITH FEEDBACK IMPEDANCE FOR STABLE OUTPUT
MP3486W	MP3486WOCN	CN	2011800086454	1/3/2011	ZL2011800086	3/2/2016	In Force	POWER AMPLIFIER WITH FEEDBACK IMPEDANCE FOR STABLE OUTPUT
MP3486W	MP3486WCEP	EP	EP11717722.0	1/3/2011			Pending	Power Amplifier with Feedback Impedance for Stable Output
MP3487	MP3487C1	US	14/299,465	6/9/2014	9117734	8/25/2015	In Force	Integrated Circuit Architecture For Light Emitting Diode-Based Displays
MP3487T	MP3487TW	TW	99144449	12/17/2010	1524513	3/1/2016	In Force	Integrated Buck Power Supply Architectures For Led-Based Displays
MP3487W	MP3487WID1	JP	2015-211092	10/27/2015			Pending	Integrated Buck Power Supply Architectures For LED-Based Displays
MP3487W	MP3487WO	WO	PCT/US2010/06	12/15/2010			Pending	Integrated Buck Power Supply Architectures For LED-Based Displays
MP3487W	MP3487WOCN	CN	2010800575872	12/15/2010	ZL2010800575	7/15/2015	In Force	INTEGRATED BUCK POWER SUPPLY ARCHITECTURES FOR LED-BASED
MP3487W	MP3487WCEP	EP	EP20100801034	12/15/2010	EP2514272	11/29/2017	In Force	Integrated Buck Power Supply Architectures for LED-Based Displays
MP3487W	MP3487WOJP	JP	2012-544730	12/15/2010	5910969	4/8/2016	In Force	INTEGRATED BUCK POWER SUPPLY ARCHITECTURES FOR LED-BASED
MP3487W	MP3487WOKR	KR	1020127014764	12/15/2010	101780366	9/14/2017	In Force	Integrated Buck Power Supply Architectures For LED-Based Displays
MP3487W	MP3487WUFR	FR	10801034.9	12/15/2010	2514272	11/29/2017	In Force	INTEGRATED BUCK POWER SUPPLY ARCHITECTURES FOR LED-BASED
MP3487W	MP3487WUGB	GB	10801034.9	12/15/2010	2514272	11/29/2017	In Force	INTEGRATED BUCK POWER SUPPLY ARCHITECTURES FOR LED-BASED
MP3488	MP3488I1	US	13/495,882	6/13/2012	9678334	6/13/2017	In Force	OSCILLATING MIRROR LINE BASED IMAGE TRANSFORMATION
MP3489	MP3489	US	12/944,025	11/11/2010	8543838	9/24/2013	In Force	Cryptographic Module With Secure Processor
MP3489	MP3489C1	US	14/031,653	9/19/2013	9251380	2/2/2016	In Force	Method And Storage Device For Isolating And Preventing Access To P
MP3490	MP3490	US	12/966,324	12/13/2010	8839372	9/16/2014	In Force	Station-To-Station Security Associations In Personal Basic Service Sets
MP3490W	MP3490WO	WO	PCT/US2010/06	12/12/2010			Pending	Station-To-Station Security Associations In Personal Basic Service Sets
MP3490W	MP3490WOCN	CN	2010800621721	12/12/2010	ZL2010800621	11/25/2015	In Force	STATION-TO-STATION SECURITY ASSOCIATIONS IN PERSONAL BASIC
MP3490W	MP3490WCEP	EP	EP10801064.6	12/12/2010			Pending	Station-to-Station Security Associations In Personal Basic Service Sets

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MP3490W	MP3490W/OIP	JP	2012-546188	12/22/2010	5786233	8/7/2015	In Force	STATION-TO-STATION SECURITY ASSOCIATIONS IN PERSONAL BASIC
MP3490W	MP3490W/OKR	KR	1020127018965	12/22/2010	101717719	3/13/2017	In Force	Station-To-Station Security Associations in Personal Basic Service Sets
MP3491	MP3491	US	12/969,171	12/15/2010	8358002	1/22/2013	In Force	WINDOW BALL GRID ARRAY (BGA) SEMICONDUCTOR PACKAGES
MP3491	MP3491C1	US	13/745,491	1/18/2013	8716875	5/6/2014	In Force	WINDOW BALL GRID ARRAY (BGA) SEMICONDUCTOR PACKAGES
MP3491	MP3491C1C1	US	14/268,327	5/12/2014	9159691	10/13/2015	In Force	WINDOW BALL GRID ARRAY (BGA) SEMICONDUCTOR PACKAGES
MP3491	MP3491CN	CN	2010106061528	12/23/2010	212010106081	11/25/2015	In Force	WINDOW BALL GRID ARRAY (BGA) SEMICONDUCTOR PACKAGES
MP3492	MP3492	US	12/961,537	12/17/2010	8312188	11/13/2012	In Force	Systems And Methods For Dynamic Buffer Allocation
MP3492	MP3492C1	US	13/674,454	11/12/2012	8782307	7/15/2014	In Force	Systems And Methods For Dynamic Buffer Allocation
MP3493	MP3493C1	US	14/285,700	5/23/2014	9195295	11/24/2015	In Force	AUDIO CODEC WITH AUDIO BUFFER TO STORED DECODED AUDIO D
MP3494	MP3494	US	12/987,669	12/14/2010	8504887	8/6/2013	In Force	LOW POWER LDPC DECODING UNDER DEFECTS/ERASURES/PUNCTU
MP3500	MP3500	US	12/951,518	11/22/2010	8755315	6/17/2014	In Force	Packet Classification Based Power Saving Receiver
MP3500	MP3500C1	US	14/303,959	6/13/2014	9100910	8/4/2015	In Force	Packet Classification Based Power Saving Receiver
MP3505	MP3505	US	12/973,298	12/20/2010	8525478	9/3/2013	In Force	Power Management Circuit Of Rechargeable Battery Stack
MP3505	MP3505C1	US	14/016,567	9/3/2013	8937457	1/20/2015	In Force	Power Management Circuit Of Rechargeable Battery Stack
MP3505	MP3505CN	CN	2011100064501	1/5/2011	212011100064	4/29/2015	In Force	POWER MANAGEMENT CIRCUIT OF RECHARGEABLE BATTERY STACK
MP3505	MP3505CND1	CN	2014103484904	1/5/2011	212014103484	4/10/2018	In Force	POWER MANAGEMENT CIRCUIT OF RECHARGEABLE BATTERY STACK
MP3506	MP3506	US	12/987,419	1/10/2011	8751912	6/10/2014	In Force	A Layered Low Density Parity Check Decoder
MP3506	MP3506C1	US	14/299,127	6/9/2014	9490844	11/8/2016	In Force	Syndrome Computation in a Layered Low Density Parity Check Decod
MP3514E	MP3514EP	EP	11150610.1	1/11/2011	NCR		Pending	ANALYTICAL COMPUTATION OF CUBIC METRIC
MP3515	MP3515	US	13/017,448	1/31/2011	8787163	7/22/2014	In Force	Method and Apparatus for Adjusting the Size of a Buffer in a Network
MP3518	MP3518CN	CN	2011100081051	1/11/2011			Pending	ATTACHING PASSIVE COMPONENTS TO A SEMICONDUCTOR PACKAG
MP3518	MP3518D1	US	14/665,321	3/23/2015	9171744	10/27/2015	In Force	ATTACHING PASSIVE COMPONENTS TO A SEMICONDUCTOR PACKAG
MP3518T	MP3518TW	TW	100101079	1/12/2011	1442486	6/21/2014	In Force	ATTACHING PASSIVE COMPONENTS TO A SEMICONDUCTOR PACKAG
MP3519	MP3519	US	13/004,659	1/11/2011	8731029	5/20/2014	In Force	FREQUENCY OFFSET ACQUISITION FOR WIRELESS COMMUNICATION
MP3519	MP3519C1	US	14/281,012	5/19/2014	9356648	5/31/2016	In Force	FREQUENCY OFFSET ACQUISITION FOR WIRELESS COMMUNICATION
MP3521	MP3521	US	13/005,850	1/13/2011	8893143	11/18/2014	In Force	HARDWARE VIRTUALIZATION FOR MEDIA PROCESSING
MP3521	MP3521C1	US	14/543,007	11/17/2014	9189267	11/17/2015	In Force	HARDWARE VIRTUALIZATION FOR MEDIA PROCESSING
MP3525	MP3525	US	12/956,427	11/30/2010	8819161	8/26/2014	In Force	Auto-Synchronization And Time-Of-Day Synchronization For Master-Sla
MP3526	MP3526C1	US	14/678,304	4/3/2015	9275929	3/1/2016	In Force	PACKAGE ASSEMBLY HAVING A SEMICONDUCTOR SUBSTRATE
MP3526F	MP3526FWCN	CN	2012800608669	10/10/2012			Pending	PACKAGE ASSEMBLY INCLUDING A SEMICONDUCTOR SUBSTRATE WI
MP3526F	MP3526FWKR	KR	10-2014-701216	10/10/2012			Pending	PACKAGE ASSEMBLY INCLUDING A SEMICONDUCTOR SUBSTRATE WI
MP3526I	MP3526ITW	TW	101137543	10/11/2012			Pending	PACKAGE ASSEMBLY INCLUDING A SEMICONDUCTOR SUBSTRATE WI
MP3526I	MP3526IWO	WO	PCT/US2012/05	10/10/2012			Pending	PACKAGE ASSEMBLY INCLUDING A SEMICONDUCTOR SUBSTRATE WI
MP3526T	MP3526TW	TW	100101828	1/18/2011	1521616	2/11/2016	In Force	PACKAGE ASSEMBLY HAVING A SEMICONDUCTOR SUBSTRATE

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MP3526W	MP3526WO	WO	PCT/US2010/06	12/21/2010			Pending	PACKAGE ASSEMBLY HAVING A SEMICONDUCTOR SUBSTRATE
MP3526W	MP3526WOCN	CN	201080061743	12/21/2010	ZL2010800617	11/30/2016	In Force	PACKAGE ASSEMBLY HAVING A SEMICONDUCTOR SUBSTRATE
MP3526W	MP3526WOKR	KR	10-2012-702163	12/21/2010			Pending	PACKAGE ASSEMBLY HAVING A SEMICONDUCTOR SUBSTRATE
MP3531	MP3531	US	13/008,917	1/19/2011	8831653	9/9/2014	In Force	METHODS AND SYSTEMS FOR CONTROL-CHANNEL RESOURCE ALLOC
MP3531W	MP3531WOCN	CN	2011800041071	1/19/2011	ZL2011800041	7/1/2015	In Force	MULTI-USER COMPRESSION SCHEMES
MP3532	MP3532	US	13/010,102	1/20/2011	9159355	10/13/2015	In Force	DVD-RAM Header or Land/Groove Detection
MP3533	MP3533	US	12/978,718	12/27/2010	8811464	8/19/2014	In Force	Method & Apparatus for Adaptively Determining Equalizer Settings
MP3534	MP3534	US	13/028,631	2/16/2011	8621185	12/31/2013	In Force	Processor Load Determination And Speed Control
MP3535	MP3535	US	13/008,958	1/19/2011	8351258	1/8/2013	In Force	Adapting Read Reference Voltage in Flash Memory Device
MP3535	MP3535C1	US	13/735,256	1/7/2013	8659942	2/25/2014	In Force	Adapting Read Reference Voltage in Flash Memory Device
MP3535	MP3535C1C1	US	14/179,919	2/13/2014	9142312	9/22/2015	In Force	Adapting Read Reference Voltage in Flash Memory Device
MP3536	MP3536C1	US	14/338,908	7/23/2014	9425721	8/23/2016	In Force	Systems And Methods For Adaptive Motor Speed Control
MP3536	MP3536CN	CN	201110032753	1/27/2011	ZL2011100327	1/21/2015	In Force	SYSTEMS AND METHODS FOR ADAPTIVE TORQUE ADJUSTMENT AND
MP3537	MP3537	US	12/986,529	1/7/2011	8415785	4/9/2013	In Force	METAL RING TECHNIQUES AND CONFIGURATIONS
MP3538	MP3538	US	13/014,611	1/26/2011	9712459	7/18/2017	In Force	Low-to-High Speed Cut-Through Communication
MP3538	MP3538C1	US	15/612,682	6/2/2017	9843537	12/12/2017	In Force	Low-to-High Speed Cut-Through Communication
MP3540	MP3540	US	13/004,397	1/11/2011	8372692	2/12/2013	In Force	METHOD OF STACKING FLIP-CHIP ON WIRE-BONDED CHIP
MP3540	MP3540C1	US	13/763,439	2/8/2013	8624377	1/7/2014	In Force	METHOD OF STACKING FLIP-CHIP ON WIRE-BONDED CHIP
MP3540	MP3540CN	CN	201110031396	1/26/2011			Pending	METHOD OF STACKING FLIP-CHIP ON WIRE-BONDED CHIP
MP3540T	MP3540TW	TW	100102657	1/25/2011	1446466	7/21/2014	In Force	METHOD OF STACKING FLIP-CHIP ON WIRE-BONDED CHIP
MP3545	MP3545	US	13/018,449	2/1/2011	8638883	1/28/2014	In Force	DC OFFSET CANCELLATION IN DIRECT CONVERSION RECEIVERS
MP3545	MP3545C1	US	14/138,074	12/22/2013	8831143	9/9/2014	In Force	DC OFFSET CANCELLATION IN DIRECT CONVERSION RECEIVERS
MP3545	MP3545CN	CN	201110036091	2/9/2011	ZL2011100360	9/2/2015	In Force	DC OFFSET CANCELLATION IN DIRECT CONVERSION RECEIVERS
MP3545E	MP3545EP	EP	EP11152977.2	2/2/2011	2355361	10/10/2018	In Force	DC Offset Cancellation in Direct Conversion Receivers
MP3545E	MP3545EPDE	DE	EP11152977.2	2/2/2011	2355361	10/10/2018	In Force	DC Offset Cancellation in Direct Conversion Receivers
MP3545E	MP3545EPFR	FR	EP11152977.2	2/2/2011	2355361	10/10/2018	In Force	DC Offset Cancellation in Direct Conversion Receivers
MP3545E	MP3545EPGB	GB	EP11152977.2	2/2/2011	2355361	10/10/2018	In Force	DC Offset Cancellation in Direct Conversion Receivers
MP3546	MP3546	US	13/016,603	1/28/2011	9084144	7/14/2015	In Force	POWER SAVING FEATURES IN A COMMUNICATION DEVICE
MP3546W	MP3546WO	WO	PCT/US2011/02	1/28/2011			Pending	POWER SAVING FEATURES IN A COMMUNICATION DEVICE
MP3546W	MP3546WOCN	CN	2011800070201	1/28/2011	ZL2011800070	8/3/2016	In Force	POWER SAVING FEATURES IN A COMMUNICATION DEVICE
MP3546W	MP3546WOCF	EP	EP20110705731	1/28/2011	EP2531986	6/1/2016	In Force	Power Saving Features in a Communication Device
MP3546W	MP3546WOCFDE	DE	11705731.5	1/28/2011	2531986	6/1/2016	In Force	POWER SAVING FEATURES IN A COMMUNICATION DEVICE
MP3546W	MP3546WOCFPR	FR	11705731.5	1/28/2011	2531986	6/1/2016	In Force	POWER SAVING FEATURES IN A COMMUNICATION DEVICE
MP3546W	MP3546WOCFGB	GB	11705731.5	1/28/2011	2531986	6/1/2016	In Force	POWER SAVING FEATURES IN A COMMUNICATION DEVICE

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MP3546W	MP3546W0JP	JP	2012-551356	1/28/2011	5783575	7/31/2015	In Force	POWER SAVING FEATURES IN A COMMUNICATION DEVICE
MP3546W	MP3546W0KR	KR	10-2012-702020	1/28/2011	10-1750966	6/26/2017	In Force	POWER SAVING FEATURES IN A COMMUNICATION DEVICE
MP3547	MP3547	US	13/012,644	1/24/2011	9257410	2/9/2016	In Force	PACKAGE ASSEMBLY INCLUDING A SEMICONDUCTOR SUBSTRATE IN
MP3547	MP3547D1	US	15/017,397	2/5/2016	9768144	9/19/2017	In Force	PACKAGE ASSEMBLY INCLUDING A SEMICONDUCTOR SUBSTRATE IN
MP3547T	MP3547TW	TW	100103446	1/28/2011	1441285	6/11/2014	In Force	RECESSED SEMICONDUCTOR SUBSTRATES)
MP3547W	MP3547WO	WO	PCT/US2011/02	1/25/2011			Pending	RECESSED SEMICONDUCTOR SUBSTRATES
MP3547W	MP3547W0CN	CN	2011800053817	1/25/2011	ZL2011800053	3/4/2015	In Force	RECESSED SEMICONDUCTOR SUBSTRATES
MP3547W	MP3547W0KR	KR	10-2012-701581	1/25/2011	101830904	2/22/2018	In Force	Recessed Semiconductor Substrates
MP3548	MP3548	US	13/009,132	1/19/2011	9380401	6/28/2016	In Force	Signaling Schemes Allowing Discovery Of Network Devices Capable Of
MP3549W	MP3549WO	WO	PCT/US2011/02	3/2/2011			Pending	DISTRIBUTED METADATA CACHE
MP3549W	MP3549W0CN	CN	201180043779.1	3/2/2011			Pending	DISTRIBUTED METADATA CACHE
MP3549W	MP3549W0EP	EP	11707975.6	3/2/2011	2617177	12/30/2015	In Force	Distributed Metadata Cache
MP3549W	MP3549W0JP	JP	2013-528189	3/2/2011	5824519	11/25/2015	In Force	Distributed Metadata Cache
MP3552	MP3552	US	13/020,018	2/3/2011	8559567	10/15/2013	In Force	CHANNEL ESTIMATION USING REDUCED-COMPLEXITY CASCADED ON
MP3552	MP3552C1	US	14/027,248	9/16/2013	9014308	4/21/2015	In Force	CHANNEL ESTIMATION USING CASCADED ONE-DIMENSIONAL FILTERI
MP3555	MP3555	US	13/020,270	2/3/2011	8705552	4/22/2014	In Force	Controlling Latency Variations In A Packet Node
MP3555	MP3555C1	US	14/258,318	4/22/2014	8995458	3/31/2015	In Force	Method And Apparatus For Delay Jitter Reduction In Networking Devi
MP3556	MP3556	US	13/023,555	2/9/2011	8611448	12/17/2013	In Force	CODEBOOK ADAPTATION IN MIMO COMMUNICATION SYSTEMS USI
MP3556	MP3556C1	US	14/106,774	12/15/2013	8761297	6/24/2014	In Force	CODEBOOK ADAPTATION IN MIMO COMMUNICATION SYSTEMS USI
MP3556	MP3556CN	CN	201110093894.1	2/10/2011			Pending	CODEBOOK ADAPTATION IN MIMO COMMUNICATION SYSTEMS USI
MP3556E	MP3556EP	EP	EP11154602.7	2/10/2011	2360853	5/30/2018	In Force	Codebook Adaptation In MIMO Communication Systems Using Multit
MP3556E	MP3556EPDE	DE	EP11154602.7	2/10/2011	2360853	5/30/2018	In Force	Codebook Adaptation In MIMO Communication Systems Using Multit
MP3556E	MP3556EPRR	FR	EP11154602.7	2/10/2011	2360853	5/30/2018	In Force	Codebook Adaptation In MIMO Communication Systems Using Multit
MP3556E	MP3556EPGB	GB	EP11154602.7	2/10/2011	2360853	5/30/2018	In Force	Codebook Adaptation In MIMO Communication Systems Using Multit
MP3556J	MP3556JP	JP	2011-024295	2/7/2011	5258002	5/2/2013	In Force	APPARATUS, MOBILE COMMUNICATION TERMINAL, CHIP SET and M
MP3557	MP3557	US	12/949,693	11/18/2010	8508879	8/13/2013	In Force	WRITE CLOCK REPHASE FOR MAGNETIC RECORDING DEVICE
MP3557	MP3557C1	US	13/963,849	8/9/2013	8755140	6/17/2014	In Force	WRITE CLOCK REPHASE FOR MAGNETIC RECORDING DEVICE
MP3557	MP3557C1C1	US	14/298,574	6/6/2014	8988808	3/24/2015	In Force	WRITE CLOCK REPHASE FOR MAGNETIC RECORDING DEVICE
MP3558	MP3558	US	12/949,681	11/18/2010	8508878	8/13/2013	In Force	WRITE CLOCK PHASE TRACKING FOR MAGNETIC RECORDING DEVICE
MP3558	MP3558C1	US	13/963,800	8/9/2013	8760785	6/24/2014	In Force	WRITE CLOCK PHASE TRACKING FOR MAGNETIC RECORDING DEVICE
MP3558	MP3558C1C1	US	14/311,644	6/23/2014	9013819	4/21/2015	In Force	WRITE CLOCK PHASE TRACKING FOR MAGNETIC RECORDING DEVICE
MP3559	MP3559	US	13/025,081	2/10/2011	8532221	9/10/2013	In Force	TRANSMISSION PROTECTION FOR WIRELESS COMMUNICATIONS
MP3559	MP3559C1	US	14/022,064	9/9/2013	8995564	3/31/2015	In Force	TRANSMISSION PROTECTION FOR WIRELESS COMMUNICATIONS
MP3559	MP3559C1C1	US	14/673,216	3/30/2015	9661647	5/23/2017	In Force	TRANSMISSION PROTECTION FOR WIRELESS COMMUNICATIONS

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MP3559W	MP3559W/D1	JP	2015-039860	3/2/2015	5861952	1/8/2016	In Force	TRANSMISSION PROTECTION FOR WIRELESS COMMUNICATIONS
MP3559W	MP3559W/O	WO	PCT/US2011/02	2/10/2011			Pending	TRANSMISSION PROTECTION FOR WIRELESS COMMUNICATIONS
MP3559W	MP3559W/CN	CN	2011800137475	2/10/2011	ZL2011800137	6/10/2015	In Force	TRANSMISSION PROTECTION FOR WIRELESS COMMUNICATIONS
MP3559W	MP3559W/CEP	EP	EP20110742815	2/10/2011			Pending	Transmission Protection for Wireless Communications
MP3559W	MP3559W/OJP	JP	2012-553005	2/10/2011	5709323	3/13/2015	In Force	TRANSMISSION PROTECTION FOR WIRELESS COMMUNICATIONS
MP3559W	MP3559W/KR	KR	10-2012-702366	2/10/2011	10-1760073	7/14/2017	In Force	TRANSMISSION PROTECTION FOR WIRELESS COMMUNICATIONS
MP3561	MP3561	US	13/023,379	2/8/2011	9497807	11/15/2016	In Force	DIMMER CIRCUIT FOR ELECTRONIC LOADS
MP3561W	MP3561W/O	WO	PCT/US2011/02	2/8/2011			Pending	DIMMER CIRCUIT FOR ELECTRONIC LOADS
MP3561W	MP3561W/CEP	EP	11742694.0	2/8/2011	EP2534928	4/12/2017	In Force	Dimmer Circuit for Electronic Loads
MP3561W	MP3561W/KR	KR	10-2012-702115	2/8/2011			Pending	DIMMER CIRCUIT FOR ELECTRONIC LOADS
MP3561W	MP3561W/DE	DE	11742694.0	2/8/2011	602011036901	4/12/2017	In Force	Dimmer Circuit for Electronic Loads
MP3562	MP3562	US	13/013,274	1/25/2011	9582443	2/28/2017	In Force	Serial Control Channel Processor for Executing Time-based Instructions
MP3565	MP3565	US	13/017,430	1/31/2011	8363478	1/29/2013	In Force	GROUP BASED READ REFERENCE VOLTAGE MANAGEMENT IN FLASH
MP3565	MP3565C1	US	13/746,707	1/22/2013	8675414	3/18/2014	In Force	GROUP BASED READ REFERENCE VOLTAGE MANAGEMENT IN FLASH
MP3565	MP3565C1C1	US	14/191,589	2/27/2014	8971122	3/3/2015	In Force	GROUP BASED READ REFERENCE VOLTAGE MANAGEMENT IN FLASH
MP3566	MP3566	US	13/023,905	2/9/2011	8549214	10/1/2013	In Force	Protection Against Data Corruption For Multi-Level Memory Cell (MLC)
MP3566	MP3566C1	US	14/039,585	9/27/2013	8751735	6/10/2014	In Force	Protection Against Data Corruption For Multi-Level Memory Cell (MLC)
MP3566W	MP3566W/O	WO	PCT/US2011/02	2/16/2011			Pending	Protection Against Data Corruption for Multi-Level Memory Cell (MLC)
MP3566W	MP3566W/CN	CN	201180099543	2/16/2011	ZL2011800099	9/7/2016	In Force	PROTECTION AGAINST DATA CORRUPTION FOR MULTI-LEVEL MEMORY
MP3568	MP3568	US	13/026,938	2/14/2011	8582332	11/12/2013	In Force	Dual Output DC-DC Charge Pump Regulator
MP3568	MP3568C1	US	14/077,928	11/12/2013	9350234	5/24/2016	In Force	Voltage Regulator And Method For Regulating Dual Output Voltages
MP3568W	MP3568W/O	WO	PCT/US2011/02	2/14/2011			Pending	DUAL OUTPUT DIRECT CURRENT (DC) DC REGULATOR
MP3570	MP3570	US	13/033,569	2/23/2011	8539162	9/17/2013	In Force	CACHING BASED ON SPATIAL DISTRIBUTION OF ACCESSES TO DATA S
MP3570	MP3570C1	US	14/028,085	9/16/2013	8812790	8/19/2014	In Force	CACHING BASED ON SPATIAL DISTRIBUTION OF ACCESSES TO DATA S
MP3570W	MP3570W/O	WO	PCT/US2011/02	2/23/2011			Pending	CACHING BASED ON SPATIAL DISTRIBUTION OF ACCESSES TO DATA S
MP3570W	MP3570W/CN	CN	2011800181425	2/23/2011	ZL2011800181	1/13/2016	In Force	CACHING BASED ON SPATIAL DISTRIBUTION OF ACCESSES TO DATA S
MP3570W	MP3570W/CEP	EP	11706135.8	2/23/2011	2539821	8/28/2019	In Force	Caching Based on Spatial Distribution of Accesses to Data Storage De
MP3570W	MP3570W/CEP/DE	DE	11706135.8	2/23/2011	603011061581	8/28/2019	In Force	Caching Based on Spatial Distribution of Accesses to Data Storage De
MP3570W	MP3570W/CEP/FR	FR	11706135.8	2/23/2011	2539821	8/28/2019	In Force	Caching Based on Spatial Distribution of Accesses to Data Storage De
MP3570W	MP3570W/CEP/GB	GB	11706135.8	2/23/2011	2539821	8/28/2019	In Force	Caching Based on Spatial Distribution of Accesses to Data Storage De
MP3570W	MP3570W/OJP	JP	2012-555121	2/23/2011	5700262	2/27/2015	In Force	CACHING BASED ON SPATIAL DISTRIBUTION OF ACCESSES TO DATA S
MP3570W	MP3570W/KR	KR	10-2012-702506	2/23/2011	10-1779992	9/13/2017	In Force	CACHING BASED ON SPATIAL DISTRIBUTION OF ACCESSES TO DATA S
MP3572	MP3572	US	13/034,409	2/24/2011	8811203	8/19/2014	In Force	METHODS AND APPARATUS FOR DETERMINING A COMPOSITE CHAN
MP3572	MP3572C1	US	13/034,421	2/24/2011	8923118	12/30/2014	In Force	METHODS AND APPARATUS FOR CLEAR CHANNEL ASSESSMENT

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MP3573	MP3573	US	13/086,767	4/14/2011	8719621	5/6/2014	In Force	Solid-State Disk Cache Assisted Redundant Array Of Independent Disk
MP3573	MP3573C1	US	14/269,535	5/5/2014	9081716	7/14/2015	In Force	Solid-State Disk Cache-Assisted Redundant Array Of Independent Dis
MP3574	MP3574	US	13/091,491	4/21/2011	9201762	12/1/2015	In Force	Processor Implemented Systems and Methods for Reversible Debug
MP3579W	MP3579WO	WO	PCT/US2011/03	5/5/2011			Pending	Caching Storage Adapter Architecture
MP3579W	MP3579WOCN	CN	2011800224312	5/5/2011			Pending	CACHING STORAGE ADAPTER ARCHITECTURE
MP3579W	MP3579WOEP	EP	EP20110732532	5/5/2011			Pending	Caching Storage Adapter Architecture
MP3579W	MP3579WOJP	JP	2013-509270	5/5/2011			Pending	CACHING STORAGE ADAPTER ARCHITECTURE
MP3581	MP3581	US	13/038,401	3/2/2011	8595402	11/26/2013	In Force	DYNAMIC ARBITRATION SCHEMES FOR MULTI-MASTER MEMORY SY
MP3582	MP3582	US	13/038,778	3/2/2011	8483645	7/9/2013	In Force	Dual Carrier Amplifier Circuits And Methods
MP3582	MP3582C1	US	13/936,400	7/8/2013	9042859	5/26/2015	In Force	Dual Carrier Amplifier Circuits And Methods
MP3582T	MP3582TW	TW	100107235	3/2/2011	1472149	2/1/2015	In Force	Dual Carrier Amplifier Circuits And Methods
MP3582W	MP3582WO	WO	PCT/US2011/02	3/2/2011			Pending	Dual Carrier Amplifier Circuits And Methods
MP3582W	MP3582WOCN	CN	2011800119689	3/2/2011	Z12011800119	7/15/2015	In Force	DUAL CARRIER AMPLIFIER CIRCUITS AND METHODS
MP3584	MP3584C1	US	14/093,587	12/2/2013	9436210	9/6/2016	In Force	CONTROL_MECHANISM FOR SELECTIVELY SHORTING CLOCK GRID BY
MP3585	MP3585	US	13/045,186	3/10/2011	8688922	4/1/2014	In Force	Hardware-Supported Memory Management
MP3586	MP3586C1	US	14/149,090	1/7/2014	9157962	10/13/2015	In Force	Apparatus And Method For Testing A Scan Chain Including Modifying
MP3587	MP3587	US	13/044,183	3/9/2011	8378750	2/19/2013	In Force	Class AB Amplifiers
MP3587	MP3587C1	US	13/769,700	2/8/2013	8736374	5/27/2014	In Force	Class AB Amplifiers
MP3587Q	MP3587QADE	DE	13192544.8	3/9/2011	2736168	5/6/2015	In Force	Class AB Amplifiers
MP3587Q	MP3587QAFR	FR	13192544.8	3/9/2011	2736168	5/6/2015	In Force	Class AB Amplifiers
MP3587Q	MP3587QAGB	GB	13192544.8	3/9/2011	2736168	5/6/2015	In Force	Class AB Amplifiers
MP3587Q	MP3587QZDE	DE	14177087.5	3/9/2011	602011027197	6/1/2016	In Force	Class AB Amplifiers
MP3587Q	MP3587QZFR	FR	14177087.5	3/9/2011	2804315	6/1/2016	In Force	Class AB Amplifiers
MP3587Q	MP3587QZGB	GB	14177087.5	3/9/2011	2804315	6/1/2016	In Force	Class AB Amplifiers
MP3587W	MP3587WCD1	CN	2015101888043	3/9/2011	Z12015101888	5/29/2018	In Force	CLASS AB AMPLIFIERS
MP3587W	MP3587WKRA	KR	1020177020070	3/9/2011	101820499	1/19/2018	In Force	Class AB Amplifiers
MP3587W	MP3587WO	WO	PCT/US2011/02	3/9/2011			Pending	Class AB Amplifiers
MP3587W	MP3587WOCN	CN	2011800198286	3/9/2011	Z12011800198	1/27/2016	In Force	CLASS AB AMPLIFIERS
MP3587W	MP3587WOEP	EP	EP20110709572	3/9/2011	EP2545545	1/7/2015	In Force	Class AB Amplifiers
MP3587W	MP3587WOJP	JP	2012-557217	3/9/2011	JP5823990	10/16/2015	In Force	CLASS AB AMPLIFIERS
MP3587W	MP3587WOKR	KR	1020127026182	3/9/2011	101762300	7/21/2017	In Force	Class AB Amplifiers
MP3587W	MP3587WUAA	EP	141770875	3/9/2011	EP2804315	6/1/2016	In Force	Class AB Amplifiers
MP3587W	MP3587WUD1	EP	131925448	3/9/2011	EP2736168	5/6/2015	In Force	Class AB Amplifiers
MP3587W	MP3587WUDE	DE	11709572.9	3/9/2011	2545545	1/7/2015	In Force	Class AB Amplifiers

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MP3587W	MP3587WJFR	FR	11709572.9	3/9/2011	2545645	1/7/2015	In Force	Class AB Amplifiers
MP3587W	MP3587WJUGB	GB	11709572.9	3/9/2011	2545645	1/7/2015	In Force	Class AB Amplifiers
MP3591	MP3591	US	13/039,359	3/3/2011	8862954	10/14/2014	In Force	Integrated Circuit Scan Testing With Stop-Clock And Auto-Step Feature
MP3592	MP3592	US	12/763,027	4/19/2010	8159308	4/17/2012	In Force	LOW POWER VOLTAGE CONTROLLED OSCILLATOR (VCO)
MP3592	MP3592D1C1	US	14/263,289	4/28/2014	9190952	11/17/2015	In Force	Low-Power Voltage-Controlled Oscillator
MP3593	MP3593	US	13/044,412	3/9/2011	8644352	2/4/2014	In Force	SYSTEM AND METHOD FOR ACCURATE TIME SAMPLING IN PRESENCE
MP3593	MP3593C1	US	14/169,327	1/31/2014	9042411	5/26/2015	In Force	SYSTEM AND METHOD FOR ACCURATE TIME SAMPLING IN PRESENCE
MP3597	MP3597	US	13/049,550	3/16/2011	8338934	12/25/2012	In Force	EMBEDDED DIE WITH PROTECTIVE INTERPOSER
MP3597	MP3597CN	CN	201110068207.1	3/18/2011	ZI2011100682	7/23/2014	In Force	EMBEDDED DIE WITH PROTECTIVE INTERPOSER
MP3597J	MP3597JP	JP	2011-058010	3/16/2011	JP5327654	8/2/2013	In Force	EMBEDDED DIE WITH PROTECTIVE INTERPOSER
MP3597T	MP3597TW	TW	100109299	3/18/2011	1560824	12/1/2016	In Force	EMBEDDED DIE WITH PROTECTIVE INTERPOSER
MP3598	MP3598	US	13/038,266	3/1/2011	8327047	12/4/2012	In Force	BUFFER MANAGER AND METHODS FOR MANAGING MEMORY
MP3598	MP3598C1	US	13/692,635	12/3/2012	9769081	9/19/2017	In Force	BUFFER MANAGER AND METHODS FOR MANAGING MEMORY
MP3598	MP3598CN	CN	201110069637.1	3/17/2011	ZI2011100694	11/25/2015	In Force	A BUFFER MANAGER AND METHODS FOR MANAGING MEMORY
MP3598I	MP3598IL	IL	211607	3/7/2011	211607	7/1/2015	In Force	A BUFFER MANAGER AND METHODS FOR MANAGING MEMORY
MP3598J	MP3598JP	JP	2011-045565	3/2/2011	JP5783894	8/14/2015	In Force	A Buffer Manager and Methods for Managing Memory
MP3606	MP3606	US	13/047,056	3/14/2011	8687751	4/1/2014	In Force	Multiple-Input Multiple-Output Receivers Using Successive Interferen
MP3606	MP3606C1	US	14/226,037	3/26/2014	8873686	10/28/2014	In Force	Successive Interference Cancellation In Multiple-Input Multiple-Outp
MP3607	MP3607	US	13/077,623	3/31/2011	8855067	10/7/2014	In Force	MULTI-USER COMMUNICATION GROUP MANAGEMENT AND SIGNAL
MP3607W	MP3607WO	WO	PCT/US2011/03	4/1/2011			Pending	MULTI-USER COMMUNICATION GROUP MANAGEMENT AND SIGNAL
MP3607W	MP3607WOCN	CN	2011800177237	4/1/2011	ZI2011800177	8/26/2015	In Force	MULTI-USER COMMUNICATION GROUP MANAGEMENT AND SIGNAL
MP3607W	MP3607WCEP	EP	EP20110763533	4/1/2011			Pending	Multi-User Communication Group Management and Signaling
MP3607W	MP3607WQJP	JP	2013-502899	4/1/2011	JP5722429	4/3/2015	In Force	MULTI-USER COMMUNICATION GROUP MANAGEMENT AND SIGNAL
MP3607W	MP3607WOKR	KR	10-2012-702847	4/1/2011	10-1766849	8/3/2017	In Force	MULTI-USER COMMUNICATION GROUP MANAGEMENT AND SIGNAL
MP3608	MP3608	US	13/078,568	4/1/2011	8446108	5/21/2013	In Force	LED Controller With Compensation For Die-To-Die Variation And Tem
MP3608W	MP3608WO	WO	PCT/US2011/03	4/1/2011			Pending	LED Controller With Compensation For Die-To-Die Variation And Tem
MP3608W	MP3608WOCN	CN	2011800230953	4/1/2011	ZI2011800230	7/15/2015	In Force	LED CONTROLLER WITH COMPENSATION FOR DIE-TO-DIE VARIATION
MP3608W	MP3608WCEP	EP	EP11115631.3	4/1/2011	EP1554017	1/15/2014	In Force	LED CONTROLLER WITH COMPENSATION FOR DIE-TO-DIE VARIATION
MP3608W	MP3608WQJP	JP	2013-502895	4/1/2011	JP5842288	11/27/2015	In Force	LED CONTROLLER WITH COMPENSATION FOR DIE-TO-DIE VARIATION
MP3608W	MP3608WUDE	DE	11715632.3	4/1/2011	2554017	1/15/2014	In Force	LED CONTROLLER WITH COMPENSATION FOR DIE-TO-DIE VARIATION
MP3608W	MP3608WUFR	FR	11715632.3	4/1/2011	2554017	1/15/2014	In Force	LED CONTROLLER WITH COMPENSATION FOR DIE-TO-DIE VARIATION
MP3608W	MP3608WUGB	GB	11715632.3	4/1/2011	2554017	1/15/2014	In Force	LED CONTROLLER WITH COMPENSATION FOR DIE-TO-DIE VARIATION
MP3610	MP3610	US	13/115,813	5/25/2011	9190859	11/17/2015	In Force	Voltage Reference Buffer Using Voltage Battery Level Shifter
MP3612W	MP3612WO	WO	PCT/US2011/03	4/7/2011			Pending	NON-BINARY LDPC CODE DECODER

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP3616	MP3616	US	13/082,018	4/7/2011	8638513	1/28/2014	In Force	METHOD AND SYSTEM FOR COMPENSATING FOR ADJACENT TRACKS
MP3616	MP3616C1	US	14/133,991	12/19/2013	8861114	10/14/2014	In Force	METHOD AND SYSTEM FOR COMPENSATING FOR ADJACENT TRACKS
MP3616	MP3616I1	US	13/372,759	2/14/2012	8559508	12/3/2013	In Force	METHOD AND SYSTEM FOR COMPENSATING FOR ADJACENT TRACKS
MP3616	MP3616I1C1	US	14/060,878	10/23/2013	8879193	11/4/2014	In Force	METHOD AND SYSTEM FOR COMPENSATING FOR ADJACENT TRACKS
MP3620	MP3620	US	13/051,114	3/18/2011	8514129	8/20/2013	In Force	Buffering Techniques For Rapid Processing Of Samples Of Signals Mo
MP3623	MP3623	US	13/086,933	4/14/2011	9048790	6/2/2015	In Force	METHOD AND APPARATUS FOR POWER REDUCTION
MP3626	MP3626C1	US	14/096,274	12/4/2013	8891195	11/13/2014	In Force	METHOD AND SYSTEM FOR COMPENSATING FOR TRACK SQUEEZE
MP3627	MP3627	US	13/052,694	3/21/2011	8487811	7/16/2013	In Force	Sync Feedback for Time to First Fix
MP3627	MP3627C1C1	US	14/176,415	2/10/2014	9547087	1/17/2017	In Force	Sync Feedback for Time to First Fix
MP3628	MP3628	US	13/088,667	4/18/2011	8611352	12/17/2013	In Force	System and Method for Adapting a Packet Processing Pipeline
MP3628	MP3628C1	US	14/107,661	12/16/2013	9191315	11/17/2015	In Force	SYSTEM AND METHOD FOR ADAPTING A PACKET PROCESSING PIPELI
MP3628	MP3628C1RE	US	15/316,968	11/17/2017			Pending	SYSTEM AND METHOD FOR ADAPTING A PACKET PROCESSING PIPELI
MP3628	MP3628CN	CN	201110104289.1	4/19/2011	ZL2011101042	1/20/2016	In Force	SYSTEM AND METHOD FOR ADAPTING A PACKET PROCESSING PIPELI
MP3629	MP3629	US	12/764,837	4/21/2010	8174863	5/8/2012	In Force	CIRCUITS AND METHODS FOR MULTIPLE PROGRAMMABLE MEMORY
MP3629	MP3629C1	US	13/461,624	5/1/2012	8472233	6/25/2013	In Force	CIRCUITS AND METHODS FOR MULTIPLE PROGRAMMABLE MEMORY
MP3633W	MP3633WO	WO	PCT/US2011/03	5/2/2011			Pending	SYSTEM AND METHOD OF TUNING CURRENT FOR LEDs
MP3633W	MP3633WOCN	CN	201180021723.1	5/2/2011	ZL2011800217	12/16/2015	In Force	SYSTEM AND METHOD OF TUNING CURRENT FOR LEDs
MP3633W	MP3633WOEP	EP	11775705.4	5/2/2011			Pending	SYSTEM AND METHOD OF TUNING CURRENT FOR LEDs
MP3636	MP3636	US	13/088,724	4/18/2011	9036481	5/19/2015	In Force	METHOD AND APPARATUS FOR ADAPTIVE PACKET LOAD BALANCING
MP3637W	MP3637WO	WO	PCT/US2011/03	5/3/2011			Pending	MAGNETICALLY SHIELDED INDUCTOR STRUCTURE
MP3637W	MP3637WOCN	CN	201180022433.1	5/3/2011			Pending	MAGNETICALLY SHIELDED INDUCTOR STRUCTURE
MP3639	MP3639	US	13/101,628	5/5/2011	8773966	7/8/2014	In Force	Signal Power Measurement And Automatic Gain Control In Orthogon
MP3639	MP3639C1	US	14/319,133	6/30/2014	9094843	7/28/2015	In Force	Method And Apparatus For Adjusting A Gain Of A Receiver In A Wirel
MP3645	MP3645	US	13/101,439	5/5/2011	8630155	1/14/2014	In Force	METHOD AND APPARATUS FOR OFFSET AND GAIN CORRECTION
MP3645	MP3645C1	US	14/153,765	1/13/2014	9061631	4/7/2015	In Force	METHOD AND APPARATUS FOR OFFSET AND GAIN CORRECTION
MP3645	MP3645I1	US	13/586,487	8/28/2012	8687471	4/1/2014	In Force	METHOD AND APPARATUS FOR OFFSET AND GAIN CORRECTION
MP3645I	MP3645I1WC	CN	201280051618.1	8/28/2012	ZL2012800516	5/3/2017	In Force	METHOD AND APPARATUS FOR OFFSET AND GAIN CORRECTION
MP3645I	MP3645I1WO	WO	PCT/US2012/05	8/28/2012			Pending	IMPROVED METHOD AND APPARATUS FOR OFFSET AND GAIN CORRE
MP3645I	MP3645I1WU	EP	12770308.0	8/28/2012			Pending	Method and Apparatus for Offset and Gain Correction
MP3645W	MP3645WO	WO	PCT/US2011/03	5/5/2011			Pending	METHOD AND APPARATUS FOR OFFSET AND GAIN CORRECTION
MP3645W	MP3645WOCN	CN	201180023487.1	5/5/2011	ZL2011800234	4/13/2016	In Force	METHOD AND APPARATUS FOR OFFSET AND GAIN CORRECTION
MP3647	MP3647	US	13/098,719	5/2/2011	8782336	7/15/2014	In Force	Hybrid Storage System With Control Module Embedded Solid State
MP3647	MP3647C1	US	14/331,725	7/15/2014	9507543	11/29/2016	In Force	Method And Apparatus For Transferring Data Between A Host And B
MP3647	MP3647CN	CN	201110126008.1	5/10/2011	ZL2011101260	8/3/2016	In Force	HYBRID STORAGE SYSTEM WITH CONTROL MODULE EMBEDDED SOL

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MP36648	MP36648	US	13/100,644	5/4/2011	8627183	1/7/2014	In Force	SYSTEMS AND METHODS FOR STORING VARIABLE RATE PRODUCT CO
MP36649	MP36649C1	US	14/070,089	11/1/2013	9153336	10/6/2015	In Force	DECODER PARAMETER ESTIMATION USING MULTIPLE MEMORY REA
MP36652	MP36652	US	13/090,600	4/20/2011	8354869	1/15/2013	In Force	Systems And Methods For Controlling Power Supply Current Using CI
MP36652	MP36652C1	US	13/741,705	1/15/2013	8552786	10/8/2013	In Force	SYSTEM AND METHOD OF ADJUSTING CURRENT DRAWN BY AN INTE
MP36654	MP36654	US	13/107,729	5/13/2011	8838042	9/16/2014	In Force	RF Front-End With Integrated T/R Switch
MP36654W	MP36654WO	WO	PCT/US2011/03	5/13/2011			Pending	RF Front-End With Integrated T/R Switch
MP36654W	MP36654WOCN	CN	2011800251421	5/13/2011	ZL2011800251	5/27/2015	In Force	RF FRONT-END WITH INTEGRATED T/R SWITCH
MP36656	MP36656	US	13/093,628	4/25/2011	8610935	12/17/2013	In Force	PRINTING AND SCANNING USING MOBILE DEVICES
MP36656	MP366561	US	14/030,785	9/18/2013	9521277	12/13/2016	In Force	METHOD TO ASSOCIATE MOBILE DEVICE WITH PRINTER
MP36657	MP36657	US	13/110,508	5/18/2011	8479129	7/2/2013	In Force	Dynamic Time Domain Randomization Techniques for SOC and IP Ver
MP36657	MP36657C1	US	13/915,187	6/11/2013	8904323	12/2/2014	In Force	Dynamic Time Domain Randomization Techniques for SOC and IP Ver
MP36659	MP36659	US	13/111,435	5/19/2011	8423946	4/16/2013	In Force	CIRCUITRY HAVING PROGRAMMABLE POWER RAILS, ARCHITECTURE
MP36660	MP36660	US	13/113,561	5/23/2011	8516347	8/20/2013	In Force	NON-BINARY LDPC EXTRINSIC CALCULATION UNIT (LECU) FOR ITERAT
MP36661	MP36661C1	US	14/538,179	11/11/2014	9639423	5/2/2017	In Force	Method And Apparatus For Recovering Metadata Lost During An Une
MP36661W	MP36661WO	WO	PCT/US2011/03	5/19/2011			Pending	Memory Access Table Saving And Restoring System And Methods
MP36661W	MP36661WOP	JP	2013-511090	5/19/2011	JP5850048	12/11/2015	In Force	MEMORY ACCESS TABLE SAVING AND RESTORING SYSTEM AND MET
MP36661W	MP36661WOKR	KR	1020127032837	5/19/2011	101759690	7/13/2017	In Force	Memory Access Table Saving And Restoring System And Methods
MP36663	MP36663	US	13/115,670	5/25/2011	9590910	3/7/2017	In Force	METHODS AND APPARATUS FOR HANDLING MULTICAST PACKETS IN
MP36664	MP36664CN	CN	2011101534561	6/3/2011	ZL2011101534	3/19/2014	In Force	PAD CONFIGURATIONS FOR AN ELECTRONIC PACKAGE ASSEMBLY
MP36664	MP36664D1	US	14/511,948	10/10/2014	9331052	5/3/2016	In Force	PAD CONFIGURATIONS FOR AN ELECTRONIC PACKAGE ASSEMBLY
MP36664	MP36664D1C1	US	15/137,776	4/25/2016	9543236	1/10/2017	In Force	PAD CONFIGURATIONS FOR AN ELECTRONIC PACKAGE ASSEMBLY
MP3664T	MP3664TW	TW	100119804	6/7/2011	1458062	10/21/2014	In Force	Pad Configurations for An Electronic Package Assembly
MP36665	MP36665	US	13/104,404	5/10/2011	8421542	4/16/2013	In Force	METHOD AND APPARATUS FOR DRIFT COMPENSATION IN PLL
MP36665	MP36665C1	US	13/854,498	4/1/2013	8981855	3/17/2015	In Force	METHOD AND APPARATUS FOR DRIFT COMPENSATION IN PLL
MP3665W	MP3665WO	WO	PCT/US2011/03	5/10/2011			Pending	METHOD AND APPARATUS FOR DRIFT COMPENSATION IN PLL
MP3665W	MP3665WOCN	CN	2011800263861	5/10/2011	ZL2011800263	5/18/2016	In Force	METHOD AND APPARATUS FOR DRIFT COMPENSATION IN PLL
MP3665W	MP3665WOP	JP	2013-512640	5/10/2011			Pending	METHOD AND APPARATUS FOR DRIFT COMPENSATION IN PLL
MP3665W	MP3665WOKR	KR	10-2012-703094	5/10/2011	10-1786764	10/11/2017	In Force	METHOD AND APPARATUS FOR DRIFT COMPENSATION IN PLL
MP36667	MP36667	US	13/151,927	6/2/2011	8804733	8/12/2014	In Force	CENTRALIZED PACKET PROCESSOR FOR A NETWORK
MP3667	MP3667C1	US	14/456,871	8/11/2014	9258219	2/9/2016	In Force	MULTI-UNIT SWITCH EMPLOYING VIRTUAL PORT FORWARDING
MP3672W	MP3672WO	WO	PCT/US2011/03	5/24/2011			Pending	TOGGING METHOD AND APPARATUS IN CONTROLLERS FOR HOME
MP3673	MP3673	US	13/155,085	6/7/2011	8718079	5/6/2014	In Force	Physical Layer Devices For Network Switches
MP3673	MP3673D1	US	13/174,096	6/30/2011	8576865	11/5/2013	In Force	Physical Layer Devices For Network Switches
MP3674	MP3674	US	13/157,917	6/10/2011	8699592	4/15/2014	In Force	SYSTEMS AND METHODS FOR ESTIMATING DECODER NOISE POWER I

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MP3674	MP3674C1	US	14/250,787	4/11/2014	9001910	4/7/2015	In Force	SYSTEMS AND METHODS FOR ESTIMATING DECODER NOISE POWER
MP3680	MP3680	US	13/159,241	6/13/2011	8378731	2/19/2013	In Force	ASYMMETRIC CORRECTION CIRCUIT WITH NEGATIVE RESISTANCE
MP3680	MP3680C1	US	13/768,706	2/15/2013	8674743	3/18/2014	In Force	ASYMMETRIC CORRECTION CIRCUIT WITH NEGATIVE RESISTANCE
MP3682	MP3682	US	13/155,985	6/8/2011	8817643	8/26/2014	In Force	METHOD AND APPARATUS FOR MONITORING A RADIO LINK IN A CO
MP3683	MP3683	US	13/159,143	6/13/2011	9021341	4/28/2015	In Force	LDPC CODING IN A COMMUNICATION SYSTEM
MP3684	MP3684	US	13/161,209	6/15/2011	9209861	12/8/2015	In Force	ALTERNATE FEEDBACK TYPES FOR DOWNLINK MULTIPLE USER MIM
MP3684	MP3684C1	US	14/961,420	12/7/2015	9629128	4/13/2017	In Force	ALTERNATE FEEDBACK TYPES FOR DOWNLINK MULTIPLE USER MIM
MP3684W	MP3684WO	WO	PCT/US2011/04	6/15/2011			Pending	ALTERNATE FEEDBACK TYPES FOR DOWNLINK MULTIPLE USER MIM
MP3684W	MP3684WOCN	CN	2011800230941	6/15/2011	ZL2011800230	6/22/2016	In Force	ALTERNATE FEEDBACK TYPES FOR DOWNLINK MULTIPLE USER MIM
MP3684W	MP3684WCEP	EP	EP11728134.1	6/15/2011	2583385	4/18/2018	In Force	ALTERNATE FEEDBACK TYPES FOR DOWNLINK MULTIPLE USER MIM
MP3684W	MP3684WCEPDE	DE	EP11728134.1	6/15/2011	2583385	4/18/2018	In Force	ALTERNATE FEEDBACK TYPES FOR DOWNLINK MULTIPLE USER MIM
MP3684W	MP3684WCEPFR	FR	EP11728134.1	6/15/2011	2583385	4/18/2018	In Force	ALTERNATE FEEDBACK TYPES FOR DOWNLINK MULTIPLE USER MIM
MP3684W	MP3684WCEPGB	GB	EP11728134.1	6/15/2011	2583385	4/18/2018	In Force	ALTERNATE FEEDBACK TYPES FOR DOWNLINK MULTIPLE USER MIM
MP3684W	MP3684WCEPJ	J	2013-515481	6/15/2011	JP5876041	1/29/2016	In Force	ALTERNATE FEEDBACK TYPES FOR DOWNLINK MULTIPLE USER MIM
MP3684W	MP3684WOKR	KR	10-2012-703225	6/15/2011	101923200	11/29/2018	In Force	ALTERNATE FEEDBACK TYPES FOR DOWNLINK MULTIPLE USER MIM
MP3685	MP3685	US	13/151,964	6/2/2011	8576969	11/5/2013	In Force	METHOD AND APPARATUS FOR DETECTING SYNC MARK
MP3686	MP3686	US	13/167,344	6/23/2011	8306506	10/30/2012	In Force	OVERWRITE PARAMETER DETERMINATION FOR OPTICAL DRIVE
MP3690	MP3690D1	US	14/673,233	3/30/2015	9980391	5/22/2018	In Force	METHOD OF MANUFACTURING AN ON-BOARD WIRELESS MODULE A
MP3691	MP3691	US	13/164,106	6/20/2011	8621071	12/31/2013	In Force	Method and Apparatus for Automatically Selecting an Access Point
MP3691	MP3691C1	US	13/164,125	6/20/2011	9009792	4/14/2015	In Force	Method and Apparatus for Automatically Configuring a Secure Wirele
MP3692	MP3692C1	US	14/148,355	1/6/2014	9159008	10/13/2015	In Force	METHOD AND APPARATUS FOR ESTIMATING USAGE OF PRINT SUBST
MP3694	MP3694C1	US	14/248,093	4/8/2014	9282428	3/8/2016	In Force	POSITIONING REQUEST IN PAGING MESSAGES OF WIRELESS COMMU
MP3696	MP3696	US	13/155,226	6/7/2011	8509527	8/13/2013	In Force	INTENSITY BASED PIXEL QUANTIZATION
MP3697	MP3697	US	13/164,111	6/20/2011	8630379	1/14/2014	In Force	METHODS AND APPARATUS FOR MULTIPLE INPUT MULTIPLE OUTPU
MP3697	MP3697C1	US	14/142,291	12/27/2013	9191044	11/17/2015	In Force	METHODS AND APPARATUS FOR MULTIPLE INPUT MULTIPLE OUTPU
MP3700	MP3700C1	US	14/221,828	3/21/2014	9136890	9/15/2015	In Force	SIGNAL DETECTION WITH ADJUSTABLE NUMBER OF INTERFERING SI
MP3701	MP3701	US	13/218,495	8/26/2011	8937893	1/20/2015	In Force	Method And Apparatus For Delaying Wireless Message Transmission
MP3703	MP3703	US	13/173,810	6/30/2011	8395977	3/12/2013	In Force	METHOD AND APPARATUS FOR CALIBRATING WRITE STRATEGY
MP3704W	MP3704WO	WO	PCT/US2011/04	6/13/2011			Pending	PRIMARY SIDE VOLTAGE CONTROL IN FLYBACK CONVERTER
MP3708	MP3708	US	13/177,236	7/6/2011	8542659	9/24/2013	In Force	Cyclic Shift Delays In Multi-User Packets With Resolvable Very High T
MP3708	MP3708C1	US	14/092,656	9/20/2013	8982850	3/17/2015	In Force	Cyclic Shift Delays In Multi-User Packets With Resolvable Very High T
MP3708W	MP3708WO	WO	PCT/US2011/04	7/7/2011			Pending	Cyclic Shift Delays In Multi-User Packets With Resolvable Very High T
MP3708W	MP3708WOCN	CN	2011800374574	7/7/2011	ZL2011800374	3/16/2016	In Force	A SYSTEM AND METHOD FOR WIRELESS COMMUNICATIONS
MP3708W	MP3708WCEP	EP	EP11745600.4	7/7/2011			Pending	Cyclic Shift Delays In Multi-User Packets With Resolvable Very High T

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MP3708W	MP3708WOJP	JP	2013-518818	7/7/2011	JP5835594	11/13/2015	In Force	CYCLIC SHIFT DELAYS IN MULTI-USER PACKETS WITH RESOLVABLE VE
MP3708W	MP3708WOKR	KR	1020137003323	7/7/2011	101856116	5/10/2018	In Force	Cyclic Shift Delays in Multi-User Packets With Resolvable Very High T
MP3709	MP3709C1	US	14/199,307	3/6/2014	8843723	9/23/2014	In Force	Multi-Dimension Memory Timing Tuner
MP3710	MP3710	US	13/166,340	6/22/2011	8868852	10/21/2014	In Force	Interface Management Control Systems And Methods For Non-Volati
MP3710	MP3710C1	US	14/516,133	10/16/2014	9183141	11/10/2015	In Force	METHOD AND APPARATUS FOR PARALLEL TRANSFER OF BLOCKS OF
MP3710	MP3710I1	US	14/132,810	12/18/2013	9141538	9/22/2015	In Force	Apparatus And Method For Generating Descriptors To Transfer Data
MP3710	MP3710I1I1	US	14/208,814	3/13/2014	9135168	9/15/2015	In Force	Apparatus And Method For Generating Descriptors To Reaccess A N
MP3710F	MP3710FWO	WO	PCT/US2014/02	3/14/2014			Pending	Apparatus And Method For Generating Descriptors To Reaccess A N
MP3710I	MP3710I1C1	WO	PCT/US2013/07	12/19/2013			Pending	Apparatus And Method For Generating Descriptors To Transfer Data
MP3710I	MP3710I1WC	CN	2013800722682	12/19/2013	ZL2013800722	4/10/2018	In Force	APPARATUS AND METHOD FOR GENERATING DESCRIPTORS TO TRAN
MP3710I	MP3710I1WK	KR	1020157019144	12/19/2013			Pending	Apparatus And Method For Generating Descriptors To Transfer Data
MP3710I	MP3710I1WO	WO	PCT/US2013/07	12/19/2013			Pending	APPARATUS AND METHOD FOR GENERATING DESCRIPTORS TO TRAN
MP3710I	MP3710IWCN	CN	2014800220501	3/14/2014	ZL2014800220	4/10/2018	In Force	APPARATUS AND METHOD FOR GENERATING DESCRIPTORS TO REAC
MP3710I	MP3710ITW	TW	100123864	7/6/2011	1506638	11/11/2015	In Force	Interface Management Control Systems And Methods For Non-Volati
MP3710W	MP3710WO	WO	PCT/US2011/04	6/28/2011			Pending	Interface Management Control Systems And Methods For Non-Volati
MP3710W	MP3710WOCN	CN	2011800384353	6/28/2011	ZL2011800384	8/17/2016	In Force	INTERFACE MANAGEMENT CONTROL SYSTEMS AND METHODS FOR
MP3710W	MP3710WOKR	KR	1020137003322	6/28/2011	101837111	4/19/2018	In Force	Interface Management Control Systems And Methods For Non-Volati
MP3711	MP3711	US	13/167,896	6/24/2011	8531888	9/10/2013	In Force	Determining Optimal Reference Voltages For Progressive Reads In Fla
MP3711	MP3711C1	US	14/021,383	9/9/2013	8885415	11/11/2014	In Force	DETERMINING OPTIMAL REFERENCE VOLTAGES FOR PROGRESSIVE R
MP3711	MP3711CN	CN	2011101952204	7/7/2011	ZL2011101952	8/13/2014	In Force	DETERMINING OPTIMAL REFERENCE VOLTAGES FOR PROGRESSIVE R
MP3713W	MP3713WO	WO	PCT/IB2012/002	12/18/2012			Pending	Output Stage for Wireless Transmitter
MP3713W	MP3713WOCN	CN	2012800623503	12/18/2012	ZL2012800623	10/17/2017	In Force	OUTPUT STAGE FOR WIRELESS TRANSMITTER
MP3715	MP3715C1	US	14/293,242	6/2/2014	9082187	7/14/2015	In Force	Method And Apparatus For Correcting Distortion In An Image Due To
MP3716	MP3716	US	13/180,723	7/12/2011	8683277	3/25/2014	In Force	DEFECT DETECTION USING PATTERN MATCHING ON DETECTED DATA
MP3716	MP3716C1	US	14/179,146	2/12/2014	9368235	6/14/2016	In Force	DEFECT DETECTION USING PATTERN MATCHING ON DETECTED DATA
MP3717	MP3717	US	13/183,676	7/15/2011	9171507	10/27/2015	In Force	Controller for Updating Pixels in an Electronic Paper Display
MP3717W	MP3717WO	WO	PCT/US2011/04	7/15/2011			Pending	Controller for Updating Pixels in an Electronic Paper Display
MP3717W	MP3717WOCN	CN	2011800350103	7/15/2011	ZL2011800350	3/2/2016	In Force	CONTROLLER FOR UPDATING PIXELS IN AN ELECTRONIC PAPER DISPL
MP3719	MP3719	US	13/170,791	6/28/2011	8599962	12/3/2013	In Force	Power Control Using Distortion Measurement
MP3720	MP3720	US	13/184,836	7/18/2011	8451041	5/28/2013	In Force	Charge-Injection Sense-Amp Logic
MP3720W	MP3720WO	WO	PCT/US2011/04	7/18/2011			Pending	Charge-Injection Sense-Amp Logic
MP3720W	MP3720WOKR	KR	1020137004049	7/18/2011	101811019	12/20/2017	In Force	Charge-Injection Sense-Amp Logic
MP3722W	MP3722WO	WO	PCT/US2011/04	7/12/2011			Pending	CUSTOMIZATION OF A BUS ADAPTER CARD
MP3722W	MP3722WOCN	CN	201180035010	7/12/2011	ZL20110103083	1/20/2016	In Force	CUSTOMIZATION OF A BUS ADAPTER CARD

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MP3722W	MP3722WOEP	EP	11733774.1	7/12/2011	2553871	8/23/2017	In Force	CUSTOMIZATION OF A BUS ADAPTER CARD
MP3723	MP3723	US	13/190,782	7/26/2011	8773110	7/8/2014	In Force	METHOD AND APPARATUS FOR WAVE DETECTION
MP3723W	MP3723WO	WO	PCT/IB2011/002	7/26/2011			Pending	RECTIFIED SINE WAVE DETECTION FOR POWER FACTOR CORRECTIO
MP3723W	MP3723WOCN	CN	201180036823.1	7/26/2011	ZL2011800368	1/20/2016	In Force	RECTIFIED SINE WAVE DETECTION FOR POWER FACTOR CORRECTIO
MP3723W	MP3723WOEP	EP	11784776.4	7/26/2011			Pending	Method and Apparatus for Wave Detection
MP3723W	MP3723WOJP	JP	2013-521249	7/26/2011	JP5830822	11/6/2015	In Force	Method and Apparatus for Wave Detection
MP3728	MP3728	US	13/184,304	7/15/2011	8618654	12/31/2013	In Force	STRUCTURES EMBEDDED WITHIN CORE MATERIAL AND METHODS C
MP3728	MP3728D1	US	14/140,149	12/24/2013	9087835	7/21/2015	In Force	STRUCTURES EMBEDDED WITHIN CORE MATERIAL AND METHODS C
MP3728T	MP3728TW	TW	100125576	7/20/2011	1527187	3/21/2016	In Force	Embedded Structures and Methods Thereof
MP3728W	MP3728WO	WO	PCT/US2011/04	7/18/2011			Pending	EMBEDDED STRUCTURES AND METHODS OF MANUFACTURE THERE
MP3728W	MP3728WOCN	CN	201180044155.1	7/18/2011	ZL2011800441	5/25/2016	In Force	EMBEDDED STRUCTURES AND METHODS OF MANUFACTURE THERE
MP3728W	MP3728WOEP	EP	11749029.2	6/6/2011	2586231	11/12/2014	In Force	EMBEDDED STRUCTURES AND METHODS OF MANUFACTURE THERE
MP3728W	MP3728WOEPDE	DE	11749029.2	6/6/2011	2586231	11/12/2014	In Force	EMBEDDED STRUCTURES AND METHODS OF MANUFACTURE THERE
MP3728W	MP3728WOEPFR	FR	11749029.2	6/6/2011	2586231	11/12/2014	In Force	EMBEDDED STRUCTURES AND METHODS OF MANUFACTURE THERE
MP3728W	MP3728WOEPGB	GB	11749029.2	6/6/2011	2586231	11/12/2014	In Force	EMBEDDED STRUCTURES AND METHODS OF MANUFACTURE THERE
MP3728W	MP3728WOJP	JP	2013-520779	7/18/2011	JP6198322	9/1/2017	In Force	EMBEDDED STRUCTURES AND METHODS OF MANUFACTURE THERE
MP3728W	MP3728WOKR	KR	10-2013-700415	7/18/2011			Pending	EMBEDDED STRUCTURES AND METHODS OF MANUFACTURE THERE
MP3732	MP3732	US	13/185,505	7/19/2011	8483641	7/9/2013	In Force	VALIDATION AND STABILIZATION OF NOISE MATRICES
MP3732	MP3732C1	US	13/914,652	6/11/2013	9077422	7/7/2015	In Force	VALIDATION AND STABILIZATION OF NOISE MATRICES
MP3734	MP3734	US	13/191,329	7/26/2011	840197	3/19/2013	In Force	FRACTIONAL SPUR REDUCTION USING CONTROLLED CLOCK JITTER
MP3734	MP3734C11	US	14/519,671	10/21/2014	9397647	7/19/2016	In Force	CLOCK SPURS REDUCTION TECHNIQUE
MP3734a	MP3734aFWC	CN	201480065785.1	10/21/2014	ZL2014800657	7/12/2019	In Force	CLOCK SPURS REDUCTION TECHNIQUE
MP3734a	MP3734aFWO	WO	PCT/IB2014/002	10/21/2014			Pending	CLOCK SPURS REDUCTION TECHNIQUE
MP3734a	MP3734aFWU	EP	14815427.1	10/21/2014			Pending	Clock Spurs Reduction Technique
MP3734W	MP3734WO	WO	PCT/US2011/04	7/26/2011			Pending	FRACTIONAL SPUR REDUCTION USING CONTROLLED CLOCK JITTER
MP3734W	MP3734WOCN	CN	201180036819.1	7/26/2011	ZL2011800368	2/9/2018	In Force	FRACTIONAL SPUR REDUCTION USING CONTROLLED CLOCK JITTER
MP3734W	MP3734WOEP	EP	11813056.6	7/26/2011	2599226	6/20/2018	In Force	Fractional Spur Reduction Using Controlled Clock Jitter
MP3734W	MP3734WUDE	DE	11813056.6	7/26/2011	2599226	6/20/2018	In Force	Fractional Spur Reduction Using Controlled Clock Jitter
MP3735	MP3735	US	13/168,978	6/26/2011	8526889	9/3/2013	In Force	SHARED SOFT METRIC BUFFER FOR CARRIER AGGREGATION RECEIVE
MP3737	MP3737	US	13/194,089	7/29/2011	8442462	5/14/2013	In Force	Modular Frequency Divider And Mixer Configuration
MP3737	MP373711	US	14/279,780	5/16/2014	9325541	4/26/2016	In Force	Modular Frequency Divider With Switch Configuration To Reduce Par
MP3737a	MP3737aFWO	WO	PCT/IB2014/061	5/21/2014			Pending	Modular Frequency Divider With Switch Configuration To Reduce Par
MP3737W	MP3737WO	WO	PCT/IB2011/001	7/29/2011			Pending	Modular Frequency Divider And Mixer Configuration
MP3737W	MP3737WOCN	CN	201180043792.1	7/29/2011	ZL2011800437	8/26/2015	In Force	MODULAR FREQUENCY DIVIDER AND MIXER CONFIGURATION

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MP3737W	MP3737WOEP	EP	11758557.0	7/29/2011			Pending	Modular Frequency Divider and Mixer Configuration
MP3737W	MP3737WOGR	KR	1020137006734	7/29/2011	101797695	11/14/2017	In Force	Modular Frequency Divider And Mixer Configuration
MP3751	MP3751CN	CN	200480001900.1	1/8/2004			Pending	METHOD FOR THE TIME AND FREQUENCY-DOMAIN SYNCHRONISATI
MP3751E	MP3751EP	EP	04700705.9	1/8/2004	EP1583267	3/12/2008	In Force	Method for the Time- and Frequency-Domain Synchronisation of Mul
MP3751E	MP3751EPDE	DE	04700705.9	1/8/2004	1583267	3/12/2008	In Force	METHOD FOR THE TIME- AND FREQUENCY-DOMAIN SYNCHRONISATI
MP3751E	MP3751EPES	ES	04700705.9	1/8/2004	1583267	3/12/2008	In Force	METHOD FOR THE TIME- AND FREQUENCY-DOMAIN SYNCHRONISATI
MP3751E	MP3751EPFR	FR	04700705.9	1/8/2004	1583267	3/12/2008	In Force	METHOD FOR THE TIME- AND FREQUENCY-DOMAIN SYNCHRONISATI
MP3751E	MP3751EPGB	GB	04700705.9	1/8/2004	1583267	3/12/2008	In Force	METHOD FOR THE TIME- AND FREQUENCY-DOMAIN SYNCHRONISATI
MP3751E	MP3751ES	ES	200300052	1/10/2003	2212744	2/18/2005	In Force	Method For The Time- And Frequency-Domain Synchronisation Of M
MP3751I	MP3751IL	IL	169272	1/8/2004	169272	2/1/2011	In Force	Method For The Time-And Frequency-Domain Synchronization Of Mu
MP3751I	MP3751IN	IN	1193KOLNP2001	1/8/2004	265686	3/4/2015	In Force	A System For Synchronization Of An Orthogonal Frequency Division
MP3751K	MP3751KR	KR	1020057012858	1/8/2004	101083795	11/9/2011	In Force	Method For The Time- And Frequency-Domain Synchronisation Of M
MP3751T	MP3751TW	TW	93100663	1/12/2004	1256790	6/11/2006	In Force	Method For The Time- And Frequency-Domain Synchronisation Of M
MP3751W	MP3751WO	WO	PCT/ES2004/001	1/8/2004			Pending	Method For The Time- And Frequency-Domain Synchronisation Of M
MP3754E	MP3754ES	ES	200301104	5/13/2003	2219183	1/9/2006	In Force	Des Algorithm-Based Encryption Method
MP3754T	MP3754TW	TW	93113534	5/13/2004	1265711	11/1/2006	In Force	Des Algorithm-Based Encryption Method
MP3754W	MP3754WO	WO	PCT/ES2004/001	5/11/2004			Pending	Des Algorithm-Based Encryption Method
MP3771E	MP3771EPDE	DE	8787598.5	6/10/2008	2173040	1/11/2017	In Force	SINGLE-PORT SIGNAL REPEATER
MP3771E	MP3771EPFR	FR	8787598.5	6/10/2008	2173040	1/11/2017	In Force	SINGLE-PORT SIGNAL REPEATER
MP3771E	MP3771EPGB	GB	8787598.5	6/10/2008	2173040	1/11/2017	In Force	SINGLE-PORT SIGNAL REPEATER
MP3775E	MP3775ES	ES	200801866	6/23/2008	2382349	1/31/2011	In Force	Method For Selectively Sharing A Communication Channel Between C
MP3775T	MP3775TW	TW	98121064	6/23/2009			Pending	Method For Sharing A Communication Channel Selectively Between C
MP3775W	MP3775WO	WO	PCT/ES2009/071	6/16/2009			Pending	Method For Selectively Sharing A Communication Channel Between C
MP3775W	MP3775WOEP	EP	09769383.2	6/16/2009	2346277	5/30/2018	In Force	Method for Selectively Sharing a Communication Channel Between C
MP3775W	MP3775WOKR	KR	1020117001691	6/16/2009	101576234	12/3/2015	In Force	Method For Selectively Sharing A Communication Channel Between C
MP3775W	MP3775WUDE	DE	09769383.2	6/16/2009	2346277	5/30/2018	In Force	Method for Selectively Sharing a Communication Channel Between C
MP3775W	MP3775WUFR	FR	09769383.2	6/16/2009	2346277	5/30/2018	In Force	Method for Selectively Sharing a Communication Channel Between C
MP3775W	MP3775WUGB	GB	09769383.2	6/16/2009	2346277	5/30/2018	In Force	Method for Selectively Sharing a Communication Channel Between C
MP3776	MP3776EWOI	US	14/180,178	2/13/2014	10141984	11/27/2018	In Force	MULTI-BAND TRANSMISSION SYSTEM
MP3776	MP3776EWOICI	US	16/199,991	11/26/2018			Pending	MULTI-BAND TRANSMISSION SYSTEM
MP3776	MP3776EWUS	US	13/054,024	11/29/2011	8982909	3/17/2015	In Force	Multi-Band Data Transmission
MP3776E	MP3776ES	ES	200802091	7/14/2008	2349515	7/5/2011	In Force	Multi-Band Data Transmission
MP3776E	MP3776EWCN	CN	200980127284.1	7/3/2009	Z12009801272	1/7/2015	In Force	Multi-Band Data Transmission Method
MP3776E	MP3776EWEP	EP	09800095.3	7/3/2009	2302818	9/5/2018	In Force	Multi-Band Data Transmission Method

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MP3776E	MP3776EWPDE	DE	09800095.3	7/3/2009	2302818	9/5/2018	In Force	Multi-Band Data Transmission Method
MP3776E	MP3776EWEFER	FR	09800095.3	7/3/2009	2302818	9/5/2018	In Force	Multi-Band Data Transmission Method
MP3776E	MP3776EWEFGB	GB	09800095.3	7/3/2009	2302818	9/5/2018	In Force	Multi-Band Data Transmission Method
MP3776E	MP3776EWIN	WO	PCT/IB2014/00C	2/14/2014			Pending	MULTI-BAND TRANSMISSION SYSTEM
MP3776E	MP3776EWKR	KR	1020117003194	7/3/2009	101911032	10/23/2018	In Force	Multi-Band Data Transmission Method
MP3776T	MP3776TWTW	TW	98123805	7/14/2009			Pending	MultiBand Data Transmission
MP3776W	MP3776WOWO	WO	PCT/ES2009/07C	7/3/2009			Pending	Multi-Band Data Transmission
MP3778E	MP3778EOWW	WO	PCT/US2011/02	3/22/2011			Pending	Communication Node And Procedure For Various Means Of Transmis
MP3778E	MP3778ES	ES	201030411	3/22/2010	2393890	10/18/2013	In Force	Node And Procedure Of Communications In Various Means Of Transm
MP3778E	MP3778ESCN	CN	2011100738541	3/22/2011	212011100733	2/26/2014	In Force	COMMUNICATION NODE AND PROCEDURE FOR VARIOUS MEANS OF
MP3778E	MP3778EWEFEP	EP	11710990.0	3/22/2011	2550753	12/7/2016	In Force	Communication Node and Procedure for Various Means of Transmiss
MP3778E	MP3778EWJP	JP	2013-501384	3/22/2011	JP5769156	7/3/2015	In Force	COMMUNICATION NODE AND PROCEDURE FOR VARIOUS MEANS OF
MP3778P	MP3778PUDE	DE	11710990.0	3/22/2011	60201103311K	12/7/2016	In Force	Communication Node And Procedure For Various Means Of Transmis
MP3778P	MP3778PUFR	FR	11710990.0	3/22/2011	2550753	12/7/2016	In Force	Communication Node And Procedure For Various Means Of Transmis
MP3778P	MP3778PUGB	GB	11710990.0	3/22/2011	2550753	12/7/2016	In Force	Communication Node And Procedure For Various Means Of Transmis
MP3779	MP3779US	US	13/196,116	8/2/2011	8516249	9/3/2013	In Force	METHODS AND SYSTEMS FOR DETECTING AND CORRECTING TIMING
MP3779	MP3779C1	US	14/012,743	8/28/2013	8804441	8/12/2014	In Force	METHODS AND SYSTEMS FOR DETECTING AND CORRECTING TIMING
MP3780	MP3780US	US	13/197,019	8/3/2011	8626102	1/7/2014	In Force	Radio Coexistence Using Clock Rate Adaptation
MP3782W	MP3782WOWO	WO	PCT/US2011/05	9/1/2011			Pending	Back-EMF Detection for Motor Control
MP3782W	MP3782WOCN	CN	2011800447411	9/1/2011	212011800447	8/31/2016	In Force	BACK-EMF DETECTION FOR MOTOR CONTROL
MP3782W	MP3782WQJP	JP	2013-528785	9/1/2011	JP5967662	7/15/2016	In Force	BACK-EMF DETECTION FOR MOTOR CONTROL
MP3783	MP3783US	US	13/197,353	8/3/2011	8661326	2/25/2014	In Force	Non-Binary LDPC Code Decoding Early Termination
MP3787	MP3787US	US	13/184,949	7/18/2011	8732543	5/20/2014	In Force	TENSOR PRODUCT CODES FOR FLASH
MP3788	MP3788US	US	13/310,587	12/2/2011	8581762	11/12/2013	In Force	Continuous Time Sigma-Delta ADC with Embedded Low-Pass Filter
MP3788W	MP3788WOWO	WO	PCT/US2011/06	12/2/2011			Pending	A CONTINUOUS TIME SIGMA-DELTA ADC WITH EMBEDDED LOW-PAS
MP3788W	MP3788WOCN	CN	2011800581211	12/2/2011	212011800581	11/9/2016	In Force	ANALOG TO DIGITAL CONVERTER WITH NOISE REDUCTION FEEDBAC
MP3790	MP3790US	US	13/005,709	1/13/2011	8742985	6/3/2014	In Force	Ultra Low Power Global Navigation Satellite System (GNSS) Receiver
MP3790	MP3790C1	US	14/291,429	5/30/2014	9645246	5/9/2017	In Force	Ultra Low Power Global Navigation Satellite System (GNSS) Receiver
MP3791	MP3791US	US	13/207,093	8/10/2011	9391742	7/12/2016	In Force	CHANNEL DESCRIPTION FEEDBACK IN A COMMUNICATION SYSTEM
MP3791	MP3791C1	US	14/859,993	9/21/2015	9391811	7/12/2016	In Force	CHANNEL DESCRIPTION FEEDBACK IN A COMMUNICATION SYSTEM
MP3791	MP3791C1C1	US	15/205,735	7/8/2016	9762413	9/12/2017	In Force	CHANNEL DESCRIPTION FEEDBACK IN A COMMUNICATION SYSTEM
MP3791	MP3791C1C1C1	US	15/647,627	7/12/2017	9979565	5/22/2018	In Force	CHANNEL DESCRIPTION FEEDBACK IN A COMMUNICATION SYSTEM
MP3791K	MP3791KRO1	KR	10-2018-760877	8/10/2011	10-1863306	5/31/2018	In Force	CHANNEL DESCRIPTION FEEDBACK IN A COMMUNICATION SYSTEM
MP3791W	MP3791WCD1	CN	201610287341	8/10/2011			Pending	CHANNEL DESCRIPTION FEEDBACK IN A COMMUNICATION SYSTEM

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MP3791W	MP3791WO	WO	PCT/US2011/04	8/10/2011			Pending	CHANNEL DESCRIPTION FEEDBACK IN A COMMUNICATION SYSTEM
MP3791W	MP3791WOCN	CN	2011800389821	8/10/2011	ZL20111800389	6/1/2016	In Force	CHANNEL DESCRIPTION FEEDBACK IN A COMMUNICATION SYSTEM
MP3791W	MP3791WOEP	EP	11751713.6	8/10/2011	2603984	7/17/2019	In Force	Channel Description Feedback in a Communication System
MP3791W	MP3791WOEPDE	DE	11751713.6	8/10/2011	602011060506	7/17/2019	In Force	Channel Description Feedback in a Communication System
MP3791W	MP3791WOEPFR	FR	11751713.6	8/10/2011	2603984	7/17/2019	In Force	Channel Description Feedback in a Communication System
MP3791W	MP3791WOEPGB	GB	11751713.6	8/10/2011	2603984	7/17/2019	In Force	Channel Description Feedback in a Communication System
MP3791W	MP3791WOJP	JP	2013-524204	8/10/2011	JP5867932	1/15/2016	In Force	CHANNEL DESCRIPTION FEEDBACK IN A COMMUNICATION SYSTEM
MP3791W	MP3791WOKR	KR	10-2013-700610	8/10/2011	101829403	3/29/2018	In Force	CHANNEL DESCRIPTION FEEDBACK IN A COMMUNICATION SYSTEM
MP3796	MP3796	US	13/210,812	8/16/2011	8654851	4/8/2014	In Force	Adaptive Multiple-Input Multiple-Output Successive Interference Cancellation
MP3796	MP3796C1	US	14/242,091	4/1/2014	8949683	2/3/2015	In Force	Adaptive Successive Interference Cancellation Receivers
MP3799W	MP3799WO	WO	PCT/US2011/04	8/24/2011			Pending	Device Interface And Apparatus
MP3799W	MP3799WOCN	CN	2011800409341	8/24/2011	ZL2011800409	8/3/2016	In Force	Device Interface And Apparatus
MP3799W	MP3799WOEP	EP	11754788.5	8/24/2011			Pending	Device Interface and Apparatus
MP3799W	MP3799WOJP	JP	2013-526126	8/24/2011	JP5812502	10/2/2015	In Force	DEVICE INTERFACE AND APPARATUS
MP3799W	MP3799WOKR	KR	1020137007066	8/24/2011	101861788	5/28/2018	In Force	Device Interface And Apparatus
MP3803	MP3803	US	13/212,771	8/18/2011	8654681	1/7/2014	In Force	Low Noise CMOS Ring Oscillator
MP3803	MP3803C1	US	14/149,073	1/7/2014	9148131	9/29/2015	In Force	Inverter Cell For A Ring Oscillator
MP3803W	MP3803WO	WO	PCT/US2011/04	8/18/2011			Pending	Low Noise CMOS Ring Oscillator
MP3803W	MP3803WOCN	CN	2011800409361	8/18/2011	ZL2011800409	3/2/2016	In Force	LOW NOISE CMOS RING OSCILLATOR
MP3803W	MP3803WOEP	EP	11820415.5	8/18/2011	2609681	7/22/2015	In Force	Low Noise CMOS Ring Oscillator
MP3803W	MP3803WOEPDE	DE	11820415.5	8/18/2011	2609681	7/22/2015	In Force	LOW NOISE CMOS RING OSCILLATOR
MP3803W	MP3803WUFR	FR	11820415.5	8/18/2011	2609681	7/22/2015	In Force	LOW NOISE CMOS RING OSCILLATOR
MP3803W	MP3803WUGB	GB	11820415.5	8/18/2011	2609681	7/22/2015	In Force	LOW NOISE CMOS RING OSCILLATOR
MP3810	MP3810	US	13/212,440	8/18/2011	8659594	4/15/2014	In Force	Optimum Interleaver Design for a Communication System
MP3810W	MP3810WO	WO	PCT/US2011/04	8/18/2011			Pending	OPTIMUM INTERLEAVER DESIGN FOR A COMMUNICATION SYSTEM
MP3810W	MP3810WOCN	CN	2011800422341	8/18/2011	ZL2011800422	8/17/2016	In Force	OPTIMUM INTERLEAVER AND INTERLEAVING METHOD FOR A COMMUNICATION SYSTEM
MP3810W	MP3810WOEP	EP	11752393.6	8/18/2011	2612460	12/13/2017	In Force	Optimum Interleaver Design for a Communication System
MP3810W	MP3810WOJP	JP	2013-527102	8/18/2011	JP5839416	11/20/2015	In Force	OPTIMUM INTERLEAVER DESIGN FOR A COMMUNICATION SYSTEM
MP3810W	MP3810WOKR	KR	10-2013-700622	8/18/2011	101858193	5/16/2018	In Force	OPTIMUM INTERLEAVER DESIGN FOR A COMMUNICATION SYSTEM
MP3810W	MP3810WUDE	DE	11752393.6	8/18/2011	602011094167	12/13/2017	In Force	Optimum Interleaver Design for a Communication System
MP3810W	MP3810WUFR	FR	11752393.6	8/18/2011	2612460	12/13/2017	In Force	Optimum Interleaver Design for a Communication System
MP3810W	MP3810WUGB	GB	11752393.6	8/18/2011	2612460	12/13/2017	In Force	Optimum Interleaver Design for a Communication System
MP3811	MP3811	US	13/215,730	8/23/2011	9531498	12/27/2016	In Force	LINK ADAPTATION IN A COMMUNICATION NETWORK
MP3811W	MP3811WO	WO	PCT/US2011/04	8/23/2011			Pending	LINK ADAPTATION A COMMUNICATION NETWORK

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MP3811W	MP3811WOCN	CN	201180047862	8/23/2011	ZL2011800478	4/12/2017	In Force	METHOD AND APPARATUS FOR LINK ADAPTION IN A COMMUNICATI
MP3811W	MP3811WOEP	EP	11752400.9	8/23/2011	2612461	6/26/2019	In Force	Link Adaptation in a Communication Network
MP3811W	MP3811WOEPDE	DE	11752400.9	8/23/2011	603011060036	6/26/2019	In Force	Link Adaptation in a Communication Network
MP3811W	MP3811WOEPFR	FR	11752400.9	8/23/2011	2612461	6/26/2019	In Force	Link Adaptation in a Communication Network
MP3811W	MP3811WOEPGB	GB	11752400.9	8/23/2011	2612461	6/26/2019	In Force	Link Adaptation in a Communication Network
MP3811W	MP3811WOJP	JP	2013-527115	8/23/2011	JP6029110	10/28/2016	In Force	LINK ADAPTATION A COMMUNICATION NETWORK
MP3811W	MP3811WOKR	KR	10-2013-700821	8/23/2011	101829852	2/20/2018	In Force	LINK ADAPTATION IN A COMMUNICATION NETWORK
MP3812	MP3812	US	13/218,811	8/26/2011	8571591	10/29/2013	In Force	COEXISTENCE SUPPORT FOR MULTI-CHANNEL WIRELESS COMMUNIC
MP3812	MP3812C1	US	14/065,222	10/28/2013	9386475	7/5/2016	In Force	COEXISTENCE SUPPORT FOR MULTI-CHANNEL WIRELESS COMMUNIC
MP3812W	MP3812WO	WO	PCT/US2011/04	8/26/2011			Pending	COEXISTENCE SUPPORT FOR MULTI-CHANNEL WIRELESS COMMUNIC
MP3812W	MP3812WOCN	CN	201180048619	8/26/2011	ZL2011800486	1/20/2016	In Force	METHOD, APPARATUS AND SYSTEM FOR MULTI-CHANNEL WIRELESS
MP3812W	MP3812WOEP	EP	11822411.2	8/26/2011			Pending	Coexistence Support for Multi-Channel Wireless Communications
MP3812W	MP3812WOJP	JP	2013-527148	8/26/2011	JP5577550	7/18/2014	In Force	COEXISTENCE SUPPORT FOR MULTI-CHANNEL WIRELESS COMMUNIC
MP3812W	MP3812WOKR	KR	10-2013-700784	8/26/2011	10-1765852	8/1/2017	In Force	COEXISTENCE SUPPORT FOR MULTI-CHANNEL WIRELESS COMMUNIC
MP3813	MP3813C1	US	14/167,699	1/29/2014	9066886	5/5/2015	In Force	METHODS AND SYSTEMS FOR RECONFIGURABLE LDPC DECODERS
MP3814	MP3814CN	CN	201110265700	9/5/2011	ZL2011102387	5/4/2016	In Force	BALANCING CACHING LOAD IN A PEER-TO-PEER BASED NETWORK FIL
MP3815	MP3815C1	US	14/176,624	2/10/2014	9069693	6/30/2015	In Force	Decoder Based Data Recovery
MP3815	MP3815CN	CN	201110280519	9/7/2011	ZL2011102805	8/31/2016	In Force	DECODER BASED DATA RECOVERY
MP3817	MP3817	US	13/228,071	9/8/2011	8780872	7/15/2014	In Force	Dual-Technology Wireless Coexistence For Multi-Antenna Devices
MP3817	MP3817C1	US	14/330,455	7/14/2014	9578682	2/21/2017	In Force	Systems And Methods For Selecting Between Coexistence Modes in
MP3819	MP3819C1	US	14/212,085	3/14/2014	8930728	1/6/2015	In Force	SYSTEM AND METHOD FOR SELECTING A POWER MANAGEMENT CO
MP3820	MP3820	US	13/219,528	8/26/2011	8929430	1/6/2015	In Force	ELECTRO-MAGNETIC INTERFERENCE REDUCTION FOR SWITCHED SIG
MP3820	MP3820C1	US	14/575,574	12/18/2014	9264269	2/16/2016	In Force	ELECTRO-MAGNETIC INTERFERENCE REDUCTION FOR SWITCHED SIG
MP3820W	MP3820WO	WO	PCT/US2011/04	8/26/2011			Pending	ELECTRO-MAGNETIC INTERFERENCE REDUCTION FOR SWITCHED SIG
MP3821	MP3821	US	13/230,192	9/12/2011	8427216	4/23/2013	In Force	Ser Tolerant Flip Flop Having a Redundant Latch
MP3821	MP3821C1	US	13/863,162	4/15/2013	9559672	1/31/2017	In Force	Ser Tolerant Flip Flop Having a Redundant Latch
MP3823	MP3823	US	13/310,541	12/2/2011	8665005	3/4/2014	In Force	Process And Temperature Insensitive Inverter
MP3823	MP3823C1	US	14/191,845	2/27/2014	8963621	2/24/2015	In Force	Methods And Apparatus For Tuning A Current Source And Selecting A
MP3823W	MP3823WO	WO	PCT/US2011/06	12/2/2011			Pending	Process And Temperature Insensitive Inverter
MP3823W	MP3823WOCN	CN	2011800581281	12/2/2011	ZL2011800581	3/2/2016	In Force	PROCESS AND TEMPERATURE INSENSITIVE INVERTER
MP3824	MP3824C1	US	14/200,677	3/7/2014	9223920	12/29/2015	In Force	METHOD AND APPARATUS FOR TIMING CLOSURE
MP3824W	MP3824WO	WO	PCT/US2011/05	9/19/2011			Pending	METHOD AND APPARATUS FOR TIMING CLOSURE
MP3824W	MP3824WOCN	CN	2011800444274	9/19/2011	ZL2011800444	4/6/2016	In Force	METHOD AND APPARATUS FOR TIMING CLOSURE
MP3825	MP3825	US	13/221,487	9/13/2011	8619599	12/31/2013	In Force	PACKET PROCESSOR VERIFICATION METHODS AND SYSTEMS

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MP3827	MP3827C1	US	14/259,174	4/23/2014	9385776	7/5/2016	In Force	DATA CHANNEL NOISE ESTIMATION USING PILOT CHANNEL
MP3828	MP3828	US	13/231,971	9/14/2011	8982866	3/17/2015	In Force	DATA CHANNEL NOISE ESTIMATION USING CONTROL CHANNEL
MP3829	MP3829	US	13/372,908	2/14/2012	8750273	6/10/2014	In Force	Tunneled Direct Link Setup Optimization
MP3829	MP3829D1	US	14/239,061	6/9/2014	9445448	9/13/2016	In Force	Tunneled Direct Link Setup Optimization
MP3830W	MP3830WO	WO	PCT/US2011/05	9/27/2011			Pending	Average Output Current Estimation Using Primary-Side Sensing
MP3832	MP3832	US	13/221,670	9/13/2011	9178651	11/3/2015	In Force	STREAM PARISING IN A COMMUNICATION SYSTEM
MP3832W	MP3832WO	WO	PCT/US2011/05	9/13/2011			Pending	STREAM PARISING IN A COMMUNICATION SYSTEM
MP3832W	MP3832WOCN	CN	201180044740J	9/13/2011	ZL2011800447	9/21/2016	In Force	STREAM PARISING IN A COMMUNICATION SYSTEM
MP3832W	MP3832WCEP	EP	11761447.9	9/13/2011	2622764	11/28/2018	In Force	Stream Parsing in a Communication System
MP3832W	MP3832WCEPDE	DE	11761447.9	9/13/2011	60201105431E	11/28/2018	In Force	Stream Parsing in a Communication System
MP3832W	MP3832WCEPFR	FR	11761447.9	9/13/2011	2622764	11/28/2018	In Force	Stream Parsing in a Communication System
MP3832W	MP3832WCEPGB	GB	11761447.9	9/13/2011	2622764	11/28/2018	In Force	Stream Parsing in a Communication System
MP3832W	MP3832WQJP	JP	2013-531621	9/13/2011	JP5892496	3/4/2016	In Force	STREAM PARISING IN A COMMUNICATION SYSTEM
MP3832W	MP3832WQKR	KR	10-2013-700958	9/13/2011	101923202	2/27/2019	In Force	STREAM PARISING IN A COMMUNICATION SYSTEM
MP3838	MP3838	US	13/247,857	9/28/2011	8581374	11/12/2013	In Force	PLACING HEAT SINK INTO PACKAGING BY STRIP FORMATION ASSEM
MP3838	MP3838D1	US	14/075,987	11/8/2013	8741694	6/3/2014	In Force	PLACING HEAT SINK INTO PACKAGING BY STRIP FORMATION ASSEM
MP3839	MP3839	US	13/252,710	10/4/2011	8731090	5/20/2014	In Force	COMPRESSED FEEDBACK FORMAT FOR WLAN
MP3839	MP3839C1	US	14/281,555	5/19/2014	9232429	1/5/2016	In Force	COMPRESSED FEEDBACK FORMAT FOR WLAN
MP3839	MP3839C1C1	US	14/987,316	1/4/2016	9712365	7/13/2017	In Force	COMPRESSED FEEDBACK FORMAT FOR WLAN
MP3839	MP3839C1C1C1	US	15/651,143	7/17/2017	10075349	9/11/2018	In Force	COMPRESSED FEEDBACK FORMAT FOR WLAN
MP3839W	MP3839WCD1	CN	201610489340J	10/4/2011			Pending	COMPRESSED FEEDBACK FORMAT FOR WLAN
MP3839W	MP3839WO	WO	PCT/US2011/05	10/4/2011			Pending	COMPRESSED FEEDBACK FORMAT FOR WLAN
MP3839W	MP3839WOCN	CN	201180056197J	10/4/2011	ZL2011800561	8/10/2016	In Force	COMPRESSED FEEDBACK FORMAT FOR WLAN
MP3839W	MP3839WCEP	EP	11770970.9	10/4/2011	2625832	2/21/2018	In Force	Compressed Feedback Format for WLAN
MP3839W	MP3839WQJP	JP	2013-531962	10/4/2011	JP6124304	4/14/2017	In Force	COMPRESSED FEEDBACK FORMAT FOR WLAN
MP3839W	MP3839WQKR	KR	10-2013-701125	10/4/2011	101923203	11/29/2018	In Force	COMPRESSED FEEDBACK FORMAT FOR WLAN
MP3839W	MP3839WUDE	DE	11770970.9	10/4/2011	60201104581E	2/21/2018	In Force	Compressed Feedback Format for WLAN
MP3839W	MP3839WUFR	FR	11770970.9	10/4/2011	2625832	2/21/2018	In Force	Compressed Feedback Format for WLAN
MP3839W	MP3839WUGB	GB	11770970.9	10/4/2011	2625832	2/21/2018	In Force	Compressed Feedback Format for WLAN
MP3840	MP3840	US	13/232,218	10/4/2011	8750404	6/10/2014	In Force	CODEBOOK SUBSAMPLING FOR PUCCH FEEDBACK
MP3840	MP3840C1	US	14/296,474	6/5/2014	9178591	11/3/2015	In Force	CODEBOOK SUBSAMPLING FOR PUCCH FEEDBACK
MP3840	MP3840CN	CN	201110317046J	10/8/2011			Pending	CODEBOOK SUBSAMPLING FOR PUCCH FEEDBACK
MP3840E	MP3840EP	EP	11183952.8	10/5/2011			Pending	CODEBOOK SUBSAMPLING FOR PUCCH FEEDBACK
MP3840I	MP3840IP	JP	2011-215522	9/29/2011			Pending	CODEBOOK SUBSAMPLING FOR PUCCH FEEDBACK

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MP3841	MP3841	US	13/253,078	10/5/2011	8615052	12/24/2013	In Force	ENHANCED CHANNEL FEEDBACK FOR MULTI-USER MIMO
MP3841	MP3841CN	CN	201110391622.1	10/8/2011			Pending	ENHANCED CHANNEL FEEDBACK FOR MULTI-USER MIMO
MP3841D	MP3841DE	DE	202011111022	10/6/2011	202011111022	5/3/2018	In Force	Enhanced Channel Feedback for Multi-User MIMO
MP3841E	MP3841EP	EP	EP11184053.4	10/6/2011			Pending	Enhanced Channel Feedback for Multi-User MIMO
MP3841J	MP3841JP	JP	2011-221992	10/6/2011	JP5880925	2/12/2016	In Force	ENHANCED CHANNEL FEEDBACK FOR MULTI-USER MIMO
MP3842W	MP3842WO	WO	PCT/US2011/05	9/23/2011			Pending	Distributed Cache Coherency Protocol
MP3842W	MP3842WOCN	CN	201180048070.1	9/6/2011		3/15/2017	In Force	Distributed Cache Coherency Protocol
MP3842W	MP3842WOKR	KR	10-2013-70114E	9/23/2011	101845110	4/3/2018	In Force	Distributed Cache Coherency Protocol
MP3844	MP3844	US	13/250,661	9/30/2011	9467406	8/2/2016	In Force	tone REORDERING IN A WIRELESS COMMUNICATION SYSTEM
MP3844	MP3844C1	US	15/225,319	8/1/2016	9680616	6/13/2017	In Force	tone REORDERING IN A WIRELESS COMMUNICATION SYSTEM
MP3844	MP3844C1C1	US	15/619,999	6/12/2017	9998266	6/12/2018	In Force	tone REORDERING IN A WIRELESS COMMUNICATION SYSTEM
MP3844W	MP3844WO	WO	PCT/US2011/05	9/30/2011			Pending	tone REORDERING IN A WIRELESS COMMUNICATION SYSTEM
MP3844W	MP3844WOCN	CN	201180048405.1	9/30/2011	ZL2011800484	9/8/2017	In Force	tone REORDERING IN A WIRELESS COMMUNICATION SYSTEM
MP3844W	MP3844WOPR	EP	11767589.2	9/30/2011	2625813	5/31/2017	In Force	tone REORDERING IN A WIRELESS COMMUNICATION SYSTEM
MP3844W	MP3844WOJP	JP	2013-533849	9/30/2011	JP5867934	1/15/2016	In Force	tone REORDERING IN A WIRELESS COMMUNICATION SYSTEM
MP3844W	MP3844WOKR	KR	10-2013-700821	9/30/2011	101923201	2/27/2019	In Force	tone REORDERING IN A WIRELESS COMMUNICATION SYSTEM
MP3844W	MP3844WUDE	DE	602011038375.1	9/30/2011	602011103837	5/31/2017	In Force	tone REORDERING IN A WIRELESS COMMUNICATION SYSTEM
MP3844W	MP3844WUFR	FR	11767589.2	9/30/2011	2625813	5/31/2017	In Force	tone REORDERING IN A WIRELESS COMMUNICATION SYSTEM
MP3844W	MP3844WUGB	GB	11767589.2	9/30/2011	2625813	5/31/2017	In Force	tone REORDERING IN A WIRELESS COMMUNICATION SYSTEM
MP3846	MP3846	US	13/252,416	10/4/2011	8645716	2/4/2014	In Force	Method and Apparatus for Overwriting an Encryption Key of a Media
MP3846W	MP3846WOCN	CN	2011800507966	10/19/2011	ZL2011800507	8/3/2016	In Force	USB-To-SATA High-Speed Bridge
MP3847	MP3847	US	13/648,406	10/10/2012	8847735	9/30/2014	In Force	Antenna Interface for Radio Frequency Identification Circuit
MP3847	MP3847C1	US	14/492,131	9/22/2014	9361491	6/7/2016	In Force	Antenna Interface for Radio Frequency Identification Circuit
MP3847W	MP3847WO	WO	PCT/US2012/05	10/10/2012			Pending	Antenna Interface for Radio Frequency Identification Circuit
MP3847W	MP3847WOCN	CN	201280059791.1	10/10/2012	ZL2012800597	2/8/2017	In Force	ANTENNA INTERFACE FOR RADIO FREQUENCY IDENTIFICATION CIRCUIT
MP3847W	MP3847WOPR	EP	12813959.9	10/10/2012	2765857	9/7/2016	In Force	Antenna Interface for Radio Frequency Identification Circuit
MP3847W	MP3847WOPDE	DE	12813959.9	10/10/2012	602012022585	9/7/2016	In Force	ANTENNA INTERFACE FOR RADIO FREQUENCY IDENTIFICATION CIRCUIT
MP3847W	MP3847WOPFR	FR	12813959.9	10/10/2012	2765857	9/7/2016	In Force	ANTENNA INTERFACE FOR RADIO FREQUENCY IDENTIFICATION CIRCUIT
MP3847W	MP3847WOPGB	GB	12813959.9	10/10/2012	2765857	9/7/2016	In Force	ANTENNA INTERFACE FOR RADIO FREQUENCY IDENTIFICATION CIRCUIT
MP3847W	MP3847WOJP	JP	2014-535180	10/10/2012	JP6233601	11/2/2017	In Force	ANTENNA INTERFACE FOR RADIO FREQUENCY IDENTIFICATION CIRCUIT
MP3847W	MP3847WOKR	KR	20147012756	5/12/2014	10-1956873	3/5/2019	In Force	ANTENNA INTERFACE FOR RADIO FREQUENCY IDENTIFICATION CIRCUIT
MP3851	MP3851	US	13/272,446	10/13/2011	8766954	8/5/2014	In Force	DIMMING CONTROL FOR ELECTRONIC LAMP
MP3851W	MP3851WO	WO	PCT/US2011/05	10/13/2011			Pending	DIMMING CONTROL FOR ELECTRONIC LAMP
MP3851W	MP3851WOCN	CN	201180049638.1	10/13/2011	ZL2011800496	4/20/2016	In Force	AN APPARATUS AND METHOD FOR DIMMING CONTROL FOR A LAMP

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MP3851W	MP3851WOEP	EP	1183385.5	10/13/2011			Pending	Dimming Control for Electronic Lamp
MP3852	MP3852	US	13/243,459	9/23/2011	856435	10/22/2013	In Force	LOW POWER PAD
MP3855	MP3855CN	CN	201110339541.1	10/28/2011	Z12011103395	8/17/2016	In Force	SCALABLE EMBEDDED MEMORY PROGRAMMING
MP3856	MP3856	US	13/249,786	9/30/2011	8730504	5/20/2014	In Force	Image Acquisition Using Cloud Services
MP3856W	MP3856WO	WO	PCT/US2011/05	10/4/2011			Pending	Image Acquisition Using Cloud Services
MP3856W	MP3856WOCN	CN	201180050762.1	10/4/2011	Z12011800507	6/1/2016	In Force	IMAGE ACQUISITION USING CLOUD SERVICES
MP3856W	MP3856WOEP	EP	11771343.8	10/4/2011			Pending	Image Acquisition Using Cloud Services
MP3856W	MP3856WOJP	JP	2013-534934	10/4/2011	JP5885111	2/19/2016	In Force	IMAGE ACQUISITION USING CLOUD SERVICES
MP3856W	MP3856WOKR	KR	1020137012921	10/4/2011	101853041	4/27/2018	In Force	Image Acquisition Using Cloud Services
MP3857	MP3857	US	13/249,740	9/30/2011	8817662	8/26/2014	In Force	Pre-Association Discovery
MP3857	MP3857C1	US	14/466,687	8/22/2014	9332488	5/3/2016	In Force	Pre-Association Discovery
MP3857W	MP3857WO	WO	PCT/US2011/05	9/30/2011			Pending	Pre-Association Discovery
MP3857W	MP3857WOCN	CN	201180050795.1	9/30/2011	Z12011800507	11/30/2016	In Force	PRE-ASSOCIATION DISCOVERY
MP3857W	MP3857WOEP	EP	11781679.3	9/30/2011	2630827	11/21/2018	In Force	Pre-Association Discovery
MP3857W	MP3857WOEPDE	DE	11781679.3	9/30/2011	602011105411	11/21/2018	In Force	Pre-Association Discovery
MP3857W	MP3857WOEPFR	FR	11781679.3	9/30/2011	2630827	11/21/2018	In Force	Pre-Association Discovery
MP3857W	MP3857WOEPGB	GB	11781679.3	9/30/2011	2630827	11/21/2018	In Force	Pre-Association Discovery
MP3857W	MP3857WOJP	JP	2013-534930	9/30/2011	JP5776128	7/17/2015	In Force	PRE-ASSOCIATION DISCOVERY
MP3857W	MP3857WOKR	KR	1020137012920	9/30/2011	101616491	4/22/2016	In Force	Pre-Association Discovery
MP3860	MP3860	US	13/269,486	10/7/2011	8732283	5/20/2014	In Force	MANAGEMENT OF NETWORK MEMBERSHIP
MP3861	MP3861	US	13/276,525	10/19/2011	8667361	3/4/2014	In Force	SYSTEMS AND METHODS FOR PERFORMING BIT FLIPPING IN AN LDP
MP3862	MP3862	US	13/272,745	10/13/2011	8996892	3/31/2015	In Force	Power Smart Peripheral Devices With External Power Supplies
MP3863	MP3863	US	13/277,140	10/19/2011	8946890	2/3/2015	In Force	POWER/GROUND LAYOUT FOR CHIPS
MP3863T	MP3863TW	TW	100138127	10/20/2011	1566366	1/11/2017	In Force	IMPROVED POWER/GROUND LAYOUT
MP3863W	MP3863WO	WO	PCT/US2011/05	10/20/2011			Pending	POWER/GROUND LAYOUT FOR CHIPS
MP3863W	MP3863WOCN	CN	201180061291.1	10/20/2011	Z12011800612	10/26/2016	In Force	POWER/GROUND LAYOUT FOR CHIPS
MP3863W	MP3863WOKR	KR	10-2013-701273	10/20/2011	101888176	8/14/2018	In Force	POWER/GROUND LAYOUT FOR CHIPS
MP3864	MP3864	US	13/276,371	10/19/2011	8595406	11/26/2013	In Force	USB-TO-SATA HIGH-SPEED BRIDGE
MP3864	MP3864C1	US	14/090,182	11/26/2013	9471526	10/18/2016	In Force	Bridge Between Two Different Controllers For Transferring Data Betw
MP3864T	MP3864TW	TW	100138373	10/21/2011	1541652	7/11/2016	In Force	USB-To-SATA High-Speed Bridge
MP3864W	MP3864WO	WO	PCT/US2011/05	10/19/2011			Pending	USB-To-SATA High-Speed Bridge
MP3864W	MP3864WOCN	CN	201180050796.1	10/19/2011	Z12011800507	8/3/2016	In Force	USB-TO-SATA HIGH-SPEED BRIDGE
MP3864W	MP3864WOEP	EP	EP11779511.2	10/19/2011	2630565	12/12/2018	In Force	USB-to-Sata High-Speed Bridge
MP3864W	MP3864WOEPDE	DE	EP11779511.2	10/19/2011	602011054784	12/12/2018	In Force	USB-to-Sata High-Speed Bridge

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MP3864W	MP3864WOEPFR	FR	EP11779511.2	10/19/2011	2630565	12/12/2018	In Force	USB-to-Sata High-Speed Bridge
MP3864W	MP3864WOEPGB	GB	EP11779511.2	10/19/2011	2630565	12/12/2018	In Force	USB-to-Sata High-Speed Bridge
MP3867	MP3867	US	13/277,505	10/20/2011	8461886	6/11/2013	In Force	CIRCUIT AND CIRCUIT METHOD FOR REDUCTION OF PFD NOISE CON
MP3870	MP3870	US	13/274,707	10/17/2011	8868804	10/21/2014	In Force	Unified I/O Adapter
MP3870W	MP3870WO	WO	PCT/US2011/05	10/17/2011			Pending	Unified I/O Adapter
MP3870W	MP3870WOCN	CN	201180048076.1	10/17/2011	Z12011800480	8/3/2016	In Force	UNIFIED I/O ADAPTER
MP3870W	MP3870WOCEP	EP	11776655	10/17/2011	2630579	12/30/2015	In Force	Unified I/O Adapter
MP3870W	MP3870WOCR	KR	10-2013-70118	10/17/2011	101862803	5/30/2018	In Force	Unified I/O Adapter
MP3870W	MP3870WUDE	DE	11776655.0	10/17/2011	602011022306	12/30/2015	In Force	UNIFIED I/O ADAPTER
MP3870W	MP3870WUFR	FR	11776655.0	10/17/2011	2630579	12/30/2015	In Force	UNIFIED I/O ADAPTER
MP3870W	MP3870WUGB	GB	11776655.0	10/17/2011	2630579	12/30/2015	In Force	UNIFIED I/O ADAPTER
MP3874	MP3874	US	13/279,914	10/24/2011	8892809	11/18/2014	In Force	DATA COMPRESSION AND ENCODING IN A MEMORY SYSTEM
MP3874	MP3874CN	CN	201110342639.4	10/25/2011	Z12011103426	9/21/2016	In Force	DATA COMPRESSION AND ENCODING IN A MEMORY SYSTEM
MP3875	MP3875	US	13/273,487	10/14/2011	8467750	6/18/2013	In Force	GAIN CONTROL IN A SHARED RF FRONT-END PATH FOR DIFFERENT S
MP3875	MP3875C1	US	13/903,324	5/28/2013	8886013	4/11/2014	In Force	GAIN CONTROL IN A SHARED RF FRONT-END PATH FOR DIFFERENT S
MP3875T	MP3875TW	TW	100137804	10/18/2011	1513241	12/11/2015	In Force	GAIN CONTROL IN A SHARED RF FRONT-END PATH FOR DIFFERENT S
MP3876	MP3876	US	13/269,441	10/7/2011	8812723	8/19/2014	In Force	ASSIGNMENT OF NETWORK ADDRESSES
MP3876W	MP3876WO	WO	PCT/US2011/05	10/7/2011			Pending	ASSIGNMENT OF NETWORK ADDRESSES
MP3876W	MP3876WQJP	JP	2013-533902	10/7/2011	5971488	7/22/2016	In Force	ASSIGNMENT OF NETWORK ADDRESSES
MP3877	MP3877	US	13/165,832	6/22/2011	8539288	9/17/2013	In Force	METHODS AND APPARATUS FOR MULTIPLE INPUT MULTIPLE OUTPU
MP3877	MP3877C1	US	14/016,425	9/3/2013	8769352	7/1/2014	In Force	METHODS AND APPARATUS FOR MULTIPLE INPUT MULTIPLE OUTPU
MP3878W	MP3878WO	WO	PCT/US2011/05	10/21/2011			Pending	Cluster Cache Coherency Protocol
MP3878W	MP3878WOCN	CN	201180048410.1	10/5/2011			In Force	Cluster Cache Coherency Protocol
MP3878W	MP3878WOCR	KR	10-2013-701045	10/21/2011			Pending	Cluster Cache Coherency Protocol
MP3880	MP3880	US	13/332,190	12/20/2011	8514023	8/20/2013	In Force	ACCURATE BIAS TRACKING FOR PROCESS VARIATION AND SUPPLY M
MP3880W	MP3880WO	WO	PCT/US2011/06	12/20/2011			Pending	ACCURATE BIAS TRACKING FOR PROCESS VARIATION AND SUPPLY M
MP3880W	MP3880WOCN	CN	201180062253.1	12/20/2011	Z12011800622	7/20/2016	In Force	ACCURATE BIAS TRACKING FOR PROCESS VARIATION AND SUPPLY M
MP3880W	MP3880WOCEP	EP	11824285.8	12/20/2011			Pending	Accurate Bias Tracking for Process Variation and Supply Modulation
MP3890W	MP3890WQJP	JP	2013-545532	12/20/2011	5907514	4/1/2016	In Force	ACCURATE BIAS TRACKING FOR PROCESS VARIATION AND SUPPLY M
MP3890W	MP3890WOCR	KR	10-2013-701670	12/20/2011	10-1761308	7/19/2017	In Force	ACCURATE BIAS TRACKING FOR PROCESS VARIATION AND SUPPLY M
MP3881	MP3881C1	US	14/224,153	3/25/2014	9246451	1/26/2016	In Force	Power Amplifiers With Push-Pull Transistors, Capacitive Coupling For
MP3881W	MP3881WO	WO	PCT/US2011/06	12/23/2011			Pending	CMOS PUSH-PULL POWER AMPLIFIER WITH EVEN-HARMONIC CANCE
MP3882W	MP3882WO	WO	PCT/US2011/06	12/23/2011			Pending	Techniques On Input Transformer To Push The OP1DB Higher In Pow
MP3883W	MP3883WO	WO	PCT/US2011/06	12/23/2011			Pending	TECHNIQUES TO IMPROVE THE STRESS ISSUE IN CASCODE POWER A

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MP3884	MP3884	US	13/332,200	12/20/2011	8400232	3/19/2013	In Force	Figure 8 Balun
MP3884	MP3884C1	US	13/836,183	3/18/2013	8791769	7/29/2014	In Force	Figure 8 Balun
MP3884	MP3884C1C1	US	14/339,976	7/24/2014	9035713	5/19/2015	In Force	CONDUCTOR WINDING AND INDUCTORS ARRANGED TO FORM A BAL
MP3884W	MP3884WO	WO	PCT/US2011/06	12/20/2011			Pending	Figure 8 Balun
MP3884W	MP3884WOCN	CN	201180061095.1	12/20/2011	ZL2011800610	8/19/2015	In Force	FIGURE 8 BALUN
MP3884W	MP3884WOEP	EP	11822901.2	12/20/2011	2656433	8/8/2018	In Force	Figure 8 Balun
MP3884W	MP3884WOEPDE	DE	11822901.2	12/20/2011	602011050917	8/8/2018	In Force	Figure 8 Balun
MP3884W	MP3884WOEPFR	FR	11822901.2	12/20/2011	2656433	8/8/2018	In Force	Figure 8 Balun
MP3884W	MP3884WOEPGB	GB	11822901.2	12/20/2011	2656433	8/8/2018	In Force	Figure 8 Balun
MP3884W	MP3884WOJP	JP	2013-545530	12/20/2011	5686353	1/30/2015	In Force	FIGURE 8 BALUN
MP3884W	MP3884WOKR	KR	1020137016841	12/20/2011	101904364	11/30/2018	In Force	Figure 8 Balun
MP3885	MP3885CN	CN	201110038800.1	2/9/2011			Pending	RECESSED SEMICONDUCTOR SUBSTRATES AND ASSOCIATED TECHNI
MP3885	MP3885D1	US	14/715,170	5/18/2015	9391045	7/12/2016	In Force	RECESSED SEMICONDUCTOR SUBSTRATES AND ASSOCIATED TECHNI
MP3885T	MP3885TW	TW	100103448	1/28/2011	1451505	9/1/2014	In Force	RECESSED SEMICONDUCTOR SUBSTRATES AND ASSOCIATED TECHNI
MP3886	MP3886CN	CN	201110038808.1	2/9/2011			Pending	TECHNIQUES AND CONFIGURATIONS FOR RECESSED SEMICONDUCT
MP3886T	MP3886TW	TW	100103443	1/28/2011	1425581	2/1/2014	In Force	TECHNIQUES AND CONFIGURATIONS FOR RECESSED SEMICONDUCT
MP3887	MP3887	US	13/220,840	8/30/2011	9407200	8/2/2016	In Force	Crystal Oscillator With Low-Power Mode
MP3887W	MP3887WO	WO	PCT/US2011/04	8/30/2011			Pending	Crystal Oscillator With Low-Power Mode
MP3887W	MP3887WOCN	CN	201180051715.1	8/30/2011	ZL2011800517	6/23/2017	In Force	CRYSTAL OSCILLATOR WITH LOW-POWER MODE
MP3887W	MP3887WOEP	EP	11760903.2	8/30/2011	2633619	3/15/2017	In Force	Crystal Oscillator with Low-Power Mode
MP3887W	MP3887WOKR	KR	1020137013206	8/30/2011	101706701	2/8/2017	In Force	Crystal Oscillator With Low-Power Mode
MP3887W	MP3887WUDE	DE	11760903.2	8/30/2011	602011035981	3/15/2017	In Force	CRYSTAL OSCILLATOR WITH LOW-POWER MODE
MP3887W	MP3887WUFR	FR	11760903.2	8/30/2011	2633619	3/15/2017	In Force	CRYSTAL OSCILLATOR WITH LOW-POWER MODE
MP3887W	MP3887WUGB	GB	11760903.2	8/30/2011	2633619	3/15/2017	In Force	CRYSTAL OSCILLATOR WITH LOW-POWER MODE
MP3893	MP3893	US	13/275,782	10/18/2011	8461934	6/11/2013	In Force	External Oscillator Detector
MP3893	MP3893C1	US	13/902,066	5/24/2013	9055460	6/23/2015	In Force	External Oscillator Detector
MP3897	MP3897	US	13/272,560	10/13/2011	8519756	8/27/2013	In Force	PLL DUAL EDGE LOCK DETECTOR
MP3897	MP3897C1	US	13/950,427	7/25/2013	8624642	1/7/2014	In Force	PLL DUAL EDGE LOCK DETECTOR
MP3897	MP3897C1C1	US	14/100,274	12/9/2013	8749283	6/10/2014	In Force	PLL DUAL EDGE LOCK DETECTOR
MP3897W	MP3897WO	WO	PCT/US2011/05	10/13/2011			Pending	PLL DUAL EDGE LOCK DETECTOR
MP3897W	MP3897WOCN	CN	201180049897.1	10/13/2011	ZL2011800498	2/15/2017	In Force	PLL DUAL EDGE LOCK DETECTOR
MP3897W	MP3897WOCND1	CN	201611208651.1	10/13/2011			Pending	PLL DUAL EDGE LOCK DETECTOR
MP3897W	MP3897WOEP	EP	11836846.3	10/13/2011	2633620	2/28/2018	In Force	PLL Dual Edge Lock Detector
MP3897W	MP3897WOKR	KR	10-2013-700953	10/13/2011	101811020	12/20/2017	In Force	PLL DUAL EDGE LOCK DETECTOR

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MP3897W	MP3897WUDE	DE	11836846.3	10/13/2011	607011046136	2/28/2018	In Force	PLL Dual Edge Lock Detector
MP3897W	MP3897WUFR	FR	11836846.3	10/13/2011	2633620	2/28/2018	In Force	PLL Dual Edge Lock Detector
MP3897W	MP3897WUGB	GB	11836846.3	10/13/2011	2633620	2/28/2018	In Force	PLL Dual Edge Lock Detector
MP3903W	MP3903WOCN	WO	PCT/US2011/05	10/17/2011			Pending	DITHER-SYNCHRONIZED IMAGE SCANNING
MP3903W	MP3903WOCN	CN	201180051724.1	10/17/2011			Pending	DITHER-SYNCHRONIZED IMAGE SCANNING
MP3903W	MP3903WCEP	EP	11779900.7	10/17/2011			Pending	Dither-Synchronized Image Scanning
MP3903W	MP3903WOLP	JP	2013-536660	10/17/2011			Pending	DITHER-SYNCHRONIZED IMAGE SCANNING
MP3903W	MP3903WOKR	KR	10-2013-701187	10/17/2011			Pending	DITHER-SYNCHRONIZED IMAGE SCANNING
MP3908	MP3908	US	13/285,327	10/31/2011	8875000	10/28/2014	In Force	METHODS AND SYSTEMS FOR ENCODING AND DECODING IN TRELLIS
MP3908	MP3908CN	CN	201110351166.4	11/1/2011	ZL2011103511	8/19/2015	In Force	METHODS AND SYSTEMS FOR ENCODING AND DECODING IN TRELLIS
MP3909	MP3909	US	13/285,723	10/31/2011	8769380	7/1/2014	In Force	METHODS AND APPARATUS FOR ERROR RECOVERY IN MEMORY SYS
MP3909	MP3909C1	US	14/310,673	6/20/2014	9397698	7/19/2016	In Force	METHODS AND APPARATUS FOR ERROR RECOVERY IN MEMORY SYS
MP3910	MP3910	US	13/288,276	11/3/2011	8879185	11/4/2014	In Force	DISK SYNCHRONOUS WRITE ARCHITECTURE FOR BIT-PATTERNED REC
MP3913	MP3913	US	13/285,188	10/31/2011	8873484	10/28/2014	In Force	Hybrid Beamforming Architecture
MP3913	MP3913C1	US	14/524,272	10/27/2014	9369192	6/14/2016	In Force	Hybrid Beamforming Architecture
MP3913	MP3913C1C1	US	15/180,235	6/13/2016	9780857	10/3/2017	In Force	HYBRID BEAMFORMING ARCHITECTURE
MP3924	MP3924	US	13/292,640	11/9/2011	9363036	6/7/2016	In Force	METHODS AND APPARATUS FOR DEFECT DETECTION AND CORRECTI
MP3927	MP3927	US	13/290,672	11/7/2011	8497787	7/30/2013	In Force	SYSTEMS AND METHODS FOR PERFORMING EFFICIENT DECODING US
MP3927	MP3927CN	CN	201110368928.1	11/14/2011	ZL2011103689	4/1/2015	In Force	SYSTEMS AND METHODS FOR PERFORMING EFFICIENT DECODING US
MP3930	MP3930	US	13/291,782	11/8/2011	8698555	4/15/2014	In Force	Low Distortion Variable Gain Amplifier (VGA)
MP3930I	MP3930IP	JP	2011-247734	11/11/2011	5799462	9/4/2015	In Force	LOW DISTORTION VARIABLE GAIN AMPLIFIER(VGA)
MP3931	MP3931	US	13/300,323	11/18/2011	8694868	4/8/2014	In Force	SYSTEMS AND METHODS FOR PERFORMING MULTI-STATE BIT FLIPP
MP3933	MP3933CN	CN	2011103792615	11/18/2011	ZL2011103792	2/3/2016	In Force	SOXc HA Mirroring Solution in Cloudstorage Environment
MP3934	MP3934	US	13/297,420	11/16/2011	8571497	10/29/2013	In Force	Closed-Loop Power Control in Conjunction With Adaptive Power Am
MP3936	MP3936	US	13/301,199	11/21/2011	8930781	1/6/2015	In Force	METHOD AND APPARATUS FOR DEFECT RECOVERY
MP3936W	MP3936WO	WO	PCT/US2011/06	11/21/2011			Pending	METHOD AND APPARATUS FOR DEFECT RECOVERY
MP3937	MP3937	US	13/299,616	11/18/2011	8495479	7/23/2013	In Force	Defect Detection And Correction Via Monitoring Of Syndromes And B
MP3937	MP3937C1	US	13/947,611	7/22/2013	8887032	11/11/2014	In Force	Defect Detection And Correction Via Monitoring Of Syndromes And B
MP3939	MP3939	US	13/302,837	11/22/2011	8514651	8/20/2013	In Force	SHARING ACCESS TO A MEMORY AMONG CLIENTS
MP3939W	MP3939WO	WO	PCT/US2011/06	11/22/2011			Pending	SHARING ACCESS TO A MEMORY AMONG CLIENTS
MP3939W	MP3939WOCN	CN	201180064707.1	11/22/2011	ZL2011800647	8/24/2016	In Force	METHOD AND APPARATUS FOR SHARING ACCESS TO A MEMORY AM
MP3939W	MP3939WCEP	EP	11802819.0	11/22/2011	2643763	9/4/2019	In Force	Sharing Access to a Memory Among Clients
MP3939W	MP3939WCEPDF	DE	11802819.0	11/22/2011	602011061831	9/4/2019	In Force	Sharing Access to a Memory Among Clients
MP3939W	MP3939WCEPFR	FR	11802819.0	11/22/2011	2643763	9/4/2019	In Force	Sharing Access to a Memory Among Clients

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MP3939W	MP3939WCEPGB	GB	11802819.0	11/22/2011	2643763	9/4/2019	In Force	Sharing Access to a Memory Among Clients
MP3940	MP3940	US	13/297,078	11/15/2011	8918625	12/23/2014	In Force	Speculative scheduling of memory instructions in out-of-order proces
MP3941	MP3941	US	13/115,865	5/25/2011	9565035	2/7/2017	In Force	ENHANCED AUDIO VIDEO BRIDGING (AVB) METHODS AND APPARAT
MP3941	MP3941C1	US	13/115,868	5/25/2011	9584342	2/28/2017	In Force	ENHANCED AUDIO VIDEO BRIDGING (AVB) METHODS AND APPARAT
MP3941	MP3941C1C1	US	15/425,401	2/6/2017	9887938	2/6/2018	In Force	ENHANCED AUDIO VIDEO BRIDGING (AVB) METHODS AND APPARAT
MP3941	MP3941C1C1C1	US	15/888,690	2/5/2018	10032556	10/1/2019	In Force	ENHANCED AUDIO VIDEO BRIDGING (AVB) METHODS AND APPARAT
MP3942	MP3942	US	13/115,682	5/25/2011	9565118	2/7/2017	In Force	METHODS AND APPARATUS FOR HANDLING MANAGEMENT PACKET
MP3943	MP3943	US	12/964,534	12/9/2010	8503367	8/6/2013	In Force	WIRELESS COMMUNICATION SIGNALING FOR AGGREGATE DATA UNI
MP3943	MP3943C1	US	13/959,585	8/5/2013	9125235	9/1/2015	In Force	WIRELESS COMMUNICATION SIGNALING FOR AGGREGATE DATA UNI
MP3943W	MP3943WO	WO	PCT/US2010/05	12/9/2010			Pending	WIRELESS COMMUNICATION SIGNALING FOR AGGREGATE DATA UNI
MP3943W	MP3943WOCN	CN	201080056092.1	12/9/2010	ZL2010800560	1/28/2015	In Force	WIRELESS COMMUNICATION SIGNALING FOR AGGREGATE DATA U
MP3943W	MP3943WCEP	EP	10836719.4	12/9/2010	2510661	12/6/2017	In Force	Wireless Communication Signaling for Aggregate Data Units
MP3943W	MP3943WOJP	JP	2012-543294	12/9/2010	5666549	9/5/2014	In Force	WIRELESS COMMUNICATION SIGNALING FOR AGGREGATE DATA UNI
MP3943W	MP3943WOKR	KR	10-2012-701495	12/9/2010	101798163	11/15/2017	In Force	WIRELESS COMMUNICATION SIGNALING FOR AGGREGATE DATA UNI
MP3943W	MP3943WUDE	DE	10836719.4	12/9/2010	603010047256	12/6/2017	In Force	WIRELESS COMMUNICATION SIGNALING FOR AGGREGATE DATA UNI
MP3943W	MP3943WUFR	FR	10836719.4	12/9/2010	2510661	12/6/2017	In Force	WIRELESS COMMUNICATION SIGNALING FOR AGGREGATE DATA UNI
MP3943W	MP3943WUGB	GB	10836719.4	12/9/2010	2510661	12/6/2017	In Force	WIRELESS COMMUNICATION SIGNALING FOR AGGREGATE DATA UNI
MP3946	MP3946C1	US	14/165,893	1/28/2014	9166740	10/20/2015	In Force	Low Complexity Distance Metrics for Maximum Likelihood Receivers
MP3947	MP3947C1	US	14/065,158	10/28/2013	9148188	9/29/2015	In Force	SYSTEM AND METHOD FOR TUNING A RECEIVER BY ITERATIVELY DIVI
MP3948	MP3948	US	13/325,756	12/14/2011	8736999	5/27/2014	In Force	METHOD AND APPARATUS FOR COMMON BASE AMPLIFIER RECOVER
MP3949W	MP3949WO	WO	PCT/US2011/06	12/23/2011			Pending	Reducing Voltage Stress in A Flyback Converter Design
MP3949W	MP3949WOCN	CN	201180062616.1	12/23/2011			Pending	REDUCING VOLTAGE STRESS IN A FLYBACK CONVERTER DESIGN
MP3949W	MP3949WCEP	EP	11851028.8	12/23/2011			Pending	Reducing Voltage Stress in a Flyback Converter Design
MP3949W	MP3949WOJP	JP	2013-546451	12/23/2011	6113077	3/24/2017	In Force	REDUCING VOLTAGE STRESS IN A FLYBACK CONVERTER DESIGN
MP3949W	MP3949WOKR	KR	10201337018831	12/23/2011			Pending	Reducing Voltage Stress in A Flyback Converter Design
MP3950	MP3950	US	13/308,325	11/30/2011	8688066	4/1/2014	In Force	RF PEAK DETECTION SCHEME USING BASEBAND CIRCUITS
MP3950	MP3950C1	US	14/199,725	3/6/2014	8948717	2/3/2015	In Force	RF PEAK DETECTION SCHEME USING BASEBAND CIRCUITS
MP3950W	MP3950WO	WO	PCT/US2011/06	11/30/2011			Pending	RF PEAK DETECTION SCHEME USING BASEBAND CIRCUITS
MP3950W	MP3950WOCN	CN	201180058901	11/30/2011	ZL2011800589	7/1/2015	In Force	RF PEAK DETECTION SCHEME USING BASEBAND CIRCUITS
MP3951	MP3951C1	US	14/062,308	10/24/2013	9130582	9/8/2015	In Force	Systems And Methods For Correcting An Offset At An Output Of A Di
MP3951W	MP3951WO	WO	PCT/US2011/06	12/7/2011			Pending	Digital To Analog Converter Circuits And Methods
MP3951W	MP3951WOCN	CN	201180058903.3	12/7/2011	ZL2011800589	9/21/2016	In Force	DIGITAL TO ANALOG CONVERTER CIRCUITS AND METHODS
MP3951W	MP3951WCEP	EP	11847104.4	12/7/2011			Pending	Digital to Analog Converter Circuits and Methods
MP3953	MP3953	US	13/307,471	11/30/2011	8767616	7/1/2014	In Force	Synchronized Interference Mitigation Scheme for Heterogeneous Wir

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MP39533	MP39533C1	US	14/320,501	6/30/2014	9402265	7/26/2016	In Force	SYNCHRONIZED INTERFERENCE MITIGATION SCHEME FOR HETEROG
MP39534	MP39534C1	US	14/039,076	9/27/2013	8816763	8/26/2014	In Force	INTEGRATOR INPUT ERROR CORRECTION CIRCUIT AND CIRCUIT MET
MP3954W	MP3954WO	WO	PCT/US2011/06	12/7/2011			Pending	INTEGRATOR INPUT ERROR CORRECTION CIRCUIT AND CIRCUIT MET
MP3955	MP3955C1C1	US	14/070,889	11/4/2013	8841961	9/23/2014	In Force	Apparatus For Powering A Device
MP3955	MP3955C1C1	US	14/492,384	9/22/2014	9501133	11/22/2016	In Force	Method And Apparatus For Transitioning A Device Between Operatin
MP3955W	MP3955WO	WO	PCT/US2011/06	12/12/2011			Pending	Fast Power Up Comparator
MP3955W	MP3955WOCN	CN	201180059748.1	12/12/2011	ZL2011800597	5/24/2017	In Force	FAST POWER UP COMPARATOR
MP3958	MP3958	US	13/314,215	12/8/2011	9237459	1/12/2016	In Force	SYSTEM AND METHOD FOR MEASURING CHARACTERISTICS OF NEIG
MP3960	MP3960	US	13/313,502	12/7/2011	9054787	6/9/2015	In Force	METHOD AND APPARATUS FOR ALIGNMENT IN NEIGHBORING NETW
MP3960W	MP3960WO	WO	PCT/IB2011/003	12/7/2011			Pending	METHOD AND APPARATUS FOR ALIGNMENT IN NEIGHBORING NETW
MP3960W	MP3960WOCN	CN	201180065619.1	12/7/2011	ZL2011800656	12/16/2015	In Force	METHOD AND APPARATUS FOR ALIGNMENT IN NEIGHBORING NETW
MP3963	MP3963	US	13/306,289	11/29/2011	8441320	5/14/2013	In Force	Cancellation Of Gain Change Due To Amplifier Self-Heating
MP3963	MP3963C1	US	13/893,867	5/14/2013	8823456	9/2/2014	In Force	Cancellation Of Gain Change Due To Amplifier Self-Heating
MP3963W	MP3963WO	WO	PCT/US2011/06	12/6/2011			Pending	CANCELLATION OF GAIN CHANGE DUE TO AMPLIFIER SELF-HEATING
MP3963W	MP3963WOCN	CN	201180059747.1	12/6/2011	ZL2011800597	1/20/2016	In Force	CANCELLATION OF GAIN CHANGE DUE TO AMPLIFIER SELF-HEATING
MP3963W	MP3963WOEP	EP	11802585.7	12/6/2011			Pending	Cancellation of Gain Change Due to Amplifier Self-Heating
MP3963W	MP3963WOKR	KR	1020137014592	12/6/2011	101850933	4/20/2018	In Force	Cancellation Of Gain Change Due To Amplifier Self-Heating
MP3965	MP3965	US	13/331,749	12/20/2011	8788781	7/22/2014	In Force	DESCRIPTOR SCHEDULER
MP3965T	MP3965TW	TW	100147290	12/20/2011	1536268	6/1/2016	In Force	DESCRIPTOR SCHEDULER
MP3965W	MP3965WO	WO	PCT/US2011/06	12/19/2011			Pending	DESCRIPTOR SCHEDULER
MP3965W	MP3965WOCN	CN	201180066712.1	12/19/2011	ZL2011800667	9/18/2018	In Force	DESCRIPTOR SCHEDULER
MP3965W	MP3965WOEP	EP	11811236.6	12/19/2011	2656216	12/19/2018	In Force	Descriptor Scheduler
MP3965W	MP3965WOEPDE	DE	11811236.6	12/19/2011	6020113054994	12/19/2018	In Force	Descriptor Scheduler
MP3965W	MP3965WOEPFR	FR	11811236.6	12/19/2011	2656216	12/19/2018	In Force	Descriptor Scheduler
MP3965W	MP3965WOEPGB	GB	11811236.6	12/19/2011	2656216	12/19/2018	In Force	Descriptor Scheduler
MP3970	MP3970	US	13/311,988	12/6/2011	9195524	11/24/2015	In Force	Hardware Support for Performance Analysis
MP3973	MP3973	US	13/331,775	12/20/2011	8581765	11/12/2013	In Force	Low-Memory-Usage Arbitrary Waveform Representation of Generati
MP3973	MP3973C1	US	14/072,532	11/5/2013	8736477	5/27/2014	In Force	Low-Memory-Usage Arbitrary Waveform Representation of Generati
MP3973W	MP3973WO	WO	PCT/US2011/06	12/20/2011			Pending	LOW-MEMORY-USAGE ARBITRARY WAVEFORM REPRESENTATION O
MP3973W	MP3973WOCN	CN	201180061721.1	12/20/2011	ZL2011800617	4/6/2016	In Force	LOW-MEMORY-USAGE ARBITRARY WAVEFORM REPRESENTATION O
MP3974W	MP3974WO	WO	PCT/US2011/06	12/12/2011			Pending	IMAGE PROCESSING IN CONTONE SPACE AND HALFTONE SPACE
MP3976W	MP3976WO	WO	PCT/US2011/06	12/16/2011			Pending	Systems and Methods for Converting a 2D Image to a 3D Image
MP3977	MP3977CN	CN	201110459156.1	12/23/2011	ZL2011104591	12/14/2016	In Force	METHOD AND APPARATUS FOR VIDEO FRAME ROTATION
MP3979	MP3979	US	13/335,250	12/22/2011	8686547	4/1/2014	In Force	STACK DIE STRUCTURE FOR STRESS REDUCTION AND FACILITATION O

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MP3980	MP3980D1	US	14/739,699	6/15/2015	9397036	7/19/2016	In Force	SEMICONDUCTOR PACKAGE ASSEMBLY
MP3983	MP3983	US	13/333,058	12/21/2011	8564471	10/22/2013	In Force	High Resolution Sampling-Based Time to Digital Converter
MP3985	MP3985	US	13/336,165	12/23/2011	8908814	12/9/2014	In Force	Low Latency SIMD Architecture for Iterative Decoders
MP3985W	MP3985WO	WO	PCT/US2011/06	12/23/2011			Pending	LOW LATENCY SIMD ARCHITECTURE FOR ITERATIVE DECODERS
MP3985W	MP3985WOCN	CN	201180049082.1	12/23/2011	ZL2011800490	10/27/2017	In Force	LOW LATENCY SIMD ARCHITECTURE FOR MULTIPLE ITERATIVE DECO
MP3988	MP3988	US	13/342,992	1/4/2012	8724758	5/13/2014	In Force	POWER-EFFICIENT VARIABLE-CLOCK-RATE DIGRF INTERFACE
MP3988E	MP3988EP	EP	EP12150216.5	1/5/2012			Pending	Power-Efficient DIGRF Interface
MP3991	MP3991	US	13/350,212	1/13/2012	9360571	3/29/2016	In Force	METHOD AND APPARATUS FOR HANDLING MULTICAST TRAFFIC
MP3991	MP3991C1	US	15/053,125	2/25/2016	9705789	7/11/2017	In Force	METHOD AND APPARATUS FOR HANDLING MULTICAST TRAFFIC
MP3991W	MP3991WO	WO	PCT/IB2012/006	1/13/2012			Pending	METHOD AND APPARATUS FOR HANDLING MULTICAST TRAFFIC
MP3992	MP3992	US	13/346,941	1/10/2012	8730605	5/20/2014	In Force	Write Zero Phase Shift For Phase Synchronization In Bit Patterned Re
MP3992	MP3992C1	US	14/280,934	5/19/2014	9202515	12/1/2015	In Force	Apparatus And Method For Adjusting A Phase Of A Data Clock Signal
MP3994	MP3994C1C1	US	14/723,565	5/28/2015	9391639	7/12/2016	In Force	LDPC Multi-Decoder Architectures
MP3994W	MP3994WO	WO	PCT/US2012/02	1/11/2012			Pending	LDPC Multi-Decoder Architectures
MP3994W	MP3994WOCN	CN	201280003336.1	1/11/2012	ZL2012800033	11/9/2016	In Force	LDPC MULTI-DECODER ARCHITECTURES
MP3995	MP3995	US	13/346,737	1/10/2012	9048970	6/2/2015	In Force	FEEDBACK FOR COOPERATIVE MULTIPPOINT TRANSMISSION SYSTEMS
MP3998	MP3998	US	13/348,397	1/11/2012	8624673	1/7/2014	In Force	Self-Biasing Radio Frequency Circuitry
MP3998	MP3998C1	US	14/134,223	12/19/2013	9362872	6/7/2016	In Force	Self-Biasing Radio Frequency Circuitry
MP3998T	MP3998TW	TW	101101224	1/12/2012	1552519	10/1/2016	In Force	SELF-BIASING RADIO FREQUENCY CIRCUITRY
MP3998W	MP3998WO	WO	PCT/US2012/02	1/12/2012			Pending	SELF-BIASING RADIO FREQUENCY CIRCUITRY
MP3998W	MP3998WOCN	CN	201280005541.1	1/12/2012	ZL2012800055	3/2/2016	In Force	SELF-BIASING RADIO FREQUENCY CIRCUITRY
MP3998W	MP3998WOKR	KR	1020137021823	1/12/2012	101867546	6/15/2018	In Force	Self-Biasing Radio Frequency Circuitry
MP4003	MP4003C1	US	14/257,455	4/21/2014	9252708	2/2/2016	In Force	CLASS-AB XTAL CIRCUIT
MP4004	MP4004C1	US	14/218,256	3/18/2014	9178497	11/3/2015	In Force	PRECISION OSCILLATOR WITH TEMPERATURE COMPENSATION
MP4004W	MP4004WO	WO	PCT/US2012/02	1/13/2012			Pending	PRECISION OSCILLATOR WITH TEMPERATURE COMPENSATION
MP4009	MP4009	US	13/349,756	1/13/2012	8508391	8/13/2013	In Force	Code Word Formatter of Shortened Non-Binary Linear Error Correcti
MP4009	MP4009C1	US	13/962,159	8/8/2013	9160364	10/13/2015	In Force	Code Word Formatter of Shortened Non-Binary Linear Error Correcti
MP4011	MP4011	US	13/354,256	1/19/2012	8989252	3/24/2015	In Force	METHODS AND APPARATUS FOR POWER EFFICIENT ITERATIVE EQUA
MP4012	MP4012C1	US	14/323,067	7/3/2014	9323269	4/26/2016	In Force	Voltage Regulator With Positive And Negative Power Supply Spike Re
MP4013	MP4013	US	13/352,744	1/18/2012	8570681	10/29/2013	In Force	Dual Channel HDD Systems And Methods
MP4013	MP4013C1	US	14/064,449	10/28/2013	8705194	4/22/2014	In Force	Method And System For Reading From Or Writing To Multiple Surfac
MP4013W	MP4013WO	WO	PCT/US2012/02	1/18/2012			Pending	Dual Channel HDD Systems And Methods
MP4013W	MP4013WOCN	CN	201280005392.1	1/18/2012	ZL2012800053	11/9/2016	In Force	DUAL CHANNEL HDD SYSTEMS AND METHODS
MP4013W	MP4013WOKP	JP	2013-550559	1/18/2012	JP6083677	2/3/2017	In Force	DUAL CHANNEL HDD SYSTEMS AND METHODS

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MP4013W	MP4013WOKR	KR	1020137021824	1/18/2012	101844497	4/3/2018	In Force	Dual Channel HDD Systems And Methods
MP4013W	MP4013WOCMY	MY	PI2013002567	1/18/2012	MY1635584	9/29/2017	In Force	Dual Channel HDD Systems And Methods
MP4013W	MP4013WOSG	SG	2013052634	1/18/2012	191903	1/29/2016	In Force	Dual Channel HDD Systems And Methods
MP4013W	MP4013WOTH	TH	1301004040	7/19/2013			Pending	Dual Channel HDD Systems And Methods
MP4014	MP4014	US	13/364,185	2/1/2012	8564342	10/22/2013	In Force	REFERENCE CLOCK COMPENSATION FOR FRACTIONAL-N PHASE LOCK
MP4014	MP4014C1	US	14/050,911	10/10/2013	8717074	5/6/2014	In Force	REFERENCE CLOCK COMPENSATION FOR FRACTIONAL-N PHASE LOCK
MP4014	MP4014C1C1	US	14/267,747	5/1/2014	9219484	12/22/2015	In Force	REFERENCE CLOCK COMPENSATION FOR FRACTIONAL-N PHASE LOCK
MP4014W	MP4014WOCN	WO	PCT/US2012/02	2/1/2012			Pending	REFERENCE CLOCK COMPENSATION FOR FRACTIONAL-N PHASE LOCK
MP4014W	MP4014WOCN	CN	2012800676591	2/1/2012	ZL2012800076	8/10/2016	In Force	REFERENCE CLOCK COMPENSATION FOR FRACTIONAL-N PHASE LOCK
MP4014W	MP4014WOKR	KR	10-2013-701897	2/1/2012	101829404	2/19/2018	In Force	REFERENCE CLOCK COMPENSATION FOR FRACTIONAL-N PHASE LOCK
MP4015	MP4015	US	13/025,101	2/10/2011	9320048	4/19/2016	In Force	WIRELESS COMMUNICATIONS WITH PRIMARY AND SECONDARY ACC
MP4015	MP4015C1	US	15/132,179	4/18/2016	9883522	1/30/2018	In Force	WIRELESS COMMUNICATIONS WITH PRIMARY AND SECONDARY ACC
MP4015W	MP4015WO	WO	PCT/US2011/02	2/10/2011			Pending	WIRELESS COMMUNICATIONS WITH PRIMARY AND SECONDARY ACC
MP4015W	MP4015WOCN	CN	2011800480771	2/10/2011	ZL2011800480	4/12/2017	In Force	WIRELESS COMMUNICATIONS WITH PRIMARY AND SECONDARY ACC
MP4015W	MP4015WOCF	EP	11820292.8	2/10/2011	2609696	5/15/2019	In Force	Wireless Communications with Primary and Secondary Access Catego
MP4015W	MP4015WOCFDE	DE	11820292.8	2/10/2011	603011059024	5/15/2019	In Force	Wireless Communications with Primary and Secondary Access Catego
MP4015W	MP4015WOCFFR	FR	11820292.8	2/10/2011	2609696	5/15/2019	In Force	Wireless Communications with Primary and Secondary Access Catego
MP4015W	MP4015WOCFGB	GB	11820292.8	2/10/2011	2609696	5/15/2019	In Force	Wireless Communications with Primary and Secondary Access Catego
MP4015W	MP4015WOCJP	JP	2013-525902	2/10/2011	JP5910948	4/8/2016	In Force	WIRELESS COMMUNICATIONS WITH PRIMARY AND SECONDARY ACC
MP4015W	MP4015WOKR	KR	10-2013-70074C	2/10/2011	10-1760074	7/14/2017	In Force	WIRELESS COMMUNICATIONS WITH PRIMARY AND SECONDARY ACC
MP4016	MP4016	US	13/350,969	1/16/2012	9130746	9/8/2015	In Force	SINGLE PAIR PHY WITH AUTO-NEGOTIATION
MP4016	MP4016C1	US	14/845,934	9/4/2015	9853803	12/26/2017	In Force	SINGLE PAIR PHY WITH AUTO-NEGOTIATION
MP4017	MP4017TW	TW	101102910	1/30/2012	1559695	11/21/2016	In Force	CIRCUIT INTERCONNECT WITH EQUALIZED CROSSTALK
MP4018	MP4018	US	13/359,336	1/26/2012	8867653	10/21/2014	In Force	PHYSICAL LAYER FRAME FORMAT FOR LONG RANGE WLAN
MP4018	MP4018C1	US	14/518,846	10/20/2014	9998950	6/12/2018	In Force	PHYSICAL LAYER FRAME FORMAT FOR LONG RANGE WLAN
MP4018W	MP4018WO	WO	PCT/US2012/02	1/26/2012			Pending	PHYSICAL LAYER FRAME FORMAT FOR LONG RANGE WLAN
MP4018W	MP4018WOCN	CN	2012800142811	1/26/2012	ZL2012800142	8/31/2016	In Force	PHYSICAL LAYER FRAME FORMAT FOR LONG RANGE WLAN
MP4018W	MP4018WOCF	EP	12703655.6	1/26/2012	2668736	4/25/2018	In Force	Physical Layer Frame Format for Long Range WLAN
MP4018W	MP4018WOCJP	JP	2013-551344	1/26/2012	JP5936280	5/20/2016	In Force	PHYSICAL LAYER FRAME FORMAT FOR LONG RANGE WLAN
MP4018W	MP4018WOKR	KR	10-2013-702205	1/26/2012	10-1967412	4/3/2019	In Force	PHYSICAL LAYER FRAME FORMAT FOR LONG RANGE WLAN
MP4018W	MP4018WUDE	DE	12703655.6	1/26/2012	2668736	4/25/2018	In Force	Physical Layer Frame Format for Long Range WLAN
MP4018W	MP4018WUFR	FR	12703655.6	1/26/2012	2668736	4/25/2018	In Force	Physical Layer Frame Format for Long Range WLAN
MP4018W	MP4018WUGB	GB	12703655.6	1/26/2012	2668736	4/25/2018	In Force	Physical Layer Frame Format for Long Range WLAN
MP4019	MP4019	US	13/358,405	1/25/2012	8673689	3/18/2014	In Force	SINGLE LAYER BGA SUBSTRATE PROCESS

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MP4019T	MP4019TW	TW	101102903	1/30/2012	1583267	5/11/2017	In Force	Single Layer BGA Substrate Process
MP4019W	MP4019WO	WO	PCT/US2012/02	1/26/2012			Pending	SINGLE LAYER BGA SUBSTRATE PROCESS
MP4019W	MP4019WOCN	CN	201280006939.1	1/26/2012	ZL2012800069	4/26/2017	In Force	SINGLE LAYER BGA SUBSTRATE PROCESS
MP4019W	MP4019WJOP	JP	2013-551346	1/26/2012	JP5999102	9/9/2016	In Force	SINGLE LAYER BGA SUBSTRATE PROCESS
MP4019W	MP4019WOKR	KR	10-2013-702081	1/26/2012	101938344	1/8/2019	In Force	Single Layer BGA Substrate Process
MP4020	MP4020	US	13/359,785	1/27/2012	8767240	7/1/2014	In Force	Method And Apparatus For Encoding Data To Perform Real-Time Re
MP4023	MP4023	US	13/358,203	1/25/2012	8812939	8/19/2014	In Force	SOFT DECODING SYSTEMS AND METHODS FOR FLASH BASED MEMO
MP4023	MP4023CN	CN	201210063709.1	1/29/2012	ZL2012100637	12/14/2016	In Force	SOFT DECODING SYSTEMS AND METHODS FOR FLASH BASED MEMO
MP4025	MP4025	US	13/353,373	1/19/2012	8767002	7/1/2014	In Force	Systems and Methods for Performing Color Adjustment of Pixels on
MP4025	MP4025C1	US	14/284,939	5/22/2014	9349345	5/24/2016	In Force	Systems and Methods for Performing Color Adjustment of Pixels on a
MP4025T	MP4025TW	TW	101102184	1/19/2012	1569255	2/1/2017	In Force	SYSTEMS AND METHODS FOR PERFORMING COLOR ADJUSTMENT OF
MP4025W	MP4025WO	WO	PCT/US2012/02	1/19/2012			Pending	SYSTEMS AND METHODS FOR PERFORMING COLOR ADJUSTMENTS O
MP4025W	MP4025WOCN	CN	201280007088.1	1/19/2012	ZL2012800070	9/7/2016	In Force	SYSTEMS AND METHODS FOR PERFORMING COLOR ADJUSTMENTS O
MP4026	MP4026	US	13/359,705	1/27/2012	8868000	10/21/2014	In Force	METHODS AND SYSTEMS FOR EFFICIENT DECODING OF CONCATENA
MP4026	MP4026CN	CN	2012100224401.1	1/31/2012	ZL2012100244	12/29/2017	In Force	METHODS AND SYSTEMS FOR EFFICIENT DECODING OF CONCATENA
MP4027	MP4027	US	13/357,567	1/24/2012	9026769	5/5/2015	In Force	Detecting and Reissuing of Loop Instructions in Reorder Structure
MP4028	MP4028C1	US	14/474,015	8/29/2014	9152558	10/6/2015	In Force	MAPPING DIFFERENT PORTIONS OF DATA TO DIFFERENT PAGES OF
MP4028W	MP4028WO	WO	PCT/US2012/02	1/30/2012			Pending	MAPPING DATA TO NON-VOLATILE MEMORY
MP4028W	MP4028WOCN	CN	201280007091.1	1/30/2012	ZL2012800070	8/24/2016	In Force	MAPPING DATA TO NON-VOLATILE MEMORY
MP4029	MP4029	US	13/356,796	1/24/2012	9101025	8/4/2015	In Force	Systems and Methods for Driving Light Emitting Diodes
MP4029	MP4029D1	US	14/815,212	7/31/2015	9313843	4/12/2016	In Force	Systems and Methods for Driving Light Emitting Diodes
MP4029W	MP4029WO	WO	PCT/US2012/02	1/24/2012			Pending	Systems and Methods for Driving Light Emitting Diodes
MP4030	MP4030	US	13/364,783	2/2/2012	8860973	11/4/2014	In Force	DETECTOR-DECODER INTERFACE FOR GFIQ ITERATIVE DECODING
MP4031	MP4031	US	13/366,064	2/3/2012	9130727	9/8/2015	In Force	CONTROL MODE PHY FOR WLAN
MP4031	MP4031C1	US	13/366,038	2/3/2012	8885740	11/11/2014	In Force	CONTROL MODE PHY FOR WLAN
MP4031	MP4031C2	US	14/846,186	9/4/2015	10397033	8/27/2019	In Force	METHOD AND APPARATUS FOR GENERATING A PHY DATA UNIT
MP4031	MP403111	US	14/044,710	10/2/2013	9178745	11/3/2015	In Force	CONTROL MODE PHY FOR WLAN
MP4031	MP403111C1	US	14/930,434	11/2/2015	9419849	8/16/2016	In Force	METHOD AND APPARATUS FOR GENERATING A PHY DATA UNIT
MP4031W	MP4031WO	WO	PCT/US2012/02	2/3/2012			Pending	CONTROL MODE PHY FOR WLAN
MP4031W	MP4031WOCN	CN	201280015282.1	2/3/2012	ZL2012800152	7/14/2017	In Force	CONTROL MODE PHY FOR WLAN
MP4031W	MP4031WOEP	EP	12703673.9	2/3/2012	2671337	4/11/2018	In Force	Control Mode PHY for WLAN
MP4031W	MP4031WOJP	JP	2013-552688	2/3/2012	JP6057435	12/16/2016	In Force	CONTROL MODE PHY FOR WLAN
MP4031W	MP4031WOKR	KR	10-2013-702324	2/3/2012			Pending	CONTROL MODE PHY FOR WLAN
MP4031W	MP4031WUD1	EP	18150686.6	2/3/2012	3327965	9/25/2019	In Force	Control Mode PHY for WLAN

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MP4031W	MP4031WUD1DE	DE	18150686.6	2/3/2012	3327965	9/25/2019	In Force	Control Mode PHY for WLAN
MP4031W	MP4031WUD1FR	FR	18150686.6	2/3/2012	3327965	9/25/2019	In Force	Control Mode PHY for WLAN
MP4031W	MP4031WUD1GB	GB	18150686.6	2/3/2012	3327965	9/25/2019	In Force	Control Mode PHY for WLAN
MP4031W	MP4031WUDE	DE	12703673.9	2/3/2012	2671337	4/11/2018	In Force	Control Mode PHY for WLAN
MP4031W	MP4031WUFR	FR	12703673.9	2/3/2012	2671337	4/11/2018	In Force	Control Mode PHY for WLAN
MP4031W	MP4031WUGB	GB	12703673.9	2/3/2012	2671337	4/11/2018	In Force	Control Mode PHY for WLAN
MP4032	MP4032	US	13/370,960	2/10/2012	8677225	3/13/2014	In Force	Low-Density Parity-Check Decoder
MP4034	MP4034	US	13/367,940	2/7/2012	9362721	6/7/2016	In Force	Circuits and Methods for Protecting Integrated Circuits from Power
MP4035	MP4035	US	13/365,658	2/3/2012	8649120	2/11/2014	In Force	Systems And Methods For Gain Compensation And Re-Timing in Ma
MP4036	MP4036	US	13/365,696	2/3/2012	8537482	9/17/2013	In Force	Systems and Methods For Inter-Track Interference Cancellation in M
MP4036	MP4036C1	US	14/028,805	9/17/2013	8773792	7/8/2014	In Force	Systems And Methods For Inter-Track Interference Cancellation in M
MP4040	MP4040	US	13/365,950	2/3/2012	8644128	2/4/2014	In Force	MULTI-CLOCK PHY PREAMBLE DESIGN AND DETECTION
MP4040	MP4040C1	US	13/365,963	2/3/2012	8665974	3/4/2014	In Force	MULTI-CLOCK PHY PREAMBLE DESIGN AND DETECTION
MP4040	MP4040C1C1	US	14/193,428	2/28/2014	9130704	9/8/2015	In Force	MULTI-CLOCK PHYSICAL LAYER PREAMBLE DESIGN AND DETECTION
MP4040	MP4040I1	US	14/702,473	5/1/2015	9674317	6/6/2017	In Force	MULTI-CLOCK PHY PREAMBLE DESIGN AND DETECTION
MP4040I	MP4040I1WO	WO	PCT/US2015/02	5/1/2015			Pending	MULTI-CLOCK PHY PREAMBLE DESIGN AND DETECTION
MP4040I	MP4040IWCN	CN	201580033650.1	5/1/2015	ZL2015800336	9/13/2019	In Force	A METHOD FOR GENERATING A PHY DATA UNIT AND A COMMUNICA
MP4040I	MP4040IWER	EP	15723608.4	5/1/2015			Pending	Multi-Clock PHY Preamble Design and Detection
MP4040I	MP4040IWP	JP	2016-565193	5/1/2015	JP6478131	2/15/2019	In Force	MULTI-CLOCK PHY PREAMBLE DESIGN AND DETECTION
MP4040I	MP4040IWKR	KR	10-2016-703232	5/1/2015			Pending	MULTI-CLOCK PHY PREAMBLE DESIGN AND DETECTION
MP4040W	MP4040WO	WO	PCT/US2012/02	2/3/2012			Pending	MULTI-CLOCK PHY PREAMBLE DESIGN AND DETECTION
MP4041	MP4041	US	13/372,315	2/13/2012	8802554	8/12/2014	In Force	PATTERNS OF PASSIVATION MATERIAL ON BOND PADS AND METHO
MP4041	MP4041CN	CN	201210036508.1	2/15/2012	ZL2012100365	6/17/2015	In Force	PATTERNS OF PASSIVATION MATERIAL ON BOND PADS AND METHO
MP4044	MP4044	US	13/370,354	2/10/2012	8799737	8/5/2014	In Force	Soft-Information Modification in Concatenated Decoding Systems
MP4045	MP4045	US	13/371,438	2/12/2012	8787471	7/22/2014	In Force	ALIGNMENT OF DIGRF4 LANE
MP4047	MP4047	US	13/386,610	2/15/2012	8582768	11/12/2013	In Force	RECOVERY FROM DECRYPTION ERRORS IN A SEQUENCE OF COMMU
MP4047	MP4047C1C1	US	14/596,312	1/14/2015	9749298	8/29/2017	In Force	RECOVERY FROM DECRYPTION ERRORS IN A SEQUENCE OF COMMU
MP4047	MP4047EPDE	DE	12155532.0	2/15/2012	60301206466	10/9/2019	In Force	Recovery from Decryption Errors in a Sequence of Communication Pa
MP4047	MP4047EPFR	FR	12155532.0	2/15/2012	2490470	10/9/2019	In Force	Recovery from Decryption Errors in a Sequence of Communication Pa
MP4047	MP4047EPGB	GB	12155532.0	2/15/2012	2490470	10/9/2019	In Force	Recovery from Decryption Errors in a Sequence of Communication Pa
MP4047E	MP4047EP	EP	12155532.0	2/15/2012			Pending	Recovery from Decryption Errors in a Sequence of Communication Pa
MP4048	MP4048C1	US	14/311,660	6/23/2014	9189161	11/17/2015	In Force	Systems And Methods For Performing A Scatter-Gather Data Transfer
MP4049	MP4049	US	13/396,902	2/15/2012	9298334	3/29/2016	In Force	Method and Apparatus for Providing a User Interface Having a Guide
MP4051W	MP4051WO	WO	PCT/IB2012/00C	2/15/2012			Pending	METHOD AND APPARATUS FOR MOUNTING A DISC

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MP4054	MP4054C1	US	14/548,832	11/20/2014	9829513	11/28/2017	In Force	Line Voltage Detection Circuit
MP4054W	MP4054WO	WO	PCT/US2012/02	2/16/2012			Pending	TRIAC Dimmer Detection
MP4055	MP4055C1	US	14/194,388	2/28/2014	9087598	7/21/2015	In Force	Reliability Metrics Management for Soft Decoding
MP4055W	MP4055WO	WO	PCT/US2012/02	2/16/2012			Pending	RELIABILITY METRICS MANAGEMENT FOR SOFT DECODING
MP4055W	MP4055WOCN	CN	201280009420.1	2/16/2012	ZL2012800094	4/27/2016	In Force	A METHOD FOR READING A TARGET MEMORY SECTOR OF A MEMOR
MP4060	MP4060	US	13/416,301	3/9/2012	9047086	6/2/2015	In Force	METHOD AND APPARATUS FOR SUPPLYING POWER TO A DEVICE OV
MP4060	MP4060C1	US	14/725,157	5/29/2015	9639141	5/2/2017	In Force	METHOD AND APPARATUS FOR SUPPLYING POWER TO A DEVICE OV
MP4061	MP4061	US	13/406,342	2/27/2012	9036763	5/19/2015	In Force	METHODS AND DEVICES FOR IMPLEMENTING ALL-DIGITAL PHASE LO
MP4061	MP4061D1	US	14/713,945	5/15/2015	9366586	4/5/2016	In Force	METHODS AND DEVICES FOR IMPLEMENTING ALL-DIGITAL PHASE LO
MP4061W	MP4061WO	WO	PCT/US2012/02	2/27/2012			Pending	METHODS AND DEVICES FOR IMPLEMENTING ALL-DIGITAL PHASE LO
MP4061W	MP4061WOEP	EP	12752654.9	2/27/2012	2681844	6/13/2018	In Force	Methods and Devices for Implementing All-Digital Phase Locked Loop
MP4061W	MP4061WUDE	DE	12752654.9	2/27/2012	2681844	6/13/2018	In Force	Methods and Devices for Implementing All-Digital Phase Locked Loop
MP4061W	MP4061WUFR	FR	12752654.9	2/27/2012	2681844	6/13/2018	In Force	Methods and Devices for Implementing All-Digital Phase Locked Loop
MP4061W	MP4061WUGB	GB	12752654.9	2/27/2012	2681844	6/13/2018	In Force	Methods and Devices for Implementing All-Digital Phase Locked Loop
MP4063	MP4063	US	13/409,130	3/1/2012	8861391	10/14/2014	In Force	CHANNEL FEEDBACK FOR TDM SCHEDULING IN HETEROGENEOUS NE
MP4064	MP4064	US	13/419,977	5/24/2012	8742798	6/3/2014	In Force	METHOD AND APPARATUS FOR LOCAL OSCILLATION DISTRIBUTION
MP4064	MP4064C1	US	14/293,488	6/2/2014	9294036	3/22/2016	In Force	METHOD AND APPARATUS FOR LOCAL OSCILLATION DISTRIBUTION
MP4064W	MP4064WO	WO	PCT/US2012/03	5/24/2012			Pending	METHOD AND APPARATUS FOR LOCAL OSCILLATION DISTRIBUTION
MP4067	MP4067C1	US	14/532,155	11/4/2014	9396140	7/19/2016	In Force	Method And Apparatus For Transferring Frames With Different Worl
MP4068	MP4068	US	13/409,996	3/1/2012	8760793	6/24/2014	In Force	WRITING IN HARD DISK DRIVES
MP4069	MP4069	US	13/407,226	2/28/2012	9068659	4/14/2015	In Force	CELL RESELECTION METHODS BASED ON TRACKING AREA FOR CELLU
MP4071	MP4071	US	13/421,041	3/15/2012	9288836	3/15/2016	In Force	Electronic Bracelet
MP4072	MP4072C1	US	14/518,461	10/20/2014	9071280	6/30/2015	In Force	Method And Apparatus For Controlling A Number Of Decoding Iterati
MP4073W	MP4073WO	WO	PCT/US2012/02	2/27/2012			Pending	Two-Peak Current Control For Flyback Voltage Converters
MP4074	MP4074	US	13/415,112	3/8/2012	8982896	3/17/2015	In Force	Data Blocking Systems For Non-Arbitrary Networks
MP4074	MP4074C1	US	14/658,747	3/16/2015	9246835	1/26/2016	In Force	Method And Apparatus For Deblocking And Blocking Transmission Of
MP4074	MP4074C1C1	US	15/002,462	1/21/2016	9998389	6/12/2018	In Force	Method And Apparatus For Blocking Transmission Of Frames From A
MP4074W	MP4074WO	WO	PCT/US2012/02	3/8/2012			Pending	Data Blocking Systems For Non-Arbitrary Networks
MP4074W	MP4074WOCN	CN	201280019628.1	3/8/2012	ZL2012800196	5/11/2016	In Force	DATA BLOCKING SYSTEMS FOR NON-ARBITRARY NETWORKS
MP4074W	MP4074WOEP	EP	12715250.2	3/8/2012	2684321	9/20/2017	In Force	Data Blocking Systems for Non-Arbitrary Networks
MP4074W	MP4074WQJP	JP	2013-557864	3/8/2012	JP5939489	5/27/2016	In Force	DATA BLOCKING SYSTEMS FOR NON-ARBITRARY NETWORKS
MP4074W	MP4074WOKR	KR	1020137025673	3/8/2012	101918695	11/14/2018	In Force	Data Blocking Systems For Non-Arbitrary Networks
MP4074W	MP4074WUDE	DE	12715250.2	3/8/2012	602012637534	9/20/2017	In Force	Data Blocking Systems For Non-Arbitrary Networks
MP4074W	MP4074WUFR	FR	12715250.2	3/8/2012	2684321	9/20/2017	In Force	Data Blocking Systems For Non-Arbitrary Networks

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MP4074W	MP4074WUGB	GB	12715,50.2	3/8/2012	2684321	9/20/2017	In Force	Data Blocking Systems For Non-Arbitrary Networks
MP4080	MP4080	US	13/423,220	3/18/2012	8731119	5/20/2014	In Force	APPARATUS AND METHOD FOR REDUCING RECEIVER FREQUENCY ERROR
MP4080	MP4080C1	US	14/242,935	4/2/2014	9113475	8/18/2015	In Force	APPARATUS AND METHOD FOR MAINTAINING SYNCHRONIZATION BETWEEN
MP4080	MP4080CN	CN	201210075636.1	3/19/2012	ZL2012100756	12/14/2016	In Force	DRX OPERATION WITH CRYSTAL OSCILLATOR
MP4080E	MP4080EP	EP	EP12160026.6	3/19/2012	EP2501185	6/17/2015	In Force	DRX OPERATION WITH CRYSTAL OSCILLATOR
MP4080E	MP4080EPDE	DE	12160026.6	3/19/2012	2501185	6/17/2015	In Force	DRX OPERATION WITH CRYSTAL OSCILLATOR
MP4080E	MP4080EPGB	GB	12160026.6	3/19/2012	2501185	6/17/2015	In Force	DRX OPERATION WITH CRYSTAL OSCILLATOR
MP4082	MP4082	US	13/413,049	3/6/2012	8625215	1/7/2014	In Force	METHOD AND SYSTEM FOR COMPENSATING FOR ADJACENT TRACKS
MP4083	MP4083	US	13/152,044	6/2/2011	8787385	7/22/2014	In Force	METHOD AND APPARATUS FOR DETERMINING CHANNEL BANDWIDTH
MP4083	MP4083C1	US	13/152,040	6/2/2011	8737405	5/27/2014	In Force	METHOD AND APPARATUS FOR DETERMINING CHANNEL BANDWIDTH
MP4083	MP4083C1AA	US	14/992,876	1/11/2016	9769071	9/19/2017	In Force	METHOD AND APPARATUS FOR DETERMINING CHANNEL BANDWIDTH
MP4083	MP4083C1C1	US	14/286,691	5/23/2014	9237081	1/12/2016	In Force	METHOD AND APPARATUS FOR DETERMINING CHANNEL BANDWIDTH
MP4083W	MP4083WO	WO	PCT/US2011/03	6/2/2011			Pending	METHOD AND APPARATUS FOR DETERMINING CHANNEL BANDWIDTH
MP4083W	MP4083WOCN	CN	201180028739.1	6/2/2011	ZL2011800287	8/12/2015	In Force	METHOD AND APPARATUS FOR DETERMINING CHANNEL BANDWIDTH
MP4083W	MP4083WOEP	EP	11727030.6	6/2/2011	2560897	8/8/2018	In Force	Method and Apparatus for Determining Channel Bandwidth
MP4083W	MP4083WOEPDE	DE	11727030.6	6/2/2011	2560897	8/8/2018	In Force	Method and Apparatus for Determining Channel Bandwidth
MP4083W	MP4083WOEPFR	FR	11727030.6	6/2/2011	2560897	8/8/2018	In Force	Method and Apparatus for Determining Channel Bandwidth
MP4083W	MP4083WOEPGB	GB	11727030.6	6/2/2011	2560897	8/8/2018	In Force	Method and Apparatus for Determining Channel Bandwidth
MP4083W	MP4083WOJP	JP	2013-514222	6/2/2011	JP5783579	7/31/2015	In Force	METHOD AND APPARATUS FOR DETERMINING CHANNEL BANDWIDTH
MP4083W	MP4083WOKR	KR	10-2012-703242	6/2/2011	10189886	9/14/2018	In Force	METHOD AND APPARATUS FOR CHANNELING BANDWIDTH
MP4084	MP4084	US	13/419,938	3/14/2012	8576105	11/5/2013	In Force	METHOD AND APPARATUS FOR TIMING RECOVERY
MP4085	MP4085	US	13/423,221	3/18/2012	9289422	3/22/2016	In Force	INITIAL ACQUISITION USING CRYSTAL OSCILLATOR
MP4087	MP4087	US	13/433,293	3/29/2012	9124327	9/1/2015	In Force	CHANNEL FEEDBACK FOR COOPERATIVE MULTIPPOINT TRANSMISSION
MP4087W	MP4087WOCN	CN	201280024267.1	3/29/2012	ZL2012800242	7/21/2017	In Force	CHANNEL FEEDBACK FOR COOPERATIVE MULTIPPOINT TRANSMISSION
MP4087W	MP4087WOEP	EP	EP12765629.6	3/29/2012	2692068	6/19/2019	In Force	Channel Feedback for Cooperative Multipoint Transmission
MP4087W	MP4087WOEPDE	DE	EP12765629.6	3/29/2012	602012061181	6/19/2019	In Force	Channel Feedback for Cooperative Multipoint Transmission
MP4087W	MP4087WOEPFR	FR	EP12765629.6	3/29/2012	2692068	6/19/2019	In Force	Channel Feedback for Cooperative Multipoint Transmission
MP4087W	MP4087WOEPGB	GB	EP12765629.6	3/29/2012	2692068	6/19/2019	In Force	Channel Feedback for Cooperative Multipoint Transmission
MP4087W	MP4087WOJP	JP	2014-501794	3/29/2012	JP6019502	10/14/2016	In Force	CHANNEL FEEDBACK FOR COOPERATIVE MULTIPPOINT TRANSMISSION
MP4088	MP4088	US	13/439,130	4/4/2012	8766723	7/1/2014	In Force	Methods and Devices for Folded Push Pull Power Amplifiers
MP4089	MP4089C1	US	14/667,546	4/15/2015	9246395	1/26/2016	In Force	Circuits and Methods for Determining Peak Current
MP4093	MP4093	US	13/428,645	3/23/2012	8674858	3/18/2014	In Force	Methods for Compression and Real-Time Decompression of Executable
MP4093W	MP4093WO	WO	PCT/US2012/03	3/23/2012			Pending	Methods for Compression and Real-Time Decompression of Executable
MP4093W	MP4093WOCN	CN	201280028966.1	3/23/2012	ZL2012800289	7/14/2017	In Force	METHOD FOR COMPRESSION AND REAL-TIME DECOMPRESSION OF EXECUTABLE

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MP4093W	MP4093WOEP	EP	12719512.1	3/23/2012			Pending	Method for Compression and Real-Time Decompression of Executable
MP4099	MP4099	US	13/450,209	4/18/2012	9374782	6/21/2016	In Force	REDUCING POWER CONSUMPTION IN A WIRELESS COMMUNICATION
MP4099	MP4099C1D1	US	15/242,118	8/19/2016			Pending	PRIORITIZING SENSOR DATA AND CELLULAR OFFLOAD DATA IN A WI
MP4099	MP4099C2	US	13/450,222	4/18/2012	9226233	12/29/2015	In Force	REDUCING POWER CONSUMPTION IN A WIRELESS COMMUNICATION
MP4099W	MP4099WO	WO	PCT/US2012/03	4/18/2012			Pending	REDUCING POWER CONSUMPTION IN AN WIRELESS COMMUNICATION
MP4099W	MP4099WOEN	CN	201280028364.0	4/18/2012	ZL2012800283	12/26/2017	In Force	REDUCING POWER CONSUMPTION IN AN WIRELESS COMMUNICATION
MP4099W	MP4099WOEP	EP	12718004.0	4/18/2012	2760271	8/23/2017	In Force	Reducing Power Consumption in an Wireless Communication System
MP4099W	MP4099WOEPDE	DE	12718004.0	4/18/2012	60201203627E	8/23/2017	In Force	REDUCING POWER CONSUMPTION IN A WIRELESS COMMUNICATION
MP4099W	MP4099WOEPFR	FR	12718004.0	4/18/2012	2760271	8/23/2017	In Force	REDUCING POWER CONSUMPTION IN A WIRELESS COMMUNICATION
MP4099W	MP4099WOEPGB	GB	12718004.0	4/18/2012	2760271	8/23/2017	In Force	REDUCING POWER CONSUMPTION IN A WIRELESS COMMUNICATION
MP4099W	MP4099WOJP	JP	2014-506514	4/18/2012	JP5978507	8/5/2016	In Force	Reducing Power Consumption in a Wireless Communication System
MP4099W	MP4099WOKR	KR	10-2013-702985	11/11/2013	10-1944475	1/25/2019	In Force	REDUCING POWER CONSUMPTION IN A WIRELESS COMMUNICATION
MP4101	MP4101	US	13/479,124	5/23/2012	8750278	6/10/2014	In Force	Method and Apparatus for Off-Channel Device Invitation
MP4101	MP4101C1	US	14/275,615	5/12/2014	9078108	7/7/2015	In Force	METHOD AND APPARATUS FOR OFF-CHANNEL INVITATION
MP4102	MP4102	US	13/446,816	4/13/2012	8830039	9/9/2014	In Force	METHOD AND APPARATUS FOR PROVIDING COMMUNICATIONS BETWEEN
MP4103	MP4103	US	13/450,173	4/18/2012	9507742	11/29/2016	In Force	Variable length Arbitration
MP4103T	MP4103TW	TW	101113969	4/19/2012	1559228	11/21/2016	In Force	Variable length Arbitration
MP4103W	MP4103WO	WO	PCT/US2012/03	4/18/2012			Pending	Variable length Arbitration
MP4103W	MP4103WOEN	CN	201280019395.1	4/18/2012	ZL2012800193	10/5/2016	In Force	A METHOD, AN APPARATUS, AND A SYSTEM FOR VARIABLE LENGTH
MP4103W	MP4103WOEP	EP	12773767.4	4/18/2012	2760014	6/20/2018	In Force	Variable length Arbitration
MP4103W	MP4103WOJP	JP	2014-506518	4/18/2012	JP6041324	11/18/2016	In Force	VARIABLE LENGTH ARBITRATION
MP4103W	MP4103WOKR	KR	1020137030169	4/18/2012	10-1960870	3/15/2019	In Force	Variable length Arbitration
MP4103W	MP4103WUDE	DE	12773767.4	4/18/2012	2760014	6/20/2018	In Force	Variable length Arbitration
MP4103W	MP4103WUFR	FR	12773767.4	4/18/2012	2760014	6/20/2018	In Force	Variable length Arbitration
MP4103W	MP4103WUGB	GB	12773767.4	4/18/2012	2760014	6/20/2018	In Force	Variable length Arbitration
MP4104	MP4104	US	13/440,214	4/5/2012	8971350	3/3/2015	In Force	ACCESSING CHANNELS IN A MULTI-CHANNEL COMMUNICATION SYSTEM
MP4106	MP4106	US	13/449,979	4/18/2012	8886136	11/11/2014	In Force	TWO-PIN TR SWITCH WITH SWITCHED CAPACITOR
MP4110	MP4110	US	13/455,279	4/25/2012	8824489	9/2/2014	In Force	Physical Layer (PHY) Devices For Use in Automotive And Industrial Ap
MP4111	MP4111	US	13/449,633	4/18/2012	9391452	7/12/2016	In Force	Method and Apparatus for Implementing Power Line Communication
MP4113	MP4113	US	13/449,468	4/18/2012	8838053	9/16/2014	In Force	Frequency Multiplication Using Self-Mixing
MP4113W	MP4113WO	WO	PCT/US2012/03	4/18/2012			Pending	FREQUENCY MULTIPLICATION USING SELF-MIXING
MP4113W	MP4113WOEN	CN	201280020851.1	4/18/2012	ZL2012800208	12/14/2016	In Force	FREQUENCY MULTIPLICATION USING SELF-MIXING
MP4113W	MP4113WOEP	EP	12718519.7	4/18/2012			Pending	Frequency Multiplication Using Self-Mixing
MP4113W	MP4113WOJP	JP	2014-508424	4/18/2012	JP5995208	9/2/2016	In Force	FREQUENCY MULTIPLICATION USING SELF-MIXING

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MP4113W	MP4113WOKR	KR	1020137030060	4/18/2012	101905785	10/10/2018	In Force	Frequency Multiplication Using Self-Mixing
MP4115	MP4115	US	13/462,972	5/3/2012	8664782	4/8/2014	In Force	Wireless Authentication Using Beacon Messages
MP4115	MP4115C1	US	14/243,129	4/2/2014	9113330	8/18/2015	In Force	Wireless Authentication Using Beacon Messages
MP4115W	MP4115WO	WO	PCT/US2012/03	5/3/2012			Pending	Wireless Authentication Using Beacon Messages
MP4115W	MP4115WOCN	CN	201280029895.1	5/3/2012	ZL2012800298	4/19/2019	In Force	WIRELESS AUTHENTICATION USING BEACON MESSAGES
MP4118	MP4118	US	13/464,850	5/4/2012	8788777	7/22/2014	In Force	MEMORY ON-DEMAND, MANAGING POWER IN MEMORY
MP4118W	MP4118WO	WO	PCT/US2012/03	5/4/2012			Pending	MEMORY ON-DEMAND, MANAGING POWER IN MEMORY
MP4119	MP4119	US	13/461,273	5/1/2012	8811426	8/19/2014	In Force	Method and Apparatus for Dynamically Switching an Operating Band
MP4120	MP4120ID1	US	14/628,422	2/23/2015	9711448	7/18/2017	In Force	Finger Metal Oxide Metal Capacitor Formed In A Plurality Of Metal Ill
MP4120T	MP4120TW	TW	101116535	5/9/2012	1482187	4/21/2015	In Force	METAL OXIDE METAL CAPACITOR STRUCTURES
MP4120W	MP4120WO	WO	PCT/US2012/03	5/8/2012			Pending	Metal Oxide Metal Capacitor Structures
MP4120W	MP4120WOCN	CN	201280030072.1	5/8/2012	ZL2012800300	10/19/2016	In Force	METAL OXIDE METAL CAPACITOR STRUCTURES
MP4121	MP4121	US	13/449,407	4/18/2012	8742735	6/3/2014	In Force	High-Voltage Startup Circuit
MP4121	MP4121C1	US	14/289,922	5/29/2014	9502957	11/22/2016	In Force	System And Method For Supplying Power At Startup
MP4121T	MP4121TW	TW	101117234	5/15/2012	1577118	4/1/2017	In Force	HIGH-VOLTAGE STARTUP CIRCUIT
MP4121W	MP4121WO	WO	PCT/US2012/03	5/11/2012			Pending	High-Voltage Startup Circuit
MP4121W	MP4121WOCN	CN	201280023860.1	5/11/2012	ZL2012800238	8/17/2016	In Force	HIGH-VOLTAGE STARTUP CIRCUIT
MP4121W	MP4121WOJP	JP	2014-511414	5/11/2012	JP6109817	3/17/2017	In Force	HIGH VOLTAGE STARTUP CIRCUIT
MP4121W	MP4121WOKR	KR	1020137032121	5/11/2012	101909696	12/19/2018	In Force	High-Voltage Startup Circuit
MP4124	MP4124	US	13/467,173	5/9/2012	8949513	2/3/2015	In Force	Data Compression and Compacting For Memory Devices
MP4124W	MP4124WO	WO	PCT/US2012/03	5/10/2012			Pending	DATA COMPRESSION AND COMPACTING FOR MEMORY DEVICES
MP4124W	MP4124WOCN	CN	201280031526.1	5/10/2012	ZL2012800315	3/15/2017	In Force	DATA COMPRESSION AND COMPACTING FOR MEMORY DEVICES
MP4126	MP4126	US	13/448,176	4/16/2012	8669784	3/11/2014	In Force	Programmable Pulse Generator Using Inverter Chain
MP4129	MP4129	US	13/464,467	5/4/2012	8982889	3/17/2015	In Force	PREAMBLE DESIGNS FOR SUB-1GHZ FREQUENCY BANDS
MP4129	MP4129C1	US	14/658,609	3/16/2015	9706546	7/11/2017	In Force	Preambles For Sub-1GHz Frequency Bands
MP4129	MP4129C1C1	US	15/645,300	7/10/2017	10178665	1/8/2019	In Force	Systems And Methods For Transmitting Packets In Sub-1GHz Frequen
MP4129W	MP4129WID1	JP	2015-166870	8/26/2015	JP6016142	10/7/2016	In Force	Preamble Designs For Sub-1-GHz Frequency Bands
MP4129W	MP4129WO	WO	PCT/US2012/03	5/8/2012			Pending	Preamble Designs For Sub-1GHz Frequency Bands
MP4129W	MP4129WOCN	CN	201280024009.1	5/8/2012	ZL2012800240	10/26/2016	In Force	PREAMBLE DESIGNS FOR SUB-1GHZ FREQUENCY BANDS
MP4129W	MP4129WCEP	EP	12724011.7	5/8/2012			Pending	Preamble Designs for SUB-1GHZ Frequency Bands
MP4129W	MP4129WQJP	JP	2014-511395	5/8/2012	JP5801476	9/4/2015	In Force	PREAMBLE DESIGNS FOR SUB-1GHZ FREQUENCY BANDS
MP4129W	MP4129WOKR	KR	1020137033240	5/8/2012	101923204	11/22/2018	In Force	Preamble Designs For Sub-1GHz Frequency Bands
MP4130	MP4130CN	CN	201210160839.1	5/16/2012	ZL2012101608	5/3/2017	In Force	A METHOD FOR TESTING ELECTRONIC DEVICES, A TESTING SYSTEM,
MP4130T	MP4130TW	TW	101117245	5/15/2012	1550284	9/21/2016	In Force	METHOD AND APPARATUS FOR TESTING INTEGRATED CIRCUITS

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MP4131	MP4131	US	13/465,347	5/7/2012	8873680	10/28/2014	In Force	Short Guard Interval with Green Field Preamble
MP4131	MP4131C1	US	14/533,328	10/24/2014	9305786	4/5/2016	In Force	Short Guard Interval with Green Field Preamble
MP4131	MP4131C1C1	US	15/086,311	3/31/2016	10291453	5/14/2019	In Force	MIXED GUARD INTERVALS IN OFDM SIGNAL DATA SEGMENTS
MP4131W	MP4131WO	WO	PCT/US2012/03	5/7/2012			Pending	Short Guard Interval with Green Field Preamble
MP4131W	MP4131WOCN	CN	201280030073.4	5/7/2012	ZL2012800300	12/21/2016	In Force	SHORT GUARD INTERVAL WITH GREEN FIELD PREAMBLE
MP4131W	MP4131WOEP	EP	12721137.3	5/7/2012	2710759	8/28/2019	In Force	Short Guard Interval with Green Field Preamble
MP4131W	MP4131WOEPDE	DE	12721137.3	5/7/2012	602012063356	8/28/2019	In Force	Short Guard Interval with Green Field Preamble
MP4131W	MP4131WOEPFR	FR	12721137.3	5/7/2012	2710759	8/28/2019	In Force	Short Guard Interval with Green Field Preamble
MP4131W	MP4131WOEPGB	GB	12721137.3	5/7/2012	2710759	8/28/2019	In Force	Short Guard Interval with Green Field Preamble
MP4131W	MP4131WOJP	JP	2014-511392	5/7/2012	JP6073294	1/13/2017	In Force	SHORT GUARD INTERVAL WITH GREEN FIELD PREAMBLE
MP4131W	MP4131WOKR	KR	10-2013-70332	5/7/2012			Pending	Short Guard Interval with Green Field Preamble
MP4132	MP4132	US	10/188,274	7/1/2002	6961373	11/1/2005	In Force	METHOD AND APPARATUS FOR CHANNEL EQUALIZATION
MP4132	MP4132C1	US	11/220,508	9/6/2005	7406577	7/15/2008	In Force	METHOD AND APPARATUS FOR CHANNEL EQUALIZATION
MP4132	MP4132I1	US	10/852,854	5/24/2004	7257181	8/14/2007	In Force	Method And Apparatus For Channel Equalization
MP4132T	MP4132TW	TW	94142253	12/1/2005	1341679	5/1/2011	In Force	Isolation Of Transmit And Receive Signals
MP4132W	MP4132WOEPDE	DE	03762110.9	6/27/2003	603310826	1/20/2010	In Force	Method And Apparatus For Channel Equalization
MP4132W	MP4132WOEPGB	GB	03762110.9	6/27/2003	1540820	1/20/2010	In Force	Method And Apparatus For Channel Equalization
MP4133	MP4133	US	10/194,775	7/10/2002	7133425	11/7/2006	In Force	COMMUNICATION SYSTEM
MP4134	MP4134	US	10/603,417	6/24/2003	6912208	6/28/2005	In Force	METHOD AND APPARATUS FOR EQUALIZATION AND CROSSTALK MIT
MP4135	MP4135I1	US	11/431,929	5/9/2006	7809021	10/5/2010	In Force	Communication System And Encoding Method Having Low Overhead
MP4135	MP4135IIAA	US	13/925,605	6/24/2013	9451057	9/20/2016	In Force	COMMUNICATION SYSTEM AND ENCODING METHOD HAVING LOW
MP4135	MP4135I1C1	US	12/924,565	9/28/2010	8472478	6/25/2013	In Force	Communication system and encoding method having low overhead
MP4136	MP4136	US	10/424,421	4/28/2003	7002897	2/21/2006	In Force	MULTIPLE CHANNEL INTERFERENCE CANCELLATION
MP4136	MP4136C1	US	11/250,827	10/13/2005	7742386	6/22/2010	In Force	Multiple Channel Interference Cancellation
MP4136W	MP4136WO	WO	PCT/US2004/01	4/22/2004			Pending	Multiple Channel Interference Cancellation
MP4136W	MP4136WOCN	CN	200408018242.1	4/22/2004	ZL2004080182	7/20/2011	In Force	MULTIPLE CHANNEL INTERFERENCE CANCELLATION
MP4136W	MP4136WOEP	EP	04750611.8	4/22/2004	1625668	11/12/2014	In Force	Multiple Channel Interference Cancellation
MP4136W	MP4136WOEPDE	DE	04750611.8	4/22/2004	60330404612	11/12/2014	In Force	Multiple Channel Interference Cancellation
MP4136W	MP4136WOEPFR	FR	04750611.8	4/22/2004	1625668	11/12/2014	In Force	Multiple Channel Interference Cancellation
MP4136W	MP4136WOEPGB	GB	04750611.8	4/22/2004	1625668	11/12/2014	In Force	Multiple Channel Interference Cancellation
MP4136W	MP4136WOCHK	HK	61106489	9/25/2006			Pending	Multiple Channel Interference Cancellation
MP4136W	MP4136WOJP	JP	2011-117051	5/25/2011	JP5255671	4/26/2013	In Force	ADAPTIVE CANCELLER SYSTEM
MP4137	MP4137C1	US	11/273,347	11/14/2005	7352687	4/1/2008	In Force	MIXED DOMAIN CANCELLATION
MP4137	MP4137C1I1	US	12/074,256	2/29/2008	8363535	1/29/2013	In Force	FREQUENCY DOMAIN ECHO AND NEXT CANCELLATION

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MP4137a	MP4137a1WC	CN	200980115218.1	2/18/2009	ZL2009801152	1/8/2014	In Force	FREQUENCY DOMAIN ECHO AND NEXT CANCELLATION
MP4137a	MP4137a1WO	WO	PCT/US2009/00	2/18/2009			Pending	FREQUENCY DOMAIN ECHO AND NEXT CANCELLATION
MP4137a	MP4137a1WU	EP	09716519.5	2/18/2009	2255453	4/5/2017	In Force	Frequency Domain Echo and Next Cancellation
MP4137a	MP4137a1QN	DE	09716519.5	2/18/2009	602009045206	4/5/2017	In Force	FREQUENCY DOMAIN ECHO AND NEXT CANCELLATION
MP4137a	MP4137a1QR	FR	09716519.5	2/18/2009	2255453	4/5/2017	In Force	FREQUENCY DOMAIN ECHO AND NEXT CANCELLATION
MP4137a	MP4137a1QT	GB	09716519.5	2/18/2009	2255453	4/5/2017	In Force	FREQUENCY DOMAIN ECHO AND NEXT CANCELLATION
MP4138	MP4138	US	10/350,597	1/24/2003	6924724	8/2/2005	In Force	METHOD AND APPARATUS FOR TRANSFORMER BANDWIDTH ENHANCEMENT
MP4139	MP4139	US	10/603,498	6/24/2003	7164764	1/16/2007	In Force	METHOD AND APPARATUS FOR PRECODE CROSSTALK MITIGATION
MP4139	MP4139C1	US	11/650,190	1/4/2007	7567666	7/28/2009	In Force	METHOD AND APPARATUS FOR CROSSTALK MITIGATION
MP4139W	MP4139W1P2	JP	2007-121646	5/2/2007	JP5064877	8/17/2012	In Force	METHOD AND APPARATUS FOR EQUALIZATION AND CROSSTALK MITIGATION
MP4139W	MP4139WOCN	CN	200480016799.1	5/10/2004	ZL2004800167	4/10/2013	In Force	MULTI-CHANNEL COMMUNICATION SYSTEM
MP4139W	MP4139WCEP	EP	04751806.3	5/10/2004	EP1632048	4/18/2012	In Force	Method and Apparatus for Equalization and Crosstalk Mitigation
MP4139W	MP4139WCEPDE	DE	04751806.3	5/10/2004	602004037426	4/18/2012	In Force	Method And Apparatus For Equalization And Crosstalk Mitigation
MP4139W	MP4139WCEPFR	FR	04751806.3	5/10/2004	1632048	4/18/2012	In Force	Method And Apparatus For Equalization And Crosstalk Mitigation
MP4139W	MP4139WCEPGB	GB	04751806.3	5/10/2004	1632048	4/18/2012	In Force	Method And Apparatus For Equalization And Crosstalk Mitigation
MP4139W	MP4139WODP	JP	2006-532924	5/10/2004	JP4881736	12/9/2011	In Force	METHOD AND APPARATUS FOR EQUALIZATION AND CROSSTALK MITIGATION
MP4139W	MP4139WUD1	EP	EP12002639.8	5/10/2004	EP2503704	11/30/2016	In Force	Method and Apparatus for Equalization and Crosstalk Mitigation
MP4139W	MP4139WUD1DE	DE	12002639.8	5/10/2004	602004050423	11/30/2016	In Force	Method And Apparatus For Equalization And Crosstalk Mitigation
MP4139W	MP4139WUD1FR	FR	12002639.8	5/10/2004	2503704	11/30/2016	In Force	Method And Apparatus For Equalization And Crosstalk Mitigation
MP4139W	MP4139WUD1GB	GB	12002639.8	5/10/2004	2503704	11/30/2016	In Force	Method And Apparatus For Equalization And Crosstalk Mitigation
MP4140	MP4140	US	10/688,005	10/16/2003	7069452	3/7/2006	In Force	METHOD AND APPARATUS FOR INCREASING THE LINEARITY AND BANDWIDTH
MP4141	MP4141	US	12/157,452	6/9/2008	7898407	10/5/2010	In Force	Sub-Channel Distortion Mitigation in Parallel Digital Systems
MP4141	MP4141C1	US	12/924,708	9/30/2010	8294606	10/23/2012	In Force	Sub-Channel Distortion Mitigation in Parallel Digital Systems
MP4141	MP4141C1C1	US	13/657,214	10/22/2012	8912934	12/16/2014	In Force	Systems with Bias Offset and Gain Mismatch Removal from Parallel
MP4141	MP4141CN	CN	200810131495.1	6/13/2008	ZL2008101314	1/16/2013	In Force	SUB-CHANNEL DISTORTION MITIGATION IN PARALLEL DIGITAL SYSTEMS
MP4141E	MP4141EP	EP	EP08252001.6	6/10/2008	EP2015454	8/7/2013	In Force	Sub-Channel Distortion Mitigation in Parallel Digital Systems
MP4141J	MP4141JP	JP	2008-156915	6/16/2008	4994915	5/18/2012	In Force	ANALOG SIGNAL PROCESSING SYSTEM, PARALLEL ANALOG/DIGITAL CONVERSION
MP4142	MP4142	US	11/002,953	12/2/2004	7466662	12/2/2008	In Force	ISOLATION OF TRANSMIT AND RECEIVE SIGNALS IN FULL-DUPLEX COMMUNICATION
MP4142	MP41421	US	11/242,970	10/4/2005	7738654	6/15/2010	In Force	Isolation Of Transmit And Receive Signals
MP41421	MP41421WO	WO	PCT/US2005/04	11/29/2005			Pending	Isolation Of Transmit And Receive Signals
MP41421	MP41421WCN	CN	200580041593.1	11/29/2005	ZL2005800415	9/28/2011	In Force	METHOD AND DEVICE OF ISOLATE TRANSMIT AND RECEIVE SIGNALS
MP41421	MP41421WER	EP	05852480.2	11/29/2005	EP1829351	10/21/2015	In Force	ISOLATION OF TRANSMIT AND RECEIVE SIGNALS
MP41421	MP41421WJP	JP	2007-544450	11/29/2005	4866979	11/25/2011	In Force	ISOLATION OF TRANSMIT AND RECEIVE SIGNALS
MP4142W	MP4142WUN	DE	05852480.2	11/29/2005	1829351	10/21/2015	In Force	ISOLATION OF TRANSMIT AND RECEIVE SIGNALS

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MP4142W	MP4142WJF	FR	05852480.2	11/29/2005	1829351	10/21/2015	In Force	ISOLATION OF TRANSMIT AND RECEIVE SIGNALS
MP4142W	MP4142WJUT	GB	05852480.2	11/29/2005	1829351	10/21/2015	In Force	ISOLATION OF TRANSMIT AND RECEIVE SIGNALS
MP4143	MP4143	US	11/894,990	8/21/2007	776068	5/18/2010	In Force	Method And System For A Multi-Rate Gigabit Media Independent Int
MP4143	MP4143RE	US	13/474,515	5/17/2012	RE46523	8/22/2017	In Force	METHOD AND SYSTEM FOR A MULTI-RATE GIGABIT MEDIA INDEPENDEN
MP4143	MP4143REC1	US	15/682,324	8/21/2017			Pending	METHOD AND SYSTEM FOR A MULTI-RATE GIGABIT MEDIA INDEPENDEN
MP4144	MP4144	US	11/904,631	9/26/2007	7948862	5/24/2011	In Force	CROSSTALK CANCELLATION USING SLIDING FILTERS
MP4144	MP4144RE	US	13/902,532	5/24/2013	RE45794	11/3/2015	In Force	CROSSTALK CANCELLATION USING SLIDING FILTERS
MP4145	MP4145C1	US	13/006,308	1/13/2011	8345418	1/1/2013	In Force	COOLING CONFIGURATION FOR COMMUNICATION BOARDS
MP4146	MP4146	US	12/291,725	11/12/2008	8984304	3/17/2015	In Force	Active Idle Communication System
MP4146	MP4146C1	US	14/659,570	3/16/2015	9883457	1/30/2018	In Force	Method And Apparatus For Reducing Power Consumption Of A Com
MP4146W	MP4146WOCN	CN	200880120603.1	11/12/2008	ZL2008801206	1/15/2014	In Force	ACTIVE IDLE COMMUNICATION SYSTEM
MP4146W	MP4146WCEP	EP	08849192.3	11/12/2008	2213049	8/29/2018	In Force	Active Idle Communication System
MP4146W	MP4146WJJP	JP	2010-533125	11/12/2008	5240590	4/12/2013	In Force	Active Idle Communication System
MP4146W	MP4146WJDE	DE	08849192.3	11/12/2008	2213049	8/29/2018	In Force	Active Idle Communication System
MP4146W	MP4146WUFR	FR	08849192.3	11/12/2008	2213049	8/29/2018	In Force	Active Idle Communication System
MP4146W	MP4146WUGB	GB	08849192.3	11/12/2008	2213049	8/29/2018	In Force	Active Idle Communication System
MP4148	MP4148	US	12/583,086	8/12/2009	8049537	11/1/2011	In Force	Method and Apparatus For Reducing Transmitter AC-Coupling Droop
MP4148	MP4148C1	US	13/285,589	10/31/2011	8578074	10/29/2013	In Force	Method And Apparatus For Reducing Transmitter AC-Coupling Droop
MP4149	MP4149	US	12/583,087	8/11/2009	8270389	9/18/2012	In Force	METHOD OF SYNCHRONIZATION FOR LOW POWER IDLE
MP4149	MP4149C1	US	13/616,425	9/14/2012	8711876	4/29/2014	In Force	METHOD OF SYNCHRONIZATION FOR LOW POWER IDLE
MP4149W	MP4149WO	WO	PCT/US2609/00	8/11/2009			Pending	METHOD OF SYNCHRONIZATION FOR LOW POWER IDLE
MP4150	MP4150	US	12/653,273	12/9/2009	8461848	6/11/2013	In Force	CABLE DIAGNOSTICS FOR BASE-T SYSTEMS
MP4150	MP4150D1	US	13/914,215	6/10/2013	9103869	8/11/2015	In Force	CABLE DIAGNOSTICS FOR BASE-T SYSTEMS
MP4153	MP4153	US	13/452,418	4/20/2012	8503275	8/6/2013	In Force	Sequential-Access of Storage Media
MP4153	MP4153C1	US	13/959,284	8/5/2013	8767329	7/1/2014	In Force	Sequential-Access of Storage Media
MP4153	MP4153C1C1	US	14/288,862	5/28/2014	9007711	4/14/2015	In Force	Sequential-Access of Storage Media
MP4154	MP4154	US	13/477,678	5/22/2012	8681564	3/25/2014	In Force	SYSTEMS AND METHODS FOR GENERATING SOFT INFORMATION IN
MP4154	MP4154C1	US	14/177,609	2/11/2014	9153323	10/6/2015	In Force	SYSTEMS AND METHODS FOR GENERATING SOFT INFORMATION IN
MP4154	MP4154CN	CN	201210167149.1	5/23/2012	ZL2012101671	10/26/2016	In Force	SYSTEMS AND METHODS FOR GENERATING SOFT INFORMATION IN
MP4155	MP4155	US	13/477,575	5/22/2012	9157823	9/15/2015	In Force	Downlink And Uplink Staggering Techniques With Aid Bitmap Segmen
MP4158	MP4158	US	13/461,899	5/2/2012	9678676	6/13/2017	In Force	Method For Storage Devices To Achieve Low Write Amplification Wit
MP4158W	MP4158WO	WO	PCT/US2012/03	5/2/2012			Pending	Method For Storage Devices To Achieve Low Write Amplification Wit
MP4158W	MP4158WOCN	CN	201280024279.1	5/2/2012	ZL2012800242	9/14/2018	In Force	METHOD FOR STORAGE DEVICES TO ACHIEVE LOW WRITE AMPLIFIC
MP4158W	MP4158WCEP	EP	12724191.7	5/2/2012	2715510	5/2/2018	In Force	Method for Storage Devices to Achieve Low Write Amplification with

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MP4158W	MP4158WOEPDE	DE	12724191.7	5/2/2012	2715510	5/2/2018	In Force	Method for Storage Devices to Achieve Low Write Amplification with
MP4158W	MP4158WOEPFR	FR	12724191.7	5/2/2012	2715510	5/2/2018	In Force	Method for Storage Devices to Achieve Low Write Amplification with
MP4158W	MP4158WOEPGB	GB	12724191.7	5/2/2012	2715510	5/2/2018	In Force	Method for Storage Devices to Achieve Low Write Amplification with
MP4158W	MP4158WOJP	JP	2014-513851	5/2/2012	6016137	10/7/2016	In Force	METHOD FOR STORAGE DEVICES TO ACHIEVE LOW WRITE AMPLIFIC
MP4158W	MP4158WOKR	KR	1020137034035	5/2/2012			Pending	Method For Storage Devices To Achieve Low Write Amplification Wit
MP4161	MP4161	US	13/477,920	5/22/2012	8948283	2/3/2015	In Force	SOUNDING PACKET FORMAT FOR LONG RANGE WLAN
MP4161	MP4161C1	US	14/612,176	2/2/2015	9621322	4/11/2017	In Force	NBP PACKET FORMAT FOR A COMMUNICATION PROTOCOL WITH M
MP4161	MP4161C1C1	US	15/483,437	4/10/2017	10664094	8/28/2018	In Force	NULL DATA PACKET FORMAT FOR LONG RANGE WLAN
MP4161W	MP4161WO	WO	PCT/US2012/03	5/22/2012			Pending	SOUNDING PACKET FORMAT FOR LONG RANGE WLAN
MP4161W	MP4161WOCN	CN	201280035739.1	5/22/2012	212012800357	9/15/2017	In Force	SOUNDING PACKET FORMAT FOR LONG RANGE WLAN
MP4161W	MP4161WOEP	EP	12725214.6	5/22/2012	2715965	10/18/2017	In Force	Sounding Packet Format for Long Range WLAN
MP4161W	MP4161WOEPDE	DE	12725214.6	5/22/2012	2715965	10/18/2017	In Force	SOUNDING PACKET FORMAT
MP4161W	MP4161WOEPFR	FR	12725214.6	5/22/2012	2715965	10/18/2017	In Force	SOUNDING PACKET FORMAT
MP4161W	MP4161WOEPGB	GB	12725214.6	5/22/2012	2715965	10/18/2017	In Force	SOUNDING PACKET FORMAT
MP4161W	MP4161WOJP	JP	2014-513946	5/22/2012	6014948	10/7/2016	In Force	SOUNDING PACKET FORMAT FOR LONG RANGE WLAN
MP4161W	MP4161WOKR	KR	10-2013-703326	5/22/2012	10-2005055	7/23/2019	In Force	SOUNDING PACKET FORMAT FOR LONG RANGE WLAN
MP4162	MP4162	US	13/485,635	5/31/2012	8878354	11/4/2014	In Force	METHOD AND APPARATUS FOR SUPPLYING POWER TO A SYSTEM ON
MP4163	MP4163	US	13/478,555	5/23/2012	9118103	8/25/2015	In Force	Position Determination Using Transmit Beamforming
MP4168	MP4168CN	CN	201210193022.1	6/7/2012	212012101930	7/14/2017	In Force	METHOD FOR OPERATING MEMORY AND PROCESSING UNIT
MP4169	MP4169	US	13/490,026	6/6/2012	8824587	9/2/2014	In Force	SYSTEMS AND METHODS FOR COMPRESSED FEEDBACK AND SUBCAR
MP4169	MP4169C1	US	14/467,817	8/25/2014	9059754	6/16/2015	In Force	SYSTEMS AND METHODS FOR COMPRESSED FEEDBACK AND SUBCAR
MP4169W	MP4169WO	WO	PCT/US2012/04	6/6/2012			Pending	SYSTEMS AND METHODS FOR COMPRESSED FEEDBACK AND SUBCAR
MP4169W	MP4169WOCN	CN	201280019836.1	6/6/2012	212012800198	3/9/2016	In Force	SYSTEMS AND METHODS FOR COMPRESSED FEEDBACK AND SUBCAR
MP4169W	MP4169WOEP	EP	12729274.6	6/6/2012	2719092	8/9/2017	In Force	Systems and Methods for Compressed Feedback and Subcarrier Gro
MP4169W	MP4169WOEPDE	DE	12729274.6	6/6/2012	60201203563	8/9/2017	In Force	SYSTEMS AND METHODS FOR COMPRESSED FEEDBACK AND SUBCAR
MP4169W	MP4169WOEPFR	FR	12729274.6	6/6/2012	2719092	8/9/2017	In Force	SYSTEMS AND METHODS FOR COMPRESSED FEEDBACK AND SUBCAR
MP4169W	MP4169WOEPGB	GB	12729274.6	6/6/2012	2719092	8/9/2017	In Force	SYSTEMS AND METHODS FOR COMPRESSED FEEDBACK AND SUBCAR
MP4169W	MP4169WOJP	JP	2014-513589	6/6/2012	6019503	10/14/2016	In Force	SYSTEMS AND METHODS FOR COMPRESSED FEEDBACK AND SUBCAR
MP4171	MP4171	US	13/492,452	6/8/2012	8995367	3/31/2015	In Force	EFFICIENT TRANSMISSION FOR LOW DATA RATE WLAN
MP4171	MP4171C1	US	13/492,572	6/8/2012	9019914	4/28/2015	In Force	EFFICIENT TRANSMISSION FOR LOW DATA RATE WLAN
MP4171	MP4171C2	US	13/492,522	6/8/2012	8867467	10/21/2014	In Force	EFFICIENT TRANSMISSION FOR LOW DATA RATE WLAN
MP4171	MP4171C3	US	13/492,538	6/8/2012	9088908	7/21/2015	In Force	EFFICIENT TRANSMISSION FOR LOW DATA RATE WLAN
MP4171	MP4171C3D1	US	14/804,260	7/20/2015	9769703	9/19/2017	In Force	EFFICIENT TRANSMISSION FOR LOW DATA RATE WLAN
MP4171W	MP4171WED1	EP	16177592.9	6/8/2012	3094131	8/7/2019	In Force	Efficient Transmission for Low Data Rate wlan

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MP4171W	MP4171WED1DE	DE	16177592.9	6/8/2012	602012062856	8/7/2019	In Force	Efficient Transmission for Low Data Rate WLAN
MP4171W	MP4171WED1FR	FR	16177592.9	6/8/2012	3094131	8/7/2019	In Force	Efficient Transmission for Low Data Rate WLAN
MP4171W	MP4171WED1GB	GB	16177592.9	6/8/2012	3094131	8/7/2019	In Force	Efficient Transmission for Low Data Rate WLAN
MP4171W	MP4171WOCN	WO	PCT/US2012/04	6/8/2012			Pending	EFFICIENT TRANSMISSION FOR LOW DATA RATE WLAN
MP4171W	MP4171WOCN	CN	2012800384771	6/8/2012	ZL2012800384	2/23/2018	In Force	EFFICIENT TRANSMISSION FOR LOW DATA RATE WLAN
MP4171W	MP4171WCEP	EP	12727576.6	6/8/2012	EP2719219	12/7/2016	In Force	EFFICIENT TRANSMISSION FOR LOW DATA RATE WLAN
MP4171W	MP4171WCEPDE	DE	12727576.6	6/8/2012	2719219	12/7/2016	In Force	EFFICIENT TRANSMISSION FOR LOW DATA RATE WLAN
MP4171W	MP4171WCEPFR	FR	12727576.6	6/8/2012	2719219	12/7/2016	In Force	EFFICIENT TRANSMISSION FOR LOW DATA RATE WLAN
MP4171W	MP4171WCEPGB	GB	12727576.6	6/8/2012	2719219	12/7/2016	In Force	EFFICIENT TRANSMISSION FOR LOW DATA RATE WLAN
MP4171W	MP4171WQJP	JP	2014-514894	6/8/2012	5907496	4/1/2016	In Force	EFFICIENT TRANSMISSION FOR LOW DATA RATE WLAN
MP4171W	MP4171WOKR	KR	10-2014-700021	6/8/2012	101898901	9/14/2018	In Force	EFFICIENT TRANSMISSION FOR LOW DATA RATE WLAN
MP4172	MP4172	US	13/482,137	6/8/2012	9456328	9/27/2016	In Force	Method And Apparatus For Dynamically Adjusting A Configurable Par
MP4172	MP4172C1	US	15/269,118	9/19/2016	9723475	8/1/2017	In Force	System And Method For Adjusting A Discovery Protocol In A Wireless
MP4172W	MP4172WOCN	WO	PCT/US2012/04	6/8/2012			Pending	Method And Apparatus For Dynamically Adjusting A Configurable Par
MP4172W	MP4172WOCN	CN	2012800274601	6/8/2012	ZL2012800274	8/10/2018	In Force	METHOD AND APPARATUS FOR DYNAMICALLY ADJUSTING A CONFIG
MP4172W	MP4172WCEP	EP	12729281.1	6/8/2012	2719208	8/22/2018	In Force	Method and Apparatus for Dynamically Adjusting a Configurable Para
MP4172W	MP4172WCEPDE	DE	12729281.1	6/8/2012	2719208	8/22/2018	In Force	Method and Apparatus for Dynamically Adjusting a Configurable Para
MP4172W	MP4172WCEPFR	FR	12729281.1	6/8/2012	2719208	8/22/2018	In Force	Method and Apparatus for Dynamically Adjusting a Configurable Para
MP4172W	MP4172WCEPGB	GB	12729281.1	6/8/2012	2719208	8/22/2018	In Force	Method and Apparatus for Dynamically Adjusting a Configurable Para
MP4172W	MP4172WQJP	JP	2014-514891	6/8/2012	5999606	9/9/2016	In Force	METHOD AND APPARATUS FOR DYNAMICALLY ADJUSTING A CONFIG
MP4172W	MP4172WOKR	KR	1020137032550	6/8/2012	101820073	1/18/2018	In Force	Method And Apparatus For Dynamically Adjusting A Configurable Par
MP4173W	MP4173W	TW	101120127	6/5/2012			Pending	Super-High-Voltage Resistor On Silicon
MP4173W	MP4173WOCN	WO	PCT/US2012/03	5/11/2012			Pending	Super-High-Voltage Resistor On Silicon
MP4173W	MP4173WOCN	CN	201280036481	5/11/2012	ZL2012800364	10/17/2017	In Force	SUPER-HIGH-VOLTAGE RESISTOR ON SILICON
MP4174	MP4174	US	13/488,537	6/5/2012	8873392	10/28/2014	In Force	Method and Apparatus for Controlling the Flow of Packets in a Data
MP4174	MP4174C1	US	14/534,055	10/27/2014	9246827	1/26/2016	In Force	Method and Apparatus for Controlling the Flow of Packets in a Data
MP4176	MP4176	US	13/490,182	6/6/2012	8660548	2/25/2014	In Force	Multi-Radio Time Base
MP4176	MP4176C1	US	14/188,116	2/24/2014	9014682	4/21/2015	In Force	Method And System For Retaining Synchronization Across Multiple
MP4177	MP4177	US	13/474,095	5/17/2012	8751906	6/10/2014	In Force	SYSTEMS AND METHODS FOR OPERATING ON A STORAGE DEVICE US
MP4177	MP4177CN	CN	2012101992951	6/12/2012	ZL2012101992	5/24/2017	In Force	SYSTEMS AND METHODS FOR OPERATING ON A STORAGE DEVICE US
MP4180	MP4180	US	13/487,737	6/4/2012	9001599	4/7/2015	In Force	Systems and Methods for DOS Gating
MP4180W	MP4180WO	WO	PCT/US2012/04	6/4/2012			Pending	Systems and Methods for DOS Gating
MP4180W	MP4180WOCN	CN	2012800294201	6/4/2012	ZL2012800294	10/26/2016	In Force	SYSTEM AND METHOD FOR DOS GATING
MP4180W	MP4180WCEP	EP	12727523.8	6/4/2012	2721500	8/12/2015	In Force	Systems and Methods for DOS Gating

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MP4180W	MP4180W/JP	JP	2014-515863	6/4/2012	6057438	12/16/2016	In Force	Systems and Methods for DQS Gating
MP4180W	MP4180W/KR	KR	10-2013-703824	6/4/2012	101903718	10/4/2018	In Force	Systems and Methods for DQS Gating
MP4180W	MP4180W/DE	DE	12727523.8	6/4/2012	EP2721500	8/12/2015	In Force	Systems and Methods for DQS Gating
MP4180W	MP4180W/FR	FR	12727523.8	6/4/2012	2721500	8/12/2015	In Force	Systems and Methods for DQS Gating
MP4180W	MP4180W/GB	GB	12727523.8	6/4/2012	2721500	8/12/2015	In Force	Systems and Methods for DQS Gating
MP4181	MP4181	US	13/473,753	5/17/2012	8953907	2/10/2015	In Force	Modified Bicubic Interpolation
MP4181	MP4181/C1	US	14/612,338	2/3/2015	9563935	2/7/2017	In Force	Modified Bicubic Interpolation
MP4181W	MP4181W/O	WO	PCT/US2012/03	5/17/2012			Pending	Modified Bicubic Interpolation
MP4183	MP4183/C1	US	14/500,067	9/29/2014	9101018	8/4/2015	In Force	Apparatuses for Bleeding Current from A Transformer Of A Solid-State
MP4183W	MP4183W/O	WO	PCT/US2012/04	6/18/2012			Pending	Triac Dimming Systems For Solid-State Loads
MP4183W	MP4183W/CN	CN	201280002831.1	6/18/2012	ZL2012800028	6/29/2016	In Force	TRIAC DIMMING SYSTEMS FOR SOLID-STATE LOADS
MP4183W	MP4183W/EP	EP	12737636.6	6/18/2012	EP2596680	4/5/2017	In Force	Triac Dimming Systems For Solid-State Loads
MP4183W	MP4183W/DE	DE	12737636.6	6/18/2012	602012030764	4/5/2017	In Force	Triac Dimming Systems For Solid-State Loads
MP4183W	MP4183W/FR	FR	12737636.6	6/18/2012	2596680	4/5/2017	In Force	Triac Dimming Systems For Solid-State Loads
MP4183W	MP4183W/GB	GB	12737636.6	6/18/2012	2596680	4/5/2017	In Force	Triac Dimming Systems For Solid-State Loads
MP4185	MP4185	US	13/493,541	6/7/2012	8881640	11/18/2014	In Force	UPLINK TRAINING FOR MIMO IMPLICIT BEAMFORMING
MP4185	MP4185/C1	US	14/543,055	11/17/2014	9312939	4/12/2016	In Force	UPLINK TRAINING FOR MIMO IMPLICIT BEAMFORMING
MP4185	MP4185/C1/C1	US	15/094,535	4/8/2016	9742476	8/22/2017	In Force	UPLINK TRAINING FOR MIMO IMPLICIT BEAMFORMING
MP4185	MP4185/C1/C1/C1	US	15/682,087	8/21/2017	9948365	4/17/2018	In Force	UPLINK TRAINING FOR MIMO IMPLICIT BEAMFORMING
MP4185W	MP4185W/O	WO	PCT/US2012/04	6/7/2012			Pending	UPLINK TRAINING FOR MIMO IMPLICIT BEAMFORMING
MP4185W	MP4185W/CN	CN	201280039197.1	6/7/2012	ZL2012800391	7/5/2019	In Force	UPLINK TRAINING FOR MIMO IMPLICIT BEAMFORMING
MP4185W	MP4185W/EP	EP	12730297.4	6/7/2012	2724479	4/4/2018	In Force	Uplink Training for MIMO Implicit Beamforming
MP4185W	MP4185W/DE	DE	12730297.4	6/7/2012	2724479	4/4/2018	In Force	Uplink Training for MIMO Implicit Beamforming
MP4185W	MP4185W/FR	FR	12730297.4	6/7/2012	2724479	4/4/2018	In Force	Uplink Training for MIMO Implicit Beamforming
MP4185W	MP4185W/GB	GB	12730297.4	6/7/2012	2724479	4/4/2018	In Force	Uplink Training for MIMO Implicit Beamforming
MP4185W	MP4185W/JP	JP	2014-517005	6/7/2012	5958944	7/1/2016	In Force	UPLINK TRAINING FOR MIMO IMPLICIT BEAMFORMING
MP4185W	MP4185W/KR	KR	10-2013-70349C	6/7/2012			Pending	UPLINK TRAINING FOR MIMO IMPLICIT BEAMFORMING
MP4187	MP4187	US	13/539,920	6/21/2012	8711697	4/29/2014	In Force	METHOD AND APPARATUS FOR PRIORITIZING DATA TRANSFER
MP4187	MP4187W	TW	101120132	6/5/2012	1536539	6/1/2016	In Force	Ballast Resistor For Super-High-Voltage Devices
MP4188W	MP4188W/O	WO	PCT/US2012/04	5/31/2012			Pending	BALLAST RESISTOR FOR SUPER-HIGH-VOLTAGE DEVICES
MP4188W	MP4188W/CN	CN	201280031851.1	5/31/2012	ZL2012800318	10/26/2016	In Force	INTEGRATED CIRCUIT COMPRISING A MOSFET HAVING BALLAST RESI
MP4190	MP4190	US	13/532,444	6/25/2012	8674483	3/18/2014	In Force	METHODS AND ARRANGEMENTS RELATING TO SEMICONDUCTOR PA
MP4190	MP4190/D1	US	14/218,631	3/18/2014	9105610	8/11/2015	In Force	METHODS AND ARRANGEMENTS RELATING TO SEMICONDUCTOR PA
MP4190T	MP4190TW	TW	101122803	6/26/2012	1540653	7/1/2016	In Force	METHODS AND ARRANGEMENTS RELATING TO SEMICONDUCTOR PA

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MP4190W	MP4190WO	WO	PCT/US2012/04	6/26/2012			Pending	METHODS AND ARRANGEMENTS RELATING TO SEMICONDUCTOR PA
MP4190W	MP4190WOCN	CN	2012800318531	6/26/2012	ZL2012800318	9/28/2016	In Force	METHODS AND ARRANGEMENTS RELATING TO SEMICONDUCTOR PA
MP4190W	MP4190WCEP	EP	12740756.7	6/26/2012			Pending	METHODS AND ARRANGEMENTS RELATING TO SEMICONDUCTOR PA
MP4190W	MP4190WOCIN	IN	2379/INJUNMP/5	6/26/2012			Pending	METHODS AND ARRANGEMENTS RELATING TO SEMICONDUCTOR PA
MP4192	MP4192	US	13/533,019	6/26/2012	9274801	3/1/2016	In Force	ROM-CODE PROGRAMMABLE DIGITAL SIGNAL PROCESSOR
MP4192	MP4192C1	US	15/052,074	2/24/2016	9996352	6/12/2018	In Force	ROM-CODE PROGRAMMABLE DIGITAL SIGNAL PROCESSOR
MP4194W	MP4194WO	WO	PCT/US2012/04	6/12/2012			Pending	Verifying Server Identity
MP4202	MP4202	US	13/546,859	7/11/2012	9362933	6/7/2016	In Force	NOISE-PREDICTIVE DETECTOR ADAPTATION WITH CORRECTED DATA
MP4202	MP4202C1	US	15/154,831	5/13/2016	9838044	12/5/2017	In Force	NOISE-PREDICTIVE DETECTOR ADAPTATION WITH CORRECTED DATA
MP4204	MP4204	US	13/537,256	6/29/2012	9026158	5/5/2015	In Force	Systems and Method for Scheduling Measurement Gaps and On-Dura
MP4205	MP4205	US	13/490,901	6/7/2012	8638168	1/28/2014	In Force	Digital Power Amplifier
MP4205	MP4205C1	US	14/143,751	12/30/2013	8981845	3/17/2015	In Force	Digital Power Amplifier
MP4206	MP4206C1	US	14/301,457	6/11/2014	9413567	8/9/2016	In Force	Systems and Methods for Finite Impulse Response Adaptation for Gal
MP4210	MP4210	US	13/538,867	6/29/2012	9275985	3/1/2016	In Force	RC Networks that Include an Integrated High Resistance Resistor on T
MP4211	MP4211	US	13/550,078	7/16/2012	9088466	7/21/2015	In Force	Coexistence Of A Normal-Rate Physical Layer And A Low-Rate Physica
MP4211	MP4211C1	US	14/802,370	7/17/2015	9713065	7/18/2017	In Force	Coexistence Of Devices Operating At Different Data Rates In Wireless
MP4211W	MP4211WO	WO	PCT/US2012/04	7/16/2012			Pending	COEXISTENCE OF A NORMAL-RATE PHYSICAL LAYER AND A LOW-RAT
MP4211W	MP4211WOCN	CN	2012800347531	7/16/2012	ZL2012800347	1/19/2018	In Force	COEXISTENCE OF A NORMAL-RATE PHYSICAL LAYER AND A LOW-RAT
MP4211W	MP4211WODE	DE	12741185.8	7/16/2012	6.02012E+12	9/9/2015	In Force	Coexistence Of A Normal-Rate Physical Layer And A Low-Rate Physica
MP4211W	MP4211WCEP	EP	12741185.8	7/16/2012	EP2732669	9/9/2015	In Force	Coexistence of a Normal-Rate Physical Layer and a Low-Rate Physical
MP4211W	MP4211WOFR	FR	12741185.8	7/16/2012	2732669	9/9/2015	In Force	Coexistence Of A Normal-Rate Physical Layer And A Low-Rate Physica
MP4211W	MP4211WOG8	G8	12741185.8	7/16/2012	2732669	9/9/2015	In Force	Coexistence Of A Normal-Rate Physical Layer And A Low-Rate Physica
MP4211W	MP4211WOFJP	JP	2014-520406	7/16/2012	6053197	12/9/2016	In Force	COEXISTENCE OF A NORMAL-RATE PHYSICAL LAYER AND A LOW-RAT
MP4211W	MP4211WOKR	KR	1020147001249	7/16/2012	101947108	2/13/2019	In Force	Coexistence Of A Normal-Rate Physical Layer And A Low-Rate Physica
MP4212T	MP4212TW	TW	101125783	7/18/2012	1622316	4/21/2018	In Force	CORRELATED COLOR TEMPERATURE CONTROL METHODS AND DEVIC
MP4212W	MP4212WO	WO	PCT/US2012/04	7/13/2012			Pending	CORRELATED COLOR TEMPERATURE CONTROL METHODS AND DEVIC
MP4214W	MP4214WO	WO	PCT/US2012/04	6/13/2012			Pending	COLLIMATOR FOR OPTICAL PICK-UP UNITS
MP4214W	MP4214WOCN	CN	2012800357441	6/13/2012	ZL2012800357	6/15/2016	In Force	COLLIMATOR FOR OPTICAL PICK-UP UNITS
MP4216	MP4216	US	13/536,041	6/28/2012	8812941	8/19/2014	In Force	VITERBI DEVICE AND METHOD USING A POSTERIORI INFORMATION
MP4216W	MP4216WO	WO	PCT/US2012/04	6/28/2012			Pending	IMPROVED VITERBI DEVICE AND METHOD USING A POSTERIORI INFO
MP4217	MP4217	US	13/551,016	7/17/2012	8653118	4/8/2014	In Force	Timing Acquisition Robust To Inter-Track-Interference
MP4217	MP4217C1	US	14/220,720	3/31/2014	8810938	8/19/2014	In Force	Systems And Methods For Performing Timing Acquisition On An Expe
MP4217	MP4217C1C1	US	14/461,767	8/18/2014	9019638	4/28/2015	In Force	Systems And Methods For Performing Timing Acquisition In A Read/
MP4219	MP4219	US	13/549,376	7/13/2012	8787860	7/22/2014	In Force	IMAGE CANCELLATION IN RECEIVERS USING DUAL ADAPTIVE FILTERS

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MP4219	MP4219CN	CN	201210259812.1	7/23/2012	ZL2012102598	5/4/2016	In Force	IMAGE CANCELLATION IN RECEIVERS USING DUAL ADAPTIVE FILTERS
MP4220	MP4220	US	13/533,088	6/26/2012	9213401	12/15/2015	In Force	Zero Power Hibernation Mode with Instant On
MP4220W	MP4220WO	WO	PCT/US2012/04	6/26/2012			Pending	Zero Power Hibernation Mode with Instant On
MP4220W	MP4220WOEP	EP	12735976.8	6/26/2012			Pending	
MP4220W	MP4220WOJP	JP	2014-522831	6/26/2012	5994157	9/2/2016	In Force	ZERO POWER HIBERNATION MODE WITH INSTANT ON
MP4220W	MP4220WOKR	KR	10-2014-70038C	6/26/2012			Pending	Zero Power Hibernation Mode with Instant On
MP4221	MP4221	US	13/552,421	7/18/2012	9141394	9/22/2015	In Force	Switching Between Processor Cache and Random-Access Memory
MP4221W	MP4221WO	WO	PCT/US2012/04	7/19/2012			Pending	Switching Between Processor Cache and Random-Access Memory
MP4221W	MP4221WOCN	CN	201280033662.1	7/19/2012	ZL2012800336	2/15/2017	In Force	SWITCHING BETWEEN PROCESSOR CACHE AND RANDOM-ACCESS M
MP4221W	MP4221WOEP	EP	12740449.9	7/19/2012	2737399	5/15/2019	In Force	
MP4221W	MP4221WOEPDE	DE	12740449.9	7/19/2012	602012060145	5/15/2019	In Force	Switching Between Processor Cache and Random-Access Memory
MP4221W	MP4221WOEPFR	FR	12740449.9	7/19/2012	2737399	5/15/2019	In Force	Switching Between Processor Cache and Random-Access Memory
MP4221W	MP4221WOEPGB	GB	12740449.9	7/19/2012	2737399	5/15/2019	In Force	Switching Between Processor Cache and Random-Access Memory
MP4221W	MP4221WOJP	JP	2014-522891	7/19/2012	6137700	5/12/2017	In Force	SWITCHING BETWEEN PROCESSOR CACHE AND RANDOM-ACCESS M
MP4222W	MP4222WO	WO	PCT/US2012/04	7/17/2012			Pending	BLU-RAY OPTICAL PICK-UP DEVICES AND METHODS
MP4222W	MP4222WOCN	CN	201280036284.1	7/17/2012	ZL2012800362	8/17/2016	In Force	BLU-RAY OPTICAL PICK-UP DEVICES AND METHODS
MP4223	MP4223	US	13/550,345	7/16/2012	9009393	4/14/2015	In Force	Hybrid Solid-State Disk (SSD)/Hard Disk Drive (HDD) Architectures
MP4225W	MP4225WOCN	CN	201280029894.1	7/26/2012	ZL2012800298	5/11/2018	In Force	REDUCING PLMN SEARCH TIME
MP4225W	MP4225WOEP	EP	EP12818347.2	7/26/2012	EP2737753	7/12/2017	In Force	Reducing PLMN Search Time
MP4225W	MP4225WOJP	JP	2014-522200	7/26/2012	5954717	6/24/2016	In Force	REDUCING PLMN SEARCH TIME
MP4225W	MP4225WUDE	DE	12818347.2	7/26/2012	2737753	7/12/2017	In Force	REDUCING PLMN SEARCH TIME
MP4225W	MP4225WUFR	FR	12818347.2	7/26/2012	2737753	7/12/2017	In Force	REDUCING PLMN SEARCH TIME
MP4225W	MP4225WUGB	GB	12818347.2	7/26/2012	2737753	7/12/2017	In Force	REDUCING PLMN SEARCH TIME
MP4227	MP4227	US	13/565,228	8/2/2012	8792598	7/29/2014	In Force	COEXISTENCE OF MULTIPLE COMMUNICATION TECHNOLOGIES ON A
MP4228	MP4228	US	13/551,118	7/17/2012	9445475	9/13/2016	In Force	Buck Regulator For LED Lighting Color Mixing And/Or Current Comp
MP4229	MP4229	US	13/566,157	8/3/2012	9184839	11/10/2015	In Force	Method and Apparatus for Compensating Impairments in Radio Freq
MP4230	MP4230	US	13/550,272	7/16/2012	8867675	10/21/2014	In Force	LOW BANDWIDTH PHY WITH FREQUENCY OFFSET CONSTRAINTS
MP4232	MP4232	US	13/553,170	7/19/2012	8914019	12/16/2014	In Force	Feedback Spoofing For Coexistence Among Multiple Wireless Commu
MP4232	MP4232C1	US	14/570,104	12/15/2014	9148890	9/29/2015	In Force	Systems And Methods For Scheduling Transmission Of Wireless Signa
MP4233	MP4233C1	US	14/551,121	11/3/2014	9070396	6/30/2015	In Force	METHOD AND APPARATUS FOR INITIAL SELF-SERVO WRITING BASED
MP4234	MP4234	US	13/562,816	7/31/2012	8953791	2/10/2015	In Force	Key Derivative Function for Network Communications
MP4234W	MP4234WO	WO	PCT/US2012/04	7/31/2012			Pending	Key Derivative Function for Network Communications
MP4234W	MP4234WOCN	CN	201280048991.1	7/31/2012	ZL2012800489	6/5/2018	In Force	KEY DERIVATIVE FUNCTION FOR NETWORK COMMUNICATIONS
MP4237	MP4237	US	13/571,870	8/10/2012	8839405	9/16/2014	In Force	Intelligent PHY With Security Detection For Ethernet Networks

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MP4237W	MP4237WO	WO	PCT/US2012/05	8/10/2012			Pending	Intelligent PHY With Security Detection For Ethernet Networks
MP4237W	MP4237WOCN	CN	2012800495441	8/10/2012	ZL2012800495	5/3/2017	In Force	INTELLIGENT PHY WITH SECURITY DETECTION FOR ETHERNET NETW
MP4237W	MP4237WCEP	EP	12751206.9	8/10/2012	EP2742649	6/3/2015	In Force	INTELLIGENT PHY WITH SECURITY DETECTION FOR ETHERNET NETW
MP4237W	MP4237WJJP	JP	2014-525176	8/10/2012	6074776	1/20/2017	In Force	INTELLIGENT PHY WITH SECURITY DETECTION FOR ETHERNET NETW
MP4237W	MP4237WOKR	KR	1020147004063	8/10/2012			Pending	Intelligent PHY With Security Detection For Ethernet Networks
MP4237W	MP4237WUDE	DE	12751206.9	8/10/2012	2742649	6/3/2015	In Force	INTELLIGENT PHY WITH SECURITY DETECTION FOR ETHERNET NETW
MP4237W	MP4237WUFR	FR	12751206.9	8/10/2012	2742649	6/3/2015	In Force	INTELLIGENT PHY WITH SECURITY DETECTION FOR ETHERNET NETW
MP4237W	MP4237WUGB	GB	12751206.9	8/10/2012	2742649	6/3/2015	In Force	INTELLIGENT PHY WITH SECURITY DETECTION FOR ETHERNET NETW
MP4239	MP4239	US	13/586,678	8/15/2012	9131398	9/8/2015	In Force	LONG RANGE WLAN DATA UNIT FORMAT
MP4239	MP4239D1	US	13/586,690	8/15/2012	9083590	7/14/2015	In Force	LONG RANGE WLAN DATA UNIT FORMAT
MP4239	MP4239D2	US	13/586,698	8/15/2012	9131399	9/8/2015	In Force	CONTROL DATA UNIT FORMAT FOR WLAN
MP4239	MP4239D3	US	13/586,704	8/15/2012	8982792	3/17/2015	In Force	LONG RANGE WLAN DATA UNIT FORMAT
MP4239	MP4239D4	US	14/846,229	9/4/2015	9832057	11/28/2017	In Force	CONTROL FRAME FORMAT FOR WLAN
MP4239W	MP4239W/KD1	KR	1020197014903	8/15/2012			Pending	LONG RANGE WLAN DATA UNIT FORMAT
MP4239W	MP4239W/O	WO	PCT/US2012/05	8/15/2012			Pending	Long Range WLAN Data Unit Format
MP4239W	MP4239WOCN	CN	2012800506081	8/15/2012	ZL2012800506	12/26/2017	In Force	METHODS AND APPARATUSSES FOR TRANSMITTING A DATA UNIT IN
MP4239W	MP4239WCEP	EP	12751689.6	8/15/2012	2745481	4/10/2019	In Force	Long Range WLAN Data Unit Format
MP4239W	MP4239WCEPDE	DE	12751689.6	8/15/2012	607012058855	4/10/2019	In Force	Long Range WLAN Data Unit Format
MP4239W	MP4239WCEPFR	FR	12751689.6	8/15/2012	2745481	4/10/2019	In Force	Long Range WLAN Data Unit Format
MP4239W	MP4239WCEPGB	GB	12751689.6	8/15/2012	2745481	4/10/2019	In Force	Long Range WLAN Data Unit Format
MP4239W	MP4239WJJP	JP	2014-526177	8/15/2012	6238139	11/10/2017	In Force	LONG RANGE WLAN DATA UNIT FORMAT
MP4239W	MP4239WOKR	KR	10-2014-700694	8/15/2012	10-1984250	5/24/2019	In Force	LONG RANGE WLAN DATA UNIT FORMAT
MP4240	MP4240	US	13/557,265	7/25/2012	8791738	7/29/2014	In Force	START-UP CIRCUIT
MP4240	MP4240C1	US	14/340,725	7/25/2014	9362816	6/7/2016	In Force	START-UP CIRCUIT
MP4240W	MP4240WO	WO	PCT/IB2012/001	7/25/2012			Pending	START-UP CIRCUIT
MP4240W	MP4240WOCN	CN	2012800403521	7/25/2012	ZL2012800403	12/8/2017	In Force	START-UP CIRCUIT
MP4240W	MP4240WCEP	EP	12778397.5	7/25/2012	2745368	4/10/2019	In Force	Start-Up-Circuit
MP4240W	MP4240WCEPDE	DE	12778397.5	7/25/2012	607012058874	4/10/2019	In Force	Start-Up-Circuit
MP4240W	MP4240WCEPFR	FR	12778397.5	7/25/2012	2745368	4/10/2019	In Force	Start-Up-Circuit
MP4240W	MP4240WCEPGB	GB	12778397.5	7/25/2012	2745368	4/10/2019	In Force	Start-Up-Circuit
MP4241	MP4241	US	13/557,268	7/25/2012	9357623	5/31/2016	In Force	PROTECTION CIRCUIT IN TRIAC APPLICATIONS
MP4241W	MP4241WO	WO	PCT/IB2012/001	7/25/2012			Pending	PROTECTION CIRCUIT IN TRIAC APPLICATIONS
MP4241W	MP4241WOCN	CN	2012800403531	7/25/2012	ZL2012800403	5/24/2017	In Force	PROTECTION CIRCUIT IN TRIAC APPLICATIONS
MP4241W	MP4241WCEP	EP	12780801.2	7/25/2012	EP2745369	5/25/2016	In Force	Protection Circuit in Triac Applications

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MP4241W	MP4241WUDE	DE	12780801.2	7/25/2012	2745369	5/25/2016	In Force	PROTECTION CIRCUIT IN TRIAC APPLICATIONS
MP4241W	MP4241WUFR	FR	12780801.2	7/25/2012	2745369	5/25/2016	In Force	PROTECTION CIRCUIT IN TRIAC APPLICATIONS
MP4241W	MP4241WUGB	GB	12780801.2	7/25/2012	2745369	5/25/2016	In Force	PROTECTION CIRCUIT IN TRIAC APPLICATIONS
MP4242	MP4242	US	13/557,765	7/25/2012	9258863	2/9/2016	In Force	METHOD AND APPARATUS FOR TRIAC APPLICATIONS
MP4242W	MP4242WO	WO	PCT/182012/001	7/25/2012			Pending	METHOD AND APPARATUS FOR TRIAC APPLICATIONS
MP4242W	MP4242WOCN	CN	201280049543.1	7/25/2012	212012800495	10/19/2016	In Force	METHOD AND APPARATUS FOR TRIAC APPLICATIONS
MP4242W	MP4242WCEP	EP	12784319.1	7/25/2012			Pending	Method and Apparatus for TRIAC Applications
MP4243	MP4243C1	US	14/202,055	3/10/2014	9165601	10/20/2015	In Force	Method And Apparatus For Decoding And Correcting A First Byte Based On A Second Byte
MP4243W	MP4243WO	WO	PCT/US2012/04	7/25/2012			Pending	Error Correction Code Techniques For Matrices With Interleaved Codewords
MP4246	MP4246	US	13/569,622	8/8/2012	9729431	8/8/2017	In Force	USING STANDARD FIELDS TO CARRY META-INFORMATION
MP4252	MP4252	US	13/584,027	8/13/2012	9209163	12/8/2015	In Force	PACKAGE-ON-PACKAGE STRUCTURES
MP4252	MP4252D1	US	14/960,794	12/7/2015	9666571	5/30/2017	In Force	PACKAGE-ON-PACKAGE STRUCTURES
MP4252F	MP4252FWCN	CN	201480017384.1	2/11/2014			Pending	PACKAGE-ON-PACKAGE STRUCTURES
MP4252F	MP4252FWKR	KR	10-2015-702143	2/11/2014			Pending	Package-on-Package Structures
MP4252I	MP4252I1TW	TW	103104424	2/11/2014			Pending	Package-on-Package Structures
MP4252I	MP4252I1WO	WO	PCT/US2014/01	2/11/2014			Pending	PACKAGE-ON-PACKAGE STRUCTURES
MP4252T	MP4252TW	TW	101129900	8/17/2012			Pending	PACKAGE-ON-PACKAGE STRUCTURES
MP4252W	MP4252WO	WO	PCT/US2012/05	8/15/2012			Pending	PACKAGE-ON-PACKAGE STRUCTURES
MP4252W	MP4252WOCN	CN	201280050766.1	8/15/2012			Pending	PACKAGE-ON-PACKAGE STRUCTURES
MP4252W	MP4252WOKR	KR	10-2014-700514	8/15/2012			Pending	PACKAGE-ON-PACKAGE STRUCTURES
MP4254	MP4254	US	13/589,937	8/20/2012	9185755	11/10/2015	In Force	Regulator For LED Lighting Color Mixing
MP4254W	MP4254WO	WO	PCT/US2012/05	8/20/2012			Pending	Regulator For LED Lighting Color Mixing
MP4255W	MP4255WO	WO	PCT/US2012/05	8/14/2012			Pending	METHOD AND APPARATUS FOR LED LIGHTING
MP4256	MP4256CN	CN	201310051065.4	1/31/2013			Pending	HANDHELD ELECTRONIC DEVICE WITH SIDE TOUCH-SENSORS AND IT
MP4259	MP4259C1	US	14/553,185	11/25/2014	9236350	1/12/2016	In Force	Packaging DRAM And SOC In An IC Package
MP4259W	MP4259WO	WO	PCT/US2012/05	8/22/2012			Pending	Packaging Dram And Soc In An IC Package
MP4259W	MP4259WOCN	CN	201280048525.1	8/22/2012	212012800485	11/9/2018	In Force	PACKAGING DRAM AND SOC IN AN IC PACKAGE
MP4259W	MP4259WCEP	EP	12753343.8	8/22/2012	2748850	8/22/2018	In Force	Packaging Dram and Soc in an IC Package
MP4259W	MP4259WJJP	JP	2014-527256	8/22/2012	6117787	3/31/2017	In Force	PACKAGING DRAM AND SOC IN AN IC PACKAGE
MP4259W	MP4259WOKR	KR	1020147007564	8/22/2012	101926102	12/6/2018	In Force	Packaging Dram And Soc In An IC Package
MP4259W	MP4259WUD1	EP	EP15150023.8	8/22/2012			Pending	Packaging Dram and Soc in an IC Package
MP4259W	MP4259WUDE	DE	12753343.8	8/22/2012	602012050113	8/22/2018	In Force	Packaging Dram and Soc in an IC Package
MP4259W	MP4259WUFR	FR	12753343.8	8/22/2012	2748850	8/22/2018	In Force	Packaging Dram and Soc in an IC Package
MP4259W	MP4259WUGB	GB	12753343.8	8/22/2012	2748850	8/22/2018	In Force	Packaging Dram and Soc in an IC Package

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MP4260	MP4260	US	13/593,875	8/24/2012	8797835	8/5/2014	In Force	Method And Apparatus For Detecting Crosstalk Between A Plurality O
MP4263	MP4263	US	13/598,018	8/29/2012	9365181	6/7/2016	In Force	ALTERNATE TREE FLOW CONTROL FOR STP
MP4263	MP4263D1	US	15/163,467	5/24/2016	10389638	8/20/2019	In Force	ACTIVATING AN ALTERNATE TREE FLOW CONTROL FOR SPANNING T
MP4265	MP4265	US	13/596,136	8/28/2012	9118347	8/25/2015	In Force	Method and Apparatus for OFDM Encoding and Decoding
MP4267	MP4267	US	13/600,339	8/31/2012	8861505	10/14/2014	In Force	Timing Synchronization Using Broadcast Synchronization Packets
MP4269	MP4269	US	13/587,390	8/16/2012	8787293	7/22/2014	In Force	Packet Type Spooling For Coexistence Among Multiple Wireless Com
MP4269	MP4269C1	US	14/356,167	7/21/2014	8989133	3/24/2015	In Force	System And Method For Reducing Interference Between Communic
MP4269	MP4269CN	CN	201210321657.1	8/31/2012	ZL2012103216	8/29/2017	In Force	PACKET TYPE SPOOFING FOR COEXISTENCE AMONG MULTIPLE WIREL
MP4271	MP4271	US	13/556,833	7/24/2012	8952910	2/10/2015	In Force	Touchscreen System
MP4271W	MP4271WO	WO	PCT/US2012/04	7/25/2012			Pending	Touchscreen System
MP4271W	MP4271WOCN	CN	201280042685.1	7/25/2012	ZL2012800426	8/4/2017	In Force	TOUCHSCREEN SYSTEM
MP4271W	MP4271WOEP	EP	12743319.1	7/25/2012			Pending	Touchscreen System
MP4273	MP4273	US	13/604,407	9/5/2012	9089553	6/30/2015	In Force	Switching Tasks Between Heterogeneous Cores
MP4273W	MP4273WO	WO	PCT/US2012/05	9/6/2012			Pending	Switching Tasks Between Heterogeneous Cores
MP4273W	MP4273WOCN	CN	201280043237.1	9/6/2012	ZL2012800432	3/1/2017	In Force	SWITCHING TASKS BETWEEN HETEROGENEOUS CORES
MP4273W	MP4273WOEP	EP	12762487.2	9/6/2012			Pending	
MP4274	MP4274	US	13/597,960	8/29/2012	9167614	10/20/2015	In Force	Tunneled Direct Link Setup Systems And Methods With Consistent Li
MP4274	MP4274C1	US	14/887,559	10/20/2015	9736876	8/15/2017	In Force	Method And Apparatus For Establishing Links And Maintaining Consis
MP4280	MP4280	US	13/605,241	9/6/2012	9185684	11/10/2015	In Force	Using Non-Uniform Frequency Bands For Coexistence Among Multiple
MP4280	MP4280C1	US	14/928,120	10/30/2015	9807768	10/31/2017	In Force	Systems And Methods For Managing Coexistence In Wireless Commu
MP4280W	MP4280WO	WO	PCT/US2012/05	9/13/2012			Pending	Using Non-Uniform Frequency Bands For Coexistence Among Multiple
MP4280W	MP4280WOCN	CN	201280052897.1	9/13/2012	ZL2012800528	4/27/2018	In Force	USING NON-UNIFORM FREQUENCY BANDS FOR COEXISTENCE AMON
MP4280W	MP4280WOEP	EP	12766532.1	9/13/2012	2756722	11/28/2018	In Force	Using Non-Uniform Frequency Bands for Coexistence Among Multiple
MP4280W	MP4280WOEPDE	DE	12766532.1	9/13/2012	602012054096	11/28/2018	In Force	Using Non-Uniform Frequency Bands for Coexistence Among Multiple
MP4280W	MP4280WOEPFR	FR	12766532.1	9/13/2012	2756722	11/28/2018	In Force	Using Non-Uniform Frequency Bands for Coexistence Among Multiple
MP4280W	MP4280WOEPGB	GB	12766532.1	9/13/2012	2756722	11/28/2018	In Force	Using Non-Uniform Frequency Bands for Coexistence Among Multiple
MP4280W	MP4280WOJP	JP	2014-530772	9/13/2012	6140707	5/12/2017	In Force	USING NON-UNIFORM FREQUENCY BANDS FOR COEXISTENCE AMON
MP4280W	MP4280WOKR	KR	1020147007563	9/13/2012	10-1969942	4/11/2019	In Force	Using Non-Uniform Frequency Bands for Coexistence Among Multiple
MP4281	MP4281	US	13/611,174	9/12/2012	8879999	11/4/2014	In Force	Using Frequency Bands Characterized By Type Of Unwanted Interfere
MP4281W	MP4281WO	WO	PCT/US2012/05	9/13/2012			Pending	Using Frequency Bands Characterized By Type Of Unwanted Interfere
MP4281W	MP4281WOCN	CN	201280052896.1	9/13/2012	ZL2012800528	12/23/2015	In Force	METHOD AND APPARATUS FOR AVOIDING INTERFERENCE BETWEEN
MP4281W	MP4281WOEP	EP	12773442.4	9/13/2012	2756600	6/6/2018	In Force	USING FREQUENCY BANDS CHARACTERIZED BY TYPE OF UNWANTED
MP4281W	MP4281WOJP	JP	2014-530775	9/13/2012	6150350	6/2/2017	In Force	USING FREQUENCY BANDS CHARACTERIZED BY TYPE OF UNWANTED
MP4281W	MP4281WOKR	KR	1020147008818	9/13/2012	10-1979398	5/10/2019	In Force	Using Frequency Bands Characterized By Type Of Unwanted Interfer

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MP4281W	MP4281WUDE	DE	12773442.4	9/13/2012	602012047204	6/6/2018	In Force	Using Frequency Bands Characterized by Type of Unwanted Interfere
MP4281W	MP4281WUFR	FR	12773442.4	9/13/2012	2756600	6/6/2018	In Force	Using Frequency Bands Characterized by Type of Unwanted Interfere
MP4281W	MP4281WUGB	GB	12773442.4	9/13/2012	2756600	6/6/2018	In Force	Using Frequency Bands Characterized by Type of Unwanted Interfere
MP4282	MP4282	US	13/596,489	8/28/2012	8887005	11/11/2014	In Force	Caching System with Removable Memory Card
MP4282W	MP4282WO	WO	PCT/US2012/05	8/28/2012			Pending	Caching System with Removable Memory Card
MP4282W	MP4282WOCN	CN	201280044735.1	8/28/2012	ZL2012800447	7/11/2017	In Force	CACHING SYSTEM WITH REMOVABLE MEMORY CARD
MP4283	MP4283	US	13/608,225	9/10/2012	8711507	4/29/2014	In Force	GATE TIMING FOR SHORT SERVO WEDGE IN DISK MEMORY SYSTEM
MP4284	MP4284	US	13/615,845	9/14/2012	9042825	5/26/2015	In Force	Wireless Coexistence Through Antenna Switching
MP4284	MP4284CN	CN	201210348920.1	9/14/2012	ZL2012103489	9/15/2017	In Force	Wireless Coexistence Through Antenna Switching
MP4286	MP4286	US	13/616,313	9/14/2012	9032234	5/12/2015	In Force	Systems and Methods for Monitoring and Managing Memory Blocks
MP4286	MP4286C1	US	14/687,152	4/15/2015	9274590	3/1/2016	In Force	Systems and Methods for Monitoring and Managing Memory Blocks
MP4286W	MP4286WO	WO	PCT/US2012/05	9/14/2012			Pending	Systems and Methods for Monitoring and Managing Memory Blocks
MP4286W	MP4286WOCN	CN	201280045492.1	9/14/2012	ZL2012800454	12/14/2016	In Force	SYSTEMS AND METHODS FOR MONITORING AND MANAGING MEMO
MP4292	MP4292C1	US	14/662,955	3/19/2015	9246962	1/26/2016	In Force	Conference Mixing Using Turbo-Vad
MP4292W	MP4292WOCN	CN	201280047507.1	9/27/2012	ZL2012800475	11/2/2016	In Force	CONFERENCE MIXING USING TURBO-VAD
MP4294	MP4294C1	US	14/666,083	8/22/2014	9224419	12/29/2015	In Force	DATA DETECTOR FOR DETECTING DATA IN PRESENCE OF CONTROL SI
MP4296	MP4296	US	13/627,223	9/26/2012	8891590	11/18/2014	In Force	Method and Apparatus for Processing Wireless Signals
MP4298	MP4298	US	13/628,212	9/27/2012	8694691	4/8/2014	In Force	Systems And Methods For Creating Bidirectional Communication Cha
MP4298W	MP4298WO	WO	PCT/US2012/05	9/27/2012			Pending	Systems And Methods For Creating Bidirectional Communication Cha
MP4302	MP4302	US	13/626,598	9/25/2012	9275204	3/1/2016	In Force	Enhanced Network Access-Control Credentials
MP4303	MP4303C1	US	14/330,490	7/14/2014	8953425	2/10/2015	In Force	NR2 PROCESSING METHODS AND DEVICES TO CORRECT FOR SYNC D
MP4304	MP4304CN	CN	201210482619.1	11/20/2012			Pending	DUAL WINDOW SOLUTION FOR MULTI-TASK OPERATION ON ANDROID
MP4305	MP4305	US	13/591,954	8/22/2012	8856664	11/11/2014	In Force	MULTI-RATE MEDIA INDEPENDENT INTERFACE OVER A PHYSICAL CO
MP4309	MP4309C1	US	14/500,692	9/29/2014	9312176	4/12/2016	In Force	REMOVING CONDUCTIVE MATERIAL TO FORM CONDUCTIVE FEATUR
MP4309T	MP4309TW	TW	101136601	10/3/2012	101136601	8/11/2016	In Force	REMOVING CONDUCTIVE MATERIAL TO FORM CONDUCTIVE FEATUR
MP4309W	MP4309WO	WO	PCT/US2012/00	10/3/2012			Pending	REMOVING CONDUCTIVE MATERIAL TO FORM CONDUCTIVE FEATUR
MP4309W	MP4309WOCN	CN	201280057102.1	10/3/2012	ZL2012800571	8/4/2017	In Force	REMOVING CONDUCTIVE MATERIAL TO FORM CONDUCTIVE FEATUR
MP4309W	MP4309WOCF	EP	EP12816499.3	10/3/2012			Pending	REMOVING CONDUCTIVE MATERIAL TO FORM CONDUCTIVE FEATUR
MP4310W	MP4310WO	WO	PCT/US2012/055	10/5/2012			Pending	METHOD AND APPARATUS FOR EFFECTIVELY INCREASING A COMMA
MP4310W	MP4310WOCN	CN	201280049338.1	10/5/2012	ZL2012800493	7/21/2017	In Force	METHOD AND APPARATUS FOR EFFECTIVELY INCREASING A COMMA
MP4310W	MP4310WOCF	EP	EP12813451.7	10/5/2012			Pending	Apparatus With a Maximum Command Queue Length and Method T
MP4310W	MP4310WOP	JP	2014-534036	10/5/2012	6102032	3/10/2017	In Force	APPARATUS WITH A MAXIMUM COMMAND QUEUE LENGTH AND M
MP4311	MP4311	US	13/644,256	10/3/2012	8880055	11/4/2014	In Force	METHOD AND APPARATUS FOR USING NEAR FIELD COMMUNICATIO
MP4312	MP4312	US	13/648,507	10/10/2012	9461671	10/4/2016	In Force	METHOD AND APPARATUS FOR POWER CONSERVATION IN LDC DE

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MP4312	MP4312C1	US	15/284,060	10/3/2016	10367527	7/30/2019	In Force	METHOD AND APPARATUS FOR REDUCING IDLE CYCLES DURING UDP
MP4313W	MP4313WO	WO	PCT/US2012/05	10/10/2012			Pending	Intelligent Connectors Integrating Magnetic Modular Jacks And Intelli
MP4313W	MP4313WOCN	CN	201280060794.1	10/10/2012			Pending	INTELLIGENT CONNECTORS INTEGRATING MAGNETIC MODULAR JAC
MP4313W	MP4313WOEP	EP	12791310.1	10/10/2012			Pending	INTELLIGENT CONNECTORS INTEGRATING MAGNETIC MODULAR JAC
MP4317	MP4317	US	13/671,876	11/8/2012	8972180	3/3/2015	In Force	Method And Apparatus For Managing Multiple Sensors In A Navigatio
MP4319T	MP4319TW	TW	101139472	10/25/2012	1568171	1/21/2017	In Force	LOW-STRESS CASCODE STRUCTURE
MP4319W	MP4319WO	WO	PCT/182012/002	10/25/2012			Pending	Low-Stress Cascode Structure
MP4319W	MP4319WOCN	CN	201280052481.1	10/25/2012	ZL2012800524	8/24/2016	In Force	LOW-STRESS CASCODE STRUCTURE
MP4320	MP4320	US	13/647,723	10/9/2012	8949697	2/3/2015	In Force	LOW POWER REED-SOLOMON DECODER
MP4324	MP4324	US	13/648,207	10/9/2012	9189329	11/17/2015	In Force	GENERATING ERROR CORRECTING CODE (ECC) DATA USING AN ECC
MP4324	MP4324C1	US	14/940,023	11/12/2015	10198310	2/5/2019	In Force	PROVIDING ERROR CORRECTING CODE (ECC) CAPABILITY FOR MEMO
MP4325	MP4325	US	13/648,455	10/10/2012	9185760	11/10/2015	In Force	ALTERNATING CURRENT (AC) LINE VOLTAGE DETERMINATION
MP4326	MP4326	US	13/646,461	10/5/2012	8781486	7/15/2014	In Force	RESOURCE ALLOCATION AND BAND ASSIGNMENT IN CARRIER-AGGR
MP4329	MP4329	US	13/638,102	9/27/2012	9329835	5/3/2016	In Force	Systems and Methods for Performing Mathematical Functions
MP4330	MP4330	US	13/654,925	10/18/2012	8965295	2/24/2015	In Force	Systems And Methods For Suppressing Interference In A Signal Recei
MP4330W	MP4330WO	WO	PCT/US2012/06	10/18/2012			Pending	Systems And Methods For Suppressing Interference In A Signal Recei
MP4330W	MP4330WOCN	CN	201280051599	10/18/2012	ZL2012800515	4/27/2018	In Force	SYSTEMS AND METHODS FOR SUPPRESSING INTERFERENCE IN A SIG
MP4330W	MP4330WOEP	EP	EP12787951.8	10/18/2012			Pending	SYSTEMS AND METHODS FOR SUPPRESSING INTERFERENCE IN A SIG
MP4330W	MP4330WOJP	JP	2014-537238	10/18/2012	6169581	7/7/2017	In Force	SYSTEMS AND METHODS FOR SUPPRESSING INTERFERENCE IN A SIG
MP4330W	MP4330WOKR	KR	10-2014-701047	10/18/2012			Pending	Systems And Methods For Suppressing Interference In A Signal Recei
MP4331	MP4331	US	13/655,766	10/19/2012	9054765	6/9/2015	In Force	Systems and Methods for Suppressing Interference in a Wireless Com
MP4331W	MP4331WO	WO	PCT/US2012/06	10/19/2012			Pending	Systems and Methods for Suppressing Interference in a Wireless Com
MP4333	MP4333	US	13/659,709	10/24/2012	8787483	7/22/2014	In Force	METHOD AND APPARATUS FOR DETECTING A DESIRED SIGNAL IN TH
MP4333	MP4333C1	US	14/331,557	7/15/2014	9025708	5/5/2015	In Force	METHOD AND APPARATUS FOR DETECTING A DESIRED SIGNAL IN TH
MP4336	MP4336	US	13/941,390	7/12/2013	9214973	12/15/2015	In Force	TIME VARYING NOTCH FILTER
MP4336	MP4336C1	US	14/965,535	12/10/2015	9667288	5/30/2017	In Force	TIME VARYING NOTCH FILTER
MP4337	MP4337C1	US	14/546,850	11/18/2014			Pending	IMPLEMENTATION OF LIR BIASING METHOD IN NON-BINARY ITERATI
MP4340	MP4340	US	13/660,755	10/25/2012	8957733	2/17/2015	In Force	SYSTEMS AND METHODS FOR PERFORMING MULTI-MODAL POWER
MP4340T	MP4340TW	TW	101139795	10/26/2012	1578693	4/11/2017	In Force	SYSTEMS AND METHODS FOR PERFORMING MULTI-MODAL POWER
MP4340W	MP4340WO	WO	PCT/US2012/06	10/25/2012			Pending	SYSTEMS AND METHODS FOR PERFORMING MULTI-MODAL POWER
MP4341	MP4341	US	13/648,459	10/10/2012	9152580	10/6/2015	In Force	METHOD AND APPARATUS FOR TRANSFERRING DATA BETWEEN A H
MP4342	MP4342	US	13/663,106	10/29/2012	8942320	1/27/2015	In Force	DATA UNIT FORMAT FOR MULTI-USER DATA IN LONG-RANGE WIREL
MP4342	MP4342C1	US	14/605,894	1/26/2015	9254326	3/22/2016	In Force	DATA UNIT FORMAT FOR MULTI-USER DATA IN LONG-RANGE WIREL
MP4342W	MP4342WO	WO	PCT/US2012/06	10/29/2012			Pending	Data Unit Format for Multi-User Data in Long-Range Wireless Local A

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MP4342W	MP4342WOCN	CN	201280052898.1	10/29/2012	ZL2012800528	7/14/2017	In Force	DATA UNIT FORMAT SIGNALLING FOR WIRELESS LOCAL AREA NETW
MP4342W	MP4342WCEP	EP	EP12799352.5	10/29/2012	EP2772034	5/24/2017	In Force	DATA UNIT FORMAT FOR MULTI-USER IN LONG-RANGE WIRELESS LO
MP4342W	MP4342WCEPDE	DE	12799352.5	10/29/2012	602012032826	5/24/2017	In Force	DATA UNIT FORMAT FOR MULTI-USER IN LONG-RANGE WIRELESS LO
MP4342W	MP4342WCEPFR	FR	12799352.5	10/29/2012	2772034	5/24/2017	In Force	DATA UNIT FORMAT FOR MULTI-USER IN LONG-RANGE WIRELESS LO
MP4342W	MP4342WCEPGB	GB	12799352.5	10/29/2012	2772034	5/24/2017	In Force	DATA UNIT FORMAT FOR MULTI-USER IN LONG-RANGE WIRELESS LO
MP4342W	MP4342WCEPJP	JP	2014-539112	10/29/2012	6035637	11/11/2016	In Force	DATA UNIT FORMAT FOR MULTI-USER DATA IN LONG-RANGE WIREL
MP4342W	MP4342WCEPKR	KR	2014-7012033	10/29/2012			Pending	DATA UNIT FORMAT FOR MULTI-USER IN LONG-RANGE WIRELESS LO
MP4343	MP4343	US	13/658,177	10/23/2012	8977795	3/10/2015	In Force	Method and Apparatus for Preventing Multiple Threads of a Process
MP4344	MP4344	US	13/406,246	2/27/2012	8710884	4/29/2014	In Force	METHODS AND DEVICES FOR MULTIPLE-MODE RADIO FREQUENCY S
MP4344	MP4344C1	US	14/244,710	4/3/2014	8957713	2/17/2015	In Force	METHODS AND DEVICES FOR MULTIPLE-MODE RADIO FREQUENCY S
MP4344W	MP4344WOCN	WO	PCT/US2012/02	2/27/2012			Pending	METHODS AND DEVICES FOR MULTIPLE-MODE RADIO FREQUENCY S
MP4344W	MP4344WCEP	EP	EP12618045.2	2/27/2012	EP2681866	10/5/2016	In Force	METHODS AND DEVICES FOR MULTIPLE-MODE RADIO FREQUENCY S
MP4344W	MP4344WCEPDE	DE	12818045.2	2/27/2012	602012023841	10/5/2016	In Force	METHODS AND DEVICES FOR MULTIPLE-MODE RADIO FREQUENCY S
MP4344W	MP4344WCEPFR	FR	12818045.2	2/27/2012	2681966	10/5/2016	In Force	METHODS AND DEVICES FOR MULTIPLE-MODE RADIO FREQUENCY S
MP4344W	MP4344WCEPGB	GB	12818045.2	2/27/2012	2681966	10/5/2016	In Force	METHODS AND DEVICES FOR MULTIPLE-MODE RADIO FREQUENCY S
MP4345	MP4345	US	13/666,251	11/11/2012	8779961	7/15/2014	In Force	Scalable Successive-Approximation-Register Analog-To-Digital Conver
MP4345W	MP4345WOCN	WO	PCT/US2012/06	11/11/2012			Pending	Scalable Successive-Approximation-Register Analog-To-Digital Conver
MP4346	MP4346C1	US	14/662,933	3/10/2015	9236960	1/12/2016	In Force	Calibration Circuit And Methods For Calibrating Harmonics In A Mixer
MP4348	MP4348	US	13/667,067	11/12/2012	9201825	12/1/2015	In Force	Data Storage Methods And Apparatus
MP4349	MP4349	US	13/664,884	10/31/2012	8942260	1/27/2015	In Force	SERVICES, SYSTEMS AND METHODS FOR PRECISELY ESTIMATING A D
MP4349W	MP4349WOCN	WO	PCT/IB2012/002	10/31/2012			Pending	SERVICES, SYSTEMS AND METHODS FOR PRECISELY ESTIMATING A D
MP4349W	MP4349WOCN	CN	201280053445.1	10/31/2012	ZL2012800534	6/20/2017	In Force	SERVICES, SYSTEMS AND METHODS FOR PRECISELY ESTIMATING A D
MP4349W	MP4349WCEP	EP	EP12612717.2	10/31/2012	2774291	7/17/2019	In Force	SERVICES, SYSTEMS AND METHODS FOR PRECISELY ESTIMATING A D
MP4349W	MP4349WCEPDE	DE	EP12612717.2	10/31/2012	602012062134	7/17/2019	In Force	SERVICES, SYSTEMS AND METHODS FOR PRECISELY ESTIMATING A D
MP4349W	MP4349WCEPFR	FR	EP12612717.2	10/31/2012	2774291	7/17/2019	In Force	SERVICES, SYSTEMS AND METHODS FOR PRECISELY ESTIMATING A D
MP4349W	MP4349WCEPGB	GB	EP12612717.2	10/31/2012	2774291	7/17/2019	In Force	SERVICES, SYSTEMS AND METHODS FOR PRECISELY ESTIMATING A D
MP4349W	MP4349WCEPJP	JP	2014-539422	10/31/2012	6109837	3/17/2017	In Force	SERVICES, SYSTEMS AND METHODS FOR PRECISELY ESTIMATING A D
MP4349W	MP4349WCEPKR	KR	10-2014-701173	10/31/2012	101958374	3/8/2019	In Force	SERVICES, SYSTEMS AND METHODS FOR PRECISELY ESTIMATING A D
MP4350	MP4350	US	13/667,935	11/12/2012	9088991	7/21/2015	In Force	WLAN TRANSMIT MODES AND COEXISTENCE
MP4350	MP4350D1	US	13/591,974	8/22/2012	8873579	10/28/2014	In Force	MULTI-RATE MEDIA INDEPENDENT INTERFACE OVER A PHYSICAL CO
MP4350W	MP4350WOCN	WO	PCT/US2012/06	11/12/2012			Pending	WLAN TRANSMIT MODES AND COEXISTENCE
MP4350W	MP4350WOCN	CN	201280054177.1	11/12/2012	ZL2012800541	1/12/2018	In Force	METHOD AND APPARATUS FOR WIRELESS COMMUNICATIONS
MP4350W	MP4350WCEP	EP	EP12791628.9	11/12/2012	2774439	1/9/2019	In Force	WLAN TRANSMIT MODES AND COEXISTENCE
MP4350W	MP4350WCEPDE	DE	EP12791628.9	11/12/2012	602012055764	1/9/2019	In Force	WLAN TRANSMIT MODES AND COEXISTENCE

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MP4350W	MP4350WOEPFR	FR	EP12791028.9	11/2/2012	2774439	1/9/2019	In Force	WLAN TRANSMIT MODES AND COEXISTENCE
MP4350W	MP4350WOEPGB	GB	EP12791028.9	11/2/2012	2774439	1/9/2019	In Force	WLAN TRANSMIT MODES AND COEXISTENCE
MP4350W	MP4350WOJP	JP	2014-540127	11/2/2012	6109839	3/17/2017	In Force	WLAN TRANSMIT MODES AND COEXISTENCE
MP4350W	MP4350WOKR	KR	2014-7012036	11/2/2012	101863305	5/31/2018	In Force	WLAN TRANSMIT MODES AND COEXISTENCE
MP4356	MP4356	US	13/661,883	10/26/2012	8937508	1/20/2015	In Force	DIFFERENTIAL AMPLIFIER
MP4356W	MP4356WO	WO	PCT/US2012/06	10/26/2012			Pending	DIFFERENTIAL AMPLIFIER
MP4356W	MP4356WOCN	CN	201280053209.1	10/26/2012	ZL2012800532	5/17/2017	In Force	DIFFERENTIAL AMPLIFIER
MP4356W	MP4356WOCND1	CN	201610839136	10/26/2012	ZL2016108391	4/12/2019	In Force	CIRCUIT FOR A DIFFERENTIAL AMPLIFIER
MP4356W	MP4356WOEP	EP	EP12784401.7	10/26/2012			Pending	DIFFERENTIAL AMPLIFIER
MP4356W	MP4356WOKR	KR	10-2014-701483	10/26/2012	10-1947663	2/7/2019	In Force	DIFFERENTIAL AMPLIFIER
MP4358T	MP4358TW	TW	101140714	11/2/2012	1560986	12/1/2016	In Force	REGULATED POWER SUPPLY VOLTAGE FOR DIGITAL CIRCUITS
MP4358W	MP4358WO	WO	PCT/US2012/06	10/26/2012			Pending	REGULATED POWER SUPPLY VOLTAGE FOR DIGITAL CIRCUITS
MP4358W	MP4358WOCN	CN	201280053447.1	10/26/2012			Pending	REGULATED POWER SUPPLY VOLTAGE FOR DIGITAL CIRCUITS
MP4358W	MP4358WOEP	EP	EP12791890.2	10/26/2012			Pending	REGULATED POWER SUPPLY VOLTAGE FOR DIGITAL CIRCUITS
MP4358W	MP4358WOKR	KR	10-2014-701483	10/26/2012			Pending	REGULATED POWER SUPPLY VOLTAGE FOR DIGITAL CIRCUITS
MP4362	MP4362	US	13/669,359	11/5/2012	9146824	9/29/2015	In Force	MANAGEMENT OF BIT LINE ERRORS BASED ON A STORED SET OF DA
MP4364	MP4364	US	13/662,177	10/26/2012	8984093	3/17/2015	In Force	Method and Apparatus for Arbitration of Time-Sensitive Data Transm
MP4364W	MP4364WO	WO	PCT/US2012/06	10/26/2012			Pending	Method and Apparatus for Arbitration of Time-Sensitive Data Transm
MP4364W	MP4364WOCN	CN	201280053444.1	10/26/2012	ZL2012800534	10/9/2018	In Force	METHOD AND APPARATUS FOR ARBITRATION OF TIME-SENSITIVE DA
MP4364W	MP4364WOEP	EP	EP12819132.7	10/26/2012	EP2774348	4/26/2017	In Force	METHOD AND APPARATUS FOR ARBITRATION OF TIME-SENSITIVE DA
MP4364W	MP4364WOJP	JP	2014-539423	10/26/2012	6045085	11/25/2016	In Force	METHOD AND APPARATUS FOR ARBITRATION OF TIME-SENSITIVE DA
MP4364W	MP4364WOKR	KR	1020147014311	10/26/2012			Pending	Method and Apparatus for Arbitration of Time-Sensitive Data Transm
MP4364W	MP4364WUDE	DE	12819132.7	10/26/2012	602012031705	4/26/2017	In Force	METHOD AND APPARATUS FOR ARBITRATION OF TIME-SENSITIVE DA
MP4364W	MP4364WUFR	FR	12819132.7	10/26/2012	2774348	4/26/2017	In Force	METHOD AND APPARATUS FOR ARBITRATION OF TIME-SENSITIVE DA
MP4364W	MP4364WUGB	GB	12819132.7	10/26/2012	2774348	4/26/2017	In Force	METHOD AND APPARATUS FOR ARBITRATION OF TIME-SENSITIVE DA
MP4366	MP4366	US	13/669,476	11/6/2012	9020058	4/28/2015	In Force	PRECODING FEEDBACK FOR CROSS-POLARIZED ANTENNAS BASED ON
MP4367	MP4367	US	13/669,477	11/6/2012	8933427	12/30/2014	In Force	CODEBOOK SUB-SAMPLING FOR FREQUENCY-SELECTIVE PRECODING
MP4373	MP4373	US	13/668,591	11/5/2012	9030955	5/12/2015	In Force	Network Access Mechanism Based on Power
MP4373	MP4373C1	US	14/666,877	4/15/2015	9307482	4/5/2016	In Force	Network Access Mechanism Based on Power
MP4373W	MP4373WO	WO	PCT/US2012/06	11/5/2012			Pending	Network Access Mechanism Based on Power
MP4373W	MP4373WOCN	CN	201280054178.1	11/5/2012	ZL2012800557	7/31/2018	In Force	METHOD AND SYSTEM FOR PROVIDING ACCESS TO COMMUNICATIO
MP4373W	MP4373WOEP	EP	EP12819134.3	11/5/2012	2777218	1/10/2018	In Force	NETWORK ACCESS MECHANISM BASED ON POWER
MP4373W	MP4373WOJP	JP	2014-539424	11/5/2012	6160964	6/23/2017	In Force	NETWORK ACCESS MECHANISM BASED ON POWER
MP4373W	MP4373WOKR	KR	10-2014-701125	11/5/2012			Pending	Network Access Mechanism Based on Power

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MP4373W	MP4373WUDE	DE	EP12819134.3	11/5/2012	60201204188	1/10/2018	In Force	Network Access Mechanism Based on Power
MP4373W	MP4373WUFR	FR	EP12819134.3	11/5/2012	2777218	1/10/2018	In Force	Network Access Mechanism Based on Power
MP4373W	MP4373WUGB	GB	EP12819134.3	11/5/2012	2777218	1/10/2018	In Force	Network Access Mechanism Based on Power
MP4378	MP4378	US	13/668,506	11/5/2012	9100136	8/4/2015	In Force	METHODS AND APPARATUS FOR MITIGATING KNOWN INTERFERENC
MP4378	MP4378C1	US	14/804,821	7/21/2015	9479205	10/25/2016	In Force	METHODS AND APPARATUS FOR MITIGATING KNOWN INTERFERENC
MP4378	MP4378C1C1	US	15/351,031	10/21/2016	10003365	6/19/2018	In Force	METHODS AND APPARATUS FOR MITIGATING KNOWN INTERFERENC
MP4378W	MP4378WO	WO	PCT/US2012/06	11/5/2012			Pending	METHODS AND APPARATUS FOR MITIGATING KNOWN INTERFERENC
MP4378W	MP4378WOCN	CN	201280054808.1	11/5/2012	212012800548	9/21/2018	In Force	METHODS AND APPARATUS FOR MITIGATING KNOWN INTERFERENC
MP4378W	MP4378WCEP	EP	EP12799368.1	11/5/2012	EP2777190	10/11/2017	In Force	METHODS AND APPARATUS FOR MITIGATING KNOWN INTERFERENC
MP4378W	MP4378WOJP	JP	2014-540183	11/5/2012	6253587	12/27/2017	In Force	METHODS AND APPARATUS FOR MITIGATING KNOWN INTERFERENC
MP4378W	MP4378WOKR	KR	10-2014-701535	11/5/2012			Pending	METHODS AND APPARATUS FOR MITIGATING KNOWN INTERFERENC
MP4378W	MP4378WUDE	DE	12799368.1	11/5/2012	60201203946	10/11/2017	In Force	METHODS AND APPARATUS FOR MITIGATING KNOWN INTERFERENC
MP4378W	MP4378WUFR	FR	12799368.1	11/5/2012	2777190	10/11/2017	In Force	METHODS AND APPARATUS FOR MITIGATING KNOWN INTERFERENC
MP4378W	MP4378WUGB	GB	12799368.1	11/5/2012	2777190	10/11/2017	In Force	METHODS AND APPARATUS FOR MITIGATING KNOWN INTERFERENC
MP4382	MP4382	US	13/676,440	11/14/2012	9489304	11/8/2016	In Force	Bi-Domain Bridge Enhanced Systems And Communication Methods
MP4383	MP4383	US	13/670,597	11/7/2012	8903001	12/2/2014	In Force	Channel Estimation with Decision Feedback
MP4383W	MP4383WO	WO	PCT/US2012/06	11/7/2012			Pending	Channel Estimation with Decision Feedback
MP4386	MP4386	US	13/670,469	11/7/2012	8883123	9/23/2014	In Force	TECHNIQUES FOR ACCELERATING CELLULAR MEASUREMENTS
MP4388	MP4388	US	13/672,727	11/9/2012	9031597	5/12/2015	In Force	DIFFERENTIAL COI ENCODING FOR COOPERATIVE MULTIPPOINT FEED
MP4388	MP4388C1	US	14/697,673	4/28/2015	9337985	5/10/2016	In Force	METHOD AND APPARATUS FOR DIFFERENTIAL ENCODING OF COI IN
MP4391	MP4391CN	CN	201210449525.4	11/12/2012			Pending	INFORMATION RECOGNITION SYSTEM FOR PICTURE BASED INPUT
MP4393	MP4393	US	13/673,371	11/9/2012	9069578	4/14/2015	In Force	METHODOLOGY FOR IMPROVED BIT-FLIPPING DECODER IN 1-READ A
MP4393	MP4393C1	US	14/681,801	4/8/2015	9360328	3/29/2016	In Force	METHODOLOGY FOR IMPROVED BIT-FLIPPING DECODER IN 1-READ A
MP4394	MP4394	US	13/674,503	11/12/2012	9147470	9/29/2015	In Force	Nand Flash Controller With Programmable Command Templates
MP4395	MP4395	US	13/676,864	11/14/2012	8964498	2/24/2015	In Force	SYSTEMS AND METHODS FOR REDUCING PEAK POWER CONSUMPTI
MP4395W	MP4395WO	WO	PCT/US2012/06	11/14/2012			Pending	SYSTEMS AND METHODS FOR REDUCING PEAK POWER CONSUMPTI
MP4395W	MP4395WOCN	CN	201280055784.1	11/14/2012	212012800557	10/20/2017	In Force	SYSTEMS AND METHODS FOR REDUCING PEAK POWER CONSUMPTI
MP4398	MP4398C1	US	14/723,649	5/28/2015	9739079	8/8/2017	In Force	METHOD AND APPARATUS FOR CONTROLLING TRIAC DIMMABLE DE
MP4399	MP4399	US	13/676,701	11/14/2012	9436629	9/6/2016	In Force	Dynamic Boot Image Streaming
MP4399	MP4399D1	US	15/151,016	5/10/2016	10275377	4/30/2019	In Force	Dynamic Boot Image Streaming
MP4399W	MP4399WO	WO	PCT/US2012/06	11/15/2012			Pending	Dynamic Boot Image Streaming
MP4404	MP4404CN	CN	201310017371.1	1/9/2013			Pending	A METHOD AND APPARATUS FOR REDUCING POWER CONSUMPTION
MP4408	MP4408	US	13/681,139	11/19/2012	9130776	9/8/2015	In Force	DATA PATH ACCELERATION USING HW VIRTUALIZATION
MP4415	MP4415	US	13/680,530	11/19/2012	8902625	12/2/2014	In Force	Layouts For Memory And Logic Circuits In A System-On-Chip

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MP4415T	MP4415TW	TW	101143703	11/22/2012	1616764	3/1/2018	In Force	Layouts For Memory And Logic Circuits In A System-On-Chip
MP4415W	MP4415WO	WO	PCT/US2012/06	11/21/2012			Pending	Layouts For Memory And Logic Circuits In A System-On-Chip
MP4415W	MP4415WOCN	CN	2012800573483	11/21/2012	ZL2012800573	4/12/2017	In Force	AN INTEGRATED CIRCUIT AND A SYSTEM AND METHOD THEREFOR
MP4415W	MP4415WJJP	JP	2014-543589	11/21/2012	6257044	1/10/2018	In Force	LAYOUTS FOR MEMORY AND LOGIC CIRCUITS IN A SYSTEM-ON-CHIP
MP4416	MP4416	US	13/678,952	11/16/2012	8949474	2/3/2015	In Force	Method For Inter-Chip And Intra-Chip Addressing Using Port Identify
MP4419	MP4419	US	13/747,041	1/22/2013	9053010	6/9/2015	In Force	CACHE SYSTEM USING SOLID STATE DRIVE
MP4419	MP4419C1	US	14/733,478	6/8/2015	9552300	1/24/2017	In Force	CACHE SYSTEM USING SOLID STATE DRIVE
MP4419W	MP4419WO	WO	PCT/US2013/02	1/22/2013			Pending	CACHE SYSTEM USING SOLID STATE DRIVE
MP4420	MP4420	US	13/682,552	11/20/2012	8959417	2/17/2015	In Force	PROVIDING LOW-LATENCY ERROR CORRECTING CODE CAPABILITY F
MP4420	MP4420CN	CN	201210498603	11/23/2012	ZL2012104986	6/23/2017	In Force	PROVIDING LOW-LATENCY ERROR CORRECTING CODE CAPABILITY F
MP4421	MP4421	US	13/680,831	11/19/2012	9155027	10/6/2015	In Force	802.11 Enhanced Distributed Channel Access
MP4421	MP4421C1	US	14/875,597	10/5/2015	9918340	3/13/2018	In Force	Enhanced Distributed Channel Access Parameter Variation Within Re
MP4424	MP4424	US	13/721,916	12/20/2012	9304912	4/5/2016	In Force	Systems and Methods for Building Redundancy Data in a RAID Syste
MP4425	MP4425	US	13/681,737	11/20/2012	8937511	1/20/2015	In Force	Frequency Scaling Of Variable Speed Systems For Fast Response And
MP4425W	MP4425WO	WO	PCT/US2012/06	11/21/2012			Pending	Frequency Scaling Of Variable Speed Systems For Fast Response And
MP4426	MP4426	US	13/683,638	11/21/2012	8730609	5/20/2014	In Force	DC Shift Detection And Correction
MP4428	MP4428	US	13/680,876	11/19/2012	9204371	12/1/2015	In Force	802.11 Restricted Access Windows
MP4428W	MP4428WO	WO	PCT/US2012/06	11/21/2012			Pending	802.11 Restricted Access Windows
MP4428W	MP4428WOCN	CN	2012800575915	11/21/2012	ZL2012800575	8/3/2018	In Force	802.11 RESTRICTED ACCESS WINDOWS
MP4428W	MP4428WCEP	EP	12799401.0	11/21/2012	2783544	6/13/2018	In Force	802.11 RESTRICTED ACCESS WINDOWS
MP4428W	MP4428WJJP	JP	2014-543555	11/21/2012	5943088	6/3/2016	In Force	802.11 RESTRICTED ACCESS WINDOWS
MP4428W	MP4428WOKR	KR	1020147017280	11/21/2012	10-1960905	3/13/2019	In Force	802.11 Restricted Access Windows
MP4428W	MP4428WUDE	DE	12799401.0	11/21/2012	602012047475	6/13/2018	In Force	802.11 RESTRICTED ACCESS WINDOWS
MP4428W	MP4428WUFR	FR	12799401.0	11/21/2012	2783544	6/13/2018	In Force	802.11 RESTRICTED ACCESS WINDOWS
MP4428W	MP4428WUGB	GB	12799401.0	11/21/2012	2783544	6/13/2018	In Force	802.11 RESTRICTED ACCESS WINDOWS
MP4429	MP4429	US	13/675,555	11/13/2012	8832622	9/9/2014	In Force	COVERAGE SCOREBOARD
MP4430	MP4430	US	13/684,005	11/21/2012	8964911	2/24/2015	In Force	METHOD AND APPARATUS FOR MEASURING INTERFERENCE IN A CO
MP4431W	MP4431WO	WO	PCT/US2012/06	11/28/2012			Pending	Color Mixing Systems With Buck-Boost And Flyback Topologies
MP4431W	MP4431WCEP	EP	12801673.0	11/28/2012	EP2786639	1/4/2017	In Force	Color Mixing Systems With Buck-Boost and Flyback Topologies
MP4431W	MP4431WUDE	DE	12801673.0	11/28/2012	602012027484	1/4/2017	In Force	COLOR MIXING SYSTEMS WITH BUCK-BOOST AND FLYBACK TOPOLO
MP4431W	MP4431WUFR	FR	12801673.0	11/28/2012	2786639	1/4/2017	In Force	COLOR MIXING SYSTEMS WITH BUCK-BOOST AND FLYBACK TOPOLO
MP4431W	MP4431WUGB	GB	12801673.0	11/28/2012	2786639	1/4/2017	In Force	COLOR MIXING SYSTEMS WITH BUCK-BOOST AND FLYBACK TOPOLO
MP4432W	MP4432WO	WO	PCT/US2013/02	2/25/2013			Pending	RECONFIGURABLE VOLTAGE CONTROLLED OSCILLATOR FOR SUPPOR
MP4433	MP4433	US	13/688,929	11/29/2012	9128920	9/8/2015	In Force	Interrupt Handling Systems And Methods For PCIe Bridges With Multi

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MP4433W	MP4433WO	WO	PCT/JP2012/002	11/30/2012			Pending	Interrupt Handling Systems And Methods For PCIe Bridges With Multi
MP4433W	MP4433WOCN	CN	2012800586375	11/30/2012	ZL2012800586	10/26/2016	In Force	INTERRUPT HANDLING SYSTEMS AND METHODS FOR PCIe BRIDGES
MP4440	MP4440	US	13/708,509	12/7/2012	9614386	4/4/2017	In Force	METHOD AND APPARATUS FOR POWER SWITCHING
MP4440W	MP4440WO	WO	PCT/US2012/06	12/7/2012			Pending	METHOD AND APPARATUS FOR POWER SWITCHING
MP4440W	MP4440WOCN	CN	201280060518.1	12/7/2012			Pending	METHOD AND APPARATUS FOR POWER SWITCHING
MP4450	MP4450	US	13/717,884	12/18/2012	8934840	1/13/2015	In Force	METHOD AND APPARATUS FOR PERFORMING ARBITRATION AMONG
MP4451	MP4451CN	CN	201310051061.1	1/31/2013	ZL2013100510	4/24/2018	In Force	CELLULAR COMMUNICATIONS METHOD
MP4455	MP4455	US	13/610,904	9/12/2012	9220087	12/22/2015	In Force	DYNAMIC POINT SELECTION WITH COMBINED PUSCH/PUSCH FEEDBACK
MP4456	MP4456	US	13/715,223	12/14/2012	8853964	10/7/2014	In Force	Current Balancing Circuits For Light-Emitting-Diode-Based Illuminatio
MP4456	MP4456C1	US	14/587,200	10/6/2014	9241385	1/19/2016	In Force	Current Balancing Circuits For Light-Emitting-Diode-Based Illuminatio
MP4456	MP4456D2	US	14/181,042	2/14/2014	8947013	2/3/2015	In Force	LED-Based Lamp With User-Selectable Color Temperature
MP4456	MP44561C1	US	14/733,268	6/8/2015	9554441	1/24/2017	In Force	Current Balancing For Light-Emitting-Diode-Based Illumination Syste
MP4456W	MP4456WO	WO	PCT/US2012/06	12/14/2012			Pending	Current Balancing Circuits For Light-Emitting-Diode-Based Illuminatio
MP4456W	MP4456WOCN	CN	2012800698084.1	12/14/2012	ZL20128006980	6/9/2017	In Force	CURRENT BALANCING CIRCUITS FOR LIGHT-EMITTING-DIODE-BASED
MP4456W	MP4456WOEP	EP	EP12819157.4	12/14/2012			Pending	CURRENT BALANCING CIRCUITS FOR LIGHT-EMITTING-DIODE-BASED
MP4456W	MP4456WOJP	JP	2014-547495	12/14/2012	6195420	8/25/2017	In Force	CURRENT BALANCING CIRCUITS FOR LIGHT-EMITTING-DIODE-BASED
MP4460	MP4460	US	13/706,509	12/6/2012	9385786	7/5/2016	In Force	METHOD AND APPARATUS FOR CHARGING A BATTERY IN A MOBILE
MP4460W	MP4460WOCN	CN	201280061476.1	12/10/2012	ZL2012800614	2/23/2018	In Force	METHOD AND APPARATUS FOR CHARGING A BATTERY IN A MOBILE
MP4460W	MP4460WOEP	EP	EP12815771.6	12/10/2012			Pending	METHOD AND APPARATUS FOR CHARGING A BATTERY IN A MOBILE
MP4460W	MP4460WOJP	JP	2014-546662	12/10/2012	6104933	3/10/2017	In Force	Method and Apparatus for Charging a Battery in a Mobile Device Thr
MP4460W	MP4460WOCR	KR	10-2014-701615	12/10/2012			Pending	Inductive Charge Through NFC Antenna
MP4462	MP4462CN	CN	201310117701.1	3/29/2013			Pending	BATTERY HOT-PLUG FOR CELL PHONE
MP4463	MP4463	US	13/610,908	9/12/2012	9166729	10/20/2015	In Force	ENHANCED DEMODULATION REFERENCE SIGNAL (DM-RS) DESIGN
MP4464	MP4464	US	13/713,813	12/13/2012	8982849	3/17/2015	In Force	COEXISTENCE MECHANISM FOR 802.11AC COMPLIANT 80MHZ WLA
MP4465	MP4465	US	13/713,316	12/13/2012	8913437	12/16/2014	In Force	Inter-Cell Interference Cancellation
MP4465	MP4465C1	US	14/570,552	12/15/2014	9183942	11/10/2015	In Force	Inter-Cell Interference Cancellation
MP4465	MP4465CN	CN	201210599259.1	12/17/2012	ZL2012105992	8/10/2018	In Force	INTER-CELL INTERFERENCE CANCELLATION
MP4465T	MP4465TW	TW	101147539	12/14/2012	1596616	8/21/2017	In Force	Inter-Cell Interference Cancellation
MP4470	MP4470	US	13/713,328	12/13/2012	8994362	3/31/2015	In Force	RF Power Detection Circuit With Insensitivity To Process, Temperatur
MP4470	MP4470C1	US	14/669,418	3/26/2015	9292716	3/22/2016	In Force	Method And Apparatus For Detecting An Output Power Of A Radio Fr
MP4470W	MP4470WO	WO	PCT/US2012/06	12/13/2012			Pending	RF Power Detection Circuit With Insensitivity To Process, Temperatur
MP4470W	MP4470WOCN	CN	201280061894.1	12/13/2012	ZL2012800618	9/1/2017	In Force	RF POWER DETECTION CIRCUIT WITH INSENSITIVITY TO PROCESS, TE
MP4470W	MP4470WOEP	EP	EP12809941.3	12/13/2012			Pending	RF POWER DETECTION CIRCUIT WITH INSENSITIVITY TO PROCESS, TE
MP4470W	MP4470WOJP	JP	2014-547418	12/13/2012	6205659	9/15/2017	In Force	RF POWER DETECTION CIRCUIT WITH INSENSITIVITY TO PROCESS, TEM

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MP4470W	MP4470WOKR	KR	1020147018958	12/13/2012			Pending	RF Power Detection Circuit With Insensitivity To Process, Temperature
MP4471	MP4471	US	13/712,183	12/12/2012	9239808	1/19/2016	In Force	Serial Interface for FPGA Prototyping
MP4471W	MP4471WO	WO	PCT/US2012/06	12/13/2012			Pending	Serial Interface for FPGA Prototyping
MP4471W	MP4471WOCN	CN	201280061991.1	12/13/2012	ZL2012800619	10/27/2017	In Force	SERIAL INTERFACE FOR FPGA PROTOTYPING
MP4471W	MP4471WOEP	EP	EP12816187.4	12/13/2012			Pending	Serial Interface for FPGA Prototyping
MP4472	MP4472	US	13/711,596	12/11/2012	9152553	10/6/2015	In Force	GENERIC COMMAND DESCRIPTOR FOR CONTROLLING MEMORY DEVI
MP4475	MP4475	US	13/717,375	12/17/2012	8659144	2/25/2014	In Force	POWER AND GROUND PLANES IN PACKAGE SUBSTRATE
MP4482	MP4482	US	13/719,241	12/19/2012	9367523	4/5/2016	In Force	REFERENCE SIGNAL DESIGN FOR COORDINATED MULTIPPOINT TRANSMISSION
MP4482W	MP4482WOCN	CN	201280062801.1	12/19/2012	ZL2012800628	8/10/2018	In Force	REFERENCE SIGNAL DESIGN FOR COORDINATED MULTIPPOINT TRANSMISSION
MP4482W	MP4482WOEP	EP	EP12859132.8	12/19/2012	2795827	9/4/2019	In Force	Reference Signal Design for Coordinated Multipoint Transmission
MP4482W	MP4482WOEPDE	DE	EP12859132.8	12/19/2012	602012063744	9/4/2019	In Force	Reference Signal Design for Coordinated Multipoint Transmission
MP4482W	MP4482WOEPFR	FR	EP12859132.8	12/19/2012	2795827	9/4/2019	In Force	Reference Signal Design for Coordinated Multipoint Transmission
MP4482W	MP4482WOEPGB	GB	EP12859132.8	12/19/2012	2795827	9/4/2019	In Force	Reference Signal Design for Coordinated Multipoint Transmission
MP4482W	MP4482WOJP	JP	2014-548298	12/19/2012	6085330	1/6/2017	In Force	Reference Signal Design for Coordinated Multipoint Transmission
MP4483	MP4483	US	13/735,458	1/7/2013	9036517	5/19/2015	In Force	Methods and Apparatus for Establishing a Tunneled Direct Link Setup
MP4483W	MP4483WO	WO	WO2013050206	1/8/2013			Pending	Methods and Apparatus for Establishing a Tunneled Direct Link Setup
MP4483W	MP4483WOCN	CN	201380005022.1	1/8/2013	ZL2013800050	7/6/2018	In Force	METHODS AND APPARATUS FOR ESTABLISHING A TUNNELLED DIRECT
MP4483W	MP4483WOEP	EP	EP13718622.7	1/8/2013	2803244	7/17/2019	In Force	METHODS AND APPARATUS FOR ESTABLISHING A TUNNELLED DIRECT
MP4483W	MP4483WOEPDE	DE	EP13718622.7	1/8/2013	602013057881	7/17/2019	In Force	METHODS AND APPARATUS FOR ESTABLISHING A TUNNELLED DIRECT
MP4483W	MP4483WOEPFR	FR	EP13718622.7	1/8/2013	2803244	7/17/2019	In Force	METHODS AND APPARATUS FOR ESTABLISHING A TUNNELLED DIRECT
MP4483W	MP4483WOEPGB	GB	EP13718622.7	1/8/2013	2803244	7/17/2019	In Force	METHODS AND APPARATUS FOR ESTABLISHING A TUNNELLED DIRECT
MP4483W	MP4483WOJP	JP	2014-550783	1/8/2013	6508591	4/12/2019	In Force	METHODS AND APPARATUS FOR ESTABLISHING A TUNNELLED DIRECT
MP4483W	MP4483WOKR	KR	1020147021734	1/8/2013			Pending	Methods and Apparatus for Establishing a Tunneled Direct Link Setup
MP4484	MP4484C1	US	14/097,960	12/5/2013	8922924	12/30/2014	In Force	ACQ IN THE PRESENCE OF BASELINE POP
MP4486	MP4486	US	13/737,431	1/9/2013	9356974	5/31/2016	In Force	SYSTEMS AND METHODS FOR ESTABLISHING A WI-FI DISPLAY (WFD)
MP4486W	MP4486WO	WO	PCT/US2013/02	1/9/2013			Pending	SYSTEMS AND METHODS FOR ESTABLISHING A WI-FI DISPLAY (WFD)
MP4486W	MP4486WOCN	CN	201380005021.1	1/9/2013	ZL2013800050	9/21/2018	In Force	SYSTEMS AND METHODS FOR ESTABLISHING A WI-FI DISPLAY (WFD)
MP4486W	MP4486WOJP	JP	2014-552260	1/9/2013	6072829	1/13/2017	In Force	SYSTEMS AND METHODS FOR ESTABLISHING A WI-FI DISPLAY (WFD)
MP4486W	MP4486WOKR	KR	10-2014-702232	1/9/2013			Pending	SYSTEMS AND METHODS FOR ESTABLISHING A WI-FI DISPLAY (WFD)
MP4487W	MP4487WO	WO	PCT/US2012/07	12/19/2012			Pending	METHOD AND APPARATUS FOR CURRENT CONTROL WITH LED DRIVE
MP4487W	MP4487WOEP	EP	EP12821342.8	12/19/2012			Pending	METHOD AND APPARATUS FOR CURRENT CONTROL WITH LED DRIVE
MP4488	MP4488CN	CN	201310071401.1	3/5/2013	ZL2013100714	7/28/2017	In Force	SYNCHRONIZATION OF TWO SETS OF TDMA IN TDS-CDMA
MP4489	MP4489CN	CN	201310071409.1	3/5/2013	ZL2013100714	3/2/2016	In Force	A NEW METHOD TO IMPROVE THE PRECISION OF APC TEMPERATURE
MP4491	MP4491	US	13/734,753	1/4/2013	9256247	2/9/2016	In Force	METHOD AND APPARATUS FOR COMMUNICATING TIME INFORMATION

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MP4491W	MP4491WO	WO	PCT/IB2013/00	1/4/2013			Pending	METHOD AND APPARATUS FOR COMMUNICATING TIME INFORMATION
MP4491W	MP4491WOCN	CN	2013800091474	1/4/2013	ZL2013800091	9/15/2017	In Force	METHOD AND APPARATUS FOR COMMUNICATING TIME INFORMATION
MP4491W	MP4491WOEP	EP	EP13718885	1/4/2013	EP2801162	12/7/2016	In Force	METHOD AND APPARATUS FOR COMMUNICATING TIME INFORMATION
MP4491W	MP4491WOEPDE	DE	13718885.0	1/4/2013	2801162	12/7/2016	In Force	METHOD AND APPARATUS FOR COMMUNICATING TIME INFORMATION
MP4491W	MP4491WOEPFR	FR	EP13718885	1/4/2013	2801162	12/7/2016	In Force	METHOD AND APPARATUS FOR COMMUNICATING TIME INFORMATION
MP4491W	MP4491WOEPGB	GB	13718885.0	1/4/2013	2801162	12/7/2016	In Force	METHOD AND APPARATUS FOR COMMUNICATING TIME INFORMATION
MP4491W	MP4491WOJP	JP	2014-550780	1/4/2013	6214008	9/29/2017	In Force	METHOD AND APPARATUS FOR COMMUNICATING TIME INFORMATION
MP4491W	MP4491WOKR	KR	10-2014-702183	1/4/2013			Pending	METHOD AND APPARATUS FOR COMMUNICATING TIME INFORMATION
MP4492	MP4492C1	US	14/617,070	2/9/2015	9191031	11/17/2015	In Force	METHOD AND APPARATUS FOR READING A DISC
MP4492W	MP4492WO	WO	PCT/US2012/07	12/20/2012			Pending	METHOD AND APPARATUS FOR READING A DISC
MP4494W	MP4494WO	WO	PCT/IB2012/057	12/27/2012			Pending	HIGH-THROUGHPUT ITERATIVE DECODING'S DEFECT SCAN IN RETRY
MP4495W	MP4495WO	WO	PCT/US2012/07	12/19/2012			Pending	METHOD AND APPARATUS FOR WIRELESSLY MANAGING A CLASSRO
MP4495W	MP4495WOCN	CN	2012800659711	12/19/2012			Pending	METHOD AND APPARATUS FOR WIRELESSLY MANAGING A CLASSRO
MP4502	MP4502	US	13/753,150	1/3/2013	9119082	8/25/2015	In Force	REFERENCE SIGNAL DESIGN FOR INTERFERENCE MEASUREMENT
MP4504	MP4504CN	CN	2013100714131	3/5/2013			Pending	MEASUREMENT ARRANGEMENT IN TDSCDMA DLDC SYSTEM
MP4505	MP4505CN	CN	2013100901801	3/13/2013	ZL2013100901	3/20/2018	In Force	METHOD AND APPARATUS FOR BLIND DETECTION OF CELLS
MP4506	MP4506CN	CN	2013100901401	3/13/2013	ZL2013100901	2/9/2018	In Force	USING ARCHITECTURE OF DUAL WORKING TO REALIZE UNITS AND LT
MP4508	MP4508	US	13/723,476	12/21/2012	9184824	11/10/2015	In Force	Intra-Packet Antenna Diversity Systems And Methods
MP4510	MP4510	US	13/736,959	1/9/2013	8902842	12/12/2014	In Force	CONTROL SIGNALING AND RESOURCE MAPPING FOR COORDINATED
MP4511	MP4511	US	13/738,247	1/10/2013	9182808	11/10/2015	In Force	LOW-POWER NETWORK STACKS AND DRIVER SUBSETS
MP4512	MP4512	US	13/851,908	3/27/2013	8984182	3/17/2015	In Force	SCSI I/O COMMAND AGGREGATION
MP4512	MP4512C1	US	14/659,279	3/16/2015	9176672	11/3/2015	In Force	INPUT/OUTPUT (I/O) COMMAND AGGREGATION
MP4517W	MP4517WO	WO	PCT/US2013/02	1/14/2013			Pending	SYSTEM AND METHOD FOR MODIFYING ACTIVATION OF A SENSE AM
MP4517W	MP4517WOCN	CN	2013800057311	1/14/2013	ZL2013800057	7/14/2017	In Force	SYSTEM AND METHOD FOR MODIFYING ACTIVATION OF A SENSE AM
MP4517W	MP4517WOJP	JP	2014-551690	1/14/2013			Pending	SYSTEM AND METHOD FOR MODIFYING ACTIVATION OF A SENSE AM
MP4517W	MP4517WOKR	KR	10-2014-702273	1/14/2013			Pending	System and Method for Modifying Activation of Sense Amplifier
MP4519	MP4519	US	13/744,374	1/17/2013	8949361	9/30/2014	In Force	INTELLIGENT DETECTION INTERFACE FOR WIRELESS DEVICES
MP4519T	MP4519TW	TW	102101966	1/18/2013	1604746	11/1/2017	In Force	INTELLIGENT DETECTION INTERFACE FOR WIRELESS DEVICES
MP4519W	MP4519WO	WO	PCT/IB2013/00	1/17/2013			Pending	INTELLIGENT DETECTION INTERFACE FOR WIRELESS DEVICES
MP4521	MP4521	US	13/747,059	1/22/2013	8953720	2/10/2015	In Force	PACKET TYPE AUTO-DETECTION IN A WIRELESS LOCAL AREA NETWO
MP4524	MP4524CN	CN	2013101654091	1/25/2013	ZL2013101654	2/16/2018	In Force	SYSTEMS AND METHODS FOR COMPOSITE ADAPTIVE FILTERING
MP4526	MP4526	US	13/747,800	1/23/2013	9182440	11/10/2015	In Force	PRESSURE ACTIVATED HIGH DENSITY SWITCH ARRAY
MP4528	MP4528	US	13/752,050	1/28/2013	9264368	2/16/2016	In Force	CHIP-TO-CHIP COMMUNICATIONS
MP4528T	MP4528TW	TW	102103191	1/28/2013			Pending	CHIP-TO-CHIP COMMUNICATIONS

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MP4528W	MP4528WO	WO	PCT/US2013/02	1/28/2013			Pending	CHIP-TO-CHIP COMMUNICATIONS
MP4529	MP4529	US	13/750,053	1/25/2013	9146690	9/29/2015	In Force	Systems And Methods For Dynamic Priority Control
MP4529	MP4529C1	US	14/861,168	9/22/2015	9411753	8/9/2016	In Force	SYSTEMS AND METHODS FOR DYNAMICALLY DETERMINING A PRIORI
MP4529W	MP4529WO	WO	PCT/US2013/02	1/25/2013			Pending	Systems And Methods For Dynamic Priority Control
MP4529W	MP4529WOCN	CN	2013800070463	1/25/2013	ZL2013800070	6/16/2017	In Force	SYSTEMS AND METHODS FOR DYNAMIC PRIORITY CONTROL
MP4529W	MP4529WOEP	EP	EP13721047.2	1/25/2013	2807567	3/7/2018	In Force	SYSTEMS AND METHODS FOR DYNAMIC PRIORITY CONTROL
MP4529W	MP4529WOJP	JP	2014-553826	1/25/2013	6169615	7/7/2017	In Force	SYSTEMS AND METHODS FOR DYNAMIC PRIORITY CONTROL
MP4529W	MP4529WOKR	KR	10-2014-702111	1/25/2013			Pending	Systems And Methods For Dynamic Priority Control
MP4529W	MP4529WUDE	DE	EP13721047.2	1/25/2013	2807567	3/7/2018	In Force	SYSTEMS AND METHODS FOR DYNAMIC PRIORITY CONTROL
MP4529W	MP4529WUFR	FR	EP13721047.2	1/25/2013	2807567	3/7/2018	In Force	SYSTEMS AND METHODS FOR DYNAMIC PRIORITY CONTROL
MP4529W	MP4529WUGB	GB	EP13721047.2	1/25/2013	2807567	3/7/2018	In Force	SYSTEMS AND METHODS FOR DYNAMIC PRIORITY CONTROL
MP4531	MP4531	US	13/751,788	1/28/2013	9356534	5/31/2016	In Force	METHOD AND APPARATUS FOR TURNING ON A LIGHTING DEVICE
MP4534	MP4534	US	13/753,752	1/30/2013	9480104	10/25/2016	In Force	SYSTEMS AND METHODS FOR GENERATING PREAMBLE SYMBOLS IN
MP4534	MP4534C1	US	15/215,778	7/21/2016	10148399	12/4/2018	In Force	SYSTEMS AND METHODS FOR GENERATING PREAMBLE SYMBOLS IN
MP4534	MP4534C1C1	US	16/170,891	10/25/2018			Pending	SYSTEMS AND METHODS FOR GENERATING PREAMBLE SYMBOLS IN
MP4534W	MP4534WO	WO	PCT/US2013/02	1/30/2013			Pending	SYSTEMS AND METHODS FOR GENERATING PREAMBLE SYMBOLS IN
MP4534W	MP4534WOCN	CN	201380006313.1	1/30/2013	ZL2013800063	2/9/2018	In Force	SYSTEMS AND METHODS FOR GENERATING PREAMBLE SYMBOLS IN
MP4534W	MP4534WOEP	EP	EP13703292.6	1/30/2013			Pending	SYSTEMS AND METHODS FOR GENERATING PREAMBLE SYMBOLS IN
MP4534W	MP4534WOJP	JP	2014-554946	1/30/2013	6050802	2/17/2017	In Force	SYSTEMS AND METHODS FOR GENERATING PREAMBLE SYMBOLS IN
MP4534W	MP4534WOKR	KR	10-2014-702362	1/30/2013			Pending	SYSTEMS AND METHODS FOR GENERATING PREAMBLE SYMBOLS IN
MP4537	MP4537	US	13/751,446	1/28/2013	9360767	3/29/2016	In Force	MAC HEADER COMPRESSION IN LONG-RANGE WIRELESS LOCAL ARE
MP4537	MP4537D1	US	15/077,605	3/22/2016	9462505	10/4/2016	In Force	MAC HEADER COMPRESSION IN LONG-RANGE WIRELESS LOCAL ARE
MP4537W	MP4537WO	WO	PCT/US2013/02	1/28/2013			Pending	MAC HEADER COMPRESSION IN LONG-RANGE WIRELESS LOCAL ARE
MP4537W	MP4537WOCN	CN	201380007386.1	1/28/2013	ZL2013800073	7/12/2019	In Force	MAC HEADER COMPRESSION IN LONG-RANGE WIRELESS LOCAL ARE
MP4537W	MP4537WOEP	EP	EP13703263.7	1/28/2013	EP2810478	11/16/2016	In Force	MAC HEADER COMPRESSION IN LONG-RANGE WIRELESS LOCAL ARE
MP4537W	MP4537WOEPFR	FR	13703263.7	1/28/2013	2810478	11/16/2016	In Force	MAC HEADER COMPRESSION IN LONG-RANGE WIRELESS LOCAL ARE
MP4537W	MP4537WOEPGB	GB	13703263.7	1/28/2013	2810478	11/16/2016	In Force	MAC HEADER COMPRESSION IN LONG-RANGE WIRELESS LOCAL ARE
MP4537W	MP4537WOJP	JP	2014-554917	1/28/2013	6113191	3/24/2017	In Force	MAC HEADER COMPRESSION IN LONG-RANGE WIRELESS LOCAL ARE
MP4537W	MP4537WOKR	KR	10-2014-702251	1/28/2013			Pending	MAC HEADER COMPRESSION IN LONG-RANGE WIRELESS LOCAL ARE
MP4538	MP4538	US	13/756,096	1/31/2013	9283909	3/15/2016	In Force	BALL GRID ARRAY PACKAGE SUBSTRATE WITH THROUGH HOLES AND
MP4538	MP4538CN	CN	201310233471.1	2/1/2013			Pending	BALL GRID ARRAY PACKAGE SUBSTRATE WITH THROUGH HOLES AND
MP4538T	MP4538TW	TW	102103922	2/1/2013			Pending	BALL GRID ARRAY PACKAGE SUBSTRATE WITH THROUGH HOLES AND
MP4539	MP4539	US	13/752,607	1/29/2013	9230960	1/5/2016	In Force	COMBINED TAP CELL AND SPARE CELL FOR LOGIC CIRCUIT

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MP4540	MP4540	US	13/752,795	1/29/2013	9196023	11/24/2015	In Force	METHOD AND APPARATUS FOR ADJUSTING A PROPERTY OF AN IMA
MP4542	MP4542	US	13/755,029	1/31/2013	9076001	7/7/2015	In Force	Method and Apparatus for Implementing a Secure Content Pipeline
MP4544	MP4544	US	13/762,085	2/7/2013	9258178	2/9/2016	In Force	PILOT SEQUENCE DESIGN FOR LONG RANGE WLAN
MP4544	MP4544C1	US	15/018,482	2/8/2016	9647873	5/9/2017	In Force	PILOT SEQUENCE DESIGN FOR LONG RANGE WLAN
MP4544W	MP4544WO	WO	PCT/US2013/02	2/7/2013			Pending	PILOT SEQUENCE DESIGN FOR LONG RANGE WLAN
MP4544W	MP4544WOCN	CN	2013800083641	2/7/2013	ZL2013800083	3/30/2018	In Force	PILOT SEQUENCE DESIGN FOR LONG RANGE WLAN
MP4544W	MP4544WCEP	EP	EP13705694.2	2/7/2013			Pending	PILOT SEQUENCES DESIGN FOR LONG RANGE WLAN
MP4544W	MP4544WCIJ	JP	2014-555868	2/7/2013	6314342	4/25/2018	In Force	PILOT SEQUENCE DESIGN FOR LONG RANGE WLAN
MP4544W	MP4544WCKR	KR	10-2014-702251	2/7/2013			Pending	PILOT SEQUENCES DESIGN FOR LONG RANGE WLAN
MP4545	MP4545	US	13/857,580	4/5/2013	9190973	11/17/2015	In Force	HEADPHONE AMPLIFIER
MP4545W	MP4545WO	WO	PCT/US2013/03	4/5/2013			Pending	HEADPHONE AMPLIFIER
MP4545W	MP4545WOCN	CN	2013800294761	4/5/2013	ZL2013800294	1/9/2018	In Force	HEADPHONE AMPLIFIER
MP4545W	MP4545WCEP	EP	EP13717133.6	4/5/2013			Pending	HEADPHONE AMPLIFIER
MP4547	MP4547	US	13/761,540	2/7/2013	9119020	8/25/2015	In Force	METHOD AND APPARATUS FOR DISCOVERING WIRELESS DEVICES
MP4547W	MP4547WO	WO	PCT/US2013/02	2/7/2013			Pending	METHOD AND APPARATUS FOR DISCOVERING WIRELESS DEVICES
MP4547W	MP4547WOCN	CN	2013800147511	2/7/2013	ZL2013800147	6/22/2018	In Force	METHOD AND APPARATUS FOR DISCOVERING WIRELESS DEVICES
MP4547W	MP4547WCEP	EP	EP13706348.3	2/7/2013			Pending	METHOD AND APPARATUS FOR DISCOVERING WIRELESS DEVICES
MP4547W	MP4547WCIJ	JP	2014-556653	2/7/2013			Pending	METHOD AND APPARATUS FOR DISCOVERING WIRELESS DEVICES
MP4547W	MP4547WCKR	KR	10-2014-702526	2/7/2013	10-1995546	6/26/2019	In Force	METHOD AND APPARATUS FOR DISCOVERING WIRELESS DEVICES
MP4553	MP4553	US	13/765,287	2/12/2013	9196140	11/24/2015	In Force	Bluetooth Theft Prevention
MP4554	MP4554	US	13/765,565	2/12/2013	9036993	5/19/2015	In Force	METHOD AND APPARATUS FOR DYNAMICALLY ALLOCATING BANDWI
MP4554W	MP4554WOCN	CN	2013800091461	2/13/2013			Pending	METHOD AND APPARATUS FOR DYNAMICALLY ALLOCATING BANDWI
MP4554W	MP4554WCEP	EP	EP13708273.1	2/13/2013	EP2815528	2/10/2016	In Force	METHOD AND APPARATUS FOR DYNAMICALLY ALLOCATING BANDWI
MP4554W	MP4554WCIJ	JP	2014-556817	2/13/2013	6226477	11/8/2017	In Force	METHOD AND APPARATUS FOR DYNAMICALLY ALLOCATING BANDWI
MP4554W	MP4554WCKR	KR	10-2014-702273	2/13/2013			Pending	DYNAMIC UPSTREAM BANDWIDTH ALLOCATION IN PASSIVE OPTICAL
MP4554W	MP4554WUDE	DE	13708173.1	2/13/2013	2815528	2/10/2016	In Force	METHOD AND APPARATUS FOR DYNAMICALLY ALLOCATING BANDWI
MP4554W	MP4554WUFR	FR	13708173.1	2/13/2013	2815528	2/10/2016	In Force	METHOD AND APPARATUS FOR DYNAMICALLY ALLOCATING BANDWI
MP4554W	MP4554WUGB	GB	13708173.1	2/13/2013	2815528	2/10/2016	In Force	METHOD AND APPARATUS FOR DYNAMICALLY ALLOCATING BANDWI
MP4555	MP4555CN	CN	2013100901061	3/13/2013	ZL2013100901	4/19/2019	In Force	METHOD AND APPARATUS FOR LISTENING FOR THE HS-SCCH IN WIR
MP4559	MP4559SEUS	US	13/905,479	1/12/2011	8838853	9/16/2014	In Force	ACCESS BUFFER
MP4559S	MP4559SECN	CN	2011100089841	1/17/2011	ZL2011100089	9/9/2015	In Force	ACCESS BUFFER
MP4559S	MP4559SETW	TW	100101674	1/17/2011	1498918	9/1/2015	In Force	ACCESS BUFFER
MP4561	MP4561SEja	US	13/585,871	8/15/2012	8977774	3/10/2015	In Force	Method For Reducing Buffer Capacity in A Pipeline Processor
MP4561	MP4561SEUD	US	11/722,470	12/20/2005	8250231	8/21/2012	In Force	Method For Reducing Buffer Capacity in A Pipeline Processor

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MP4561S	MP4561SETW	TW	94132516	9/20/2005	1394078	4/21/2013	In Force	A Method For A Processor, And A Processor
MP4561S	MP4561SEWC	CN	200580044572.1	12/20/2005	ZL2005800445	12/16/2009	In Force	ASYMMETRICAL ENGINE
MP4562	MP4562	US	13/170,427	6/28/2011	8442056	5/14/2013	In Force	Scheduling Packets in A Packet-Processing Pipeline
MP4562	MP4562C1	US	13/890,593	5/9/2013	9276857	3/1/2016	In Force	Method And Apparatus For Scheduling Packets For Transmission In A
MP4563	MP4563SUWO	US	12/306,029	6/12/2007	8171478	5/1/2012	In Force	METHOD FOR PACKET FLOW CONTROL USING CREDIT PARAMETERS
MP4563S	MP4563SETW	TW	96122167	6/20/2007	1435574	4/21/2014	In Force	A PROCESSOR AND A METHOD FOR A PROCESSOR
MP4563S	MP4563SUON	DE	07730098.6	6/12/2007	602607002711	10/7/2009	In Force	COUPLED TOKEN BUCKETS
MP4563S	MP4563SUQT	GB	07730098.6	6/12/2007	2036267	10/7/2009	In Force	COUPLED TOKEN BUCKETS
MP4563S	MP4563SUWC	CN	200780023760.1	6/12/2007	ZL2007800232	7/6/2011	In Force	COUPLED TOKEN BUCKETS
MP4563S	MP4563SUWU	EP	07730098.6	6/12/2007	2036267	10/7/2009	In Force	COUPLED TOKEN BUCKETS
MP4564S	MP4564SE	SE	0200383-8	2/8/2002	0200383-8	12/2/2003	In Force	DIRECTED GRAPH COMPILING
MP4565	MP4565SEUS	US	13/005,473	1/12/2011	8615629	12/24/2013	In Force	ACCESS SCHEDULER
MP4565	MP4565SU5a	US	14/091,940	11/27/2013	8990498	3/24/2015	In Force	ACCESS SCHEDULER
MP4565S	MP4565SE	SE	1056044-5	1/18/2010			Pending	ACCESS SCHEDULER
MP4565S	MP4565SECN	CN	20111008985.1	1/17/2011			Pending	ACCESS SCHEDULER
MP4565S	MP4565SETW	TW	100101673	1/17/2011	1512464	12/11/2015	In Force	ACCESS SCHEDULER
MP4567	MP4567SUWa	US	14/474,674	9/2/2014	9240953	1/19/2016	In Force	Systems And Methods For Managing Traffic In A Network Using Dyna
MP4567	MP4567SUWO	US	12/989,631	4/21/2009	8824287	9/2/2014	In Force	Method And Apparatus For Managing Traffic In A Network
MP4567S	MP4567SETW	TW	98113512	4/23/2009	1477109	3/11/2015	In Force	A Traffic Manager And A Method For A Traffic Manager
MP4567S	MP4567SUWC	CN	2009801249485	4/21/2009	ZL2009801249	12/3/2014	In Force	A TRAFFIC MANAGER AND A METHOD FOR A TRAFFIC MANAGER
MP4568S	MP4568SUWC	CN	200480002783.1	1/77/2004	ZL2004800027	5/26/2010	In Force	METHOD IN PIPELINED DATA PROCESSING
MP4569	MP4569SUWOa	US	14/149,029	1/7/2014	8964594	2/24/2015	In Force	NETWORK PROCESSOR UNIT AND A METHOD FOR A NETWORK PROC
MP4569	MP4569SUWOZ	US	14/626,117	2/19/2015	9178330	11/3/2015	In Force	NETWORK PROCESSOR UNIT AND A METHOD FOR A NETWORK PROC
MP4569S	MP4569SETW	TW	98117841	5/27/2009	1482460	4/21/2015	In Force	A NETWORK PROCESSOR UNIT AND A METHOD FOR A NETWORK PR
MP4569S	MP4569SEUW	WO	PCT/EP2009/051	5/29/2009			Pending	NETWORK PROCESSOR UNIT AND A METHOD FOR A NETWORK PROC
MP4569S	MP4569SEWU	EP	EP09753951.4	5/29/2009	2311230	8/28/2019	In Force	A Network Processor Unit and a Method for a Network Processor Uni
MP4569S	MP4569SEWUDE	DE	EP09753951.4	5/29/2009	602609059621	8/28/2019	In Force	A Network Processor Unit and a Method for a Network Processor Uni
MP4569S	MP4569SEWUFR	FR	EP09753951.4	5/29/2009	2311230	8/28/2019	In Force	A Network Processor Unit and a Method for a Network Processor Uni
MP4569S	MP4569SEWUGB	GB	EP09753951.4	5/29/2009	2311230	8/28/2019	In Force	A Network Processor Unit and a Method for a Network Processor Uni
MP4569S	MP4569SEVA	TW	104101380	5/27/2009	1527409	3/21/2016	In Force	A NETWORK PROCESSOR UNIT AND A METHOD FOR A NETWORK PR
MP4569S	MP4569SUWCA	CN	201410090177.1	5/29/2009	ZL2014100901	8/25/2017	In Force	A NETWORK PROCESSOR UNIT AND A METHOD FOR A NETWORK PR
MP4569S	MP4569SUWCN	CN	200980119421.1	5/29/2009	ZL2009801194	3/26/2014	In Force	A NETWORK PROCESSOR UNIT AND A METHOD FOR A NETWORK PR
MP4571	MP4571SUWO	US	13/521,799	12/22/2010	9769092	9/19/2017	In Force	Packet Buffer Comprising A Data Section And A Data Description Secti
MP4571S	MP4571SETW	TW	100101672	1/17/2011	1526927	3/21/2016	In Force	Data Buffer Memory, Data Buffer Memory System, And Data Storage

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MP4571S	MP4571SEUW	WO	PCT/EP2010/071	12/22/2010			Pending	A Packet Buffer Comprising A Data Section And A Data Description Sec
MP4571S	MP4571SUWC	CN	2010800655713	12/22/2010	ZL2010800655	11/9/2016	In Force	A PACKET BUFFER COMPRISING A DATA SECTION AND A DATA DESCR
MP4571S	MP4571SUWU	EP	EP10801162.8	12/22/2010	EP2526478	3/30/2016	In Force	A Packet Buffer Comprising a Data Section and a Data Description Sec
MP4571S	MP4571SUWUDE	DE	10801162.8	12/22/2010	602010031855	3/30/2016	In Force	A Packet Buffer Comprising A Data Section An A Data Description Sec
MP4571S	MP4571SUWUFR	FR	10801162.8	12/22/2010	2526478	3/30/2016	In Force	A Packet Buffer Comprising A Data Section An A Data Description Sec
MP4571S	MP4571SUWUGB	GB	10801162.8	12/22/2010	2526478	3/30/2016	In Force	A Packet Buffer Comprising A Data Section An A Data Description Sec
MP4572S	MP4572SE	SE	0100221-1	1/25/2001	0100221-1	11/25/2003	In Force	APPARATUS AND METHOD FOR PROCESSING PIPELINED DATA
MP4572S	MP4572SWCN	CN	182285.4	5/21/2001	ZL2001182228	5/27/2009	In Force	APPARATUS AND METHOD FOR PROCESSING PIPELINED DATA
MP4572S	MP4572SWEP	EP	01941347.5	5/21/2001	1360602	3/31/2010	In Force	APPARATUS AND METHOD FOR PROCESSING PIPELINED DATA
MP4572S	MP4572SWN	DE	01941347.5	5/21/2001	1360602	3/31/2010	In Force	APPARATUS AND METHOD FOR PROCESSING PIPELINED DATA
MP4572S	MP4572SWUN	ES	01941347.5	5/21/2001	1360602	3/31/2010	In Force	APPARATUS AND METHOD FOR PROCESSING PIPELINED DATA
MP4572S	MP4572SWUR	FR	01941347.5	5/21/2001	1360602	3/31/2010	In Force	APPARATUS AND METHOD FOR PROCESSING PIPELINED DATA
MP4572S	MP4572SMUT	GB	01941347.5	5/21/2001	1360602	3/31/2010	In Force	APPARATUS AND METHOD FOR PROCESSING PIPELINED DATA
MP4573	MP4573C1C1	US	15/169,011	5/31/2016	10462267	10/29/2019	In force	METHOD AND APPARATUS FOR PRE-CLASSIFYING PACKETS
MP4573T	MP4573TW	TW	101109352	3/19/2012	1569609	2/1/2017	In Force	METHOD AND APPARATUS FOR PRE-CLASSIFYING PACKETS
MP4573W	MP4573WO	WO	PCT/IB2012/006	3/16/2012			Pending	METHOD AND APPARATUS FOR PRE-CLASSIFYING PACKETS
MP4573W	MP4573WOCN	CN	201280014287.1	3/16/2012	ZL2012800142	6/29/2016	In Force	METHOD AND APPARATUS FOR PRE-CLASSIFYING PACKETS
MP4573W	MP4573WOEP	EP	EP12714840.1	3/16/2012			Pending	METHOD AND APPARATUS FOR PRE-CLASSIFYING PACKETS
MP4573W	MP4573WOEPDE	DE	EP12714840.1	3/16/2012			Pending	METHOD AND APPARATUS FOR PRE-CLASSIFYING PACKETS
MP4573W	MP4573WOEPFR	FR	EP12714840.1	3/16/2012			Pending	METHOD AND APPARATUS FOR PRE-CLASSIFYING PACKETS
MP4573W	MP4573WOEPGB	GB	EP12714840.1	3/16/2012			Pending	METHOD AND APPARATUS FOR PRE-CLASSIFYING PACKETS
MP4574	MP4574	US	13/475,445	5/18/2012	9042220	5/26/2015	In Force	NETWORK TRAFFIC SCHEDULER AND ASSOCIATED METHOD, COMPU
MP4574E	MP4574EP	EP	EP11166496.7	5/18/2011	2525534	4/18/2018	In Force	NETWORK TRAFFIC SCHEDULER AND ASSOCIATED METHOD, COMPU
MP4574E	MP4574EPDE	DE	11166496.7	5/18/2011	602011048528	4/18/2018	In Force	NETWORK TRAFFIC SCHEDULER AND ASSOCIATED METHOD, COMPU
MP4574E	MP4574EPFR	FR	11166496.7	5/18/2011	2525534	4/18/2018	In Force	NETWORK TRAFFIC SCHEDULER AND ASSOCIATED METHOD, COMPU
MP4574E	MP4574EPGB	GB	11166496.7	5/18/2011	2525534	4/18/2018	In Force	NETWORK TRAFFIC SCHEDULER AND ASSOCIATED METHOD, COMPU
MP4574T	MP4574TW	TW	101117871	5/18/2012	1593253	7/21/2017	In Force	NETWORK TRAFFIC SCHEDULER AND ASSOCIATED METHOD, COMPU
MP4574W	MP4574WO	WO	PCT/IB2012/001	5/18/2012			Pending	NETWORK TRAFFIC SCHEDULER AND ASSOCIATED METHOD, COMPU
MP4574W	MP4574WOCN	CN	201280024007.1	5/18/2012	ZL2012800240	2/8/2017	In Force	NETWORK TRAFFIC SCHEDULER AND ASSOCIATED METHOD, COMPU
MP457S	MP457S	US	13/529,698	6/21/2012	9749255	8/29/2017	In Force	METHOD, NETWORK DEVICE, COMPUTER PROGRAM AND COMPUTE
MP457S	MP457SCN	CN	201210229657.1	6/29/2012	ZL2012102296	11/21/2017	In Force	METHOD, NETWORK DEVICE AND COMPUTER PROGRAM PRODUCT F
MP457S	MP457SGB	GB	1111106.9	6/30/2011			Pending	METHOD NETWORK DEVICE, COMPUTER PROGRAM AND COMPUTE
MP4576	MP4576S0A	US	14/275,104	5/12/2014	9635145	4/25/2017	In Force	System And Method For Modifying, In A Processing Pipeline, A Lengt
MP4576S	MP4576SE	SE	0201020-5	4/4/2002	0201020-5	12/21/2004	In Force	TUNNEL DETUNNEL MECHANISM

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP4579	MP4579	US	13/772,908	2/21/2013	9219693	12/22/2015	In Force	Network Devices With Time Aware Medium Access Controller
MP4579	MP4579C1	US	14/970,587	12/16/2015	9699091	7/4/2017	In Force	Apparatus And Method For Time Aware Transfer Of Frames In A Med
MP4579W	MP4579WO	WO	PCT/US2013/02	2/22/2013			Pending	Network Devices With Time Aware Medium Access Controller
MP4579W	MP4579WOCN	CN	2013800105064	2/22/2013	ZL2013800105	9/29/2017	In Force	NETWORK DEVICES WITH TIME AWARE MEDIUM ACCESS CONTROLL
MP4579W	MP4579WCEP	EP	EP13708584.1	2/22/2013	2817921	4/25/2018	In Force	NETWORK DEVICES WITH TIME AWARE MEDIUM ACCESS CONTROLL
MP4579W	MP4579WCEPDE	DE	EP13708584.1	2/22/2013	2817921	4/25/2018	In Force	NETWORK DEVICES WITH TIME AWARE MEDIUM ACCESS CONTROLL
MP4579W	MP4579WCEPFR	FR	EP13708584.1	2/22/2013	2817921	4/25/2018	In Force	NETWORK DEVICES WITH TIME AWARE MEDIUM ACCESS CONTROLL
MP4579W	MP4579WCEPGB	GB	EP13708584.1	2/22/2013	2817921	4/25/2018	In Force	NETWORK DEVICES WITH TIME AWARE MEDIUM ACCESS CONTROLL
MP4583	MP4583	US	13/774,654	2/22/2013	9137814	9/15/2015	In Force	CROSS-LAYER SCHEDULING BASED ON LOWER LAYER FEEDBACK
MP4583W	MP4583WO	WO	PCT/US2013/02	2/22/2013			Pending	CROSS-LAYER SCHEDULING BASED ON LOWER LAYER FEEDBACK
MP4583W	MP4583WOCN	CN	2013800209964	2/22/2013	ZL2013800209	7/13/2018	In Force	CROSS-LAYER SCHEDULING BASED ON LOWER LAYER FEEDBACK
MP4583W	MP4583WCEP	EP	EP13708984.3	2/22/2013			Pending	CROSS-LAYER SCHEDULING BASED ON LOWER LAYER FEEDBACK
MP4583W	MP4583WOJP	JP	2014-558855	2/22/2013			Pending	CROSS-LAYER SCHEDULING BASED ON LOWER LAYER FEEDBACK
MP4583W	MP4583WOKR	KR	10-2014-702650	2/22/2013			Pending	CROSS-LAYER SCHEDULING BASED ON LOWER LAYER FEEDBACK
MP4586	MP4586	US	13/778,169	2/27/2013	8930965	1/6/2015	In Force	EVENT-DRIVEN STATE-MACHINE SEQUENCER
MP4588	MP4588CN	CN	2013100871031	3/12/2013			Pending	SCREEN UNLOCK ALGORITHM BASED ON RANDOM UNLOCK KEY CON
MP4595	MP4595	US	13/773,907	2/22/2013	8938035	1/20/2015	In Force	System And Method For Transferring Data Over A Two-Pair Communi
MP4598	MP4598CN	CN	2013101601111	3/19/2013			Pending	AUTOMATIC GAIN CONTROL METHOD FOR RECEIVING DISCONTINUO
MP4599	MP4599	US	13/744,466	1/18/2013	9213632	12/15/2015	In Force	Systems And Methods For Data Storage Devices To Use External Reso
MP4599	MP4599C1	US	14/967,698	12/14/2015	10013307	7/3/2018	In Force	Systems and Methods for Data Storage Devices to Use External Reso
MP4600	MP4600	US	13/771,702	2/20/2013	9015351	4/21/2015	In Force	Address Translation For SR-IOV Virtual Function Apertures
MP4601	MP4601CN	CN	2013100901571	3/13/2013	ZL2013100901	8/28/2018	In Force	METHOD AND DEVICE OF TERMINAL WAKE-UP
MP4603	MP4603	US	13/782,793	3/1/2013	9223709	12/29/2015	In Force	THREAD-AWARE CACHE MEMORY MANAGEMENT
MP4606	MP4606CN	CN	2013101108871	3/21/2013	ZL2013101108	1/1/2019	In Force	METHOD FOR TRIGGERING MEASUREMENT REPORT ON SECOND STR
MP4607	MP4607CN	CN	2013100901591	3/13/2013	ZL2013100901	10/31/2017	In Force	ADAPTIVELY INTERPOLATING AND SEARCHING TO REFINE SAMPLING
MP4608	MP4608	US	13/804,099	3/14/2013	9081668	7/14/2015	In Force	ARCHITECTURE TO ALLOW EFFICIENT STORAGE OF DATA ON NAND F
MP4608W	MP4608WO	WO	PCT/US2013/03	3/14/2013			Pending	ARCHITECTURE TO ALLOW EFFICIENT STORAGE OF DATA ON NAND F
MP4608W	MP4608WOCN	CN	2013800192401	3/14/2013	ZL2013800192	8/29/2017	In Force	ARCHITECTURE TO ALLOW EFFICIENT STORAGE OF DATA ON NAND F
MP4608W	MP4608WOJP	JP	2015-500584	3/14/2013	6201242	9/8/2017	In Force	ARCHITECTURE TO ALLOW EFFICIENT STORAGE OF DATA ON NAND F
MP4609	MP4609	US	13/786,972	3/6/2013	9125200	9/1/2015	In Force	CARRIER AGGREGATION SCHEDULING BASED ON TRAFFIC CHARACTE
MP4609W	MP4609WO	WO	PCT/US2013/02	3/6/2013			Pending	CARRIER AGGREGATION SCHEDULING BASED ON TRAFFIC CHARACTE
MP4609W	MP4609WOCN	CN	2013800249391	3/6/2013	ZL2013800249	6/5/2018	In Force	METHOD AND APPARATUS FOR SCHEDULING TRAFFIC STREAMS IN CO
MP4610	MP4610CN	CN	2013101300401	4/9/2013	ZL2013101300	9/22/2017	In Force	A METHOD AND APPARATUS FOR TRANSMITTING PHYSICAL LAYER L
MP4611	MP4611	US	13/851,543	3/27/2013	9609676	3/28/2017	In Force	EFFICIENT TRANSITION FROM DISCOVERY TO LINK ESTABLISHMENT

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MP4612	MP4612	US	13/802,490	3/13/2013	9419653	8/16/2016	In Force	SYSTEMS AND METHODS FOR COMBINING CONSTRAINED CODES AND
MP4613	MP4613CN	CN	201310073223.1	3/6/2013			Pending	METHOD AND EQUIPMENT FOR MERGING CELL MEASUREMENT
MP4614	MP4614	US	13/804,425	3/14/2013	9398455	7/19/2016	In Force	SYSTEM AND METHOD FOR GENERATING AN IDENTIFICATION BASED
MP4614W	MP4614WO	WO	PCT/US2013/03	3/14/2013			Pending	KEY CENTRIC IDENTITY
MP4615	MP4615	US	13/800,247	3/13/2013	9232499	1/5/2016	In Force	Common Channels For Wireless Network Access
MP4616W	MP4616WO	WO	PCT/US2013/03	3/14/2013			Pending	METHOD AND APPARATUS FOR STARTING UP
MP4617	MP4617	US	13/836,145	3/15/2013	9666800	3/28/2017	In Force	Method And Apparatus For Sharing Instruction Scheduling Resource
MP4618	MP4618CN	CN	201310135937.1	4/12/2013	ZL2013101359	1/2/2018	In Force	OPTIMIZATION OF E-HICH DECODING
MP4619	MP4619CN	CN	201310139799.1	4/10/2013	ZL2013101397	4/24/2018	In Force	ROAD LOSS COMPENSATION METHOD FOR SUDDEN CHANGES IN SIGNAL
MP4620	MP4620CN	CN	201310135974.1	4/12/2013			Pending	METHOD FOR SENDING HSUPA AT MINIMUM POWER IN TDSCDMA
MP4621	MP4621CN	CN	201310137953.1	4/15/2013	ZL2013101379	8/28/2018	In Force	A METHOD AND APPARATUS FOR TIME SLOT ASSIGNMENT IN HS-DS
MP4622	MP4622CN	CN	201310132583.1	4/8/2013	ZL2013101325	4/13/2018	In Force	CO-FREQUENCY INTERFERENCE ELIMINATION FOR ALL SERVICE TIME
MP4624	MP4624CN	CN	201310129849.1	4/9/2013	ZL2013101298	2/9/2018	In Force	METHOD AND DEVICE FOR ESCAPING FROM CURRENT SERVING CELL
MP4625	MP4625CN	CN	201310132643.1	4/8/2013			Pending	COMBINED AUTOMATIC GAIN CONTROL ADJUSTMENT FOR MULTIPLE
MP4627	MP4627CN	CN	201310136764.1	4/11/2013			Pending	WINDOW SPLITTING METHOD FOR ACTIVATION DETECTION IN TD-SC
MP4629	MP4629CN	CN	201310139798.1	4/10/2013			Pending	METHOD FOR IMPLEMENTING SOFTWARE DECODING OF HS-SCCHS
MP4630	MP4630CN	CN	201310136765.1	4/11/2013	ZL2013101367	10/12/2018	In Force	DMA MODIFIED MECHANISM FOR THE MULTI-CARRIER SYSTEM
MP4631	MP4631CN	CN	201310132595.1	4/8/2013	ZL2013101325	1/16/2018	In Force	MODULE INTEGRATION OF TD-SCDMA TERMINAL CODING PROCESS
MP4635	MP4635CN	CN	201310151684.1	4/18/2013	ZL2013101516	6/28/2019	In Force	A METHOD AND APPARATUS FOR DETERMINING EXECUTION MODE
MP4639	MP4639CN	CN	201310148674.1	4/19/2013	ZL2013101486	7/31/2018	In Force	A METHOD AND APPARATUS FOR DECODING PDCCH
MP4640	MP4640CN	CN	201310148672.1	4/19/2013	ZL2013101486	3/8/2019	In Force	A LOW COMPLEXITY AND FLEXIBLE PSS CORRELATOR TO HANDLE LA
MP4642	MP4642CN	CN	201310148673.1	4/19/2013			Pending	METHOD AND APPARATUS FOR DE-RATE MATCHING
MP4643	MP4643CN	CN	201310151682.1	4/18/2013	ZL2013101516	7/13/2018	In Force	A METHOD AND APPARATUS OF VERIFYING VALIDITY OF CODEWORD
MP4646	MP4646CN	CN	201310163540.1	4/23/2013	ZL2013101635	9/19/2017	In Force	ADAPTER AND ADAPTING METHOD FOR A USER EQUIPMENT
MP4649	MP4649CN	CN	201310143210.1	4/17/2013	ZL2013101432	1/2/2018	In Force	METHOD AND APPARATUS FOR PERFORMANCE ABSTRACTION IN M
MP4652	MP4652CN	CN	201310163579.1	4/23/2013	ZL2013101635	9/14/2018	In Force	METHOD AND APPARATUS FOR MMSE CHANNEL ESTIMATION
MP4653	MP4653CN	CN	201310164521.1	4/22/2013	ZL2013310645	1/19/2018	In Force	METHOD AND APPARATUS FOR DETERMINING INFORMATION ON CH
MP4654	MP4654CN	CN	201310163441.1	4/24/2013	ZL2013101634	4/10/2018	In Force	METHOD AND APPARATUS FOR TIMING AND FREQUENCY SYNCHRO
MP4655	MP4655CN	CN	201310159647.1	4/22/2013	ZL2013101596	8/17/2018	In Force	CHANNEL ESTIMATION METHOD AND APPARATUS BASED ON WIENE
MP4656	MP4656CN	CN	201310159648.1	4/22/2013	ZL2013101596	10/9/2018	In Force	METHOD AND APPARATUS FOR RATE MATCHING
MP4659	MP4659CN	CN	201310163578.1	4/23/2013	ZL2013101635	5/1/2018	In Force	A METHOD AND APPARATUS FOR GENERATING PREAMBLE OF ACCES
MP4660	MP4660CN	CN	201310137954.1	4/15/2013	ZL2013101379	1/1/2019	In Force	METHOD AND MODULE FOR CYCLIC REDUNDANCY CHECK FOR A WIR
MP4664	MP4664CN	CN	201310143208.1	4/17/2013			Pending	METHOD AND APPARATUS FOR SIGNAL DETECTION IN MIMO SYSTE
MP4673	MP4673CN	CN	201310163576.1	4/23/2013	ZL2013101635	2/27/2018	In Force	STORAGE APPARATUS AND METHOD FOR ADAPTIVE SIC DEMODULA

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MP4677	MP4677	US	13/847,156	3/19/2013	9311247	4/12/2016	In Force	Method And Apparatus For Detecting Patterns Of Memory Accesses I
MP4679	MP4679	US	13/864,550	4/17/2013	9218234	12/22/2015	In Force	Memory Dump And Analysis In A Computer System
MP4679W	MP4679WO	WO	PCT/US2013/03	4/17/2013			Pending	Memory Dump And Analysis In A Computer System
MP4679W	MP4679WOCN	CN	201380021626.4	4/17/2013	ZL2013800216	5/31/2017	In Force	MEMORY DUMP AND ANALYSIS IN A COMPUTER SYSTEM
MP4682	MP4682CN	CN	201310163444.1	4/24/2013	ZL2013101634	4/20/2018	In Force	A METHOD AND APPARATUS FOR FREQUENCY SHIFT
MP4683	MP4683CN	CN	201310163539.4	4/23/2013	ZL2013101635	4/24/2018	In Force	DISCONTINUOUS RECEPTION CONTROL DEVICE, CONTROLLER AND U
MP4686	MP4686	US	10/898,540	7/26/2004	7742486	6/22/2010	In Force	NETWORK INTERCONNECT CROSSPOINT SWITCHING ARCHITECTURE
MP4687	MP4687C1	US	12/348,426	1/5/2009	7767475	4/27/2010	In Force	64B/66B Coding Apparatus And Method
MP4688	MP4688	US	11/407,038	4/20/2006	7758473	6/15/2010	In Force	MULTICAST SWITCHING IN A CREDIT BASED UNICAST AND MULTICA
MP4689	MP4689	US	11/029,624	1/6/2005	7408947	8/5/2008	In Force	METHOD AND APPARATUS FOR SCHEDULING PACKETS AND/OR CELL
MP4689	MP4689C1	US	12/178,839	7/24/2008	7738472	6/15/2010	In Force	Method And Apparatus For Scheduling Packets And/Or Cells
MP4693	MP4693CN	CN	201310114512.1	3/25/2013			Pending	METHOD AND APPARATUS FOR IMAGE DATA COMPRESSION
MP4694	MP4694	US	13/848,817	3/22/2013	9331654	5/3/2016	In Force	Dual Squelch Detectors And Methods For Low Power States
MP4694T	MP4694TW	TW	102110671	3/26/2013	1590596	7/1/2017	In Force	Dual Squelch Detectors And Methods For Low Power States
MP4694W	MP4694WO	WO	PCT/US2013/03	3/25/2013			Pending	Dual Squelch Detectors And Methods For Low Power States
MP4694W	MP4694WOCN	JP	2015-503432	3/25/2013	6138616	4/21/2017	In Force	DUAL SQUELCH DETECTORS AND METHODS FOR LOW POWER STATE
MP4695	MP4695	US	13/850,975	3/26/2013	9471344	10/18/2016	In Force	HARDWARE SUPPORT FOR PROCESSING VIRTUAL MACHINE INSTRU
MP4703	MP4703	US	13/854,224	4/1/2013	9113469	8/18/2015	In Force	CELL DEPLOYMENT WITH DIFFERENT CHANNEL BANDWIDTH FOR CA
MP4703	MP4703C1	US	14/826,484	8/14/2015	9756640	9/5/2017	In Force	CELL DEPLOYMENT WITH DIFFERENT CHANNEL BANDWIDTH FOR CA
MP4703W	MP4703WO	WO	PCT/US2013/03	4/1/2013			Pending	CELL DEPLOYMENT WITH DIFFERENT CHANNEL BANDWIDTH FOR CA
MP4703W	MP4703WOCN	CN	201380029267.1	4/1/2013	ZL2013800292	9/14/2018	In Force	SYSTEM AND METHOD FOR TRANSMITTING INFORMATION TO USER
MP4703W	MP4703WCEP	EP	13718244.0	4/1/2013			Pending	CELL DEPLOYMENT WITH DIFFERENT CHANNEL BANDWIDTH FOR CA
MP4704W	MP4704WO	WO	PCT/US2013/03	4/2/2013			Pending	VERY COMPACT/LINEAR SOFTWARE DEFINED TRANSMITTER WITH DI
MP4707	MP4707	US	13/856,011	4/3/2013	9436403	9/6/2016	In Force	Memory Controller with On-Chip Linked List Memory
MP4708	MP4708	US	13/868,939	4/23/2013	9152459	10/6/2015	In Force	ADJUSTING THREAD PRIORITY TO IMPROVE THROUGHPUT BETWEEN
MP4708W	MP4708WO	WO	PCT/US2013/03	4/23/2013			Pending	ADJUSTING THREAD PRIORITY TO IMPROVE THROUGHPUT BETWEEN
MP4708W	MP4708WOCN	CN	201380033084.1	4/23/2013	ZL2013800330	1/26/2018	In Force	ADJUSTING THREAD PRIORITY TO IMPROVE THROUGHPUT BETWEEN
MP4708W	MP4708WCEP	EP	EP13781931.4	4/23/2013			Pending	ADJUSTING THREAD PRIORITY TO IMPROVE THROUGHPUT BETWEEN
MP4708W	MP4708WOJP	JP	2015-509086	4/23/2013	6074779	1/20/2017	In Force	ADJUSTING THREAD PRIORITY TO IMPROVE THROUGHPUT BETWEEN
MP4708W	MP4708WOKR	KR	10-2014-705295	4/23/2013	10-1978457	5/8/2019	In Force	ADJUSTING THREAD PRIORITY TO IMPROVE THROUGHPUT BETWEEN
MP4709	MP4709	US	13/856,277	4/3/2013	9131528	9/8/2015	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP4709	MP4709D1	US	14/846,280	9/4/2015	10135572	11/20/2018	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP4709	MP4709D1C1	US	16/195,528	11/19/2018			Pending	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP4709W	MP4709WO	WO	PCT/US2013/03	4/3/2013			Pending	PHYSICAL LAYER FRAME FORMAT FOR WLAN

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MP4709W	MP4709WOCN	CN	2013800187041	4/3/2013	ZL2013800187	10/2/2018	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP4709W	MP4709WCEP	EP	EP13718247.3	4/3/2013			Pending	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP4709W	MP4709WJJP	JP	2015-504708	4/3/2013	6168503	7/7/2017	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP4709W	MP4709WOKR	KR	2014-7030662	4/3/2013			Pending	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP4711	MP4711	US	13/857,112	4/4/2013	9678920	6/13/2017	In Force	SELF-CALIBRATION AND COMPENSATION OF DETECTOR OFFSET IN G
MP4714	MP4714	US	13/849,120	3/22/2013	9405542	8/2/2016	In Force	Method And Apparatus For Updating A Speculative Rename Table In
MP4715	MP4715	US	13/852,576	3/28/2013	8976886	3/10/2015	In Force	Method And Apparatus For Jointly Performing Channel Estimation A
MP4715W	MP4715WOCN	WO	PCT/IB2013/001	3/28/2013			Pending	Method And Apparatus For Jointly Performing Channel Estimation A
MP4715W	MP4715WOCN	CN	2013800249384	3/28/2013	ZL2013800249	10/17/2017	In Force	METHOD AND APPARATUS FOR JOINTLY PERFORMING CHANNEL EST
MP4716	MP4716CN	CN	2013101432072	4/17/2013	ZL2013101432	1/29/2019	In Force	METHOD AND APPARATUS FOR CONFIGURING RADIO FREQUENCY
MP4717	MP4717CN	CN	2013101632014	4/25/2013			Pending	SINGLE-TRANSCIVER DSDS TERMINAL AND RECEIVING METHOD THE
MP4719	MP4719	US	13/857,662	4/5/2013	9436197	9/6/2016	In Force	ADAPTIVE OPAMP COMPENSATION
MP4720	MP4720	US	13/852,244	3/28/2013	9148825	9/29/2015	In Force	System And Method Of Performing Idle Mode Signaling Reduction In
MP4720W	MP4720WOCN	WO	PCT/US2013/03	3/29/2013			Pending	System And Method Of Performing Idle Mode Signaling Reduction In
MP4720W	MP4720WOCN	CN	2013800249311	3/29/2013	ZL2013800249	4/27/2018	In Force	METHOD OF PERFORMING IDLE MODE SIGNALING REDUCTION IN M
MP4721	MP4721CN	CN	2013101634421	4/24/2013			Pending	METHOD AND APPARATUS FOR RECEIVING PAGING
MP4727	MP4727CN	CN	2013101669214	4/27/2013	ZL2013101669	1/8/2019	In Force	A METHOD AND APPARATUS FOR ESTIMATING SNR WITH DOUBLE A
MP4728	MP4728CN	CN	2013101596504	4/22/2013	ZL2013101596	12/19/2017	In Force	METHOD AND APPARATUS FOR TRANSMISSION POWER CONTROL
MP4729	MP4729CN	CN	2013101516071	4/18/2013	ZL2013101516	12/25/2018	In Force	INTEGRATED CIRCUIT AND USER EQUIPMENT FOR IMPLEMENTING D
MP4730	MP4730CN	CN	2013101698051	5/7/2013	ZL2013101698	5/10/2017	In Force	METHOD AND SYSTEM FOR TRANSFERRING DATA FROM A RADIO FR
MP4731	MP4731	US	13/859,219	4/9/2013	9183078	11/10/2015	In Force	PROVIDING ERROR CHECKING AND CORRECTING (ECC) CAPABILITY F
MP4732	MP4732	US	13/858,879	4/8/2013	9322717	4/26/2016	In Force	TEMPERATURE SENSOR ARCHITECTURE
MP4733	MP4733	US	13/861,353	4/11/2013	8917115	12/23/2014	In Force	ZERO CROSSING DETECTOR (ZCD) CIRCUIT
MP4733T	MP4733TW	TW	102112831	4/11/2013	1583964	5/21/2017	In Force	ZERO CROSSING DETECTOR (ZCD) CIRCUIT
MP4733W	MP4733WO	WO	PCT/US2013/03	4/11/2013			Pending	ZERO CROSSING DETECTOR (ZCD) CIRCUIT
MP4733W	MP4733WOCN	CN	2013800260824	4/11/2013	ZL2013800260	10/20/2017	In Force	ZERO CROSSING DETECTOR (ZCD) CIRCUIT
MP4734	MP4734	US	13/870,082	4/25/2013	9336792	5/10/2016	In Force	Systems And Methods For Voice Enhancement In Audio Conference
MP4735T	MP4735TW	TW	102113091	4/12/2013	1652905	3/1/2019	In Force	METHOD AND APPARATUS FOR LOCAL OSCILLATOR
MP4735W	MP4735WO	WO	PCT/US2013/03	4/11/2013			Pending	METHOD AND APPARATUS FOR LOCAL OSCILLATOR
MP4735W	MP4735WOCN	CN	2013800271861	4/11/2013			Pending	METHOD AND APPARATUS FOR LOCAL OSCILLATOR
MP4737	MP4737	US	13/856,653	4/4/2013	9442735	9/13/2016	In Force	Method And Apparatus For Processing Speculative, Out-Of-Order Me
MP4738	MP4738	US	13/864,870	4/17/2013	9703338	7/11/2017	In Force	POWER OVER ETHERNET BY 802.1X USER AUTHENTICATE PROTOCOL
MP4745	MP4745CN	CN	2013101950344	5/22/2013	ZL2013101950	7/31/2018	In Force	METHOD, APPARATUS AND USER EQUIPMENT FOR DETERMINING M
MP4746	MP4746CN	CN	2013101824111	5/13/2013			Pending	METHOD AND APPARATUS FOR ELIMINATING CO-CHANNEL INTERFE

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MP4748	MP4748	US	13/871,633	4/26/2013	8922416	12/30/2014	In Force	METHOD AND APPARATUS FOR ANALOG-TO-DIGITAL CONVERTER
MP4748W	MP4748WO	WO	PCT/US2013/03	4/26/2013			Pending	METHOD AND APPARATUS FOR ANALOG-TO-DIGITAL CONVERTER
MP4748W	MP4748WOCN	CN	201380029479.1	4/26/2013	ZL2013800294	3/27/2018	In Force	METHOD AND APPARATUS FOR ANALOG-TO-DIGITAL CONVERTER
MP4748W	MP4748WOEP	EP	EP13721234.6	4/26/2013			Pending	METHOD AND APPARATUS FOR ANALOG-TO-DIGITAL CONVERTER
MP4750	MP4750	US	13/870,042	4/25/2013	9360915	6/7/2016	In Force	DYNAMICALLY CONTROLLING CLOCKING RATE OF A PROCESSOR BAS
MP4753	MP4753	US	13/862,422	4/14/2013	9193951	9/22/2015	In Force	METHOD AND SYSTEM FOR COORDINATED MULTIPPOINT (COMP) CO
MP4753W	MP4753WOCN	CN	201380022056.1	4/14/2013	ZL2013800220	5/11/2018	In Force	METHOD AND APPARATUS FOR COORDINATED MULTIPPOINT (COMP)
MP4753W	MP4753WOEP	EP	EP13782124.5	4/14/2013	2842361	3/27/2019	In Force	Coordinated Multipoint (COMP) Communication between Base-Station
MP4753W	MP4753WOCPEP	DE	EP13782124.5	4/14/2013	602013052994	3/27/2019	In Force	Coordinated Multipoint (COMP) Communication between Base-Station
MP4753W	MP4753WOCPEFR	FR	EP13782124.5	4/14/2013	2842361	3/27/2019	In Force	Coordinated Multipoint (COMP) Communication between Base-Station
MP4753W	MP4753WOCPEGB	GB	EP13782124.5	4/14/2013	2842361	3/27/2019	In Force	Coordinated Multipoint (COMP) Communication between Base-Station
MP4753W	MP4753WOCOKR	KR	10-2014-703331	4/14/2013			Pending	COORDINATED MULTIPPOINT (COMP) COMMUNICATION BETWEEN B
MP4754	MP4754	US	13/900,126	5/22/2013	9316741	4/19/2016	In Force	System and Method for Determining GPS Receiver Position
MP4754	MP4754CN	CN	201310203872.1	5/27/2013	ZL2013102038	6/16/2017	In Force	SYSTEM AND METHOD FOR DETERMINING GPS RECEIVER POSITION
MP4756	MP4756C1	US	14/720,469	5/22/2015	9418690	8/16/2016	In Force	REPEATED RUNOUT (PRO) ZERO PHASE START (ZPS)
MP4757	MP4757	US	13/873,658	4/30/2013	9285824	3/15/2016	In Force	Systems And Methods For DQS Gating
MP4757W	MP4757WO	WO	PCT/US2013/03	4/30/2013			Pending	Systems And Methods For DQS Gating
MP4757W	MP4757WOCN	CN	201380022641.1	4/30/2013	ZL2013800226	8/4/2017	In Force	SYSTEMS AND METHODS FOR DQS GATING
MP4757W	MP4757WOEP	EP	13734164.0	4/30/2013	EP2845113	4/6/2016	In Force	Systems and Methods for DQS Gating
MP4757W	MP4757WOCPEDE	DE	13734164.0	4/30/2013	2845113	4/6/2016	In Force	Systems And Methods For DQS Gating
MP4757W	MP4757WOCPEFR	FR	13734164.0	4/30/2013	2845113	4/6/2016	In Force	Systems And Methods For DQS Gating
MP4757W	MP4757WOCPEGB	GB	13734164.0	4/30/2013	2845113	4/6/2016	In Force	Systems And Methods For DQS Gating
MP4758	MP4758	US	13/874,759	5/1/2013	9268941	2/23/2016	In Force	METHOD FOR SECURE SOFTWARE RESUME FROM LOW POWER MO
MP4759	MP4759	US	13/886,015	5/2/2013	9681375	6/13/2017	In Force	SCHEDULING SCHEMES FOR USER EQUIPMENT POWER SAVING IN A
MP4759W	MP4759WO	WO	PCT/US2013/03	5/2/2013			Pending	SCHEDULING SCHEMES FOR USER EQUIPMENT POWER SAVING IN A
MP4759W	MP4759WOCN	CN	201380023199.1	5/2/2013	ZL2013800231	10/2/2018	In Force	A METHOD OF ALLOCATING WIRELESS MEDIUM RESOURCES OR OPE
MP4760	MP4760	US	13/873,732	4/30/2013	8937974	1/20/2015	In Force	System And Method For Determining A Margin Of Latency Within A D
MP4764	MP4764	US	13/888,456	5/7/2013	9881657	1/30/2018	In Force	Computer System and Method of Memory Management
MP4764W	MP4764WO	WO	PCT/US2013/03	5/7/2013			Pending	Computer System and Method of Memory Management
MP4764W	MP4764WOCN	CN	201380024937.1	5/7/2013	ZL2013800249	1/23/2018	In Force	COMPUTER SYSTEM AND METHOD OF MEMORY MANAGEMENT
MP4764W	MP4764WOCOKR	KR	10-2014-703434	5/7/2013	10-1665611	10/6/2016	In Force	Computer System and Method of Memory Management
MP4768W	MP4768WOCN	CN	201380030198.1	5/7/2013	ZL2013800301	5/11/2018	In Force	METHOD AND SYSTEM FOR REPORTING FEEDBACK IN COOPERATIVE
MP4768W	MP4768WOCPEP	EP	EP13787645.4	5/7/2013	2847937	7/10/2019	In Force	Method and System for Reporting Feedback in Cooperative Multipoi
MP4768W	MP4768WOCPEDE	DE	EP13787645.4	5/7/2013	602013057691	7/10/2019	In Force	Method and System for Reporting Feedback in Cooperative Multipoi

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MP4768W	MP4768WOEPFR	FR	EP13787645.4	5/7/2013	2847937	7/10/2019	In Force	Method and System for Reporting Feedback in Cooperative Multipoi
MP4768W	MP4768WOEPGB	GB	EP13787645.4	5/7/2013	2847937	7/10/2019	In Force	Method and System for Reporting Feedback in Cooperative Multipoi
MP4768W	MP4768WOKR	KR	10-2014-703386	5/7/2013			Pending	METHOD AND SYSTEM FOR REPORTING FEEDBACK IN COOPERATIVE
MP4769	MP4769	US	13/550,083	7/16/2012	8837524	9/16/2014	In Force	Coexistence Of A Normal-Rate Physical Layer And A Low-Rate Physica
MP4769	MP4769C1	US	14/487,386	9/16/2014	9584383	2/28/2017	In Force	Coexistence Of A Normal-Rate Physical Layer And A Low-Rate Physica
MP4769W	MP4769WO	WO	PCT/US2012/04	7/16/2012			Pending	Coexistence Of A Normal-Rate Physical Layer And A Low-Rate Physica
MP4769W	MP4769WOCN	CN	201280042184.1	7/16/2012	ZL2012800421	11/10/2017	In Force	COEXISTENCE OF A NORMAL-RATE PHYSICAL LAYER AND A LOW-RAT
MP4769W	MP4769WOEP	EP	12743287.0	7/16/2012	EP2752066	9/14/2016	In Force	COEXISTENCE OF A NORMAL-RATE PHYSICAL LAYER AND A LOW-RAT
MP4769W	MP4769WOEPDE	DE	12743287.0	7/16/2012	602012022867	9/14/2016	In Force	Coexistence Of A Normal-Rate Physical Layer And A Low-Rate Physica
MP4769W	MP4769WOEPFR	FR	12743287.0	7/16/2012	2752066	9/14/2016	In Force	Coexistence Of A Normal-Rate Physical Layer And A Low-Rate Physica
MP4769W	MP4769WOEPGB	GB	12743287.0	7/16/2012	2752066	9/14/2016	In Force	Coexistence Of A Normal-Rate Physical Layer And A Low-Rate Physica
MP4769W	MP4769WOJP	JP	2014-528392	7/16/2012	JP6057259	12/16/2016	In Force	COEXISTENCE OF A NORMAL-RATE PHYSICAL LAYER AND A LOW-RAT
MP4769W	MP4769WOKR	KR	1020147086595	7/16/2012			Pending	Coexistence Of A Normal-Rate Physical Layer And A Low-Rate Physica
MP4770	MP4770C1	US	14/755,222	6/30/2015	9866823	1/2/2018	In Force	Method And Apparatus For Reducing Interference Between Wireless
MP4773	MP4773	US	13/881,707	5/10/2013	9254410	3/22/2016	In Force	Hybrid Dataflow Processor
MP4773W	MP4773WO	WO	PCT/US2013/04	5/10/2013			Pending	HYBRID DATAFLOW PROCESSOR
MP4773W	MP4773WOCN	CN	2013800248403	5/10/2013	ZL2013800248	2/27/2018	In Force	HYBRID DATAFLOW PROCESSOR
MP4775	MP4775	US	13/894,952	5/15/2013	9491108	11/8/2016	In Force	EXTENDED PRIORITY FOR ETHERNET PACKETS
MP4775	MP4775C1	US	14/252,465	4/14/2014	9491109	11/8/2016	In Force	EXTENDED PRIORITY FOR ETHERNET PACKETS
MP4775W	MP4775WO	WO	PCT/IB2013/001	5/15/2013			Pending	EXTENDED PRIORITY FOR ETHERNET PACKAGES
MP4775W	MP4775WOCN	CN	201380025577.1	5/15/2013	ZL2013800255	3/16/2018	In Force	EXTENDED PRIORITY FOR ETHERNET PACKETS
MP4776	MP4776	US	13/890,921	5/9/2013	9866835	10/31/2017	In Force	CLOCK SYNCHRONIZATION USING MULTIPLE NETWORK PATHS
MP4776	MP4776C1	US	15/797,695	10/30/2017	10205547	2/12/2019	In Force	CLOCK SYNCHRONIZATION USING MULTIPLE NETWORK PATHS
MP4781	MP4781	US	13/897,144	5/17/2013	9198072	11/24/2015	In Force	CALCULATING AND REPORTING CHANNEL CHARACTERISTICS
MP4781	MP4781D1	US	14/989,429	11/23/2015	9615280	4/4/2017	In Force	CALCULATING AND REPORTING CHANNEL CHARACTERISTICS
MP4781W	MP4781WO	WO	PCT/US2013/04	5/17/2013			Pending	CALCULATING AND REPORTING CHANNEL CHARACTERISTICS
MP4781W	MP4781WOCN	CN	201380025578.1	5/17/2013	ZL2013800255	5/1/2018	In Force	CALCULATING AND REPORTING CHANNEL CHARACTERISTICS
MP4781W	MP4781WOEP	EP	EP13728867.6	5/17/2013			Pending	CALCULATING AND REPORTING CHARACTERISTICS
MP4781W	MP4781WOJP	JP	2015-512899	5/17/2013	JP6260799	1/17/2018	In Force	CALCULATING AND REPORTING CHANNEL CHARACTERISTICS
MP4781W	MP4781WOKR	KR	10-2014-703406	5/17/2013			Pending	CALCULATING AND REPORTING CHANNEL CHARACTERISTICS
MP4782	MP4782	US	13/897,015	5/17/2013	9402279	7/26/2016	In Force	MULTI-LEVEL ARBITRATION FOR WIRELESS DEVICE HAVING MULTIPLE
MP4783	MP4783	US	13/937,104	7/8/2013	9226398	12/29/2015	In Force	PRINTED CIRCUIT BOARD AND PACKAGE SUBSTRATE HAVING ADDITI
MP4789	MP4789	US	13/898,942	5/21/2013	8995263	3/31/2015	In Force	METHOD AND APPARATUS FOR INTERNAL/EXTERNAL MEMORY PACK

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MP4789	MP4789CN	CN	2013102053021	5/22/2013	ZL2013102053	4/24/2018	In Force	METHOD AND APPARATUS FOR INTERNAL/EXTERNAL MEMORY PACK
MP4790	MP4790	US	13/889,121	5/21/2013	9229136	12/22/2015	In Force	METHOD AND APPARATUS FOR CONTROLLING A LIGHTING DEVICE
MP4790W	MP4790WO	WO	PCT/US2013/04	5/21/2013			Pending	METHOD AND APPARATUS FOR CONTROLLING A LIGHTING DEVICE
MP4791	MP4791	US	13/888,156	5/20/2013	9197400	11/24/2015	In Force	METHOD AND APPARATUS FOR JOINT ESTIMATION OF CARRIER FRE
MP4792	MP4792	US	13/893,871	5/14/2013	9164900	10/20/2015	In Force	Methods and Systems for Expanding Preload Capabilities of a Memor
MP4793	MP4793	US	13/900,932	5/23/2013	9119186	8/25/2015	In Force	FREQUENCY DOMAIN DUPLICATION IN A LONG-RANGE WIRELESS LO
MP4793	MP4793C1	US	14/834,210	8/24/2015	9432154	8/30/2016	In Force	FREQUENCY DOMAIN DUPLICATION IN A LONG-RANGE WIRELESS LO
MP4793W	MP4793WO	WO	PCT/US2013/04	5/23/2013			Pending	FREQUENCY DOMAIN DUPLICATION IN A LONG-RANGE WIRELESS LO
MP4793W	MP4793WOCN	CN	2013800269481	5/23/2013	ZL2013800269	9/22/2017	In Force	FREQUENCY DOMAIN DUPLICATION IN A LONG-RANGE WIRELESS LO
MP4793W	MP4793WCEP	EP	EP13729143.1	5/23/2013	2856687	4/18/2018	In Force	FREQUENCY DOMAIN DUPLICATION IN A LONG-RANGE WIRELESS LO
MP4793W	MP4793WQJP	JP	2015-514178	5/23/2013	JP6253642	12/27/2017	In Force	FREQUENCY DOMAIN DUPLICATION IN A LONG-RANGE WIRELESS LO
MP4793W	MP4793WQKR	KR	10-2014-703484	5/23/2013			Pending	FREQUENCY DOMAIN DUPLICATION IN A LONG-RANGE WIRELESS LO
MP4793W	MP4793WUDE	DE	EP13729143.1	5/23/2013	2856687	4/18/2018	In Force	FREQUENCY DOMAIN DUPLICATION IN A LONG-RANGE WIRELESS LO
MP4793W	MP4793WUFR	FR	EP13729143.1	5/23/2013	2856687	4/18/2018	In Force	FREQUENCY DOMAIN DUPLICATION IN A LONG-RANGE WIRELESS LO
MP4793W	MP4793WUGB	GB	EP13729143.1	5/23/2013	2856687	4/18/2018	In Force	FREQUENCY DOMAIN DUPLICATION IN A LONG-RANGE WIRELESS LO
MP4795	MP4795	US	13/926,525	6/25/2013	9088308	8/4/2015	In Force	METHOD AND APPARATUS FOR CODE PERFORMANCE ANALYSIS BAS
MP4795	MP4795CN	CN	201310269717.1	6/26/2013	ZL2013102697	4/13/2018	In Force	METHOD AND APPARATUS FOR CODE PERFORMANCE ANALYSIS
MP4796	MP4796	US	13/898,779	5/21/2013	9389317	7/12/2016	In Force	Method And Apparatus For Determining Position In A Global Navigati
MP4796W	MP4796WO	WO	PCT/US2013/04	5/21/2013			Pending	METHOD AND APPARATUS FOR DETERMINING POSITION IN A GLOBA
MP4796W	MP4796WOCN	CN	2013800265211	5/21/2013	ZL2013800265	11/10/2017	In Force	METHOD AND APPARATUS FOR DETERMINING POSITION IN A GLOBA
MP4798	MP4798	US	13/901,345	5/23/2013	9438527	9/6/2016	In Force	FLEXIBLE QUEUES IN A NETWORK SWITCH
MP4798	MP4798C1	US	15/256,164	9/2/2016	9887929	2/6/2018	In Force	FLEXIBLE QUEUES IN A NETWORK SWITCH
MP4798	MP4798CN	CN	201310264516.1	5/24/2013	ZL2013102645	4/2/2019	In Force	FLEXIBLE QUEUES IN A NETWORK SWITCH
MP4799	MP4799	US	13/901,857	5/24/2013	8816757	8/26/2014	In Force	SYSTEMS AND METHODS FOR LOAD TRANSIENT PREDICTION AND CO
MP4799	MP4799C1	US	14/337,760	7/22/2014	9083229	7/14/2015	In Force	SYSTEMS AND METHODS FOR LOAD TRANSIENT PREDICTION AND CO
MP4802	MP4802	US	13/905,335	5/30/2013	9288269	3/15/2016	In Force	Network Presence Offload
MP4802W	MP4802WO	WO	PCT/IB2013/054	5/30/2013			Pending	Network Presence Offload
MP4802W	MP4802WOCN	CN	201380024933.1	5/30/2013	ZL2013800249	6/29/2018	In Force	NETWORK PRESENCE OFFLOAD
MP4802W	MP4802WCEP	EP	EP13742270.5	5/30/2013	2856698	12/6/2017	In Force	NETWORK PRESENCE OFFLOAD
MP4802W	MP4802WCEPDE	DE	EP13742270.5	5/30/2013	2856698	12/6/2017	In Force	NETWORK PRESENCE OFFLOAD
MP4802W	MP4802WCEPFR	FR	EP13742270.5	5/30/2013	2856698	12/6/2017	In Force	NETWORK PRESENCE OFFLOAD
MP4802W	MP4802WCEPGB	GB	EP13742270.5	5/30/2013	2856698	12/6/2017	In Force	NETWORK PRESENCE OFFLOAD
MP4805	MP4805	US	13/905,255	5/30/2013	9275196	3/1/2016	In Force	Implementing Security Functions Using ROM
MP4805	MP4805C1	US	15/051,997	2/24/2016	9715587	7/25/2017	In Force	IMPLEMENTING SECURITY FUNCTIONS USING ROM

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MP4805W	MP4805WO	WO	PCT/US2013/04	5/30/2013			Pending	Implementing Security Functions Using ROM
MP4806	MP4806	US	13/869,503	4/24/2013	9325346	4/26/2016	In Force	SYSTEMS AND METHODS FOR HANDLING PARITY AND FORWARDED
MP4806	MP4806C1	US	15/137,605	4/25/2016	9697073	7/4/2017	In Force	SYSTEMS AND METHODS FOR HANDLING PARITY AND FORWARDED
MP4808	MP4808	US	13/905,248	5/30/2013	9268722	2/23/2016	In Force	Sharing Memory Using Processor Wait States
MP4809	MP4809	US	13/894,776	5/15/2013	9003270	4/7/2015	In Force	METHODS AND APPARATUS FOR TEMPORARILY STORING PARITY INF
MP4809	MP4809CN	CN	201310220890.1	6/3/2013	ZL2013102208	11/13/2018	In Force	METHODS AND APPARATUS FOR TEMPORARILY STORING PARITY INF
MP4813	MP4813	US	13/909,852	6/4/2013	9060041	6/16/2015	In Force	RELAYING MESSAGES IN A HETEROGENEOUS NETWORK
MP4813W	MP4813WO	WO	PCT/US2013/04	6/4/2013			Pending	RELAYING MESSAGES IN A HETEROGENEOUS NETWORK
MP4813W	MP4813WOCN	CN	201360029259.1	6/4/2013	ZL2013600292	10/24/2017	In Force	METHOD AND APPARATUS FOR PROCESSING MESSAGES IN HETERO
MP4813W	MP4813WOEP	EP	EP13729874.1	6/4/2013	2856704	3/20/2019	In Force	RELAYING MESSAGES IN A HETEROGENEOUS NETWORK
MP4813W	MP4813WOEPDE	DE	EP13729874.1	6/4/2013	602013052593	3/20/2019	In Force	RELAYING MESSAGES IN A HETEROGENEOUS NETWORK
MP4813W	MP4813WOEPFR	FR	EP13729874.1	6/4/2013	2856704	3/20/2019	In Force	RELAYING MESSAGES IN A HETEROGENEOUS NETWORK
MP4813W	MP4813WOEPGB	GB	EP13729874.1	6/4/2013	2856704	3/20/2019	In Force	RELAYING MESSAGES IN A HETEROGENEOUS NETWORK
MP4815W	MP4815WO	WO	PCT/US2013/04	6/4/2013			Pending	Wireless HDD With Integrated Storage Controller And Wireless Proce
MP4816	MP4816	US	13/911,267	6/6/2013	9220114	12/22/2015	In Force	Method And Apparatus For Restricting Channel Access To A Wireless
MP4816W	MP4816WO	WO	PCT/US2013/04	6/6/2013			Pending	Method And Apparatus For Restricting Channel Access To A Wireless
MP4817	MP4817	US	13/905,847	5/30/2013	9735855	8/15/2017	In Force	Method And Apparatus For Relaying Communication Between An Acc
MP4817W	MP4817WO	WO	PCT/US2013/04	5/31/2013			Pending	Method And Apparatus For Relaying Communication Between An Acc
MP4820	MP4820	US	13/916,196	6/12/2013	9521703	12/13/2016	In Force	Multiple Abstraction Layers Within a Communication Device
MP4820W	MP4820WO	WO	PCT/US2013/04	6/12/2013			Pending	MULTIPLE ABSTRACTION LAYERS WITHIN A COMMUNICATION DEVIC
MP4820W	MP4820WOCN	CN	201360030676.1	6/12/2013	ZL2013600306	2/23/2018	In Force	MULTIPLE ABSTRACTION LAYERS WITHIN A COMMUNICATION DEVIC
MP4820W	MP4820WOEP	EP	EP13734876.9	6/12/2013			Pending	MULTIPLE ABSTRACTION LAYERS WITHIN A COMMUNICATION DEVIC
MP4821	MP4821	US	13/913,877	6/10/2013	9357517	5/31/2016	In Force	Apparatus and Method for Wireless Baseband Processing
MP4821	MP4821CN	CN	201310333788.1	6/13/2013	ZL2013103337	5/1/2018	In Force	APPARATUS AND METHOD FOR WIRELESS BASEBAND PROCESSING
MP4821T	MP4821TW	TW	102120770	6/11/2013	1623218	5/1/2018	In Force	Apparatus and Method for Wireless Baseband Processing
MP4823	MP4823	US	13/900,053	5/22/2013	9329987	5/3/2016	In Force	SYSTEMS AND METHODS FOR DYNAMIC TRACKING OF MEMORY REG
MP4827	MP4827	US	13/899,925	5/22/2013	9396146	7/19/2016	In Force	Timing-Budget-Based Quality-Of-Service Control For A System-On-Chi
MP4828	MP4828CN	CN	201310254883.1	6/19/2013	ZL2013102548	5/18/2018	In Force	SYSTEMS AND METHODS FOR TRANSFERRING DATA OUT OF ORDER IN
MP4840	MP4840	US	13/927,908	6/26/2013	9356721	5/31/2016	In Force	METHODS AND APPARATUS FOR PRECISION TIME STAMPING
MP4840	MP4840D1	US	14/175,708	2/7/2014	9391728	7/12/2016	In Force	METHODS AND APPARATUS FOR PRECISION TIME STAMPING
MP4840W	MP4840WO	WO	PCT/IB2013/001	6/26/2013			Pending	METHODS AND APPARATUS FOR PRECISION TIME STAMPING
MP4840W	MP4840WOCN	CN	201360031438.1	6/26/2013	ZL2013600314	4/24/2018	In Force	METHODS AND APPARATUS FOR PRECISION TIME STAMPING
MP4842	MP4842	US	13/932,797	7/1/2013	9450649	9/20/2016	In Force	Shaping Near-Field Transmission Signals
MP4842W	MP4842WO	WO	PCT/US2013/04	7/2/2013			Pending	Shaping Near-Field Transmission Signals

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MP4842W	MP4842WOCN	CN	201380035211	7/2/2013	ZL2013800353	4/5/2017	In Force	SHAPING NEAR-FIELD TRANSMISSION SIGNALS
MP4842W	MP4842WOEP	EP	EP13771210.5	7/2/2013	2867998	6/26/2019	In Force	SHAPING NEAR-FIELD TRANSMISSION SIGNALS
MP4842W	MP4842WOEPDE	DE	EP13771210.5	7/2/2013	602013057127	6/26/2019	In Force	SHAPING NEAR-FIELD TRANSMISSION SIGNALS
MP4842W	MP4842WOEPFR	FR	EP13771210.5	7/2/2013	2867998	6/26/2019	In Force	SHAPING NEAR-FIELD TRANSMISSION SIGNALS
MP4842W	MP4842WOEPGB	GB	EP13771210.5	7/2/2013	2867998	6/26/2019	In Force	SHAPING NEAR-FIELD TRANSMISSION SIGNALS
MP4844	MP4844	US	13/915,858	6/12/2013	10255944	4/9/2019	In Force	SYSTEMS AND METHODS FOR READING AND DECODING ENCODED DATA
MP4844	MP4844CN	CN	2013102697181	6/26/2013	ZL2013102697	8/14/2018	In Force	SYSTEMS AND METHODS FOR READING AND DECODING ENCODED DATA
MP4852	MP4852CN	CN	2013106309274	11/21/2013			Pending	A CHIPSET, COMMUNICATION DEVICE, COMMUNICATION METHOD
MP4854	MP4854	US	13/917,262	6/13/2013	9148873	9/29/2015	In Force	METHOD AND APPARATUS FOR PROVIDING DIFFERENT TYPES OF PHASE
MP4855	MP4855	US	13/934,656	7/3/2013	9860712	1/2/2018	In Force	COMMUNICATING BROADCAST SIGNALS IN CARRIER-AGGREGATED
MP4855W	MP4855WO	WO	PCT/US2013/04	7/3/2013			Pending	COMMUNICATING BROADCAST SIGNALS IN CARRIER-AGGREGATED
MP4857	MP4857	US	13/928,269	6/26/2013	9264005	2/16/2016	In Force	TECHNIQUES FOR EFFICIENT RADIO FREQUENCY POWER AMPLIFIER
MP4858	MP4858	US	13/935,104	7/3/2013	9354837	5/31/2016	In Force	METHODS AND APPARATUS FOR INTERFACING A HOST DEVICE TO A
MP4858W	MP4858WO	WO	PCT/US2013/04	7/5/2013			Pending	METHODS AND APPARATUS FOR INTERFACING A HOST DEVICE TO A
MP4862	MP4862	US	13/938,586	7/10/2013	9037102	5/19/2015	In Force	METHOD AND APPARATUS FOR ENVELOPE TRACKING
MP4864	MP4864	US	13/939,973	7/11/2013	9195622	11/24/2015	In Force	MULTI-PORST MEMORY THAT SUPPORTS MULTIPLE SIMULTANEOUS
MP4869W	MP4869WO	WO	PCT/US2013/05	7/16/2013			Pending	OVERCOMING MULTIPLE REFLECTIONS IN PACKAGES AND CONNECT
MP4869W	MP4869WOCN	CN	201380045722.1	7/16/2013	ZL2013800457	4/24/2018	In Force	OVERCOMING MULTIPLE REFLECTIONS IN PACKAGES AND CONNECT
MP4870	MP4870	US	13/944,546	7/17/2013	9101058	8/4/2015	In Force	IC PACKAGE AND ASSEMBLY
MP4870	MP4870CN	CN	2013103087561	7/17/2013	ZL2013103087	2/9/2018	In Force	IC PACKAGE AND ASSEMBLY
MP4871	MP4871	US	13/937,572	7/9/2013	9191640	11/17/2015	In Force	Systems and Methods for Automatic White Balance of Images
MP4872	MP4872	US	13/947,936	7/22/2013	9117790	8/25/2015	In Force	METHODS AND ARRANGEMENTS RELATING TO SEMICONDUCTOR PA
MP4872W	MP4872WOCN	CN	201380046324.1	7/23/2013			Pending	METHODS AND ARRANGEMENTS RELATING TO SEMICONDUCTOR PA
MP4872W	MP4872WOJP	JP	2015-524392	1/22/2015			Pending	STACK PACKAGING SOC-DRAM
MP4872W	MP4872WOKR	KR	10-2015-700272	7/23/2013			Pending	STACK PACKAGING SOC-DRAM
MP4874	MP4874	US	13/950,654	7/25/2013	9396145	7/19/2016	In Force	In-Chip Bus Tracer
MP4875	MP4875C1	US	15/981,430	5/16/2018			Pending	SYSTEMS AND METHODS FOR DIVERSITY BIT-FLIPPING DECODING OF
MP4877	MP4877CN	CN	201310648379.1	12/2/2013			Pending	METHOD AND APPARATUS FOR BUFFERING DATA FRAMES
MP4879	MP4879	US	13/957,990	8/2/2013	9288159	3/15/2016	In Force	Systems and Methods for Deep Packet Inspection with a Virtual Mac
MP4881T	MP4881TW	TW	102136076	10/4/2013			Pending	Sensor Start Up Senseless Drive for Fanmotor Controllers
MP4881W	MP4881WO	WO	PCT/US2013/06	10/2/2013			Pending	DRIVING A ROTATING DEVICE BASED ON A COMBINATION OF SPEED
MP4881W	MP4881WOCN	CN	201380052237.1	4/3/2015			Pending	DRIVING A ROTATING DEVICE BASED ON A COMBINATION OF SPEED
MP4881W	MP4881WOJP	JP	2015-535119	3/16/2015			Pending	DRIVING A ROTATING DEVICE BASED ON A COMBINATION OF SPEED
MP4882	MP4882	US	13/961,634	8/7/2013	9173151	10/27/2015	In Force	USER MOBILITY CONTROL FOR HETEROGENEOUS WIRELESS NETWO

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MP4882	MP4882C1	US	14/922,758	10/26/2015	9980188	5/22/2018	In Force	USER MOBILITY CONTROL FOR HETEROGENEOUS WIRELESS NETWO
MP4882W	MP4882WO	WO	PCT/US2013/05	8/7/2013			Pending	USER MOBILITY CONTROL FOR HETEROGENEOUS WIRELESS NETWO
MP4882W	MP4882WOCN	CN	201380047633.1	8/7/2013	ZL2013800476	2/22/2019	In Force	USER MOBILITY CONTROL FOR HETEROGENEOUS WIRELESS NETWO
MP4882W	MP4882WOEP	EP	EP13752988.9	8/7/2013	2883390	10/10/2018	In Force	USER MOBILITY CONTROL FOR HETEROGENEOUS WIRELESS NETWOR
MP4882W	MP4882WOEPDE	DE	EP13752988.9	8/7/2013	60201304486	10/10/2018	In Force	USER MOBILITY CONTROL FOR HETEROGENEOUS WIRELESS NETWOR
MP4882W	MP4882WOEPFR	FR	EP13752988.9	8/7/2013	2883390	10/10/2018	In Force	USER MOBILITY CONTROL FOR HETEROGENEOUS WIRELESS NETWOR
MP4882W	MP4882WOEPGB	GB	EP13752988.9	8/7/2013	2883390	10/10/2018	In Force	USER MOBILITY CONTROL FOR HETEROGENEOUS WIRELESS NETWOR
MP4882W	MP4882WOJP	JP	2015-526676	1/30/2015	JP6361060	7/6/2018	In Force	USER MOBILITY CONTROL FOR HETEROGENEOUS WIRELESS NETWO
MP4882W	MP4882WOKR	KR	10-2015-76055	8/7/2013			Pending	USER MOBILITY CONTROL FOR HETEROGENEOUS WIRELESS NETWO
MP4883	MP4883	US	13/962,452	8/8/2013	9941774	4/10/2018	In Force	Controlling Fan Motors Using Capacitive Sensing
MP4883W	MP4883WO	WO	PCT/US2013/05	8/8/2013			Pending	Controlling Fan Motors Using Capacitive Sensing
MP4883W	MP4883WOCN	CN	201380052406.1	8/8/2013	ZL2013800524	7/11/2017	In Force	CONTROLLING FAN MOTORS USING CAPACITIVE SENSING
MP4884	MP4884	US	13/962,731	8/8/2013	9565770	2/7/2017	In Force	METHODS OF MAKING PACKAGES USING THIN CU FOIL SUPPORTED
MP4884W	MP4884WO	WO	PCT/US2013/05	8/8/2013			Pending	METHODS OF MAKING PACKAGES USING THIN CU FOIL SUPPORTED
MP4884W	MP4884WOCN	CN	201380052312.1	8/8/2013	ZL2013800523	1/26/2018	In Force	METHODS OF MAKING PACKAGES USING THIN CU FOIL SUPPORTED
MP4884W	MP4884WOKR	KR	10-2015-700284	8/8/2013			Pending	METHODS OF MAKING PACKAGES USING THIN CU FOIL SUPPORTED
MP4885	MP4885	US	14/151,437	1/9/2014	9167396	10/20/2015	In Force	METHOD AND APPARATUS TO TRANSMIT DATA THROUGH TONES
MP4889T	MP4889TW	TW	102119265	8/5/2013	1620427	4/1/2018	In Force	SWITCHED CONTINUOUS TIME LINEAR EQUALIZER WITH INTEGRATE
MP4889W	MP4889WO	WO	PCT/IB2013/001	8/10/2013			Pending	SWITCHED CONTINUOUS TIME LINEAR EQUALIZER WITH INTEGRATE
MP4889W	MP4889WOCN	CN	201380048529.4	8/10/2013	ZL2013800485	7/6/2018	In Force	SWITCHED CONTINUOUS TIME LINEAR EQUALIZER WITH INTEGRATE
MP4893	MP4893	US	13/955,776	7/31/2013	9445349	9/13/2016	In Force	802.11ah Duty Cycle Based Channel Access Priorities
MP4893	MP4893C1	US	15/254,219	9/1/2016	9775097	9/26/2017	In Force	Systems And Methods For Requesting Duty Cycle Based Channel Acce
MP4894	MP4894	US	13/959,498	8/5/2013	9048917	6/2/2015	In Force	METHOD AND APPARATUS FOR ESTIMATING NOISE COVARIANCE IN
MP4896	MP4896	US	13/965,754	8/13/2013	9264404	2/16/2016	In Force	Encrypting Data Using Time Stamps
MP4897W	MP4897WO	WO	PCT/US2013/05	8/14/2013			Pending	METHOD AND APPARATUS FOR PROVIDING AN ADVERTISEMENT ON
MP4898	MP4898	US	13/804,113	3/14/2013	9158675	10/13/2015	In Force	ARCHITECTURE FOR STORAGE OF DATA ON NAND FLASH MEMORY
MP4898W	MP4898WO	WO	PCT/US2013/03	3/14/2013			Pending	ARCHITECTURE FOR STORAGE OF DATA ON NAND FLASH MEMORY
MP4898W	MP4898WOCN	CN	201380019643.4	3/14/2013	ZL2013800196	12/5/2017	In Force	ARCHITECTURE FOR STORAGE OF DATA ON NAND FLASH MEMORY
MP4898W	MP4898WOJP	JP	2015-500592	3/14/2013	JP6175684	7/21/2017	In Force	ARCHITECTURE FOR STORAGE OF DATA ON NAND FLASH MEMORY
MP4901	MP4901	US	13/963,281	8/9/2013	9673878	6/6/2017	In Force	SYSTEMS AND METHODS FOR PERFORMING PRECODING IN MULTI-U
MP4901	MP4901C1	US	15/582,213	4/28/2017			Pending	SYSTEMS AND METHODS FOR PERFORMING PRECODING IN MULTI-U
MP4905	MP4905	US	13/969,767	8/19/2013	8913345	12/16/2014	In Force	System and Method for Reading Data from a Magnetic Medium by Ut
MP4906	MP4906	US	13/974,901	8/23/2013	9203432	12/1/2015	In Force	SYMBOL FLIPPING DECODERS OF NON-BINARY LOW-DENSITY PARITY
MP4906	MP4906CN	CN	201310394513.1	8/28/2013	ZL2013103945	10/9/2018	In Force	SYMBOL FLIPPING DECODERS OF NON-BINARY LOW-DENSITY PARITY

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MP4907	MP4907-1	US	13/973,751	8/22/2013	9525621	12/20/2016	In Force	SEMAPHORE SOFT AND HARD HYBRID ARCHITECTURE
MP4907	MP4907CN	CN	201310389251.1	8/29/2013			Pending	SEMAPHORE SOFT AND HARD HYBRID ARCHITECTURE
MP4909	MP4909	US	14/023,259	9/10/2013	9425139	8/23/2016	In Force	DUAL ROW QUAD FLAT NO-LEAD SEMICONDUCTOR PACKAGE
MP4909	MP4909C1	US	15/243,302	8/22/2016	9666510	5/30/2017	In Force	DUAL ROW QUAD FLAT NO-LEAD SEMICONDUCTOR PACKAGE
MP4909W	MP4909WO	WO	PCT/US2013/05	9/11/2013			Pending	DUAL ROW QUAD FLAT NO-LEAD SEMICONDUCTOR PACKAGE
MP4909W	MP4909WOCN	CN	201380047318.1	9/11/2013	Z12013800473	12/29/2017	In Force	DUAL ROW QUAD FLAT NO-LEAD SEMICONDUCTOR PACKAGE
MP4909W	MP4909WOKR	KR	10/2015-70061K	9/11/2013			Pending	DROFN Design Features
MP4912	MP4912	US	14/010,304	8/26/2013	9674730	6/6/2017	In Force	METHOD AND APPARATUS FOR DETECTING AND PROCESSING A RET
MP4912W	MP4912WO	WO	PCT/US2013/05	8/26/2013			Pending	METHOD AND APPARATUS FOR DETECTING AND PROCESSING A RET
MP4915	MP4915	US	13/974,890	8/23/2013	9392654	7/12/2016	In Force	METHOD AND APPARATUS FOR CONTROLLING A POWER ADJUSTME
MP4915T	MP4915TW	TW	102130440	8/26/2013	1600344	9/21/2017	In Force	CONTROL ALGORITHMS AND CLOSE LOOP REGULATION METHODS F
MP4915W	MP4915WO	WO	PCT/US2013/05	8/26/2013			Pending	METHOD AND APPARATUS FOR CONTROLLING A LIGHTING DEVICE
MP4916	MP4916	US	14/016,869	9/3/2013	9490695	11/8/2016	In Force	METHOD AND APPARATUS FOR ELIMINATING SHIMMERING
MP4917	MP4917	US	14/018,807	9/5/2013	9432100	8/30/2016	In Force	EFFICIENT SEARCH OF PRECODERS IN PRECODED MIMO SYSTEMS
MP4917W	MP4917WO	WO	PCT/US2013/05	9/5/2013			Pending	EFFICIENT SEARCH OF PRECODERS IN PRECODED MIMO SYSTEMS
MP4921	MP4921	US	14/037,897	10/18/2013	9571018	2/14/2017	In Force	METHOD AND APPARATUS FOR WINDOWLESS BEMF DETECTION
MP4921T	MP4921TW	TW	102138136	10/22/2013	1611656	1/11/2018	In Force	Windowless BEMF Detection
MP4921W	MP4921WOCN	CN	201380055600.1	10/22/2013	Z12013800556	3/23/2018	In Force	METHOD AND APPARATUS FOR WINDOWLESS BEMF DETECTION
MP4922	MP4922	US	14/060,361	10/22/2013	9863852	1/9/2018	In Force	FAILURE PREDICTION IN A ROTATING DEVICE
MP4922T	MP4922TW	TW	102138477	10/24/2013	1639838	11/1/2018	In Force	Fan Failure Prediction
MP4922W	MP4922WOCN	CN	201380055493.1	10/23/2013	Z12013800554	1/16/2018	In Force	FAILURE PREDICTION IN A ROTATING DEVICE
MP4925	MP4925	US	14/020,121	9/6/2013	9304961	4/5/2016	In Force	Method And Apparatus For Transferring Packets Between Interface C
MP4925	MP4925WOEPDE	DE	EP13799367.1	9/6/2013	602013062611	11/6/2019	In Force	APPARATUS FOR TRANSFERRING PACKETS BETWEEN INTERFACE CON
MP4925	MP4925WOEPFR	FR	EP13799367.1	9/6/2013	2893678	11/6/2019	In Force	APPARATUS FOR TRANSFERRING PACKETS BETWEEN INTERFACE CON
MP4925	MP4925WOEPGB	GB	EP13799367.1	9/6/2013	2893678	11/6/2019	In Force	APPARATUS FOR TRANSFERRING PACKETS BETWEEN INTERFACE CON
MP4925W	MP4925WO	WO	PCT/IB2013/058	9/6/2013			Pending	Method And Apparatus For Transferring Packets Between Interface C
MP4925W	MP4925WOCN	CN	201380046998.1	9/6/2013	Z12013800469	3/9/2018	In Force	METHOD AND APPARATUS FOR TRANSFERRING PACKETS BETWEEN I
MP4925W	MP4925WOEP	EP	EP13799367.1	9/6/2013			Pending	METHOD AND APPARATUS FOR TRANSFERRING PACKETS BETWEEN I
MP4926	MP4926	US	14/022,562	9/10/2013	9288149	3/15/2016	In Force	Method And Apparatus For Transmitting Packets In Accordance With
MP4926	MP4926C1	US	15/043,813	2/15/2016	9998380	6/12/2018	In Force	Device And Method For Selectively Transmitting Data Packets During
MP4926W	MP4926WO	WO	PCT/US2013/05	9/10/2013			Pending	Method And Apparatus For Transmitting Packets In Accordance With
MP4926W	MP4926WOCN	CN	201380047085.1	9/10/2013	Z12013800470	2/27/2018	In Force	METHOD AND APPARATUS FOR TRANSMITTING PACKETS IN ACCORD
MP4926W	MP4926WOEP	EP	EP13770553.9	9/10/2013			Pending	METHOD AND APPARATUS FOR TRANSMITTING PACKETS BETWEEN I
MP4928	MP4928	US	14/019,006	9/5/2013	9130749	9/8/2015	In Force	METHOD AND APPARATUS FOR DECODING A DATA PACKET USING SC

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MP4930	MP4930	US	14/029,594	9/17/2013	9304777	4/5/2016	In Force	METHOD AND APPARATUS FOR DETERMINING RELATIVE AGES OF EN
MP4931	MP4931	US	14/028,586	9/17/2013	9804736	10/31/2017	In Force	Modifiable Contextual Help Content Provided In-line Within An Appli
MP4931	MP4931CN	CN	201310541116.1	9/18/2013	Z12013105411	9/14/2018	In Force	MODIFIABLE CONTEXTUAL HELP CONTENT PROVIDED IN-LINE WITHI
MP4933W	MP4933WO	WO	PCT/US2013/05	9/6/2013			Pending	METHOD AND SYSTEM FOR DETECTING SYNCHRONIZATION SIGNALS
MP4935	MP4935	US	14/022,739	9/10/2013	9451569	9/20/2016	In Force	SYSTEMS AND METHODS FOR DETECTING A PRIMARY SYNCHRONIZA
MP4935	MP4935C1	US	15/767,937	9/16/2016	9986521	5/29/2018	In Force	SYSTEMS AND METHODS FOR DETECTING A PRIMARY SYNCHRONIZA
MP4938	MP4938	US	14/028,088	9/16/2013	9048893	6/2/2015	In Force	DETERMINING CHANNEL INFORMATION USING DECISION FEEDBACK
MP4939	MP4939	US	14/016,108	9/1/2013	8938016	1/20/2015	In Force	FAST IDENTIFICATION OF OFDM SIGNAL
MP4941	MP4941	US	14/026,099	9/13/2013	9792299	10/17/2017	In Force	Method and Apparatus for Enabling a First Device to Control Operat
MP4942W	MP4942WO	WO	PCT/US2013/06	10/31/2013			Pending	HIGHLY ACCURATE TRUE RMS POWER DETECTOR FOR CELLULAR APP
MP4942W	MP4942WOCN	CN	201380056944.1	10/31/2013	Z12013800569	9/29/2017	In Force	HIGHLY ACCURATE TRUE RMS POWER DETECTOR FOR CELLULAR APP
MP4943	MP4943	US	14/044,921	10/3/2013	9052864	6/9/2015	In Force	METHOD AND APPARATUS FOR PROCESSING A PAGE DESCRIPTION L
MP4944	MP4944	US	14/052,840	10/14/2013	9338733	5/10/2016	In Force	Method and Apparatus for Determining the Availability of a First Devi
MP4945	MP4945	US	14/024,802	9/12/2013	9767818	9/19/2017	In Force	STEERABLE BEAMFORMER
MP4946	MP4946	US	14/028,835	9/17/2013	9312060	4/12/2016	In Force	Transformer Circuits Having Transformers With Figure Eight And Dou
MP4946W	MP4946WO	WO	PCT/US2013/06	9/18/2013			Pending	Transformer Circuits Having Transformers With Figure Eight And Dou
MP4946W	MP4946WOCN	CN	201380056443.1	9/18/2013	Z12013800564	9/18/2018	In Force	TRANSFORMER CIRCUITS HAVING TRANSFORMERS WITH FIGURE EIG
MP4946W	MP4946WCEP	EP	EP13770605.7	9/18/2013			Pending	TRANSFORMER CIRCUITS HAVING TRANSFORMERS WITH FIGURE EIG
MP4946W	MP4946WQJP	JP	2015-533140	9/18/2013	JP6332759	5/30/2018	In Force	TRANSFORMER CIRCUITS HAVING TRANSFORMERS WITH FIGURE EIG
MP4946W	MP4946WOKR	KR	1020157010041	9/18/2013			Pending	Transformer Circuits Having Transformers With Figure Eight And Dou
MP4947W	MP4947WO	WO	PCT/US2013/06	9/19/2013			Pending	AUGMENTED TOUCH CONTROL FOR HAND-HELD DEVICES
MP4950	MP4950	US	14/042,917	10/1/2013	9430282	8/30/2016	In Force	Scheduling Multiple Tasks in Distributed Computing System to Avoid
MP4952	MP4952	US	14/031,622	9/19/2013	9214964	12/15/2015	In Force	SYSTEMS AND METHODS FOR CONFIGURING PRODUCT CODES FOR E
MP4952	MP4952C1	US	14/961,274	12/7/2015	9490849	11/8/2016	In Force	SYSTEMS AND METHODS FOR CONFIGURING PRODUCT CODES FOR E
MP4953	MP4953	US	14/031,527	9/19/2013	9160373	10/13/2015	In Force	SYSTEMS AND METHODS FOR JOINT DECODING OF SECTOR AND TRA
MP4955	MP4955	US	14/038,093	9/26/2013	9209873	12/8/2015	In Force	METHOD AND APPARATUS FOR ESTIMATING NOISE AND PROVIDING
MP4956	MP4956	US	14/031,637	9/19/2013	9285757	7/5/2016	In Force	SYSTEMS AND METHODS FOR USING A NON-BINARY SOFT OUTPUT V
MP4958	MP4958	US	14/027,894	9/16/2013	9055630	6/23/2015	In Force	SYSTEMS AND METHODS FOR DETECTING SECONDARY SYNCHRONIZ
MP4962	MP4962	US	14/040,218	9/27/2013	9306387	4/5/2016	In Force	CURRENT LIMITING CIRCUIT AND METHOD FOR LED DRIVER
MP4962W	MP4962WO	WO	PCT/US2013/06	9/27/2013			Pending	CURRENT LIMITING CIRCUIT AND METHOD FOR LED DRIVER
MP4963	MP4963	US	14/156,262	1/15/2014	9420524	8/16/2016	In Force	ADAPTIVE MULTIMODAL PROVISIONING FOR WIRELESS SENSORS
MP4964	MP4964	US	14/055,448	10/16/2013	9727073	8/8/2017	In Force	PRECISION CURRENT SOURCE WITH PROGRAMMABLE SLEW RATE C
MP4965	MP4965	US	14/042,788	10/1/2013	9325395	4/26/2016	In Force	Channel Processing with Dedicated Pilots Utilizing Information from
MP4969	MP4969	US	14/053,165	10/14/2013	9246379	1/26/2016	In Force	CURRENT LIMIT PROTECTION CIRCUITS FOR USE WITH DEPLETION M

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MP4971W	MP4971WO	WO	PCT/US2013/05	9/10/2013			Pending	SYSTEMS AND METHODS FOR CAMERA IMAGE CORRECTION AND W
MP4972	MP4972	US	14/044,281	10/2/2013	9275731	3/1/2016	In Force	Systems And Methods For Increasing The Read Sensitivity Of A Resist
MP4973	MP4973C1	US	14/609,853	1/30/2015	9147837	9/29/2015	In Force	Resistive Memory Cell And Method For Forming A Resistive Memory
MP4974	MP4974	US	14/043,867	10/2/2013	9203589	12/1/2015	In Force	ESTIMATION OF CHANNEL STATE INFORMATION (CSI) FEEDBACK USI
MP4977	MP4977	US	14/156,286	1/15/2014	9220012	12/22/2015	In Force	SYSTEMS AND METHODS FOR PROVISIONING DEVICES
MP4978	MP4978	US	14/048,189	10/8/2013	9436203	9/6/2016	In Force	METHOD AND APPARATUS FOR REGULATOR CONTROL
MP4978	MP4978CN	CN	201310726089.1	10/8/2013	ZL2013107260	3/1/2017	In Force	METHOD AND APPARATUS FOR REGULATOR CONTROL
MP4978K	MP4978KR	KR	10-2013-011975	10/8/2013			Pending	METHOD AND APPARATUS FOR REGULATOR CONTROL
MP4980	MP4980	US	14/056,237	10/17/2013	9729391	8/8/2017	In Force	METHOD AND APPARATUS FOR PATH INDICATION
MP4983	MP4983	US	14/059,261	10/21/2013	9197124	11/24/2015	In Force	METHOD AND APPARATUS TO REDUCE OVERCURRENT DURING THE
MP4983T	MP4983TW	TW	102138140	10/22/2013	1610524	1/1/2018	In Force	METHOD TO REDUCE OVER CURRENT DURING THE START UP OF A S
MP4983W	MP4983WO	WO	PCT/US2013/06	10/21/2013			Pending	METHOD AND APPARATUS TO REDUCE OVERCURRENT DURING THE
MP4984	MP4984	US	14/028,796	9/17/2013	9344215	5/17/2016	In Force	Systems and Methods for Advanced Power Management for Optical
MP4984	MP4984CN	CN	2013104830994	10/14/2013	ZL2013104830	6/7/2019	In Force	SYSTEMS AND METHODS FOR ADVANCED POWER MANAGEMENT FO
MP4991	MP4991	US	14/050,720	10/10/2013	8934285	1/13/2015	In Force	Method And Apparatus For Forming A Contact In A Cell Of A Resistive
MP4991	MP4991C1	US	14/394,940	1/12/2015	9214230	12/15/2015	In Force	Resistive Random Access Memory Cell Structure With Reduced Progr
MP4991	MP4991C1C1	US	14/955,326	12/1/2015	9490427	11/8/2016	In Force	Resistive Random Access Memory Cell Structure
MP4991	MP4991CN	CN	201310488552.1	10/15/2013	ZL2013104885	7/27/2018	In Force	METHOD AND APPARATUS FOR FORMING A CONTACT IN A CELL OF
MP4991T	MP4991TW	TW	102137135	10/15/2013	1603459	10/21/2017	In Force	METHOD AND APPARATUS FOR FORMING A CONTACT IN A CELL OF
MP4992	MP4992	US	14/045,230	10/3/2013	8873309	10/28/2014	In Force	Apparatus And Method For Repairing Resistive Memories And Increa
MP4992T	MP4992TW	TW	102137138	10/15/2013	1612520	1/21/2018	In Force	APPARATUS AND METHOD FOR REPAIRING RESISTIVE MEMORIES AN
MP4992W	MP4992WO	WO	PCT/US2013/06	10/14/2013			Pending	Apparatus And Method For Repairing Resistive Memories And Increa
MP4992W	MP4992WOCN	CN	201380053598.1	10/14/2013	ZL2013800535	3/9/2018	In Force	APPARATUS AND METHOD FOR REPAIRING RESISTIVE MEMORIES AN
MP4993	MP4993	US	14/050,678	10/10/2013	9047945	6/2/2015	In Force	Systems And Methods For Reading Resistive Random Access Memory
MP4993T	MP4993TW	TW	102137130	10/15/2013	1603925	10/21/2017	In Force	SYSTEMS AND METHODS FOR READING RESISTIVE RANDOM ACCESS
MP4993W	MP4993WO	WO	PCT/US2013/06	10/14/2013			Pending	Systems And Methods For Reading Resistive Random Access Memory
MP4993W	MP4993WOCN	CN	201380053672.1	10/14/2013	ZL2013800536	5/31/2017	In Force	SYSTEMS AND METHODS FOR READING RESISTIVE RANDOM ACCESS
MP4994	MP4994	US	14/050,696	10/10/2013	9042159	5/26/2015	In Force	Configuring Resistive Random Access Memory (RRAM) Array For Writ
MP4994T	MP4994TW	TW	102137127	10/15/2013	1625725	6/1/2018	In Force	Configuring Resistive Random Access Memory (RRAM) Array For Writ
MP4994W	MP4994WO	WO	PCT/US2013/06	10/14/2013			Pending	Configuring Resistive Random Access Memory (RRAM) Array For Writ
MP4995	MP4995DSGN	US	29/471,092	10/28/2013	0723550	3/3/2015	In Force	HANDHELD WIRELESS DEVICE
MP4996	MP4996D1	US	15/211,256	7/15/2016	10217669	2/26/2019	In Force	ISOLATION COMPONENTS FOR TRANSISTORS FORMED ON FIN FEATU
MP4996	MP4996D1C1	US	16/230,505	12/21/2018			Pending	ISOLATION COMPONENTS FOR TRANSISTORS FORMED ON FIN FEATU
MP4996T	MP4996TW	TW	102136822	10/11/2013	1612668	1/21/2018	In Force	SELF-ALIGNED ISOLATION IN FINFET

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MP4996W	MP4996WO	WO	PCT/US2013/06	10/11/2013			Pending	ISOLATION COMPONENTS FOR TRANSISTORS FORMED ON FIN FEATU
MP4996W	MP4996WOCN	CN	201380053697.1	10/11/2013	ZL2013800536	6/22/2018	In Force	ISOLATION COMPONENTS FOR TRANSISTORS FORMED ON FIN FEATU
MP4996W	MP4996WOKR	KR	10-2015-700755	10/11/2013			Pending	SELF-ALIGNED ISOLATION IN FinFET
MP5003	MP5003	US	14/050,376	10/10/2013	9253697	2/2/2016	In Force	INTER-RAT RESELECTION IN MOBILE COMMUNICATION SYSTEMS
MP5003W	MP5003WOCN	CN	201380053497.1	10/14/2013	ZL2013800534	9/25/2018	In Force	INTER-RAT RESELECTION IN MOBILE COMMUNICATION SYSTEMS
MP5005	MP5005	US	14/053,111	10/14/2013	9355558	5/31/2016	In Force	HIGH BANDWIDTH CONFIGURABLE SERIAL LINK
MP5005	MP5005CN	CN	201310491106.1	10/16/2013	ZL2013104911	5/31/2019	In Force	HIGH BANDWIDTH CONFIGURABLE SERIAL LINK
MP5006	MP5006	US	14/291,479	5/30/2014	9521645	12/13/2016	In Force	Method and Apparatus for Filtering Access Points for WiFi Positionin
MP5007T	MP5007TW	TW	102137132	10/15/2013			Pending	TEMPERATURE FOLDBACK CIRCUIT FOR LED LOAD CONTROL BY CON
MP5007W	MP5007WO	WO	PCT/US2013/06	10/9/2013			Pending	TEMPERATURE FOLDBACK CIRCUIT FOR LED LOAD CONTROL BY CON
MP5012	MP5012C1	US	14/536,761	11/10/2014	9129678	9/8/2015	In Force	Method And Apparatus For Reforming A Memory Cell Of A Memory
MP5012T	MP5012TW	TW	102138479	10/24/2013	1612521	1/21/2018	In Force	Apparatus And Method For Reforming Resistive Memory Cells
MP5012W	MP5012WO	WO	PCT/US2013/06	10/23/2013			Pending	Apparatus And Method For Reforming Resistive Memory Cells
MP5012W	MP5012WOCN	CN	201380064169.1	10/23/2013	ZL2013800641	12/26/2017	In Force	APPARATUS AND METHOD FOR REFORMING RESISTIVE MEMORY CEL
MP5012W	MP5012WCEP	EP	EP13785798.3	10/23/2013			Pending	APPARATUS AND METHOD FOR REFORMING RESISTIVE MEMORY CEL
MP5012W	MP5012WOP	JP	2015-539739	10/23/2013	JP6171222	7/14/2017	In Force	APPARATUS AND METHOD FOR REFORMING RESISTIVE MEMORY CEL
MP5013	MP5013	US	14/059,265	10/21/2013	9324137	4/26/2016	In Force	LOW-FREQUENCY COMPRESSION OF HIGH DYNAMIC RANGE IMAGES
MP5013W	MP5013WO	WO	PCT/US2013/06	10/22/2013			Pending	LOW-FREQUENCY COMPRESSION OF HIGH DYNAMIC RANGE IMAGES
MP5014	MP5014	US	14/062,553	10/24/2013	9414317	8/9/2016	In Force	DYNAMIC POWER MANAGEMENT IN A WIRELESS DEVICE
MP5014	MP5014C1	US	15/230,648	8/8/2016	9942851	4/10/2018	In Force	DYNAMIC POWER MANAGEMENT IN A WIRELESS DEVICE
MP5014W	MP5014WO	WO	PCT/US2013/06	10/24/2013			Pending	DYNAMIC POWER MANAGEMENT IN A WIRELESS DEVICE
MP5014W	MP5014WOCN	CN	201380064625.1	10/24/2013	ZL2013800646	10/2/2018	In Force	DYNAMIC POWER MANAGEMENT IN A WIRELESS DEVICE
MP5014W	MP5014WCEP	EP	EP13788837.5	10/24/2013	2912896	5/16/2018	In Force	DYNAMIC POWER MANAGEMENT IN A WIRELESS DEVICE
MP5014W	MP5014WCEPDE	DE	EP13788837.5	10/24/2013	60201303758.1	5/16/2018	In Force	DYNAMIC POWER MANAGEMENT IN A WIRELESS DEVICE
MP5014W	MP5014WCEPFR	FR	EP13788837.5	10/24/2013	2912896	5/16/2018	In Force	DYNAMIC POWER MANAGEMENT IN A WIRELESS DEVICE
MP5014W	MP5014WCEPGB	GB	EP13788837.5	10/24/2013	2912896	5/16/2018	In Force	DYNAMIC POWER MANAGEMENT IN A WIRELESS DEVICE
MP5014W	MP5014WOPJP	JP	2015-539798	10/24/2013	JP6273586	2/7/2018	In Force	DYNAMIC POWER MANAGEMENT IN A WIRELESS DEVICE
MP5015	MP5015	US	14/102,733	12/11/2013	9301325	3/29/2016	In Force	Method and Apparatus for Low-Energy Sensor Wireless Connection
MP5020	MP5020	US	14/066,817	10/30/2013	9042162	5/26/2015	In Force	SRAM Cells Suitable For Fin Field-Effect Transistor (FinFET) Process
MP5020T	MP5020TW	TW	102139583	10/31/2013	1601135	10/1/2017	In Force	SRAM CELLS SUITABLE FOR FIN FIELD-EFFECT TRANSISTOR (FINFET) P
MP5020W	MP5020WO	WO	PCT/US2013/06	10/30/2013			Pending	SRAM Cells Suitable For Fin Field-Effect Transistor (FinFET) Process
MP5026	MP5026	US	14/076,058	11/8/2013	9244107	1/26/2016	In Force	HEAT SINK BLADE PACK FOR DEVICE UNDER TEST TESTING
MP5026T	MP5026TW	TW	102140861	11/11/2013	1603100	10/21/2017	In Force	HEAT-SINK BLADE PACK FOR DUT TESTING
MP5026W	MP5026WO	WO	PCT/US2013/06	11/11/2013			Pending	HEAT SINK BLADE PACK FOR DEVICE TEST TESTING

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MP5026W	MP5026WOCN	CN	201380069846	11/11/2013	ZL2013800698	10/10/2017	In Force	HEAT SINK BLADE PACK FOR DEVICE TEST TESTING
MP5029	MP5029	US	14/066,796	10/30/2013	9142284	9/22/2015	In Force	Concurrent Use Of SRAM Cells With Both NMOS and PMOS Pass Gate
MP5029T	MP5029TW	TW	102139579	10/31/2013	613656	2/1/2018	In Force	CONCURRENT USE OF SRAM CELLS WITH BOTH NMOS AND PMOS PA
MP5029W	MP5029WO	WO	PCT/US2013/06	10/30/2013			Pending	Concurrent Use Of SRAM Cells With Both NMOS And PMOS Pass Gat
MP5029W	MP5029WOCN	CN	201380067878	10/30/2013	ZL2013800678	4/24/2018	In Force	CONCURRENT USE OF SRAM CELLS WITH BOTH NMOS AND PMOS PA
MP5030	MP5030	US	14/076,694	11/11/2013	9307302	4/5/2016	In Force	Passive Optical Network Switch
MP5030W	MP5030WO	WO	PCT/US2013/06	11/11/2013			Pending	PASSIVE OPTICAL NETWORK SWITCH
MP5030W	MP5030WOCN	CN	201380065892	11/11/2013	ZL2013800658	12/4/2018	In Force	PASSIVE OPTICAL NETWORK SWITCH
MP5030W	MP5030WGP	JP	2015-541982	11/11/2013	JP6319591	5/9/2018	In Force	PASSIVE OPTICAL NETWORK SWITCH
MP5031	MP5031	US	14/073,033	11/6/2013	9509579	11/29/2016	In Force	LINK ATTRIBUTES DISPLAY
MP5032	MP5032	US	14/078,013	11/12/2013	10057137	8/21/2018	In Force	CABLE DISCOVERY WITH VISUAL DISPLAY OF A NETWORK TOPOLOGY
MP5033	MP5033	US	14/078,712	11/13/2013	9319939	4/19/2016	In Force	METHOD AND APPARATUS FOR HANDLING CALL DEGRADATION DUR
MP5033W	MP5033WOCN	CN	201380064658	11/13/2013	ZL2013800646	3/12/2019	In Force	METHOD AND APPARATUS FOR COMMUNICATIONS, MOBILE COMM
MP5033W	MP5033WO	WO	PCT/US2013/06	11/13/2013			Pending	Systems and Methods for Image Enhancement by Local Tone Curve
MP5038	MP5038	US	14/153,244	1/13/2014	9501718	11/22/2016	In Force	Image-Based Control of Lighting Systems
MP5039	MP5039	US	14/083,508	11/19/2013	9605689	8/2/2016	In Force	LOCALLY CACHING DATA FROM A SHARED STORAGE
MP5039W	MP5039WO	WO	PCT/US2013/07	11/19/2013			Pending	LOCALLY CACHING DATA FROM A SHARED STORAGE
MP5040	MP5040	US	14/084,560	11/19/2013	9144042	9/22/2015	In Force	METHOD FOR FAST AUTOMATIC POWER CONTROL OF WIRELESS TRA
MP5040T	MP5040TW	TW	102142489	11/21/2013	1602460	10/11/2017	In Force	METHOD FOR FAST AUTOMATIC POWER CONTROL OF WIRELESS TRA
MP5040W	MP5040WO	WO	PCT/US2013/07	11/19/2013			Pending	METHOD FOR FAST AUTOMATIC POWER CONTROL OF WIRELESS TRA
MP5041	MP5041	US	14/090,368	11/26/2013	9319351	4/19/2016	In Force	Mechanism For Wire-Speed Stateful Packet Inspection In Packet Proc
MP5042	MP5042	US	14/012,878	8/28/2013	9223327	12/29/2015	In Force	UNIVERSAL ADAPTIVE VOLTAGE SCALING SYSTEM
MP5042	MP5042C1	US	14/954,833	11/30/2015	9958884	5/1/2018	In Force	UNIVERSAL ADAPTIVE VOLTAGE SCALING SYSTEM
MP5043	MP5043	US	14/086,154	11/21/2013	9450041	9/20/2016	In Force	Stackable High-Density Metal-Oxide-Metal Capacitor With Minimum
MP5043W	MP5043WO	WO	PCT/US2013/07	11/22/2013			Pending	Stackable High-Density Metal-Oxide-Metal Capacitor With Minimum
MP5043W	MP5043WOCN	CN	201380062002	11/22/2013	ZL2013800620	6/1/2018	In Force	STACKABLE HIGH-DENSITY METAL-OXIDE-METAL CAPACITOR WITH
MP5043W	MP5043WOKR	KR	1020157016772	11/22/2013			Pending	Stackable High-Density Metal-Oxide-Metal Capacitor With Minimum
MP5044	MP5044	US	14/091,226	11/26/2013	9619402	4/11/2017	In Force	Method and Apparatus for Optimizing Translation of a Virtual Memor
MP5045	MP5045	US	14/090,995	11/26/2013	9294418	3/22/2016	In Force	64B/66B CODEC FOR ETHERNET APPLICATIONS
MP5051	MP5051	US	14/148,853	1/7/2014	9571998	2/14/2017	In Force	System and Method for Asynchronous Event Reporting
MP5051	MP5051CN	CN	201410035380	1/15/2014	ZL2014100353	9/21/2018	In Force	SYSTEM AND METHOD FOR ASYNCHRONOUS EVENT REPORTING
MP5052	MP5052	US	14/092,959	11/28/2013	9386485	7/5/2016	In Force	ADAPTIVE RE-ESTABLISHMENT OF DATA SESSIONS
MP5052E	MP5052EP	EP	EP13195664.4	11/29/2013			Pending	Adaptively Selecting a Call Re-Establishment Mode Based on Given Lo
MP5054	MP5054	US	14/088,740	11/25/2013	9531434	12/27/2016	In Force	METHOD AND APPARATUS FOR INCREASING NETWORK CONNECTIVI

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MP5057	MP5057	US	14/092,961	11/28/2013	9036744	5/19/2015	In Force	ENHANCED RECEPTION OF PRECODING VECTOR SWITCHING (PVS)
MP5057W	MP5057WOCN	CN	201380053510.1	11/28/2013	ZL2013800535	10/23/2018	In Force	ENHANCED RECEPTION METHOD AND APPARATUS OF PRECODING V
MP5059	MP5059	US	14/092,307	11/27/2013	9350587	5/24/2016	In Force	SYSTEM AND METHOD FOR TIMING ERROR ESTIMATION
MP5064	MP5064	US	14/177,804	2/11/2014	9564186	2/7/2017	In Force	METHOD AND APPARATUS FOR MEMORY ACCESS
MP5065	MP5065	US	14/102,922	12/11/2013	9112133	8/13/2015	In Force	Resistive Random Access Memory And Method For Controlling Manu
MP5065	MP5065CN	CN	201310680704.1	12/12/2013	ZL2013106807	7/6/2018	In Force	RESISTIVE RANDOM ACCESS MEMORY AND METHOD FOR CONTROLL
MP5065T	MP5065TW	TW	102146176	12/13/2013	1620357	4/1/2018	In Force	Resistive Random Access Memory And Method For Controlling Manu
MP5066	MP5066	US	14/186,587	2/21/2014	9361040	6/7/2016	In Force	Systems and Methods for Data Storage Management
MP5068	MP5068	US	14/159,392	1/20/2014	9343957	5/17/2016	In Force	MULTI-CONVERTER SYSTEM INCLUDING A POWER DISTRIBUTION BA
MP5070W	MP5070WOCN	CN	201380066702.1	12/10/2013			Pending	SYSTEMS AND METHODS FOR ADAPTIVE SCALING OF DIGITAL IMAGE
MP5075	MP5075	US	14/108,897	12/17/2013	9668877	3/28/2017	In Force	NETWORK DISCOVERY APPARATUS
MP5075	MP5075C1	US	15/455,799	3/10/2017	1028464	5/7/2019	In Force	NETWORK DISCOVERY APPARATUS
MP5075W	MP5075WO	WO	PCT/US2013/07	12/17/2013			Pending	NETWORK DISCOVERY APPARATUS
MP5075W	MP5075WOCN	CN	201380065925.1	12/17/2013	ZL2013800659	9/18/2018	In Force	NETWORK DISCOVERY APPARATUS
MP5076	MP5076	US	14/109,246	12/17/2013	9337987	5/10/2016	In Force	AUTONOMOUS DENIAL OF TRANSMISSION IN DEVICE WITH COEXISTI
MP5077	MP5077	US	14/097,311	12/5/2013	9304693	4/5/2016	In Force	SYSTEM AND METHOD FOR WRITING DATA TO A DATA STORAGE STR
MP5080	MP5080	US	14/108,382	12/17/2013	9232431	1/5/2016	In Force	SELECTIVE LAYER-2 FLUSHING IN MOBILE COMMUNICATION TERMIN
MP5081W	MP5081WO	WO	PCT/IB2013/003	12/20/2013			Pending	CENTRALIZED MEMORY SHARING IN A MULTI-PROCESSING UNIT SW
MP5081W	MP5081WOCN	CN	201380066903.1	12/20/2013			Pending	MEMORY SHARING IN A NETWORK DEVICE
MP5082	MP5082	US	14/103,803	12/11/2013	9264028	2/16/2016	In Force	ZERO CROSSING DETECTOR USING CURRENT MODE APPROACH
MP5082T	MP5082TW	TW	102146170	12/13/2013	1599177	9/11/2017	In Force	ZERO CROSSING DETECTOR USING CURRENT MODE APPROACH
MP5082W	MP5082WO	WO	PCT/IB2013/002	12/12/2013			Pending	ZERO CROSSING DETECTOR USING CURRENT MODE APPROACH
MP5086	MP5086	US	14/254,374	4/16/2014	9166505	10/20/2015	In Force	Protection Circuits for Motors
MP5088	MP5088	US	14/137,519	12/20/2013	9652246	5/16/2017	In Force	BANKED PHYSICAL REGISTER DATA FLOW ARCHITECTURE IN OUT-OF-
MP5088	MP508811	US	15/051,465	2/23/2016	10607521	6/26/2018	In Force	BANKED PHYSICAL REGISTER DATA FLOW ARCHITECTURE IN OUT-OF-
MP5089	MP5089	US	14/247,685	4/8/2014	9319179	4/19/2016	In Force	Methods and Apparatus for Generating Expanded Code Words to Sup
MP5090	MP5090	US	14/215,141	3/17/2014	9647875	5/9/2017	In Force	BLOOM FILTERS WITH VARIABLE HASH
MP5092	MP5092	US	14/210,910	3/14/2014	9264925	2/16/2016	In Force	SYSTEMS AND METHODS FOR LTE INTERFERENCE DETECTION
MP5092W	MP5092WO	WO	PCT/US2014/02	3/14/2014			Pending	SYSTEMS AND METHODS FOR LTE INTERFERENCE DETECTION
MP5093	MP5093	US	14/218,876	3/18/2014	9420635	8/16/2016	In Force	IN-DEVICE COEXISTENCE OF WIRELESS COMMUNICATION TECHNOLO
MP5093	MP5093C1	US	15/226,735	8/15/2016	9565582	2/7/2017	In Force	IN-DEVICE COEXISTENCE OF WIRELESS COMMUNICATION TECHNOLO
MP5093	MP5093WOEPDE	DE	EP147222052.9	3/18/2014	602014054201	9/25/2019	In Force	IN-DEVICE COEXISTENCE OF WIRELESS COMMUNICATION TECHNOLO
MP5093	MP5093WOEPFR	FR	EP147222052.9	3/18/2014	2976849	9/25/2019	In Force	IN-DEVICE COEXISTENCE OF WIRELESS COMMUNICATION TECHNOLO
MP5093	MP5093WOEPGB	GB	EP147222052.9	3/18/2014	2976849	9/25/2019	In Force	IN-DEVICE COEXISTENCE OF WIRELESS COMMUNICATION TECHNOLO

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MP5093W	MP5093WED1	EP	19199282.5	3/18/2014			Pending	IN-DEVICE COEXISTENCE OF WIRELESS COMMUNICATION TECHNOLO
MP5093W	MP5093WO	WO	PCT/US2014/03	3/18/2014			Pending	IN-DEVICE COEXISTENCE OF WIRELESS COMMUNICATION TECHNOLO
MP5093W	MP5093WOCN	CN	201480025283.1	3/18/2014	ZL2014800252	9/13/2019	In Force	METHOD AND APPARATUS FOR WIRELESS COMMUNICATION IN MUL
MP5093W	MP5093WOEP	EP	EP14722052.9	3/18/2014	2976849	9/25/2019	In Force	IN-DEVICE COEXISTENCE OF WIRELESS COMMUNICATION TECHNOLO
MP5093W	MP5093WOJP	JP	2016-504341	3/18/2014	JP6478125	3/6/2019	In Force	IN-DEVICE COEXISTENCE OF WIRELESS COMMUNICATION TECHNOLO
MP5093W	MP5093WOKR	KR	10-2015-702996	3/18/2014			Pending	IN-DEVICE COEXISTENCE OF WIRELESS COMMUNICATION TECHNOLO
MP5094	MP5094	US	14/210,893	3/14/2014	9765985	7/11/2017	In Force	SYSTEMS AND METHODS FOR CROSS PROTOCOL AUTOMATIC SUB-C
MP5096	MP5096	US	14/220,936	3/20/2014	9407460	8/2/2016	In Force	CUT-THROUGH PROCESSING FOR SLOW AND FAST PORTS
MP5096	MP5096D1	US	14/221,006	3/20/2014	9407461	8/2/2016	In Force	CUT-THROUGH PROCESSING FOR SLOW AND FAST PORTS
MP5096W	MP5096WO	WO	PCT/US2014/03	3/20/2014			Pending	CUT-THROUGH PROCESSING FOR SLOW AND FAST PORTS
MP5096W	MP5096WOCN	CN	201480023788.1	3/20/2014			Pending	CUT-THROUGH PROCESSING FOR SLOW AND FAST PORTS
MP5097	MP5097	US	14/223,795	3/24/2014	9847937	12/19/2017	In Force	Hardware Acceleration for Routing Programs
MP5097W	MP5097WO	WO	PCT/US2014/03	3/24/2014			Pending	Hardware Acceleration for Routing Programs
MP5098	MP5098	US	14/197,426	3/5/2014	9323611	4/26/2016	In Force	SYSTEMS AND METHODS FOR MULTI-STAGE SOFT INPUT DECODING
MP5098W	MP5098WO	WO	PCT/US2014/02	3/5/2014			Pending	SYSTEMS AND METHODS FOR MULTI-STAGE SOFT INPUT DECODING
MP5098W	MP5098WOCN	CN	201480016609.1	3/5/2014	ZL2014800166	12/25/2018	In Force	SYSTEMS AND METHODS FOR MULTI-STAGE SOFT INPUT DECODING
MP5098W	MP5098WOJP	JP	2016-504300	8/17/2015	JP6451955	12/28/2018	In Force	SYSTEMS AND METHODS FOR MULTI-STAGE SOFT INPUT DECODING
MP5098W	MP5098WOKR	KR	10-2015-702996	3/5/2014			Pending	SYSTEMS AND METHODS FOR MULTI-STAGE SOFT INPUT DECODING
MP5101	MP5101CN	CN	201410043698.1	1/28/2014			Pending	MULTI-MODE MULTI-CARD MOBILE TERMINAL AND SERVICE CONFLI
MP5101T	MP5101TW	TW	103109302	5/5/2014			Pending	MULTI-MODE MULTI-CARD MOBILE TERMINAL, SERVICE CONFLICT R
MP5103T	MP5103TW	TW	103110729	3/21/2014			Pending	Guided Filter Based Detail Enhancement
MP5104	MP5104	US	14/227,400	3/27/2014	9805508	10/31/2017	In Force	Active Augmented Reality Display Enhancement
MP5108	MP5108CN	CN	201410589413.1	10/28/2014	ZL2014105894	1/18/2019	In Force	CIRCUIT, SYSTEM, AND METHOD FOR POWER MANAGEMENT
MP5109	MP5109	US	14/243,317	4/2/2014	9647725	5/9/2017	In Force	WIRELESS POWER INTERFACE USING AN NFC ANTENNA
MP5114	MP5114	US	14/228,597	3/28/2014	9467171	10/11/2016	In Force	SYSTEMS AND METHODS FOR ON-DEMAND EXCHANGE OF EXTRINSI
MP5114	MP5114C1	US	15/288,128	10/7/2016	10158378	12/18/2018	In Force	SYSTEMS AND METHODS FOR ON-DEMAND EXCHANGE OF EXTRINSI
MP5115	MP5115	US	14/248,758	4/9/2014	9088327	7/21/2015	In Force	SYSTEMS AND METHODS FOR CHANNEL QUALITY INDICATOR (CQI) S
MP5115W	MP5115WO	WO	PCT/US2014/03	4/9/2014			Pending	SYSTEMS AND METHODS OF CHANNEL QUALITY INDICATOR (CQI) SEL
MP5116	MP5116	US	14/253,467	4/15/2014	9642110	5/2/2017	In Force	GPS AND WLAN HYBRID POSITION DETERMINATION
MP5116W	MP5116WO	WO	PCT/US2014/03	4/17/2014			Pending	GPS AND WLAN HYBRID POSITION DETERMINATION
MP5116W	MP5116WOCN	CN	201480025813.1	4/17/2014	ZL2014800258	7/9/2019	In Force	GPS AND WLAN HYBRID POSITION DETERMINATION
MP5118	MP5118	US	14/248,527	4/9/2014	9116836	8/25/2015	In Force	Tunneling Transaction Packets
MP5118W	MP5118WO	WO	PCT/US2014/03	4/9/2014			Pending	Tunneling Transaction Packets
MP5121	MP5121	US	14/242,678	4/1/2014	9941982	4/10/2018	In Force	METHOD AND APPARATUS FOR TESTING THE BEAMFORMING PERFO

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MP5121	MP5121CN	CN	201410143566.1	4/9/2014	ZL2014101433	1/18/2019	In Force	METHOD AND APPARATUS FOR TESTING THE BEAMFORMING PERFO
MP5123	MP5123	US	14/245,266	4/4/2014	10095765	10/9/2018	In Force	Method and Apparatus for a Hardware-implemented AVL Tree Modu
MP5124	MP5124	US	14/245,283	4/4/2014	9507813	11/29/2016	In Force	Method and Apparatus for Hardware-implemented AVL Tree Update
MP5124	MP5124C1	US	15/295,143	10/17/2016	9836491	12/5/2017	In Force	Method and Apparatus for Hardware-implemented AVL Tree Update
MP5125W	MP5125WO	WO	PCT/US2014/03	4/11/2014			Pending	LIGHT EMITTING DIODE RECEPTACLE
MP5125W	MP5125WOJP	JP	2016-507687	9/30/2015			Pending	LIGHT EMITTING DIODE RECEPTACLE
MP5127	MP5127	US	14/246,854	4/7/2014	9250992	2/2/2016	In Force	TEST DATA REPORTING DURING MEMORY TESTING
MP5130	MP5130	US	14/186,909	2/21/2014	9357521	5/31/2016	In Force	DETERMINING AN ESTIMATED LOCATION OF A BASE STATION
MP5130	MP5130CN	CN	201410070083.1	2/27/2014	ZL2014100700	3/12/2019	In Force	DETERMINING AN ESTIMATED LOCATION OF A BASE STATION
MP5133	MP5133	US	14/140,295	12/24/2013	9430339	8/30/2016	In Force	METHOD AND APPARATUS FOR USING WEAR-OUT BLOCKS IN NONV
MP5134	MP5134	US	14/138,775	12/23/2013	9557382	1/31/2017	In Force	INTER-BLOCK SCAN TESTING WITH SHARE PADS
MP5135	MP5135	US	14/147,540	1/5/2014	9264938	2/16/2016	In Force	ENHANCED BUFFERING OF SOFT DECODING METRICS
MP5135W	MP5135WOCN	CN	201480002971.1	1/2/2014	ZL2014800029	8/2/2019	In Force	A METHOD FOR COMMUNICATIONS, A TELECOMMUNICATION APPA
MP5135W	MP5135WOEP	EP	EP14735083.9	1/2/2014			Pending	Enhanced Buffering of Soft Decoding Metrics
MP5136	MP5136	US	14/137,081	12/20/2013	9281852	3/8/2016	In Force	METHOD AND APPARATUS FOR CALIBRATING TIME ALIGNMENT
MP5136	MP5136WO	WO	PCT/US2013/07	12/20/2013			Pending	METHOD AND APPARATUS FOR CALIBRATING TIME ALIGNMENT
MP5139	MP5139	US	14/147,056	1/3/2014	9606888	3/28/2017	In Force	Hierarchical Multi-Core Debugger Interface
MP5141	MP5141	US	14/147,370	1/3/2014	9292246	3/22/2016	In Force	METHOD AND APPARATUS FOR REMOTELY RENDERING A FILE IN A F
MP5149	MP5149	US	14/147,087	1/3/2014	9575768	2/21/2017	In Force	Loading Boot Code from Multiple Memories
MP5153	MP5153	US	14/152,863	1/10/2014	9165159	10/20/2015	In Force	ENCRYPTION BASED ON TOUCH GESTURE
MP5158	MP5158WO	WO	PCT/US2014/01	1/17/2014			Pending	METHOD AND SYSTEM FOR PERFORMING IMAGE PROCESSING ON A
MP5163	MP5163	US	14/153,526	1/13/2014	9298627	3/29/2016	In Force	SHARED OP-SYMMETRIC UPDATE-SENSITIVE VARIABLES
MP5163	MP5163CN	CN	201410016223.1	1/14/2014	ZL2014100162	6/5/2018	In Force	SHARED OP-SYMMETRIC UPDATE-SENSITIVE VARIABLES
MP5163	MP5163D1	US	14/154,698	1/14/2014	9298628	3/29/2016	In Force	SHARED OP-SYMMETRIC UPDATE-SENSITIVE VARIABLES
MP5165	MP5165C1	US	15/453,262	3/8/2017	10230542	3/12/2019	In Force	INTERCONNECTED RING NETWORK IN A MULTI-PROCESSOR SYSTEM
MP5165	MP5165CN	CN	201410102655.1	1/16/2014	ZL2014101026	7/13/2018	In Force	INTERCONNECTED RING NETWORK IN A MULTI-PROCESSOR SYSTEM
MP5165	MP5165D2	US	14/161,362	1/22/2014	9521011	12/13/2016	In Force	INTERCONNECTED RING NETWORK IN A MULTI-PROCESSOR SYSTEM
MP5165	MP5165D3	US	14/161,397	1/22/2014	9654480	9/27/2016	In Force	INTERCONNECTED RING NETWORK IN A MULTI-PROCESSOR SYSTEM
MP5169	MP5169D5GN	US	29/480,168	1/23/2014	0755790	5/10/2016	In Force	An IN LINE SMART PRINT MODULE
MP5172	MP5172	US	14/157,832	1/17/2014	9727291	8/8/2017	In Force	APPARATUS FOR INTERFACING WIRELESS DEVICES TO A PERIPHERAL
MP5176	MP5176CN	CN	201410091057.1	3/10/2014			Pending	DATA COMMUNICATION METHOD, DEVICE THEREOF, AND MULTIM
MP5184	MP5184	US	14/165,844	1/28/2014	9575886	2/21/2017	In Force	METHODS AND APPARATUS FOR STORING DATA TO A SOLID STATE S
MP5184	MP5184C1	US	15/416,643	1/26/2017	10157022	12/18/2018	In Force	METHODS AND APPARATUS FOR STORING DATA TO A SOLID STATE S
MP5184	MP5184WO	WO	PCT/US2014/01	1/28/2014			Pending	METHODS AND APPARATUS FOR STORING DATA TO A SOLID STATE S

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MP5184	MP5184WOCN	CN	201480006405	1/28/2014	ZL2014800064	2/9/2018	In Force	METHODS AND APPARATUS FOR STORING DATA TO A SOLID STATE S
MP5185	MP5185	US	14/161,354	1/22/2014	9294133	3/22/2016	In Force	METHOD AND APPARATUS FOR ERROR CORRECTION
MP5186	MP5186	US	14/166,609	1/28/2014	9629202	4/18/2017	In Force	IN-DEVICE COEXISTENCE OF MULTIPLE WIRELESS COMMUNICATION
MP5186	MP5186CN	CN	2014100439461	1/29/2014	ZL2013800657	7/9/2019	In Force	IN-DEVICE COEXISTENCE OF MULTIPLE WIRELESS COMMUNICATION
MP5191	MP5191	US	14/187,146	2/21/2014	9323957	4/26/2016	In Force	ANTI-TAMPER SYSTEM BASED ON DUAL RANDOM BITS GENERATORS
MP5191	MP5191WO	WO	PCT/182014/000	2/24/2014			Pending	ANTI-TAMPER SYSTEM BASED ON DUAL RANDOM BITS GENERATOR
MP5191	MP5191WOCN	CN	2014800083294	2/24/2014	ZL2014800083	2/23/2018	In Force	ANTI-TAMPER SYSTEM BASED ON DUAL RANDOM BITS GENERATOR
MP5191	MP5191WOEP	EP	EP14710631.4	2/24/2014	2962534	4/10/2019	In Force	ANTI-TAMPER SYSTEM BASED ON DUAL RANDOM BITS GENERATOR
MP5191	MP5191WOEPDE	DE	EP14710631.4	2/24/2014	602014044405	4/10/2019	In Force	ANTI-TAMPER SYSTEM BASED ON DUAL RANDOM BITS GENERATOR
MP5191	MP5191WOEPFR	FR	EP14710631.4	2/24/2014	2962534	4/10/2019	In Force	ANTI-TAMPER SYSTEM BASED ON DUAL RANDOM BITS GENERATORS
MP5191	MP5191WOEPGB	GB	EP14710631.4	2/24/2014	2962534	4/10/2019	In Force	ANTI-TAMPER SYSTEM BASED ON DUAL RANDOM BITS GENERATORS
MP5191	MP5191WOJP	JP	2015-559566	2/24/2014	JP6494531	3/15/2019	In Force	ANTI-TAMPER SYSTEM BASED ON DUAL RANDOM BITS GENERATORS
MP5191	MP5191WOKR	KR	10-2015-702700	2/24/2014			Pending	ANTI-TAMPER SYSTEM BASED ON DUAL RANDOM BITS GENERATORS
MP5191T	MP5191TW	TW	103106730	2/27/2014	1604337	11/1/2017	In Force	ANTI-TAMPER SYSTEM BASED ON DUAL RANDOM BITS GENERATORS
MP5192	MP5192	US	14/170,027	1/31/2014	9232493	1/5/2016	In Force	FREQUENCY OFFSET COMPENSATION FOR WIFI RANGING
MP5192	MP5192C1	US	14/987,264	1/4/2016	9955446	4/24/2018	In Force	FREQUENCY OFFSET COMPENSATION FOR WIFI RANGING
MP5192	MP5192C2	US	15/958,319	4/20/2018	10455534	10/22/2019	In Force	FREQUENCY OFFSET COMPENSATION FOR WIFI RANGING
MP5192	MP5192WO	WO	PCT/US2014/01	1/31/2014			Pending	FREQUENCY OFFSET COMPENSATION FOR WIFI RANGING
MP5192	MP5192WOCN	CN	2014800121934	1/31/2014	ZL2014800121	5/3/2019	In Force	METHOD AND COMMUNICATION DEVICE FOR CLOCK COMPENSATIO
MP5192	MP5192WOEP	EP	EP14707506.3	1/31/2014	EP2952046	1/25/2017	In Force	FREQUENCY OFFSET COMPENSATION FOR WIFI RANGING
MP5192	MP5192WOEPDE	DE	14707506.3	1/31/2014	602014006355	1/25/2017	In Force	FREQUENCY OFFSET COMPENSATION FOR WIFI RANGING
MP5192	MP5192WOEPFR	FR	14707506.3	1/31/2014	2952046	1/25/2017	In Force	FREQUENCY OFFSET COMPENSATION FOR WIFI RANGING
MP5192	MP5192WOEPGB	GB	14707506.3	1/31/2014	2952046	1/25/2017	In Force	FREQUENCY OFFSET COMPENSATION FOR WIFI RANGING
MP5192	MP5192WOJP	JP	2015-556173	1/31/2014	JP6409247	10/24/2018	In Force	FREQUENCY OFFSET COMPENSATION FOR WIFI RANGING
MP5192	MP5192WOKR	KR	10-2015-702101	1/31/2014			Pending	FREQUENCY OFFSET COMPENSATION FOR WIFI RANGING
MP5195	MP5195	US	14/173,563	2/5/2014	9543995	1/10/2017	In Force	ANALOG FRONT END RECEIVERS WITH RESONANT MIXER CONFIGUR
MP5198	MP5198	US	14/170,416	1/31/2014	8947139	2/3/2015	In Force	APPARATUS FOR DOUBLING THE DYNAMIC RANGE OF A TIME TO DIG
MP5204	MP5204	US	14/174,590	2/6/2014	9141321	9/22/2015	In Force	Configurable Printer Server Device
MP5205	MP5205D5GN	US	29/481,859	2/11/2014	0755184	5/3/2016	In Force	AN IN LINE SMART PRINT MODULE INCLUDING A DISPLAY
MP5206	MP5206	US	14/178,145	2/11/2014	9299415	3/29/2016	In Force	METHOD AND APPARATUS FOR AUTOMATIC TRANSITION OF A VOLA
MP5209	MP5209WO	WO	PCT/US2014/01	2/21/2014			Pending	PACKAGE-ON-PACKAGE STRUCTURES
MP5209	MP5209WOCN	CN	201480009611	8/20/2015			Pending	PACKAGE-ON-PACKAGE STRUCTURES
MP5209	MP5209WOKR	KR	10-2015-702217	8/17/2015			Pending	Ball pad structure on top side of the bottom FCP
MP5209T	MP5209TW	TW	103105914	2/21/2014			Pending	Ball pad structure on top side of the bottom FCP

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MP5211	MP5211	US	14/174,449	2/6/2014	9377957	6/28/2016	In Force	METHOD AND APPARATUS FOR LATENCY REDUCTION
MP5211	MP5211WO	WO	PCT/US2014/01	2/7/2014			Pending	METHOD AND APPARATUS FOR LATENCY REDUCTION
MP5211T	MP5211TW	TW	103104134	2/7/2014	620159	4/1/2018	In Force	METHOD AND APPARATUS FOR LATENCY REDUCTION
MP5216	MP5216	US	14/172,420	2/4/2014	9385753	7/5/2016	In Force	SYSTEMS AND METHODS FOR BIT FLIPPING DECODING WITH RELIABI
MP5216	MP5216WO	WO	PCT/US2014/01	2/4/2014			Pending	SYSTEMS AND METHODS FOR BIT FLIPPING DECODING WITH RELIABI
MP5216	MP5216WOCN	CN	2014800087774	2/4/2014	ZL2014800087	11/30/2018	In Force	BIT FLIPPING DECODING WITH RELIABILITY INPUTS FOR LDPC CODES
MP5218	MP5218	US	14/184,255	2/19/2014	9876862	1/23/2018	In Force	SERVICE IDENTIFICATION WITH REDUCED AMBIGUITY
MP5219	MP5219	US	14/184,004	2/19/2014	9450891	9/20/2016	In Force	System And Method For Enabling G.hn Nodes To Support 1905.1 Rel
MP5219	MP5219WO	WO	PCT/US2014/01	2/20/2014			Pending	System And Method For Enabling G.hn Nodes To Support 1905.1 Rel
MP5219	MP5219WOCN	CN	2014800082024	2/20/2014	ZL2014800082	9/14/2018	In Force	SYSTEM AND METHOD FOR ENABLING G.hn NODES TO SUPPORT 19
MP5219	MP5219WOKR	KR	1020157025846	2/20/2014			Pending	System And Method For Enabling G.hn Nodes To Support 1905.1 Rel
MP5220	MP5220	US	14/184,732	2/20/2014	9543831	1/10/2017	In Force	CONFIGURABLE DC-DC CONVERTER
MP5220	MP5220CN	CN	2014100598074	2/20/2014	ZL2014100598	9/28/2018	In Force	CONFIGURABLE DC-DC CONVERTER
MP5221	MP5221WOCN	CN	2014800083453	2/20/2014			Pending	EP/LMN TRANSITION MECHANISM FOR MANUAL NETWORK REGISTR
MP5221	MP5221WCEP	EP	EP14753504.1	2/20/2014			Pending	EP/LMN Transition Mechanism for Manual Network Registration
MP5222	MP5222	US	14/184,803	2/20/2014	9436633	9/6/2016	In Force	MULTI-SLOT MULTI-POINT AUDIO INTERFACE
MP5222	MP5222WOCN	CN	2014800078941	2/20/2014	ZL2014800078	12/1/2017	In Force	MULTI-SLOT MULTI-POINT AUDIO INTERFACE
MP5224	MP5224	US	14/186,997	2/21/2014	8842571	9/23/2014	In Force	METHOD AND APPARATUS FOR DETERMINING A TIME OF ARRIVAL O
MP5225	MP5225	US	14/180,030	2/13/2014	9880856	1/30/2018	In Force	Patching Boot Code of Read-Only Memory
MP5225	MP5225WO	WO	PCT/US2014/01	2/13/2014			Pending	Patching Boot Code of Read-Only Memory
MP5225	MP5225WOCN	CN	2014800083171	2/13/2014			Pending	PATCHING BOOT CODE OF READ-ONLY MEMORY
MP5225	MP5225WCEP	EP	14708381.0	2/13/2014			Pending	PATCHING BOOT CODE OF READ-ONLY MEMORY
MP5225	MP5225WOJP	JP	2015-558880	2/13/2014	6318425	5/9/2018	In Force	PATCHING BOOT CODE OF READ-ONLY MEMORY
MP5225	MP5225WOKR	KR	1020157025232	2/13/2014			Pending	Patching Boot Code of Read-Only Memory
MP5225T	MP5225TW	TW	103104936	2/14/2014			Pending	Patching Boot Code of Read-Only Memory
MP5226	MP5226	US	14/176,433	2/10/2014	9425684	8/23/2016	In Force	REDUCED NOISE POWER CONVERTER USING NOVEL SHIELD
MP5226	MP5226WO	WO	PCT/US2014/01	2/11/2014			Pending	REDUCED NOISE POWER CONVERTER USING NOVEL SHIELD
MP5226	MP5226WOCN	CN	2014800075794	2/11/2014	ZL2014800075	5/18/2018	In Force	REDUCED NOISE BUCK/BOOST CONVERTER USING TRANSFORMER C
MP5226	MP5226WCEP	EP	EP14706455.4	2/11/2014			Pending	Reduced Noise Power Converter Using Novel Shield
MP5226	MP5226WOJP	JP	2015-558869	2/11/2014	6418402	11/7/2018	In Force	REDUCED NOISE POWER CONVERTER USING NOVEL SHIELD
MP5226	MP5226WOKR	KR	10-2015-702143	2/11/2014			Pending	REDUCED NOISE POWER CONVERTER USING NOVEL SHIELD
MP5226T	MP5226TW	TW	103104628	2/12/2014	631863	8/1/2018	In Force	REDUCED NOISE POWER CONVERTER USING NOVEL SHIELD AND PO
MP5230	MP5230	US	14/189,775	2/25/2014	9342119	5/17/2016	In Force	METHOD AND APPARATUS FOR DISSIPATING HEAT IN A MOBILE DEVI
MP5235	MP5235	US	14/192,579	2/27/2014	9819637	11/14/2017	In Force	EFFICIENT LONGEST PREFIX MATCHING TECHNIQUES FOR NETWORK

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MP5235	MP5235WO	WO	PCT/US2014/01	2/27/2014			Pending	EFFICIENT LONGEST PREFIX MATCHING TECHNIQUES FOR NETWORK
MP5235	MP5235WOCN	CN	201480021019.1	2/27/2014	ZL2014800210	6/28/2019	In Force	EFFICIENT LONGEST PREFIX MATCHING TECHNIQUES FOR NETWORK
MP5238	MP5238	US	14/248,638	4/9/2014	9747146	8/29/2017	In Force	METHOD AND SYSTEM FOR MEMORY ACCESS PROTECTION
MP5238	MP5238CN	CN	201410204214.1	5/14/2014	ZL201410204	6/25/2019	In Force	METHOD AND SYSTEM FOR MEMORY ACCESS PROTECTION
MP5241	MP5241C1	US	14/617,048	2/9/2015	9553512	1/24/2017	In Force	Method And Apparatus For Providing A Regulated Output Voltage Via
MP5244	MP5244	US	14/194,741	3/2/2014	9521670	12/13/2016	In Force	SIGNAL DECODING IN THE PRESENCE OF ALMOST-BLANK SUBFRAME
MP5244	MP5244CN	CN	201410079365.1	3/5/2014	ZL2014100793	2/22/2019	In Force	A METHOD, APPARATUS AND MOBILE COMMUNICATION TERMINAL
MP5244E	MP5244EP	EP	EP14157861.7	3/5/2014	2775772	11/21/2018	In Force	Signal Decoding in the Presence of Almost-Blank Subframes (ABS)
MP5244E	MP5244EPDE	DE	EP14157861.7	3/5/2014	2775772	11/21/2018	In Force	Signal Decoding in the Presence of Almost-Blank Subframes (ABS)
MP5244E	MP5244EPRR	FR	EP14157861.7	3/5/2014	2775772	11/21/2018	In Force	Signal Decoding in the Presence of Almost-Blank Subframes (ABS)
MP5244E	MP5244EPGB	GB	EP14157861.7	3/5/2014	2775772	11/21/2018	In Force	Signal Decoding in the Presence of Almost-Blank Subframes (ABS)
MP5245	MP5245	US	14/195,176	3/3/2014	9047599	6/2/2015	In Force	Method And Apparatus For Wirelessly Processing A Financial Transac
MP5246	MP5246	US	14/198,994	3/6/2014	9363249	6/7/2016	In Force	SECURE SIMPLE ENROLLMENT
MP5246	MP5246D1	US	15/174,303	6/6/2016	10027664	7/17/2018	In Force	SECURE SIMPLE ENROLLMENT
MP5246	MP5246WO	WO	PCT/US2014/02	3/6/2014			Pending	SECURE SIMPLE ENROLLMENT
MP5248	MP5248CN	CN	201410149538.1	4/8/2014	ZL2014101495	4/19/2019	In Force	A METHOD, DEVICE AND APPARATUS FOR RESOLVING CONFLICT IN
MP5249	MP5249	US	14/197,408	3/5/2014	9369152	6/14/2016	In Force	SYSTEMS AND METHODS FOR DECODING WITH LATE RELIABILITY INF
MP5249	MP5249WO	WO	PCT/US2014/02	3/5/2014			Pending	SYSTEMS AND METHODS FOR DECODING WITH LATE RELIABILITY INF
MP5249	MP5249WOCN	CN	201480011709.1	3/5/2014	ZL2014800117	3/22/2019	In Force	SYSTEMS AND METHODS FOR DECODING WITH LATE RELIABILITY INF
MP5249	MP5249WOJP	JP	2015-561611	3/5/2014	6446730	1/9/2019	In Force	SYSTEMS AND METHODS FOR DECODING WITH LATE RELIABILITY INF
MP5249	MP5249WOKR	KR	10-2015-70265C	3/5/2014			Pending	SYSTEMS AND METHODS FOR DECODING WITH LATE RELIABILITY INF
MP5254	MP5254	US	14/205,966	3/12/2014	9606150	3/28/2017	In Force	SENSING ARRANGEMENT FOR SENSING A VOLTAGE FROM MULTIPLE
MP5256	MP5256	US	14/202,041	3/10/2014	9367385	6/14/2016	In Force	High Speed Serial Data Receiver Architecture With Dual Error Compar
MP5256	MP5256C1	US	15/176,492	6/8/2016	9985804	5/29/2018	In Force	Systems And Methods For Processing Errors Of A Received Signal
MP5256	MP5256CN	CN	201410088861.1	3/11/2014	ZL2014100888	6/1/2018	In Force	HIGH SPEED SERIAL DATA RECEIVER ARCHITECTURE WITH DUAL ERR
MP5257	MP5257	US	14/205,594	3/12/2014	9246668	1/26/2016	In Force	Unified Control for Digital Timing Recovery and Packet Processing
MP5258	MP5258	US	14/199,269	3/6/2014	8947823	2/3/2015	In Force	METHOD AND APPARATUS FOR DETECTING DRIVE HEAD TOUCHDO
MP5259	MP5259	US	14/205,712	3/12/2014	9262318	2/16/2016	In Force	Serial Flash XIP With Caching Mechanism For Fast Program Execution
MP5260	MP5260	US	14/204,590	3/11/2014	9379738	6/28/2016	In Force	SYSTEMS AND METHODS FOR DECODING USING PARTIAL RELIABILITY
MP5260	MP5260CN	CN	201410093303.1	3/13/2014	ZL2014100933	9/18/2018	In Force	SYSTEMS AND METHODS FOR DECODING USING PARTIAL RELIABILITY
MP5261	MP5261	US	14/260,849	4/24/2014	9197998	11/24/2015	In Force	METHOD AND SYSTEM FOR POSITIONING WIRELESS DEVICE
MP5261	MP5261CN	CN	201410215898.1	5/15/2014			Pending	METHOD AND SYSTEM FOR POSITIONING WIRELESS DEVICE
MP5261	MP5261RE	US	15/967,449	4/6/2018			Pending	METHOD AND SYSTEM FOR POSITIONING WIRELESS DEVICE
MP5262	MP5262	US	14/210,644	3/14/2014	9350331	5/24/2016	In Force	High Speed Latch With Over Voltage Protection And Integrated Sum

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MP5262	MP5262WO	WO	PCT/US2014/02	3/14/2014			Pending	High Speed Latch With Over Voltage Protection And Integrated Sum
MP5268	MP5268	US	12/784,494	5/20/2010	8238492	8/7/2012	In Force	METHOD AND APPARATUS FOR PACKET DETECTION VIA CROSS-CORR
MP5269	MP5269	US	12/116,186	5/6/2008	8139698	3/20/2012	In Force	DUAL CORRELATION FRAME SYNCHRONIZATION SYSTEM AND METH
MP5271	MP5271	US	13/152,279	6/3/2011	8533992	11/26/2013	In Force	SYSTEM AND METHOD FOR THROUGHPUT ENHANCEMENT
MP5271	MP5271C1	US	14/091,295	11/26/2013	9071521	6/30/2015	In Force	SYSTEM AND METHOD FOR THROUGHPUT ENHANCEMENT
MP5272	MP5272	US	13/718,403	8/25/2011	8731080	5/20/2014	In Force	DEVICE AND METHOD FOR REDUCING MODULATION PROFILE MEMO
MP5272	MP5272C1	US	14/280,532	5/16/2014	9154345	10/6/2015	In Force	DEVICE AND METHOD FOR REDUCING MODULATION PROFILE MEMO
MP5277	MP5277	US	14/275,332	5/12/2014	10237147	3/19/2019	In Force	HEURISTIC NETWORK TRAFFIC CLASSIFICATION USING BYTE-DISTRIB
MP5277	MP5277CN	CN	201410201571.1	5/13/2014	ZL2014102015	7/9/2019	In Force	HEURISTIC NETWORK TRAFFIC CLASSIFICATION USING BYTE-DISTRIB
MP5285	MP5285	US	14/263,753	4/28/2014	9084029	7/28/2015	In Force	SYSTEMS AND METHODS FOR ORDERING CODEWORDS BASED ON P
MP5285	MP5285CN	CN	201410197156.1	5/4/2014	ZL2014101971	8/9/2019	In Force	SYSTEMS AND METHODS FOR ORDERING CODEWORDS BASED ON P
MP5286	MP5286	US	14/263,556	4/28/2014	9276618	3/1/2016	In Force	Systems and Methods for Sidelobe Cancellation
MP5287	MP5287	US	14/269,664	5/5/2014	9781018	10/3/2017	In Force	TIME EFFICIENT COUNTERS AND METERS ARCHITECTURE
MP5287	MP5287D1	US	15/722,601	10/2/2017	10333802	6/25/2019	In Force	TIME EFFICIENT COUNTERS AND METERS ARCHITECTURE
MP5287	MP5287WO	WO	PCT/IB2014/001	5/4/2014			Pending	TIME EFFICIENT COUNTERS AND METERS ARCHITECTURE
MP5287	MP5287WOCN	CN	201480034176.1	5/4/2014	ZL2014800341	3/12/2019	In Force	TIME EFFICIENT COUNTERS AND METERS ARCHITECTURE
MP5291	MP5291	US	14/247,556	4/8/2014	9485816	11/1/2016	In Force	METHOD AND APPARATUS FOR DIMMABLE LED DRIVER
MP5291	MP5291C1	US	15/335,691	10/27/2016	9669853	7/4/2017	In Force	METHOD AND APPARATUS FOR DIMMABLE LED DRIVER
MP5291	MP5291WO	WO	PCT/US2014/03	4/8/2014			Pending	METHOD AND APPARATUS FOR DIMMABLE LED DRIVER
MP5293	MP5293	US	14/282,804	5/20/2014	9729296	8/8/2017	In Force	NON-CONVEX OPTIMIZATION OF RESOURCE ALLOCATION IN MULTI-
MP5293	MP5293WO	WO	PCT/US2014/03	5/20/2014			Pending	NON-CONVEX OPTIMIZATION OF RESOURCE ALLOCATION IN MULTI-
MP5294	MP5294	US	14/331,502	7/15/2014	9405826	8/2/2016	In Force	Systems and Methods for Digital Signal Processing
MP5295	MP5295	US	14/469,189	8/26/2014	9578676	2/21/2017	In Force	SYSTEMS AND METHODS FOR REDUCING IN-DEVICE COEXISTENCE (I
MP5295	MP5295CN	CN	201410428176.1	8/27/2014	ZL2014104281	6/14/2019	In Force	SYSTEMS AND METHODS FOR REDUCING IN-DEVICE COEXISTENCE (I
MP5299	MP5299	US	14/274,722	5/11/2014			Pending	NETWORK TRAFFIC QUALITY-BASED DYNAMIC PATH CHANGE
MP5300	MP5300	US	14/274,504	5/9/2014	9444560	9/13/2016	In Force	METHOD AND APPARATUS FOR IQ IMBALANCE ESTIMATION
MP5301	MP5301	US	14/272,438	5/7/2014	9320097	4/19/2016	In Force	MULTI-STRING DIMMABLE LED DRIVER
MP5301	MP5301WO	WO	PCT/US2014/03	5/7/2014			Pending	MULTI-STRING DIMMABLE LED DRIVER
MP5301T	MP5301TW	TW	103116510	5/9/2014	1613930	2/1/2018	In Force	MULTI-STRING DIMMABLE LED DRIVER
MP5302	MP5302	US	14/271,698	5/7/2014	9397552	7/19/2016	In Force	Snubber Circuit To Increase Efficiency Of Power Converters
MP5302	MP5302WO	WO	PCT/US2014/03	5/8/2014			Pending	Snubber Circuit To Increase Efficiency Of Power Converters
MP5307	MP5307	US	14/267,307	5/1/2014	9467170	10/11/2016	In Force	NAND Flash Memory Systems With Efficient Soft Information Interfac
MP5307	MP5307CN	CN	201410219865.1	5/19/2014	ZL2014102198	4/19/2019	In Force	NAND FLASH MEMORY SYSTEMS WITH EFFICIENT SOFT INFORMATION
MP5308	MP5308	US	14/281,671	5/19/2014	9736801	8/15/2017	In Force	Methods and Apparatus for Synchronizing Devices in a Wireless Data

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MP5309	MP5309	US	14/281,693	5/19/2014	9521635	12/13/2016	In Force	Methods and Apparatus for Selecting a Device to Perform Shared Fun
MP5309	MP5309C1	US	15/362,657	11/28/2016	9860862	1/2/2018	In Force	Methods and Apparatus for Selecting a Device to Perform Shared Fun
MP5311	MP5311	US	11/024,052	12/29/2004	7081838	7/25/2006	In Force	16B/10S CODING APPARATUS AND METHOD
MP5312	MP5312	US	11/187,236	7/22/2005	8559443	10/15/2013	In Force	Efficient Message Switching In A Switching Apparatus
MP5312	MP5312C1	US	14/053,128	10/14/2013	9319352	4/19/2016	In Force	Efficient Message Switching In A Switching Apparatus
MP5313	MP5313	US	14/285,349	5/22/2014	9286155	3/15/2016	In Force	SYSTEMS AND METHODS FOR GENERATING SOFT INFORMATION IN A
MP5313	MP5313C1	US	15/066,330	3/10/2016	9912353	3/6/2018	In Force	SYSTEMS AND METHODS FOR GENERATING SOFT INFORMATION IN A
MP5314	MP5314	US	14/283,616	5/21/2014	9544754	1/10/2017	In Force	Systems And Methods For Scheduling Discovery-Related Communic
MP5315	MP5315	US	14/218,740	5/15/2014	9524255	12/20/2016	In Force	SYSTEM AND METHOD FOR AUTOMATIC DQS GATING BASED ON CO
MP5315	MP5315WO	WO	PCT/US2014/03	5/15/2014			Pending	Systems and Methods for DQS Gating
MP5318	MP5318WO	WO	PCT/US2014/03	5/20/2014			Pending	SYSTEMS AND METHODS FOR RED-EYE CORRECTION
MP5318	MP5318WO/CN	CN	20148030127.1	5/20/2014			Pending	SYSTEMS AND METHODS FOR RED-EYE CORRECTION
MP5320	MP5320	US	14/285,058	5/22/2014	9521715	12/13/2016	In Force	CURRENT SHAPING FOR DIMMABLE LED
MP5320	MP5320C1	US	14/550,359	11/21/2014	9480112	10/25/2016	In Force	CURRENT SHAPING FOR DIMMABLE LED
MP5320	MP5320CN	CN	201410228788.1	5/26/2014	212014102287	1/8/2019	In Force	CURRENT SHAPING FOR DIMMABLE LED
MP5320E	MP5320EP	EP	EP14169663.3	5/23/2014			Pending	CURRENT SHAPING FOR DIMMABLE LED
MP5321	MP5321	US	14/326,766	7/9/2014	9237418	1/12/2016	In Force	Positioning Database Management Applied to Server-based Positio
MP5321	MP5321C1	US	14/989,327	1/6/2016	9781572	10/3/2017	In Force	Positioning Database Management Applied to Server-based Positio
MP5321	MP5321CN	CN	201410370678.1	7/28/2014	212014103706	8/23/2019	In Force	POSITIONING DATABASE MANAGEMENT APPLIED TO SERVER-BASED
MP5321	MP5321RE	US	15/920,693	3/14/2018			Pending	POSITIONING DATABASE MANAGEMENT APPLIED TO SERVER-BASED
MP5325	MP5325	US	14/289,577	5/28/2014	9215107	12/15/2015	In Force	DUAL LOOP CLOCK AND DATA RECOVERY
MP5326	MP5326	US	14/284,528	5/22/2014	9454330	9/27/2016	In Force	METHOD AND APPARATUS FOR OPERATING COMMANDS ON A STOR
MP5329	MP5329CN	CN	201410407629.1	8/13/2014			Pending	ANTI-LOSS SYSTEMS AND METHODS FOR MOBILE DEVICES
MP5330	MP5330WO	WO	PCT/US2014/04	7/17/2014			Pending	Systems and Methods for Application Management on Mobile Device
MP5331	MP5331CN	CN	201410350516.1	7/17/2014			Pending	SYSTEMS AND METHODS FOR MOBILE APPLICATION PROTECTION
MP5332	MP5332	US	14/460,471	8/15/2014	10108310	10/23/2018	In Force	METHOD AND APPARATUS FOR ICON BASED APPLICATION CONTROL
MP5332	MP5332CN	CN	201410414468.1	8/18/2014			Pending	METHOD FOR USING APPLICATION ICON TO CONTROL APPLICATION
MP5334	MP5334WO	WO	PCT/US2014/05	9/3/2014			Pending	SYSTEMS AND METHODS FOR CONTROLLING OPERATING MODES OF
MP5334	MP5334WO/EP	EP	EP14809099.6	9/3/2014	3044668	8/22/2018	In Force	SYSTEMS AND METHODS FOR CONTROLLING OPERATING MODES OF
MP5334	MP5334WUDE	DE	EP14809099.6	9/3/2014	602014030956	8/22/2018	In Force	SYSTEMS AND METHODS FOR CONTROLLING OPERATING MODES OF
MP5334	MP5334WUFR	FR	EP14809099.6	9/3/2014	3044668	8/22/2018	In Force	SYSTEMS AND METHODS FOR CONTROLLING OPERATING MODES OF
MP5334	MP5334WUGB	GB	EP14809099.6	9/3/2014	3044668	8/22/2018	In Force	SYSTEMS AND METHODS FOR CONTROLLING OPERATING MODES OF
MP5337	MP5337WO	WO	PCT/US2014/04	6/4/2014			Pending	INTERNAL SEARCH ENGINE ARCHITECTURE
MP5339	MP5339	US	14/284,241	5/21/2014	9423451	8/23/2016	In Force	METHOD AND APPARATUS FOR TESTING A SEMICONDUCTOR PACKA

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MP5339	MP5339WO	WO	PCT/US2014/03	5/22/2014			Pending	METHOD AND APPARATUS FOR TESTING A SEMICONDUCTOR PACKAGE
MP5339T	MP5339TW	TW	103119227	6/3/2014	1612588	1/23/2018	In Force	TOP SOCKET DESIGN FOR PACKAGE ON PACKAGE
MP5342	MP5342	US	14/301,390	6/11/2014	9112554	8/18/2015	In Force	ESTIMATION OF CORRELATED MINO CHANNELS
MP5343	MP5343	US	14/302,444	6/12/2014	9059787	6/16/2015	In Force	ESTIMATING TRANSMISSION POWER AND NOISE LEVEL OF RECEIVED
MP5344	MP5344	US	14/266,926	5/1/2014	9367347	6/14/2016	In Force	SYSTEMS AND METHODS FOR Command Execution Order Control In Elect
MP5347	MP5347	US	14/297,793	6/6/2014	9489311	11/8/2016	In Force	Systems and Methods for Cache Management for Universal Serial B
MP5347	MP5347C1	US	15/343,997	11/4/2016	9858205	1/2/2018	In Force	Systems and Methods for Cache Management for Universal Serial B
MP5347	MP5347WO	WO	PCT/US2014/04	6/6/2014			Pending	SYSTEMS AND METHODS FOR CACHE MANAGEMENT OF UNIVERSAL
MP5347	MP5347WOCN	CN	201480032308.1	6/6/2014	ZL2014800323	11/6/2018	In Force	SYSTEMS AND METHODS FOR CACHE MANAGEMENT FOR UNIVERSAL
MP5347	MP5347WOEP	EP	EP14817716.5	6/6/2014			Pending	SYSTEMS AND METHODS FOR CACHE MANAGEMENT OF UNIVERSAL
MP5347	MP5347WOEPDE	DE	EP14817716.5	6/6/2014			Pending	SYSTEMS AND METHODS FOR CACHE MANAGEMENT OF UNIVERSAL
MP5347	MP5347WOEPFR	FR	EP14817716.5	6/6/2014			Pending	SYSTEMS AND METHODS FOR CACHE MANAGEMENT OF UNIVERSAL
MP5347	MP5347WOEPGB	GB	EP14817716.5	6/6/2014			Pending	SYSTEMS AND METHODS FOR CACHE MANAGEMENT OF UNIVERSAL
MP5347	MP5347WOJP	JP	2016-517702	6/6/2014	6336904	5/11/2018	In Force	SYSTEMS AND METHODS FOR CACHE MANAGEMENT OF UNIVERSAL
MP5353	MP5353	US	14/305,495	6/16/2014	1026462	4/16/2019	In Force	Method And Apparatus For Mitigating The Effect Of Certain Types Of
MP5353	MP5353CN	CN	201410273005.1	6/17/2014	ZL2014102730	7/23/2019	In Force	METHOD AND APPARATUS FOR MITIGATING THE EFFECT OF CERTAI
MP5355	MP5355	US	14/297,788	6/6/2014	9025274	5/5/2015	In Force	METHOD AND APPARATUS FOR WRITING TO AND READING FROM M
MP5356	MP5356	US	14/299,366	6/9/2014	9804824	10/31/2017	In Force	System And Method For Controlling Flow Of Data Through A Buffer T
MP5358	MP5358	US	14/283,585	5/21/2014	9143979	9/22/2015	In Force	Method And Apparatus For Limiting A Number Of Mobile Devices Tha
MP5358	MP5358C1	US	14/858,592	9/18/2015	9723513	8/1/2017	In Force	Method And Apparatus For Limiting A Number Of Mobile Devices Tha
MP5362	MP5362	US	14/305,383	6/16/2014	9477546	10/25/2016	In Force	METHODS AND APPARATUS FOR OPTIMIZING LIFESPAN OF A STORA
MP5362	MP5362WO	WO	PCT/US2014/04	6/18/2014			Pending	METHODS AND APPARATUS FOR OPTIMIZING LIFESPAN OF A STORA
MP5362	MP5362WOCN	CN	201480030160.1	6/18/2014			Pending	METHODS AND APPARATUS FOR OPTIMIZING LIFESPAN OF A STORA
MP5363	MP5363	US	14/310,735	6/20/2014	9632932	4/25/2017	In Force	Backup-Power-Free Cache Memory System
MP5367	MP5367	US	14/314,396	6/25/2014	9710514	7/18/2017	In Force	SYSTEMS AND METHODS FOR EFFICIENT STORAGE ACCESS USING ME
MP5368	MP5368	US	14/313,055	6/24/2014	9645920	5/9/2017	In Force	Adaptive Cache Memory Controller
MP5368	MP5368WO	WO	PCT/US2014/04	6/24/2014			Pending	Adaptive Cache Memory Controller
MP5368	MP5368WOCN	CN	201480035733.1	6/24/2014	ZL2014800357	3/8/2019	In Force	ADAPTIVE CACHE MEMORY CONTROLLER
MP5369	MP5369	US	14/324,521	7/7/2014	9544376	1/10/2017	In Force	Method and Apparatus for Securely Discovering Services in a Wireles
MP5370	MP5370	US	14/510,205	10/9/2014	9329789	5/3/2016	In Force	METHODS AND APPARATUS FOR EFFICIENTLY OPERATING ON A STO
MP5372	MP5372	US	14/319,552	6/30/2014	9596615	3/14/2017	In Force	METHOD AND APPARATUS FOR ASSESSING A CONDITION OF A WIRE
MP5373	MP5373	US	14/288,739	5/28/2014	9367456	6/14/2016	In Force	Integrated Circuit And Method For Accessing Segments Of A Cache Li
MP5374	MP5374CN	CN	201410332250.1	7/7/2014	ZL2014103322	7/31/2018	In Force	METHOD AND APPARATUS FOR JOINT DETECTION OF SIGNALS
MP5377	MP5377	US	14/326,095	7/8/2014	9614548	4/4/2017	In Force	SYSTEMS AND METHODS FOR HYBRID MESSAGE PASSING AND BIT FL

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MP5378	MP5378WO	WO	PCT/US2014/04	7/30/2014			Pending	Systems and Methods for Noise Reduction
MP5381	MP5381	US	14/327,733	7/10/2014	980698	10/24/2017	In Force	Method and Apparatus for Ordering Packets, Associated with Differ
MP5382	MP5382	US	14/327,298	7/9/2014	10038996	7/31/2018	In Force	USER GROUP SELECTION IN MULTIPLE USER MULTIPLE INPUT MULTI
MP5383	MP5383	US	14/338,450	7/23/2014	933508	4/26/2016	In Force	METHOD AND SYSTEM FOR COMPILER OPTIMIZATION
MP5383	MP5383WO	WO	PCT/US2014/04	7/23/2014			Pending	METHOD AND SYSTEM FOR COMPILER OPTIMIZATION
MP5383	MP5383WO CN	CN	201480041050.1	7/23/2014	ZL2014800410	4/19/2019	In Force	METHOD AND SYSTEM FOR COMPILER OPTIMIZATION
MP5383	MP5383WO EP	EP	14802139.7	7/23/2014			Pending	Method and System for Compiler Optimization
MP5384	MP5384	US	14/472,042	8/28/2014	9304749	4/5/2016	In Force	METHOD AND SYSTEM FOR INSTRUCTION SCHEDULING
MP5384	MP5384WO	WO	PCT/US2014/05	9/4/2014			Pending	METHOD AND SYSTEM FOR INSTRUCTION SCHEDULING
MP5384	MP5384WO CN	CN	201480042512.1	9/4/2014	ZL2014800542	1/1/2019	In Force	METHOD AND SYSTEM FOR INSTRUCTION SCHEDULING
MP5384	MP5384WO EP	EP	14835562.1	9/4/2014			Pending	Method and System for Instruction Scheduling
MP5391	MP5391	US	14/332,341	7/15/2014	9401798	7/26/2016	In Force	FREQUENCY DOMAIN MULTIPLEXER
MP5392	MP5392	US	14/333,030	7/16/2014	9367511	6/14/2016	In Force	SYSTEM METHOD FOR MANAGING USB DATA TRANSFERS BY SORTIN
MP5392	MP5392WO	WO	PCT/US2014/04	7/16/2014			Pending	Systems and Methods for Managing USB Data Transfers
MP5392	MP5392WO CN	CN	201480040581.1	7/16/2014	ZL2014800405	3/12/2019	In Force	SYSTEMS AND METHODS FOR MANAGING USB DATA TRANSFERS
MP5392	MP5392WO EP	EP	14826111.8	7/16/2014			Pending	Systems and Methods for Managing USB Data Transfers
MP5392	MP5392WO JP	JP	2016-527074	7/16/2014	6418417	11/7/2018	In Force	Systems and Methods for Managing USB Data Transfers
MP5392	MP5392WO KR	KR	10-2016-700106	7/16/2014	101832997	4/13/2018	In Force	Systems and Methods for Managing USB Data Transfers
MP5393	MP5393	US	14/331,553	7/15/2014	9311937	4/12/2016	In Force	SYSTEMS AND METHODS FOR CALIBRATING READ AND WRITE OPER
MP5393	MP5393WO	WO	PCT/US2014/04	7/15/2014			Pending	SYSTEMS AND METHODS FOR CALIBRATING READ AND WRITE OPER
MP5395	MP5395	US	14/335,663	7/18/2014	9629013	4/18/2017	In Force	CHANNEL QUALITY INDICATION WITH FILTERED INTERFERENCE
MP5395	MP5395WO	WO	PCT/US2014/04	7/18/2014			Pending	CHANNEL QUALITY INDICATION WITH FILTERED INTERFERENCE
MP5396	MP5396	US	14/330,440	7/14/2014	9692316	6/27/2017	In Force	BLEEDER METHOD USING SWITCH NODE COUPLING CAPACITOR FOR
MP5397	MP5397	US	14/334,706	7/18/2014	9489023	11/8/2016	In Force	Secure Wake On LAN with White List
MP5400	MP5400WO	WO	PCT/US2014/04	7/23/2014			Pending	ANALOG CIRCUIT WITH IMPROVED LAYOUT FOR MISMATCH OPTIMI
MP5400	MP5400WO CN	CN	201480041178.1	7/23/2014	ZL2014800411	3/1/2019	In Force	ANALOG CIRCUIT WITH IMPROVED LAYOUT FOR MISMATCH OPTIMI
MP5400T	MP5400TW	TW	103125266	7/24/2014			Pending	Analog Circuit with Improved Layout for Mismatch Optimization
MP5401	MP5401	US	14/453,263	8/6/2014	9467965	10/11/2016	In Force	POSITIONING DATA DISTRIBUTION APPLIED TO SERVER-BASED POSIT
MP5401	MP5401 CN	CN	201410407630.6	8/13/2014	ZL2014104076	7/12/2019	In Force	POSITIONING DATA DISTRIBUTION APPLIED TO SERVER-BASED POSIT
MP5402	MP5402	US	14/335,310	7/18/2014	9529732	12/27/2016	In Force	Key Rotation for a Memory Controller
MP5402	MP5402WO	WO	PCT/US2014/04	7/18/2014			Pending	KEY ROTATION FOR A MEMORY CONTROLLER
MP5402	MP5402WO CN	CN	201480041644.1	7/18/2014	ZL2014800416	4/26/2019	In Force	KEY ROTATION FOR A MEMORY CONTROLLER
MP5402	MP5402WO EP	EP	14748400.0	7/18/2014			Pending	Key Rotation for a Memory Controller
MP5402	MP5402WO JP	JP	2016-529798	1/12/2016	6452135	12/21/2018	In Force	KEY ROTATION FOR A MEMORY CONTROLLER

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MP5402	MP5402WOKR	KR	10-2016-700455	7/18/2016			Pending	Key Rotation for a Scrambling Memory Controller
MP5405	MP5405C1	US	14/739,068	6/15/2015	9425921	8/23/2016	In Force	Systems and Methods for Suppressing Interference in a Wireless Com
MP5405	MP5405C1C1	US	15/228,361	8/4/2016	9967772	5/8/2018	In Force	Systems and Methods for Suppressing Interference in a Wireless Com
MP5406	MP5406	US	14/330,416	7/14/2014	9270960	2/23/2016	In Force	System and Method for Green Imbalance Compensation in Images
MP5407	MP5407	US	14/331,132	7/14/2014	9923528	3/20/2018	In Force	ADAPTIVE BOOST SUPPLY WITH SLOPE CONTROL
MP5407	MP5407CN	CN	201480041644.1	7/16/2014	ZL2014103459	4/16/2019	In Force	ADAPTIVE BOOST SUPPLY WITH SLOPE CONTROL
MP5408	MP5408	US	14/340,492	7/24/2014	9366645	4/5/2016	In Force	INTERFERENCE AVOIDANCE FOR BEAMFORMING TRANSMISSIONS IN
MP5408	MP5408C1	US	15/090,550	4/4/2016	10110289	10/23/2018	In Force	INTERFERENCE AVOIDANCE FOR BEAMFORMING TRANSMISSIONS IN
MP5408	MP5408C1C1	US	16/166,784	10/22/2018			Pending	INTERFERENCE AVOIDANCE FOR BEAMFORMING TRANSMISSIONS IN
MP5408	MP5408WO	WO	PCT/IB2014/001	7/25/2014			Pending	INTERFERENCE AVOIDANCE FOR BEAMFORMING TRANSMISSIONS IN
MP5410	MP5410	US	14/331,472	7/15/2014	9704574	7/11/2017	In Force	METHOD AND APPARATUS FOR PATTERN MATCHING
MP5412	MP5412	US	14/333,547	7/17/2014	9385865	7/5/2016	In Force	CORRECTING DECRYPTING MIS-SYNCHRONIZATION IN A MOBILE CO
MP5412	MP5412CN	CN	201410436287.1	7/18/2014			Pending	CORRECTING DECRYPTING MIS-SYNCHRONIZATION IN A MOBILE CO
MP5412E	MP5412EP	EP	EP14177735.9	7/18/2014			Pending	Correcting Decrypting MIS-Synchronization in a Mobile Communicat
MP5415	MP5415	US	14/315,019	6/25/2014	9836306	12/5/2017	In Force	Parallelizing Boot Operations
MP5415	MP5415WO	WO	PCT/US2014/04	6/25/2014			Pending	Parallelizing Boot Operations
MP5415	MP5415WOCN	CN	201480049460.1	6/25/2014	ZL2014800494	9/17/2019	In Force	METHOD OF PARALLELIZING BOOT OPERATIONS
MP5415	MP5415WOEP	EP	14796547.9	6/25/2014			Pending	Parallelizing Boot Operations
MP5419	MP5419	US	14/445,131	7/29/2014	9256959	2/9/2016	In Force	Systems and Methods for Color Lens Shading Correction
MP5421	MP5421WO	WO	PCT/US2014/04	7/29/2014			Pending	METHOD AND APPARATUS FOR GENERATING A PHY HEADER FIELD
MP5423	MP5423	US	14/451,378	8/4/2014	9350315	5/24/2016	In Force	POWER LINE COMMUNICATION FILTER FOR MULTIPLE CONDUCTORS
MP5424	MP5424	US	14/478,893	9/5/2014	9313595	4/12/2016	In Force	LOW-POWER METHOD AND CIRCUITRY OF DETERMINING HEADPHO
MP5428	MP5428	US	14/476,992	9/4/2014	9179523	11/3/2015	In Force	START-UP VOLTAGE FOR SOLID-STATE LIGHTING CONTROLLER
MP5429	MP5429	US	14/481,649	9/9/2014	10602995	6/19/2018	In Force	MULTIPLE TRANSMISSION WINDOWS FOR OFDM SYMBOL
MP5429	MP5429WO	WO	PCT/IB2014/002	9/9/2014			Pending	MULTIPLE TRANSMISSION WINDOWS FOR OFDM SYMBOL
MP5430	MP5430	US	14/469,679	8/27/2014	9739888	8/22/2017	In Force	METHOD, SYSTEM AND DEVICE FOR POSITION DETERMINATION WIT
MP5430	MP5430CN	CN	201410465041.1	9/11/2014	ZL2014104650	5/31/2019	In Force	METHOD, SYSTEM, AND DEVICE FOR POSITION DETERMINATION WIT
MP5432	MP5432	US	14/513,289	10/14/2014	9875516	1/23/2018	In Force	Systems and Methods for Graphics Process Units Power Managemen
MP5432	MP5432WO	WO	PCT/IB2014/002	10/14/2014			Pending	Systems and Methods for Graphics Process Units Power Managemen
MP5432	MP5432WOCN	CN	201480056445.1	10/14/2014	ZL2014800564	6/28/2019	In Force	SYSTEMS AND METHODS FOR GRAPHICS PROCESS UNITS POWER MA
MP5432	MP5432WOEP	EP	14853645.1	10/14/2014			Pending	Systems and Methods for Graphics Process Units Power Managemen
MP5433	MP5433	US	14/480,489	9/8/2014	9563226	2/7/2017	In Force	DYNAMIC CLOCK REGULATION BASED ON DUTY CYCLE THRESHOLDS
MP5433	MP5433WO	WO	PCT/US2014/05	9/8/2014			Pending	DYNAMIC CLOCK REGULATION
MP5434	MP5434	US	14/450,605	8/4/2014	9536139	3/14/2017	In Force	Network System And Method For Detection And Correction Of Duple

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MP5435	MP5435	US	14/482,531	9/10/2014	9160489	10/13/2015	In Force	ORIGIN DODGING FOR TRANSMITTED SIGNAL
MP5436	MP5436	US	14/516,848	10/17/2014	9106300	8/11/2015	In Force	METHOD AND APPARATUS FOR DETECTING NARROW-BAND INTERFERENCE
MP5437	MP5437	US	14/462,072	9/10/2014	9402270	7/26/2016	In Force	Secure Device Bootstrap Identity
MP5437	MP5437WO	WO	PCT/US2014/05	9/10/2014			Pending	SECURE DEVICE BOOTSTRAP IDENTITY
MP5438	MP5438	US	14/484,522	9/12/2014	9690584	6/27/2017	In Force	Systems and Methods for Register Allocation
MP5438	MP5438WO	WO	PCT/US2014/05	9/12/2014			Pending	SYSTEMS AND METHODS FOR REGISTER ALLOCATION
MP5438	MP5438WOCN	CN	201480056891.4	9/12/2014	ZL2014800568	3/22/2019	In Force	SYSTEMS AND METHODS FOR REGISTER ALLOCATION
MP5438	MP5438WOEP	EP	14853441.5	9/12/2014			Pending	Systems and Methods for Register Allocation
MP5441	MP5441	US	14/490,808	9/19/2014	9578618	2/21/2017	In Force	Server-based Positioning System Architecture
MP5441	MP5441CN	CN	201410557176.1	10/17/2014	ZL2014105571	3/22/2019	In Force	SERVER BASED POSITIONING SYSTEM ARCHITECTURE
MP5441	MP5441RE	US	15/927,222	3/21/2018			Pending	Server-based Positioning System Architecture
MP5443	MP5443	US	14/510,467	10/9/2014	9632523	4/25/2017	In Force	PEAK DETECTOR FOR AMPLIFIER
MP5443	MP5443CN	CN	201410537747.4	10/11/2014	ZL2014105377	7/5/2019	In Force	PEAK DETECTOR FOR AMPLIFIER
MP5444	MP5444	US	14/483,106	9/10/2014	9294323	3/22/2016	In Force	EXTENDED GUARD INTERVAL FOR OUTDOOR WLAN
MP5444	MP5444C1	US	15/075,823	3/21/2016	10033563	7/24/2018	In Force	EXTENDED GUARD INTERVAL FOR OUTDOOR WLAN
MP5444	MP5444C2	US	16/042,701	7/23/2018			Pending	EXTENDED GUARD INTERVAL FOR OUTDOOR WLAN
MP5444	MP5444WO	WO	PCT/US2014/05	9/10/2014			Pending	EXTENDED GUARD INTERVAL FOR OUTDOOR WLAN
MP5444	MP5444WOCN	CN	201480058426.4	9/10/2014	ZL2014800584	9/10/2019	In Force	METHOD AND APPARATUS FOR GENERATING DATA WITH SELECTED
MP5444	MP5444WOEP	EP	14800177.9	9/10/2014			Pending	Extended Guard Interval for Outdoor WLAN
MP5444	MP5444WOJP	JP	2016-540942	9/10/2014	6253784	12/8/2017	In Force	EXTENDED GUARD INTERVAL FOR OUTDOOR WLAN
MP5444	MP5444WOKR	KR	10-2016-760872	9/10/2014			Pending	EXTENDED GUARD INTERVAL FOR OUTDOOR WLAN
MP5445	MP5445	US	14/482,980	9/10/2014	9571380	2/14/2017	In Force	MULTI-STAGE INTERCONNECT NETWORK IN A PARALLEL PROCESSING
MP5445	MP5445WO	WO	PCT/IB2014/002	9/10/2014			Pending	MULTI-STAGE INTERCONNECT NETWORK IN A PARALLEL PROCESSING
MP5445	MP5445WOCN	CN	201480061408.1	9/10/2014	ZL2014800614	2/15/2019	In Force	METHOD AND NETWORK DEVICE FOR PROCESSING NETWORK PACKAGE
MP5446	MP5446	US	14/481,931	9/10/2014	9086746	5/19/2015	In Force	ESTIMATING DOPPLER SHIFT USING PREDEFINED AUTOCORRELATION
MP5446	MP5446CN	CN	201410453540.4	9/10/2014	ZL2014104535	6/28/2019	In Force	ESTIMATING DOPPLER SHIFT USING PREDEFINED AUTOCORRELATION
MP5446E	MP5446EP	EP	EP14003695.5	10/31/2014			Pending	Estimating Doppler Shift Using Predefined Autocorrelation Vectors
MP5449	MP5449	US	14/483,112	9/10/2014	9178561	11/3/2015	In Force	METHOD AND APPARATUS FOR CORRELATING SIGNALS RECEIVED FROM
MP5449	MP5449CN	CN	201410464869.1	9/11/2014	ZL2014104648	7/5/2019	In Force	METHOD AND APPARATUS FOR CORRELATING SIGNALS RECEIVED FROM
MP5452	MP5452	US	14/488,240	9/16/2014	9629169	4/18/2017	In Force	ACCESS POINT COORDINATION FOR TRAFFIC CONTROL IN WIRELESS
MP5452	MP5452WO	WO	PCT/US2014/05	9/16/2014			Pending	ACCESS POINT COORDINATION FOR TRAFFIC CONTROL IN WIRELESS
MP5453	MP5453	US	14/474,212	9/1/2014	9831910	11/28/2017	In Force	NOISE ESTIMATION BASED ON CONTROL MESSAGES DESTINED FOR
MP5453	MP5453CN	CN	201410468129.1	9/15/2014	ZL2014104681	4/19/2019	In Force	NOISE ESTIMATION BASED ON CONTROL MESSAGES DESTINED FOR
MP5454	MP5454	US	14/489,678	9/18/2014	9910129	3/6/2018	In Force	METHOD AND APPARATUS FOR CALIBRATING TRANSMIT DELAY AND

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MP5455	MP5455	US	14/488,211	9/16/2014	9829348	11/28/2017	In Force	IDENTIFYING A POSITION OF A BRUSHLESS DC MOTOR
MP5455	MP5455WO	WO	PCT/IB2014/001	9/18/2014			Pending	IDENTIFYING A POSITION OF A BRUSHLESS DC MOTOR
MP5455	MP5455WOCN	CN	201480042221.1	9/18/2014	212014800422	9/7/2018	In Force	IDENTIFYING A POSITION OF A BRUSHLESS DC MOTOR
MP5458	MP5458	US	14/602,081	1/21/2015	9450745	11/8/2016	In Force	VOLTAGE-CONTROLLED OSCILLATOR
MP5459	MP5459	US	14/558,770	12/3/2014	9277421	3/1/2016	In Force	System and Method for Estimating Noise in a Wireless Signal Using O
MP5460	MP5460	US	14/563,071	12/8/2014	9413356	8/9/2016	In Force	Chip Of SOC Including Fusible Logic Array And Functions To Protect h
MP5461	MP5461	US	14/563,110	12/8/2014	9660802	5/23/2017	In Force	Systems And Methods For Generating And Storing Silicon Fingerprint
MP5462	MP5462	US	14/523,678	10/24/2014	9712358	7/18/2017	In Force	RANGE EXTENSION MODE FOR WIFI
MP5462	MP5462C1	US	15/647,918	7/12/2017	10153930	12/11/2018	In Force	RANGE EXTENSION MODE FOR WIFI
MP5462	MP5462C1C1	US	15/648,596	7/13/2017	10389562	8/20/2019	In Force	RANGE EXTENSION MODE FOR WIFI
MP5462	MP5462C1C1C1	US	16/544,391	8/19/2019			Pending	RANGE EXTENSION MODE FOR WIFI
MP5462	MP5462D1	US	14/954,961	11/30/2015	9667460	5/30/2017	In Force	RANGE EXTENSION MODE FOR WIFI
MP5462	MP546211	US	14/591,759	1/7/2015	10194006	1/29/2019	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP5462	MP546211C1	US	15/787,937	10/19/2017	10291752	5/14/2019	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP5462	MP546211C1C1	US	16/410,962	5/13/2019			Pending	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP5462	MP54621111	US	15/017,385	2/5/2016	10218822	2/26/2019	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP5462	MP5462WO	WO	PCT/US2014/06	10/24/2014			Pending	RANGE EXTENSION MODE FOR WIFI
MP5462	MP5462WOCN	CN	201480070012.1	10/24/2014			Pending	RANGE EXTENSION MODE FOR WIFI
MP5462	MP5462WOEP	EP	14795753.4	10/24/2014			Pending	Range Extension Mode For Wifi
MP5462	MP5462WOJP	JP	2016-524452	10/24/2014	6464493	2/6/2019	In Force	RANGE EXTENSION MODE FOR WIFI
MP5462	MP5462WOKR	KR	10-2016-701377	10/24/2014			Pending	RANGE EXTENSION MODE FOR WIFI
MP5462	MP546211WO	WO	PCT/US2015/03	1/7/2015			Pending	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP5462	MP546211WCN	CN	201580010085.4	1/7/2015			Pending	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP5462	MP546211WEP	EP	15702022.3	1/7/2015			Pending	Physical Layer Frame Format for WLAN
MP5462	MP546211WJP	JP	2016-543570	1/7/2015	6464494	1/18/2019	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP5462	MP546211WKR	KR	10-2016-702125	1/7/2015			Pending	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP5463	MP5463	US	14/501,655	9/30/2014	9929882	3/27/2018	In Force	Method And Apparatus For Accurately Estimating A Distance Between
MP5464	MP5464	US	14/482,124	9/10/2014	9123369	9/1/2015	In Force	METHOD AND APPARATUS FOR DETERMINING POSITION OF MULTIP
MP5464	MP5464WO	WO	PCT/US2014/05	9/10/2014			Pending	METHOD AND APPARATUS FOR DETERMINING POSITION OF MULTIP
MP5464	MP5464WO	WO	PCT/US2014/05	9/10/2014			Pending	METHOD AND APPARATUS FOR DETERMINING POSITION OF MULTIP
MP5465	MP5465C1	US	14/867,302	9/28/2015	9384767	7/5/2016	In Force	Generating Position Error Signal Based On Data Tracks For Rotating
MP5465	MP5465WO	WO	PCT/US2014/05	9/29/2014			Pending	Generating Position Error Signal Based On Data Tracks For Magnetic
MP5466	MP5466	US	14/503,173	9/30/2014	9559702	1/31/2017	In Force	CIRCUITS AND METHODS FOR FLICKER NOISE UPCONVERSION MAIN
MP5468	MP5468	US	14/505,110	10/2/2014	9525760	12/20/2016	In Force	Dynamic Processing Context

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MP5471	MP5471	US	14/501,255	9/30/2014	9099156	8/4/2015	In Force	Two-Dimensional Magnetic Recording Systems And Methods For Min
MP5471	MP5471CN	CN	201410723959.1	10/8/2014	ZL2014107295	10/9/2018	In Force	TWO-DIMENSIONAL MAGNETIC RECORDING SYSTEMS AND METHOD
MP5472	MP5472	US	14/501,229	9/30/2014	8982489	3/17/2015	In Force	Pseudo-Differential Magnetic Recording System And Method Incorpo
MP5472	MP5472CN	CN	201410673570.1	10/8/2014	ZL2014106735	9/18/2018	In Force	PSEUDO-DIFFERENTIAL MAGNETIC RECORDING SYSTEM AND METHO
MP5476	MP5476CN	CN	201410589503.1	10/28/2014			Pending	RAPID AND EFFECTIVE DECODING ALGORITHM FOR HS-SCCH ORDER
MP5478	MP5478CN	CN	201510109315.4	3/12/2015			Pending	A METHOD, APPARATUS AND USER DEVICE FOR CELL SEARCH
MP5480	MP5480CN	CN	201410653025.1	11/17/2014			Pending	METHOD AND APPARATUS FOR SCHEDULING NETWORK SEARCHING
MP5484	MP5484	US	14/566,507	12/10/2014	9705693	7/11/2017	In Force	PROVISIONING USING MULTICAST TRAFFIC
MP5485	MP5485CN	CN	201410667598.4	11/20/2014			Pending	INDUCTOR/TRANSFORMER OUTSIDE OF SILICON WAFER
MP5487	MP5487	US	14/540,485	11/13/2014	9356612	5/31/2016	In Force	METHOD AND APPARATUS TO CALIBRATE FREQUENCY SYNTHESIZER
MP5487	MP5487WO	WO	PCT/US2014/06	11/13/2014			Pending	METHOD AND APPARATUS TO CALIBRATE FREQUENCY SYNTHESIZER
MP5487	MP5487WOCN	CN	20148062428.1	11/13/2014	ZL2014800624	5/17/2019	In Force	METHOD AND APPARATUS TO CALIBRATE FREQUENCY SYNTHESIZER
MP5487	MP5487WOEP	EP	14812668.3	11/13/2014			Pending	Method and Apparatus to Calibrate Frequency Synthesizer
MP5488	MP5488	US	14/556,387	12/11/2014	9252824	2/2/2016	In Force	Method and Apparatus for Filtering Noise in a Signal Received by a W
MP5489	MP5489	US	14/543,642	11/17/2014	9442794	9/13/2016	In Force	Methods and Apparatus for Accessing Device Memory via a Host Bus
MP5491	MP5491	US	14/522,443	10/23/2014	9374478	6/21/2016	In Force	ADAPTIVE IMAGE SHARPENING
MP5493	MP5493	US	14/516,529	10/16/2014	9467399	10/11/2016	In Force	PROCESSING CONCURRENCY IN A NETWORK DEVICE
MP5493	MP5493D1	US	14/517,570	10/17/2014	9461939	10/4/2016	In Force	PROCESSING CONCURRENCY IN A NETWORK DEVICE
MP5493	MP5493WOCN	CN	201480066759.1	10/16/2014			Pending	PROCESSING CONCURRENCY IN A NETWORK DEVICE
MP5494	MP5494	US	14/516,500	10/16/2014	9479620	10/25/2016	In Force	PACKET PARSING AND KEY GENERATION IN A NETWORK DEVICE
MP5494	MP5494WO	WO	PCT/IB2014/002	10/16/2014			Pending	PACKET PARSING AND KEY GENERATION IN A NETWORK DEVICE
MP5494	MP5494WOCN	CN	201480066070.1	10/16/2014	ZL2014800660	2/15/2019	In Force	PACKET PARSING AND KEY GENERATION IN A NETWORK DEVICE
MP5496	MP5496	US	14/536,926	11/10/2014	9521568	12/13/2016	In Force	Wireless LAN Device Positioning
MP5496	MP5496CN	CN	201410658602.1	11/18/2014	ZL2014106586	5/14/2019	In Force	WIRELESS LAN DEVICE POSITIONING
MP5496	MP5496RE	US	15/927,237	3/21/2018			Pending	Wireless LAN Device Positioning
MP5498	MP5498CN	CN	201410746590.1	12/9/2014	ZL2014107465	5/14/2019	In Force	A METHOD, APPARATUS AND USER DEVICE FOR COMMUNICATION
MP5499	MP5499	US	14/533,540	11/5/2014	9250935	2/2/2016	In Force	Systems and Methods for Loop Suspension in Java Programming
MP5499	MP5499WO	WO	PCT/IB2014/003	11/5/2014			Pending	Systems and Methods for Loop Suspension in Java Programming
MP5499	MP5499WOCN	CN	201480064229.1	11/5/2014	ZL2014800642	3/19/2019	In Force	SYSTEMS AND METHODS FOR LOOP SUSPENSION IN JAVA PROGRAM
MP5499	MP5499WOEP	EP	14864276.2	11/5/2014			Pending	Systems and Methods for Loop Suspension in Java Programming
MP5500	MP5500CN	CN	201410682499.1	11/24/2014			Pending	SYSTEMS AND METHODS FOR DYNAMIC VOLTAGE CONTROL
MP5502	MP5502	US	14/533,521	11/5/2014	9184786	11/10/2015	In Force	Systems and Methods for Clock Calibration for Satellite Navigation
MP5502	MP5502CN	CN	201410658505.1	11/18/2014	ZL2014106585	11/13/2018	In Force	SYSTEMS AND METHODS FOR CLOCK CALIBRATION FOR SATELLITE N
MP5504	MP5504	US	14/516,411	10/16/2014	9209825	12/8/2015	In Force	METHODS FOR SAMPLING TIME SKEW COMPENSATION IN TIME-INT

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MP5505	MP5505	US	14/568,157	12/12/2014	9473871	10/18/2016	In Force	Systems and Methods for Audio Management
MP5506	MP5506	US	14/581,839	11/14/2014	9436790	9/6/2016	In Force	Systems and Methods for Integrated Circuit Design
MP5508	MP5508	US	14/522,306	10/23/2014	9544237	1/10/2017	In Force	METHOD AND APPARATUS FOR INSERTING IDLE BYTES IN A DATA ST
MP5509	MP5509	US	14/522,453	10/23/2014	9178541	11/3/2015	In Force	CARTESIAN DIGITAL POWER AMPLIFIER USING COORDINATE ROTATI
MP5509	MP5509WO	WO	PCT/US2014/06	10/23/2014			Pending	CARTESIAN DIGITAL POWER AMPLIFIER USING COORDINATE ROTATI
MP5510	MP5510	US	14/522,484	10/23/2014	9432032	8/30/2016	In Force	SAMPLE-RATE CONVERSION IN A MULTI-CLOCK SYSTEM SHARING A
MP5510	MP5510WO	WO	PCT/US2014/06	10/23/2014			Pending	SAMPLE-RATE CONVERSION IN A MULTI-CLOCK SYSTEM SHARING A
MP5510	MP5510WOCN	CN	2014800655274	10/23/2014	ZL2014800655	4/16/2019	In Force	SAMPLE-RATE CONVERSION IN A MULTI-CLOCK SYSTEM SHARING A
MP5510	MP5510WCEP	EP	14855580.8	10/23/2014	3061187	4/4/2018	In Force	Sample-Rate Conversion in a Multi-Clock System Sharing a Common
MP5510	MP5510WUDE	DE	14855580.8	10/23/2014	14855580.8	4/4/2018	In Force	Sample-Rate Conversion in a Multi-Clock System Sharing a Common
MP5510	MP5510WUFR	FR	14855580.8	10/23/2014	3061187	4/4/2018	In Force	Sample-Rate Conversion in a Multi-Clock System Sharing a Common
MP5510	MP5510WUGB	GB	14855580.8	10/23/2014	3061187	4/4/2018	In Force	Sample-Rate Conversion in a Multi-Clock System Sharing a Common
MP5512	MP5512	US	14/523,402	10/24/2014	9590566	3/7/2017	In Force	PRE-DISTORTION BASED POWER CONTROL
MP5512	MP5512WO	WO	PCT/US2014/06	10/24/2014			Pending	PRE-DISTORTION BASED POWER CONTROL
MP5512	MP5512WOCN	CN	2014800645014	10/24/2014	ZL2014800645		Pending	PRE-DISTORTION BASED POWER CONTROL
MP5512	MP5512WCEP	EP	14853951.1	10/24/2014	3061185	4/17/2019	In Force	Pre-Distortion Based Power Control
MP5512	MP5512WCEPDE	DE	14853951.1	10/24/2014	602014045086	4/17/2019	In Force	Pre-Distortion Based Power Control
MP5512	MP5512WCEPFR	FR	14853951.1	10/24/2014	3061185	4/17/2019	In Force	Pre-Distortion Based Power Control
MP5512	MP5512WCEPGB	GB	14853951.1	10/24/2014	3061185	4/17/2019	In Force	Pre-Distortion Based Power Control
MP5515	MP5515	US	14/522,695	10/24/2014	9582356	2/28/2017	In Force	System and Method for DDR Memory Timing Acquisition and Trackin
MP5516	MP5516	US	14/528,870	10/30/2014	9443733	9/13/2016	In Force	Method and Apparatus for Authenticating a Semiconductor Die
MP5516	MP5516WO	WO	WO2014US0632	10/30/2014			Pending	Unobservable Silicon Structures from Variable Mask Data Creates an
MP5516	MP5516WOCN	CN	2014800573834	10/30/2014	ZL2014800573	3/12/2019	In Force	METHOD AND APPARATUS FOR AUTHENTICATING A SEMICONDUCT
MP5516	MP5516WCEP	EP	14833466.7	10/30/2014			Pending	Method and Apparatus for Authenticating a Semiconductor Die
MP5516	MP5516WCEPDE	DE	14833466.7	10/30/2014	602014056051	10/30/2019	In Force	Method and Apparatus for Authenticating a Semiconductor Die
MP5516	MP5516WCEPFR	FR	14833466.7	10/30/2014	3066611	10/30/2019	In Force	Method and Apparatus for Authenticating a Semiconductor Die
MP5516	MP5516WCEPGB	GB	14833466.7	10/30/2014	3066611	10/30/2019	In Force	Method and Apparatus for Authenticating a Semiconductor Die
MP5518	MP5518	US	14/523,747	10/24/2014	9602266	3/21/2017	In Force	Method and Apparatus for Scheduling use of Radio Resources in a Wi
MP5518	MP5518WO	WO	PCT/US2014/002	10/24/2014			Pending	Carrier Aggregation Scheduling to Facilitate In-Device Co-Existence
MP5519	MP5519	US	14/532,816	11/4/2014	9417641	8/16/2016	In Force	MEMORY EFFECT REDUCTION USING LOW IMPEDANCE BIASING
MP5519	MP5519C1	US	15/207,362	7/11/2016	9705454	7/11/2017	In Force	MEMORY EFFECT REDUCTION USING LOW IMPEDANCE BIASING
MP5519	MP5519WO	WO	PCT/US2014/06	11/4/2014			Pending	MEMORY EFFECT REDUCTION USING LOW IMPEDANCE BIASING
MP5519	MP5519WOCN	CN	2014800603071	11/4/2014			Pending	MEMORY EFFECT REDUCTION USING LOW IMPEDANCE BIASING
MP5519	MP5519WCEP	EP	14857354.6	11/4/2014			Pending	MEMORY EFFECT REDUCTION USING LOW IMPEDANCE BIASING

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MP5520	MP5520	US	14/528,845	10/30/2014	9386662	7/5/2016	In Force	Method and Apparatus for a Customizable Lighting Controller
MP5522	MP5522	US	14/533,501	11/5/2014	9596527	3/14/2017	In Force	Method And Apparatus For Updating And Switching Between Bit Lo
MP5522	MP5522CN	CN	201410618971.1	11/6/2014	Z12014106189	8/9/2019	In Force	A CONVERSION MODULE AND A FIRST NETWORK MODULE OF AN EN
MP5523	MP5523	US	14/526,861	10/29/2014	9602293	3/21/2017	In Force	MANAGING IDLE MODE OF OPERATION IN NETWORK SWITCHES
MP5523	MP5523WO	WO	PCT/US2014/06	10/30/2014			Pending	MANAGING IDLE MODE OF OPERATION IN NETWORK SWITCHES
MP5523	MP5523WOCN	CN	201480060752.1	10/30/2014	Z12014800607	8/23/2019	In Force	A NETWORK SWITCH COUPLED TO A PLURALITY OF DEVICES AND A
MP5523	MP5523WOEP	EP	14802261.9	10/30/2014			Pending	Managing Idle Mode of Operation in Network Switches
MP5523	MP5523WOEPDE	DE	14802261.9	10/30/2014	3066785	12/4/2019	In Force	Managing Idle Mode of Operation in Network Switches
MP5523	MP5523WOEPFR	FR	14802261.9	10/30/2014	3066785	12/4/2019	In Force	Managing Idle Mode of Operation in Network Switches
MP5523	MP5523WOEPGB	GB	14802261.9	10/30/2014	3066785	12/4/2019	In Force	Managing Idle Mode of Operation in Network Switches
MP5525	MP5525	US	14/539,130	11/12/2014	9854547	12/26/2017	In Force	Method And Apparatus For Synchronizing Timing Among Devices In A
MP5525	MP5525WO	WO	PCT/US2014/06	11/12/2014			Pending	Method And Apparatus For Synchronizing Timing Among Devices In A
MP5525	MP5525WOCN	CN	201480061738.1	11/12/2014	Z12014800617	7/26/2019	In Force	METHOD AND APPARATUS FOR SYNCHRONIZING TIMING AMONG DE
MP5525	MP5525WOEP	EP	14805756.5	11/12/2014	3069559	4/11/2018	In Force	Method and Apparatus for Synchronizing Timing Among Devices in a
MP5525	MP5525WOJP	JP	2016-525542	11/12/2014	6472795	2/1/2019	In Force	Method And Apparatus For Synchronizing Timing Among Devices In A
MP5525	MP5525WOKR	KR	1020167015259	11/12/2014			Pending	Method And Apparatus For Synchronizing Timing Among Devices In A
MP5525	MP5525WUDE	DE	14805756.5	11/12/2014	602014023856	4/11/2018	In Force	Method and Apparatus for Synchronizing Timing Among Devices in a
MP5525	MP5525WUFR	FR	14805756.5	11/12/2014	3069559	4/11/2018	In Force	Method and Apparatus for Synchronizing Timing Among Devices in a
MP5525	MP5525WUGB	GB	14805756.5	11/12/2014	3069559	4/11/2018	In Force	Method and Apparatus for Synchronizing Timing Among Devices in a
MP5526	MP5526	US	14/533,246	11/5/2014	9769079	9/19/2017	In Force	Sideband Communications Over 40GBASE-R And 100GBASE-R System
MP5527	MP5527CN	CN	201410773807.1	12/12/2014	Z12014107738	1/29/2019	In Force	FREQUENCY DIVIDER
MP5538	MP5538	US	14/554,845	11/26/2014	9548759	1/17/2017	In Force	SYSTEMS AND METHODS FOR PROCESSING LDPC CODES WITH DIFFE
MP5539	MP5539	US	14/553,974	11/25/2014	9717086	7/25/2017	In Force	ORTHOGONAL FREQUENCY DIVISION MULTIPLE ACCESS FOR WIRELE
MP5539	MP5539C1	US	15/658,070	7/24/2017	9980268	5/22/2018	In Force	ORTHOGONAL FREQUENCY DIVISION MULTIPLE ACCESS FOR WIRELE
MP5539	MP5539C2	US	15/983,524	5/18/2018	10433309	10/11/2019	In Force	ORTHOGONAL FREQUENCY DIVISION MULTIPLE ACCESS FOR WIRELE
MP5539	MP5539C2C1	US	16/588,395	9/30/2019			Pending	ORTHOGONAL FREQUENCY DIVISION MULTIPLE ACCESS FOR WIRELE
MP5539	MP5539D1	US	14/955,017	11/30/2015	10009894	6/26/2018	In Force	ORTHOGONAL FREQUENCY DIVISION MULTIPLE ACCESS FOR WIRELE
MP5539	MP5539D1C1	US	16/016,781	6/25/2018	10349413	7/9/2019	In Force	ORTHOGONAL FREQUENCY DIVISION MULTIPLE ACCESS FOR WIRELE
MP5539	MP5539WO	WO	PCT/US2014/06	11/25/2014			Pending	ORTHOGONAL FREQUENCY DIVISION MULTIPLE ACCESS FOR WIRELE
MP5539	MP5539WOCN	CN	201480074063.1	11/25/2014			Pending	ORTHOGONAL FREQUENCY DIVISION MULTIPLE ACCESS FOR WIRELE
MP5539	MP5539WOEP	EP	14820983.6	11/25/2014			Pending	Orthogonal Frequency Division Multiple Access for Wireless Local Are
MP5539	MP5539WOJP	JP	2016-534934	11/25/2014	6508838	4/12/2019	In Force	ORTHOGONAL FREQUENCY DIVISION MULTIPLE ACCESS FOR WIRELE
MP5539	MP5539WOKR	KR	10-2016-701685	11/25/2014			Pending	ORTHOGONAL FREQUENCY DIVISION MULTIPLE ACCESS FOR WIRELE
MP5541	MP5541	US	14/555,084	11/26/2014	10123328	11/6/2018	In Force	COEXISTENCE TECHNIQUES FOR WIDE AREA NETWORK DEVICES OPE

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MP5541	MP5541C1	US	16/181,078	11/5/2018			Pending	COEXISTENCE TECHNIQUES FOR WIDE AREA NETWORK DEVICES OPE
MP5541	MP5541WO	WO	PCT/US2014/06	11/26/2014			Pending	COEXISTENCE TECHNIQUES FOR WIDE AREA NETWORK DEVICES OPE
MP5542	MP5542	US	14/553,982	11/25/2014	9825678	11/21/2017	In Force	UPLINK MULTI-USER MULTIPLE INPUT MULTIPLE OUTPUT FOR WIRE
MP5542	MP5542C1	US	15/818,552	11/20/2017			Pending	UPLINK MULTI-USER MULTIPLE INPUT MULTIPLE OUTPUT FOR WIRE
MP5542	MP5542D1	US	14/955,004	11/30/2015			Pending	UPLINK MULTI-USER MULTIPLE INPUT MULTIPLE OUTPUT FOR WIRE
MP5542	MP5542WO	WO	PCT/US2014/06	11/26/2014			Pending	UPLINK MULTI-USER MULTIPLE INPUT OUTPUT FOR WIRELESS LOCAL
MP5542	MP5542WOCN	CN	201480073248.1	11/26/2014			Pending	UPLINK MULTI-USER MULTIPLE INPUT MULTIPLE OUTPUT FOR WIRE
MP5542	MP5542WOEP	EP	14812851.5	11/26/2014			Pending	Uplink Multi-User Multiple Input Multiple Output for Wireless Local
MP5542	MP5542WOJP	JP	2016-535139	11/26/2014	6566949	8/9/2019	In Force	UPLINK MULTI-USER MULTIPLE INPUT MULTIPLE OUTPUT FOR WIRE
MP5542	MP5542WOKR	KR	10-2016-701685	11/26/2014			Pending	UPLINK MULTI-USER MULTIPLE INPUT MULTIPLE OUTPUT FOR WIRE
MP5545	MP5545	US	14/555,305	11/26/2014	9215055	12/15/2015	In Force	MEDIUM ACCESS PROTECTION AND BANDWIDTH NEGOTIATION IN A
MP5545	MP5545C1	US	14/968,255	12/14/2015	9853791	12/26/2017	In Force	MEDIUM ACCESS PROTECTION AND BANDWIDTH NEGOTIATION IN A
MP5545	MP5545WO	WO	PCT/US2014/06	11/26/2014			Pending	MEDIUM ACCESS PROTECTION AND BANDWIDTH NEGOTIATION IN A
MP5546	MP5546	US	14/559,017	12/3/2014	9590636	3/7/2017	In Force	Method And Apparatus For Validating A System-On-Chip Based On A
MP5547	MP5547	US	14/560,827	12/4/2014	9270311	2/23/2016	In Force	METHODS AND SYSTEMS FOR CALIBRATING AN ANALOG FILTER
MP5547	MP5547WO	WO	PCT/US2014/06	12/4/2014			Pending	METHODS AND SYSTEMS FOR CALIBRATING AN ANALOG FILTER
MP5547	MP5547WOCN	CN	201480066474.1	12/4/2014	ZL 201480066-	6/28/2019	In Force	METHODS AND SYSTEMS FOR CALIBRATING AN ANALOG FILTER
MP5548	MP5548	US	14/520,730	10/22/2014	9577675	2/21/2017	In Force	System And Method For Encoding User Data With Low-Density Parity
MP5549	MP5549	US	14/537,012	11/10/2014	9548764	1/17/2017	In Force	MEMORY EFFICIENT DECODERS FOR LOW-DENSITY PARITY-CHECK C
MP5550	MP5550CN	CN	201510021691.1	1/14/2015			Pending	METHOD, APPARATUS AND MOBILE TERMINAL FOR RESIDING ON CE
MP5551	MP5551CN	CN	201510035595.1	1/22/2015			Pending	METHOD, APPARATUS AND WIRELESS DEVICE FOR SHORTENING CEL
MP5552	MP5552	US	14/558,487	12/2/2014	9497789	11/15/2016	In Force	Methods, Apparatus, and Device for Wirelessly Connecting a Plurality
MP5555	MP5555WO	WO	PCT/US2014/06	10/21/2014			Pending	Final Level! Cache System And Corresponding Method
MP5555	MP5555WOCN	CN	201480066082.1	10/21/2014			Pending	FINAL LEVEL CACHE SYSTEM AND CORRESPONDING METHOD
MP5555	MP5555WOEP	EP	14792704.0	10/21/2014			Pending	Final Level! Cache System and Corresponding Method
MP5555	MP5555WOIN	IN	201647013841	10/21/2014			Pending	Final Level! Cache System And Corresponding Method
MP5555	MP5555WOJP	JP	2016-525567	10/21/2014	6431536	11/28/2018	In Force	Final Level! Cache System And Corresponding Method
MP5555	MP5555WOJP01	JP	2018-207296	4/21/2016			Pending	Final Level! Cache System And Corresponding Method
MP5555	MP5555WOKR	KR	1020167013471	10/21/2014			Pending	Final Level! Cache System And Corresponding Method
MP5556	MP5556	US	14/559,522	12/3/2014	9225361	12/29/2015	In Force	Frequency Planning for Digital Power Amplifier
MP5556	MP5556C1	US	14/977,547	12/21/2015	9490966	11/8/2016	In Force	Frequency Planning for Digital Power Amplifier
MP5556	MP5556WO	WO	PCT/IB2014/002	12/3/2014			Pending	FREQUENCY PLANNING FOR DIGITAL POWER AMPLIFIER
MP5556	MP5556WOCN	CN	201480066457.1	12/3/2014	ZL 2014800664	12/25/2018	In Force	FREQUENCY PLANNING FOR DIGITAL POWER AMPLIFIER
MP5556	MP5556WOKR	KR	10-2016-701775	12/3/2014			Pending	FREQUENCY PLANNING FOR DIGITAL POWER AMPLIFIER

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MP5562	MP5562CN	CN	2015100244004	1/16/2015	ZL2015100244	4/24/2018	In Force	SYSTEM, METHOD AND ELECTRONIC DEVICE FOR AUDIO OR VIDEO SI
MP5565	MP5565	US	14/564,499	12/9/2014	9959237	5/1/2018	In Force	Method And Apparatus For Transferring Information Within And Bet
MP5565	MP5565C1	US	14/711,103	5/13/2015	9535869	1/3/2017	In Force	Method And Apparatuses For Respectively Transferring Information
MP5565	MP5565WO	WO	PCT/US2014/06	12/9/2014			Pending	Method And Apparatus For Transferring Information Within And Bet
MP5565	MP5565WOCN	CN	201480067472.1	12/9/2014	ZL2014800674	5/14/2019	In Force	METHOD AND APPARATUS FOR TRANSFERRING INFORMATION WITH
MP5565	MP5565WOEP	EP	14821028.9	12/9/2014			Pending	Method and Apparatus for Transferring Information within and betw
MP5565	MP5565WOIN	IN	201647017504	12/9/2014			Pending	Method And Apparatus For Transferring Information Within And Bet
MP5565	MP5565WOJP	JP	2016-531042	12/9/2014	6541272	6/21/2019	In Force	Method And Apparatus For Transferring Information Within And Bet
MP5565	MP5565WOKR	KR	1020167018512	12/9/2014			Pending	Method And Apparatus For Transferring Information Within And Bet
MP5565T	MP5565TW	TW	103143514	12/12/2014			Pending	METHOD AND APPARATUS FOR TRANSFERRING INFORMATION WITH
MP5566	MP5566	US	14/565,377	12/9/2014	9641973	5/2/2017	In Force	Method and Apparatus for Tracking, in Real-Time, a Position of a Mo
MP5566	MP5566CN	CN	201410771955.1	12/12/2014	ZL2014107719	9/14/2018	In Force	METHOD AND APPARATUS FOR TRACKING, IN REAL-TIME, A POSITIO
MP5568	MP5568	US	14/600,131	1/20/2015	9281879	3/8/2016	In Force	METHODS AND APPARATUS FOR INDIVIDUALLY CONTROLLING POW
MP5568	MP5568C1	US	15/013,137	2/2/2016	9467213	10/11/2016	In Force	METHODS AND APPARATUS FOR INDIVIDUALLY CONTROLLING POW
MP5569	MP5569	US	14/558,805	12/3/2014	9385666	7/5/2016	In Force	Power Amplifier With Wide Band AM-AM Feedback And Digital Pre-D
MP5569	MP5569WO	WO	PCT/US2014/06	12/3/2014			Pending	Power Amplifier With Wide Band AM-AM Feedback And Digital Pre-D
MP5571	MP5571	US	14/558,239	12/2/2014	9247621	1/26/2016	In Force	METHOD AND APPARATUS FOR SELECTIVELY LOADING A DIMMER CI
MP5572	MP5572	US	14/539,754	11/12/2014	9613396	4/4/2017	In Force	METHOD AND APPARATUS FOR DEMOSACING OF COLOR FILTER AR
MP5573	MP5573	US	14/560,742	12/4/2014	9220138	12/22/2015	In Force	SOFT BLEEDER TO REMOVE STEP DIMMING
MP5576	MP5576	US	14/565,565	12/10/2014	9337772	5/10/2016	In Force	IMPULSE-ASSISTED LC TANK OSCILLATOR
MP5576	MP5576CN	CN	201410768927.1	12/11/2014	ZL2014107689	6/7/2019	In Force	Impulse-Assisted LC Tank Oscillator
MP5577	MP5577	US	14/566,756	12/11/2014	9696361	7/4/2017	In Force	Method and Apparatus for Analyzing Features or Characteristics of a
MP5578	MP5578	US	14/558,873	12/3/2014	9867185	1/9/2018	In Force	Method And Apparatus For Communicating In Sub-1 GHz Bands Acco
MP5580	MP5580	US	14/563,578	12/8/2014	9153264	10/6/2015	In Force	METHOD AND APPARATUS FOR DETERMINING RADIAL INCOHERENC
MP5581	MP5581	US	14/546,552	11/18/2014	9437212	9/6/2016	In Force	Systems and Methods for Suppressing Noise in an Audio Signal for Su
MP5584	MP5584	US	14/586,817	1/14/2015	9697938	7/4/2017	In Force	PSEUDO-8-SHAPED INDUCTOR
MP5584	MP5584WO	WO	PCT/US2015/01	1/16/2015			Pending	PSEUDO-8-SHAPED INDUCTOR
MP5584	MP5584WOCN	CN	201580010286.1	1/16/2015	ZL2015800106	9/21/2018	In Force	PSEUDO-8-SHAPED INDUCTOR
MP5585	MP5585CN	CN	201410727322.1	12/4/2014			Pending	METHOD AND APPARATUS FOR PROCESSING PACKET SERVICE SWITC
MP5587	MP5587	US	14/565,943	12/10/2014	9524385	12/20/2016	In Force	Using an Audio Channel for Authenticating a Device
MP5595	MP5595	US	14/570,886	12/15/2014	9627014	4/18/2017	In Force	MULTIPLE PORT SHARED MEMORY
MP5595	MP5595WO	WO	PCT/US2014/07	12/16/2014			Pending	MULTIPLE PORT SHARED MEMORY
MP5595	MP5595WOCN	CN	201480075682.1	12/16/2014	ZL2014800756	4/12/2019	In Force	MULTIPLE PORT SHARED MEMORY
MP5596	MP5596	US	14/556,567	12/1/2014	9584255	2/28/2017	In Force	Method And Apparatus For Transmitting And Receiving Packets In A

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MP5600	MP5600	US	14/574,088	12/17/2014	992,3813	3/20/2018	In Force	INCREASING PACKET PROCESSING RATE IN A NETWORK DEVICE
MP5600	MP5600D1	US	14/574,214	12/17/2014	981,336	11/7/2017	In Force	DEVICE AND METHOD FOR INCREASING PACKET PROCESSING RATE I
MP5601	MP5601	US	14/590,718	1/6/2015	953,7508	1/3/2017	In Force	SYSTEMS AND METHODS FOR DECODING CASCADE LDPC CODES
MP5601	MP5601C1	US	15/386,752	12/21/2016	1008,4480	9/25/2018	In Force	SYSTEMS AND METHODS FOR DECODING CASCADE LDPC CODES
MP5602	MP5602	US	14/591,636	1/7/2015	956,4931	2/7/2017	In Force	SYSTEMS AND METHODS FOR USING DECODERS OF DIFFERENT COM
MP5602	MP5602C1	US	15/399,377	1/5/2017			Pending	SYSTEMS AND METHODS FOR USING DECODERS OF DIFFERENT COM
MP5605	MP5605CN	CN	20141070781.1	11/28/2014			Pending	GENERIC SCHEDULE MECHANISM APPLICABLE TO SINGLE-ANTENNA
MP5607	MP5607	US	14/956,884	12/2/2015	962,9363	6/13/2017	In Force	System and Method for Reducing Image Noise
MP5608	MP5608	US	14/858,229	9/18/2015	971,5720	7/25/2017	In Force	System and Method for Reducing Image Noise
MP5612	MP5612CN	CN	201410712730.4	11/28/2014	21,201,410,7127	6/7/2019	In Force	SIGNAL AMPLIFIER, ELECTRONIC DEVICE AND FORMING METHODS T
MP5616	MP5616	US	14/571,715	12/16/2014	968,8678	3/28/2017	In Force	Method And Apparatus For Mitigating Interference Between Wireless
MP5616	MP5616C1	US	15/440,838	2/23/2017	1021,9162	2/26/2019	In Force	Interference Suppression Or Cancellation In Collocated Receivers
MP5618	MP5618	US	14/591,631	1/7/2015	952,4236	12/20/2016	In Force	SYSTEMS AND METHODS FOR PERFORMING MEMORY MANAGEMEN
MP5619	MP5619	US	14/587,961	12/31/2014	947,9746	10/25/2016	In Force	METHODS AND APPARATUS FOR DEMOSAICKING ARTIFACTS SUPPRE
MP5619	MP5619WO	WO	PCT/IB2015/05C	1/7/2015			Pending	METHODS AND APPARATUS FOR DEMOSAICKING ARTIFACTS SUPPRE
MP5627	MP5627WO	WO	PCT/IB2014/067	12/23/2013			Pending	ADAPTIVE VOLTAGE SCALING AND VDD TRACKING
MP5630	MP5630	US	14/578,682	12/22/2014	958,5014	2/28/2017	In Force	FAST RECOVERY FROM CIPHERING KEY MISMATCH
MP5630	MP5630CN	CN	201410810851.4	12/23/2014			Pending	FAST RECOVERY FROM CIPHERING KEY MISMATCH
MP5638	MP5638	US	14/582,568	12/24/2014	101,35582	11/20/2018	In Force	SYSTEMS AND METHODS FOR INTRODUCING TIME DIVERSITY IN WIFI
MP5638	MP5638C1	US	16/194,139	11/16/2018			Pending	SYSTEMS AND METHODS FOR INTRODUCING TIME DIVERSITY IN WIFI
MP5640	MP5640	US	14/588,649	1/2/2015	984,7925	12/19/2017	In Force	ACCURATE MEASUREMENT OF DISTRIBUTED COUNTERS
MP5640	MP5640WO	WO	PCT/IB2015/05C	1/2/2015			Pending	ACCURATE MEASUREMENT OF DISTRIBUTED COUNTERS
MP5640	MP5640WOCN	CN	201580058995.1	1/2/2015			Pending	ACCURATE MEASUREMENT OF DISTRIBUTED COUNTERS
MP5642	MP5642	US	14/586,096	12/30/2014	932,4417	4/26/2016	In Force	SYSTEMS AND METHODS FOR AVOIDING READ DISTURBANCE IN A ST
MP5643	MP5643	US	14/569,868	12/15/2014	957,8320	2/21/2017	In Force	METHODS AND APPARATUS FOR DEFECT CORRECTION IN IMAGE SEN
MP5646	MP5646	US	14/567,955	12/11/2014	942,4911	8/23/2016	In Force	METHOD AND APPARATUS FOR SCREENING MEMORY CELLS FOR DIS
MP5646T	MP5646TW	TW	103145824	12/26/2014			Pending	Method of Screening SRAM Disturb Failure
MP5647	MP5647	US	14/590,361	1/6/2015	982,7880	2/6/2018	In Force	Auto-Negotiation Over A Single Twisted Wire Pair
MP5647	MP5647.C1	US	15/888,534	2/5/2018	101,77983	1/8/2019	In Force	Auto-Negotiation Over A Single Twisted Wire Pair
MP5647	MP5647C1C1	US	16/233,255	12/27/2018			Pending	Auto-Negotiation Over A Single Twisted Wire Pair
MP5649	MP5649	US	14/583,899	12/29/2014			Pending	Paging Auto-Acknowledgement
MP5654	MP5654	US	14/567,971	12/11/2014	939,7218	7/19/2016	In Force	METHOD AND APPARATUS FOR MITIGATING EFFECTS OF PARASITIC
MP5654	MP5654WO	WO	PCT/US2014/071	12/12/2014			Pending	METHOD AND APPARATUS FOR MITIGATING EFFECTS OF PARASITIC
MP5654	MP5654WOCN	CN	201480072731.1	12/12/2014	21,201,480,0727	4/12/2019	In Force	METHOD AND APPARATUS FOR MITIGATING EFFECTS OF PARASITIC

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP5654T	MP5654TW	TW	103145827	12/26/2014	1638409	10/11/2018	In Force	Method for FinFET Parasitic Capacitance Optimization
MP5656	MP5656	US	14/596,786	1/14/2015	10050892	8/14/2018	In Force	METHOD AND APPARATUS FOR PACKET CLASSIFICATION
MP5656	MP5656CN	CN	201510019242.1	1/14/2015			Pending	METHOD AND APPARATUS FOR PACKET CLASSIFICATION
MP5660	MP5660	US	14/580,536	12/23/2014	9357193	5/31/2016	In Force	METHOD AND APPARATUS FOR COMPENSATING FOR COLOR IMBAL
MP5660	MP5660WO	WO	PCT/US2014/07	12/23/2014			Pending	METHOD AND APPARATUS FOR COMPENSATING FOR COLOR IMBAL
MP5661	MP5661	US	14/593,537	1/9/2015	9401168	7/26/2016	In Force	CONTROL DEVICE FOR ACCESSING A STORAGE MEDIUM
MP5665	MP5665	US	14/572,093	12/16/2014	9420145	8/16/2016	In Force	System and Method for Tone Mapping of Images
MP5665	MP5665WO	WO	PCT/US2014/07	12/16/2014			Pending	System and Method for Tone Mapping of Images
MP5671	MP5671	US	14/596,322	1/14/2015	9530466	12/27/2016	In Force	SYSTEM AND METHOD FOR MEMORY ACCESS DYNAMIC MODE SWIT
MP5674	MP5674	US	14/587,682	1/15/2015	9672154	6/6/2017	In Force	Methods and Apparatus for Determining Memory Access Patterns for
MP5675	MP5675	US	14/600,703	1/20/2015	9570095	2/14/2017	In Force	SYSTEMS AND METHODS FOR INSTANTANEOUS NOISE ESTIMATION
MP5680	MP5680	US	14/588,047	1/15/2015	9937702	4/10/2018	In Force	METHOD AND APPARATUS FOR CALIBRATING A THREE DIMENSIONA
MP5683	MP5683	US	14/620,702	2/12/2015	9350396	5/24/2016	In Force	Systems and Methods for Reducing Signal Distortion in Wireless Com
MP5683	MP5683CN	CN	201510130480.1	3/24/2015	212015101304	8/30/2019	In Force	SYSTEMS AND METHODS FOR REDUCING SIGNAL DISTORTION IN WI
MP5684	MP5684	US	14/709,991	5/12/2015	9712976	7/18/2017	In Force	METHOD AND SYSTEM FOR POSITION UPDATING USING CACHED RA
MP5685	MP5685	US	14/645,964	3/12/2015	9730149	8/8/2017	In Force	DEVICE AND METHOD FOR MOBILE COMMUNICATION
MP5685	MP5685CN	CN	201510110124.1	3/12/2015	212015101101	6/25/2019	In Force	DEVICE AND METHOD FOR MOBILE COMMUNICATION
MP5686	MP5686CN	CN	201410855807.1	12/31/2014	212014108558	8/30/2019	In Force	METHOD FOR INNER LOOP POWER CONTROL OF A DUAL-CARD DUAL
MP5688	MP5688CN	CN	201510110152.1	3/12/2015	212015101101	9/3/2019	In Force	METHOD AND APPARATUS FOR GEO-FENCE DETECTION
MP5688	MP5688.1	US	15/056,569	2/29/2016	10067237	9/4/2018	In Force	METHOD AND APPARATUS FOR GEO-FENCE DETECTION
MP5692	MP5692	US	14/603,003	1/22/2015	9530604	3/7/2017	In Force	CURRENT COMPARATOR
MP5693	MP5693	US	14/606,813	1/27/2015	9431965	8/30/2016	In Force	SELECTABLE-INPUT-IMPEDANCE RADIO-FREQUENCY RECEPTION AM
MP5696	MP5696	US	14/599,828	1/19/2015	9787227	10/10/2017	In Force	CONTROLLED INDUCTIVE SENSE SYSTEM
MP5697	MP5697	US	14/604,589	1/23/2015	9876450	1/23/2018	In Force	ACTIVE CLAMP FOR MOTOR DRIVER
MP5697	MP5697WO	WO	PCT/IB2015/00	1/24/2015			Pending	ACTIVE CLAMP FOR MOTOR DRIVER
MP5697	MP5697WO/CN	CN	201580015226.1	1/24/2015			Pending	ACTIVE CLAMP FOR MOTOR DRIVER
MP5698	MP5698	US	14/633,086	2/26/2015	9800217	10/24/2017	In Force	INTEGRATED AMPLIFIER SYSTEM
MP5699	MP5699	US	14/613,218	2/3/2015	9659851	5/23/2017	In Force	METHOD AND APPARATUS FOR IMPROVING THE RELIABILITY OF A C
MP5699	MP5699WO	WO	PCT/US2015/01	2/4/2015			Pending	METHOD AND APPARATUS FOR IMPROVING THE RELIABILITY OF A C
MP5699	MP5699WO/CN	CN	201580006774.1	2/4/2015			Pending	METHOD AND APPARATUS FOR IMPROVING THE RELIABILITY OF A C
MP5699	MP5699WO/KR	KR	10-2016-702081	2/4/2015			Pending	Structure to improve reliability of TGV
MP5699T	MP5699TW	TW	104104114	2/6/2015			Pending	Structure to improve reliability of TGV
MP5700	MP5700	US	14/605,906	1/26/2015	9766682	9/19/2017	In Force	WAKEUP TIME SYSTEM USING DRIFT ESTIMATION
MP5703	MP5703CN	CN	201510103211.1	3/9/2015			Pending	METHOD AND APPARATUS FOR COLLECTING LOCATION DATA USING

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MP5712K	MP5712KR	KR	10-2015-001436	1/29/2015			Pending	METHOD AND APPARATUS FOR VALID ENCODING
MP5714	MP5714	US	14/608,333	1/29/2015	9319048	4/19/2016	In Force	CLOCK DIVIDER
MP5714K	MP5714KR	KR	10-2015-001436	1/29/2015			Pending	CLOCK DIVIDER
MP5718	MP5718	US	14/609,060	1/29/2015	9954444	4/24/2018	In Force	PFM-PWM CONTROL FOR POWER REGULATORS
MP5719	MP5719	US	14/607,856	1/28/2015	9264054	2/16/2016	In Force	DLL LOCK DETECTOR
MP5723	MP5723	US	14/608,455	1/29/2015	9698760	7/4/2017	In Force	CONTINUOUS-TIME ANALOG DELAY DEVICE
MP5725	MP5725	US	14/613,072	2/3/2015	9937663	4/10/2018	In Force	METHOD FOR CONTROLLING PRINTING IN THREE-DIMENSIONAL PRINTING
MP5727	MP5727CN	CN	201510125986.3	3/20/2015			Pending	METHOD AND APPARATUS FOR DETERMINING A TOUCHED AREA IN
MP5730	MP5730WO	WO	PCT/US2015/01	2/6/2015			Pending	HIGH-BANDWIDTH DRAM USING INTEROSER AND STACKING
MP5732	MP5732	US	14/707,694	5/8/2015	9939969	4/10/2018	In Force	Systems and Methods for Capacitive Touch Detection
MP5732	MP5732CN	CN	201510236688.1	5/11/2015			Pending	SYSTEMS AND METHODS FOR CAPACITIVE TOUCH DETECTION
MP5736	MP5736	US	14/657,770	3/13/2015	9558096	1/31/2017	In Force	METHOD AND APPARATUS FOR SUPPORTING PERFORMANCE ANALY
MP5736	MP5736CN	CN	201510126355.3	3/20/2015	ZI 201510126	8/16/2019	In Force	METHOD AND APPARATUS FOR SUPPORTING PERFORMANCE ANALY
MP5738	MP5738	US	14/617,145	2/9/2015	9854607	12/26/2017	In Force	Method And Apparatus For Performing Dynamic Clear Channel Asses
MP5740	MP5740	US	14/621,310	2/12/2015	9608527	3/28/2017	In Force	OVERSHOOT PREVENTION FOR DEEP DIMMING STARTUP
MP5743	MP5743	US	14/621,182	2/12/2015	9849631	12/26/2017	In Force	THREE DIMENSIONAL (3D) PRINTING BY SELECTIVE ROTATION OF A B
MP5746	MP5746	US	14/627,354	2/20/2015	9444510	9/13/2016	In Force	Method And Apparatus For Incorporating Passive Devices In An Integ
MP5746	MP5746WO	WO	PCT/US2015/01	2/20/2015			Pending	Method And Apparatus For Incorporating Passive Devices In An Integ
MP5746T	MP5746TW	TW	104105917	2/24/2015			Pending	Method And Apparatus For Incorporating Passive Devices In An Integ
MP5747	MP5747	US	14/621,215	2/12/2015	9268506	2/23/2016	In Force	METHODS AND APPARATUS FOR DETERMINING THE STATUS OF A PE
MP5748	MP5748	US	14/629,777	2/24/2015	9559675	1/31/2017	In Force	CURRENT SHAPING SCHEME IN TRIAC DIMMABLE LED DRIVER
MP5749	MP5749	US	14/628,606	2/23/2015	9690425	6/27/2017	In Force	Systems and Methods for Tracking Baseline Signals for Touch Detecti
MP5749	MP5749CN	CN	201510178192.1	2/25/2015	ZI 201510178	8/2/2019	In Force	Systems and Methods for Tracking Baseline Signals for Touch Detecti
MP5753	MP5753	US	14/629,750	2/24/2015	9293429	12/1/2015	In Force	SYSTEMS AND METHODS FOR USING MARKOV DISTRIBUTION CODES
MP5754	MP5754	US	14/625,832	2/19/2015	9749237	8/29/2017	In Force	METHOD AND APPARATUS FOR AGGREGATING AND ENCODING RECE
MP5754	MP5754WO	WO	PCT/US2015/01	2/19/2015			Pending	Method And Apparatus For Aggregating And Encoding Received Sym
MP5754	MP5754WOEP	EP	15709788.2	2/19/2015			Pending	Method and Apparatus for Aggregating and Encoding Received Symb
MP5754	MP5754WOJP	JP	2016-549065	2/19/2015			Pending	Method And Apparatus For Aggregating And Encoding Received Sym
MP5758	MP5758	US	14/639,450	3/5/2015	9854468	12/26/2017	In Force	METHOD AND APPARATUS FOR ESTIMATING DISTANCE BETWEEN NE
MP5758	MP5758.CI	US	15/845,176	12/18/2017			Pending	METHOD AND APPARATUS FOR PERFORMING A FINE TIMING MEASU
MP5758	MP5758WO	WO	PCT/US2015/01	3/5/2015			Pending	Method And Apparatus For Estimating Distance Between Network De
MP5764	MP5764	US	14/639,477	3/5/2015	10009736	6/26/2018	In Force	Method And Apparatus For Performing Joint Transmissions In A Wirel
MP5767	MP5767D1	US	15/226,794	8/15/2016			Pending	METHOD FOR FORMING A VIA STRUCTURE USING A DOUBLE-SIDE LA
MP5767	MP5767WO	WO	PCT/US2015/01	3/10/2015			Pending	METHOD FOR FORMING A VIA STRUCTURE USING A DOUBLE-SIDE LA

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MP5767	MP5767WOJP	JP	2016-552520	3/10/2015			Pending	Multi-depth double side via structure
MP5767	MP5767WOKR	KR	10-2016-702335	3/10/2015			Pending	Multi-depth double side via structure
MP5767	MP5767TW	TW	104107627	3/10/2015			Pending	Multi-depth double side via structure
MP5773	MP5773	US	14/663,751	3/10/2015	9959936	5/1/2018	In Force	Temperature-Based Memory Access
MP5774	MP5774	US	14/643,776	3/10/2015	9820117	11/14/2017	In Force	Methods and Apparatus for Advertising a Service in a Wireless Netwo
MP5775	MP5775	US	14/657,454	3/13/2015	9674094	6/6/2017	In Force	Method and Apparatus for Controlling Transmission of Packets Over
MP5780	MP5780CN	CN	201510745238.1	11/5/2015			Pending	METHOD AND APPARATUS FOR SIGNAL SYNCHRONIZATION
MP5781	MP5781	US	14/695,386	4/24/2015	9841855	12/12/2017	In Force	Systems and Methods for Capacitive Touch Detection
MP5781	MP5781CN	CN	201510264877.1	4/27/2015	212015102048	5/31/2019	In Force	Systems and Methods for Capacitive Touch Detection
MP5782	MP5782	US	14/660,724	3/17/2015	9402238	7/26/2016	In Force	METHOD AND APPARATUS FOR CONTROLLING A TRANSMIT POWER
MP5784	MP5784	US	14/644,846	3/11/2015	9831772	11/28/2017	In Force	Transformer-Less System to Regulate Load Current
MP5786	MP5786	US	14/969,316	12/15/2015			Pending	Secure Interrupt Handling Between Security Zones
MP5787	MP5787	US	14/708,671	5/11/2015	9659348	5/23/2017	In Force	METHODS AND SYSTEMS FOR HIGH DEFINITION SCALING WITH LOW
MP5790	MP5790	US	14/661,948	3/18/2015	9673905	6/6/2017	In Force	METHOD AND DEVICE FOR DEMARCATING DATA BURSTS
MP5790	MP5790C1	US	15/614,145	6/5/2017	10277325	4/30/2019	In Force	METHOD AND DEVICE FOR DEMARCATING DATA BURSTS
MP5791	MP5791	US	14/660,147	3/17/2015	9661523	5/23/2017	In Force	Method And Apparatus For Dynamically Adapting A Transmission Rat
MP5792	MP5792	US	14/662,159	3/18/2015	1020064	2/5/2019	In Force	SYSTEMS AND METHODS FOR BIT-FLIPPING DECODERS WITH PARTIA
MP5795	MP5795	US	14/659,320	3/16/2015	9444964	9/13/2016	In Force	METHOD AND APPARATUS FOR SCANNING AN IMAGE
MP5798	MP5798	US	14/667,579	3/24/2015	10038456	7/31/2018	In Force	Decoders with Look Ahead Logic
MP5799	MP5799	US	14/639,140	3/5/2015	9628163	4/18/2017	In Force	LOW-COMPLEXITY COMMUNICATION TERMINAL WITH ENHANCED R
MP5800	MP5800	US	14/668,319	3/25/2015	10365966	7/30/2019	In Force	METHODS AND SYSTEMS FOR WORDLINE BASED ENCODING AND DE
MP5801	MP5801	US	14/666,666	3/24/2015	9363111	6/7/2016	In Force	DECISION FEEDBACK EQUALIZER (DFE) UTILIZING ERROR PROPAGATI
MP5802	MP5802	US	14/669,350	3/26/2015	9645936	5/9/2017	In Force	SYSTEM AND METHOD FOR INFORMING HARDWARE TO UNIT WRITI
MP5807	MP5807	US	14/676,694	4/1/2015	9766643	9/19/2017	In Force	VOLTAGE REGULATOR WITH STABILITY COMPENSATION
MP5808	MP5808	US	14/644,644	3/11/2015	9591458	3/7/2017	In Force	METHOD AND APPARATUS FOR ADAPTIVE POSITIONING
MP5808	MP5808CN	CN	201510109438.1	3/12/2015	212015101094	9/25/2018	In Force	METHOD AND APPARATUS FOR ADAPTIVE POSITIONING
MP5808	MP5808RE	US	15/947,517	4/6/2018			Pending	METHOD AND APPARATUS FOR ADAPTIVE POSITIONING
MP5809	MP5809	US	14/678,577	4/3/2015	9673185	10/18/2016	In Force	Impedance Matching For Variable Impedance Antennas
MP5809	MP5809WO	WO	PCT/US2015/02	4/3/2015			Pending	Impedance Matching For Variable Impedance Antennas
MP5810	MP5810	US	14/627,492	2/20/2015	9847291	12/19/2017	In Force	Circuits Incorporating Integrated Passive Devices Having Inductances
MP5810	MP5810WO	WO	PCT/US2015/01	2/20/2015			Pending	Circuits Incorporating Integrated Passive Devices Having Inductances
MP5810T	MP5810TW	TW	104105908	2/24/2015	1651826	2/21/2019	In Force	Circuits Incorporating Integrated Passive Devices Having Inductances
MP5811	MP5811	US	14/657,479	3/13/2015	9554208	1/24/2017	In Force	Concurrent Sound Source Localization of Multiple Speakers
MP5813	MP5813	US	14/676,748	4/1/2015	9530561	3/7/2017	In Force	POWER AMPLIFIER

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MP5813	MP5813WO	WO	PCT/US2015/02	4/1/2015			Pending	POWER AMPLIFIER
MP5814	MP5814	US	14/671,290	3/27/2015	9503069	11/22/2016	In Force	SELF RESETTNG LATCH
MP5814	MP5814C1	US	15/358,617	11/22/2016	9941872	4/10/2018	In Force	SELF RESETTNG LATCH
MP5816	MP5816CN	CN	201510208410.3	4/28/2015	ZL2015102084	5/7/2019	In Force	A METHOD, DEVICE, AND USER EQUIPMENT FOR RECEIVING SIGNAL
MP5818	MP5818	US	14/679,957	4/6/2015	9723582	8/1/2017	In Force	Methods and Apparatus for Synchronizing Beacon Transmissions in a
MP5818	MP5818C1	US	15/409,375	1/18/2017	9854549	12/26/2017	In Force	Methods and Apparatus for Synchronizing Beacon Transmissions in a
MP5819	MP5819WO	WO	PCT/IB2015/006	4/8/2015			Pending	System and Method for Automatically Pairing Wired Devices
MP5820	MP5820	US	14/724,261	5/28/2015	9846499	12/19/2017	In Force	TOUCH PANEL AND TOUCH DETECTION CIRCUIT
MP5820	MP5820CN	CN	201510290372.1	5/29/2015			Pending	TOUCH PANEL AND TOUCH DETECTION CIRCUIT
MP5820E	MP5820EP	EP	15169603.6	5/28/2015	2958000	4/11/2018	In Force	Touch Panel and Touch Detection Circuit
MP5820E	MP5820EPD1	EP	16193979.8	5/28/2015	3144790	1/19/2019	In Force	Touch Panel and Touch Detection Circuit
MP5820E	MP5820EPD1DE	DE	16193979.8	5/28/2015	602015025165	1/19/2019	In Force	Touch Panel and Touch Detection Circuit
MP5820E	MP5820EPD1FR	FR	16193979.8	5/28/2015	3144790	1/19/2019	In Force	Touch Panel and Touch Detection Circuit
MP5820E	MP5820EPD1GB	GB	16193979.8	5/28/2015	3144790	1/19/2019	In Force	Touch Panel and Touch Detection Circuit
MP5820E	MP5820EPDE	DE	15169603.6	5/28/2015	602015009742	4/11/2018	In Force	Touch Panel and Touch Detection Circuit
MP5820E	MP5820EPFR	FR	15169603.6	5/28/2015	2958000	4/11/2018	In Force	Touch Panel and Touch Detection Circuit
MP5820T	MP5820TW	TW	104117545	5/29/2015	1658384	5/1/2019	In Force	TOUCH PANEL AND TOUCH DETECTION CIRCUIT
MP5822	MP5822	US	14/687,267	4/15/2015	9437320	9/6/2016	In Force	Joint Detecting And Decoding System For Nonvolatile Semiconductor
MP5825	MP5825	US	14/687,705	4/15/2015	10225338	3/5/2019	In Force	PEER TO PEER RANGING EXCHANGE
MP5825	MP5825C1	US	15/419,499	1/30/2017	9974040	5/15/2018	In Force	PEER TO PEER RANGING EXCHANGE
MP5826	MP5826	US	14/687,620	4/15/2015	9443551	9/13/2016	In Force	METHOD AND SYSTEM FOR WRITING AND READING CLOSELY-SPACE
MP5826	MP5826C1	US	15/222,356	8/9/2016	9734848	8/15/2017	In Force	METHOD AND SYSTEM FOR WRITING AND READING CLOSELY-SPACE
MP5826	MP5826C1C1	US	15/646,209	7/11/2017	10224063	3/5/2019	In Force	METHOD AND SYSTEM FOR WRITING AND READING CLOSELY-SPACE
MP5828	MP5828	US	14/692,561	4/21/2015	9852257	12/26/2017	In Force	METHOD AND SYSTEM FOR DESIGNING A SEMICONDUCTOR CHIP BA
MP5829	MP5829	US	14/706,286	5/7/2015	9647314	5/9/2017	In Force	Structure Of Dual Directional Couplers For Multiple-Band Power Amp
MP5830	MP5830	US	14/681,485	4/8/2015	10097204	10/9/2018	In Force	Low-Density Parity-Check Codes For WiFi Networks
MP5830	MP58301	US	14/706,335	5/7/2015	9559722	1/31/2017	In Force	Network Devices And Methods Of Generating Low-Density Parity-Check
MP5833	MP5833	US	14/731,194	6/4/2015	9641127	5/2/2017	In Force	OPERATIONAL TRANSDUCANCE AMPLIFIER OF IMPROVED LINE
MP5834	MP5834	US	14/731,178	6/4/2015	9419587	8/16/2016	In Force	METHOD AND APPARATUS TO RECONFIGURE A FILTER
MP5835	MP5835	US	14/724,475	5/9/2014	9414932	8/9/2016	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP5835	MP5835D1	US	15/221,376	8/8/2016	10212759	2/19/2019	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP5835	MP5835WO	WO	PCT/US2014/03	5/9/2014			Pending	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP5836	MP5836	US	14/694,970	4/23/2015	9531360	12/27/2016	In Force	HIGH SPEED COMPARATOR
MP5840	MP5840	US	14/700,216	4/30/2015	10003925	6/19/2018	In Force	Low-Power Device Positioning Method Using Position Updates Calcul

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MP5844	MP5844CN	CN	201510220075.1	4/30/2015			Pending	SENSOR CENTRAL CONTROL SYSTEM AND METHOD
MP5845	MP5845	US	14/689,496	4/29/2015	9368433	6/14/2016	In Force	METHOD AND APPARATUS FOR MOUNTING SOLDER BALLS TO AN EX
MP5845	MP5845WO	WO	PCT/US2015/02	4/30/2015			Pending	METHOD AND APPARATUS FOR MOUNTING SOLDER BALLS TO AN EX
MP5845T	MP5845TW	TW	104113928	4/30/2015			Pending	Method of converting exposed pad and routing trace IC package to B
MP5846	MP5846	US	14/693,744	4/22/2015	9883472	1/30/2018	In Force	Methods and Apparatus for Beaconsing Within a Wireless Network Ba
MP5847	MP5847	US	14/701,208	4/30/2015			Pending	ADAPTIVE ORTHOGONAL FREQUENCY DIVISION MULTIPLEXING (OFDM)
MP5848	MP5848	US	14/693,603	4/22/2015	9692576	6/27/2017	In Force	METHODS AND SYSTEMS FOR TRANSMITTING HYBRID BEACON SIGN
MP5850	MP5850	US	14/702,485	5/1/2015	10861721	7/23/2019	In Force	METHODS AND NETWORK DEVICE FOR UNCODED BIT PROTECTION I
MP5850	MP5850C1	US	15/419,628	1/30/2017	10193567	1/29/2019	In Force	METHODS AND NETWORK DEVICE FOR UNCODED BIT PROTECTION I
MP5850	MP5850C2	US	16/518,460	7/22/2019			Pending	METHODS AND NETWORK DEVICE FOR UNCODED BIT PROTECTION I
MP5851	MP5851	US	14/700,755	4/30/2015	9836235	12/5/2017	In Force	Low Power Distributed Memory Network
MP5851	MP5851WO	WO	PCT/US2015/02	4/30/2015			Pending	Low Power Distributed Memory Network
MP5851	MP5851WO CN	CN	201580023659.1	4/30/2015	ZL2015800236	7/12/2019	In Force	LOW POWER DISTRIBUTED MEMORY NETWORK
MP5851	MP5851WOEP	EP	15724127.4	4/30/2015			Pending	Low Power Distributed Memory Network
MP5851	MP5851WOJP	JP	2016-562797	4/30/2015	6519089	5/10/2019	In Force	Low Power Distributed Memory Network
MP5851	MP5851WO KR	KR	1020167034269	4/30/2015			Pending	Low Power Distributed Memory Network
MP5852	MP5852	US	14/704,065	5/5/2015	9323314	4/26/2016	In Force	System and Method for Reducing Energy Usage in a Mobile Communi
MP5856	MP5856	US	14/702,078	5/1/2015	9454991	9/27/2016	In Force	Caching Systems And Methods For Hard Disk Drives And Hybrid Drive
MP5856	MP5856C1	US	15/204,368	7/7/2016	9733841	8/15/2017	In Force	Caching Systems And Methods For Hard Disk Drives And Hybrid Drive
MP5856	MP5856C1C1C1	US	16/119,182	8/31/2018			Pending	METHOD AND APPARATUS FOR CACHING DATA IN AN SOLID STATE D
MP5856	MP5856-US-CN-C	US	15/676,395	8/14/2017	10067687	9/4/2018	In Force	METHOD AND APPARATUS FOR STORING DATA IN A STORAGE SYSTE
MP5856	MP5856WO	WO	PCT/US2015/02	5/1/2015			Pending	Caching Systems And Methods For Hard Disk Drives And Hybrid Drive
MP5856	MP5856WO CN	CN	201580022274.1	5/1/2015	ZL2015800222	8/30/2019	In Force	CACHING SYSTEMS AND METHODS FOR HARD DISK DRIVES AND HYB
MP5856	MP5856WOEP	EP	15728666.7	5/1/2015			Pending	Caching Systems and Methods for Hard Disk Drives and Hybrid Drives
MP5857	MP5857	US	14/466,881	8/22/2014	8988800	3/24/2015	In Force	ERROR CORRECTION FOR STORAGE DEVICES
MP5867	MP5867	US	14/712,465	5/14/2015	9755667	9/5/2017	In Force	METHODS AND SYSTEMS FOR PARALLELIZING HIGH THROUGHPUT IT
MP5868	MP5868	US	14/710,092	5/12/2015	9484871	11/1/2016	In Force	COMPLEX BANDPASS FILTER HAVING A TRANSFER FUNCTION WITH T
MP5870	MP5870	US	14/703,356	5/4/2015	9324621	4/26/2016	In Force	PROVIDING SHALLOW TRENCH ISOLATION STRUCTURES THROUGH A
MP5873	MP5873	US	14/718,754	5/21/2015	9654066	5/16/2017	In Force	Common-Source Power Amplifiers
MP5873	MP5873WO	WO	PCT/US2015/03	5/22/2015			Pending	Common-Source Power Amplifiers
MP5874	MP5874	US	14/837,309	8/27/2015	9872166	1/16/2018	In Force	APPARATUS AND METHOD FOR PROVIDING POSITIONING DATA BAS
MP5874	MP5874CN	CN	201510659484.1	10/12/2015			Pending	APPARATUS AND METHOD FOR PROVIDING POSITIONING DATA BAS
MP5882	MP5882	US	14/730,142	6/3/2015	9768743	9/19/2017	In Force	MEMORY EFFECT REDUCTION USING LOW IMPEDANCE CASCADE BIA
MP5890	MP5890	US	14/730,479	6/4/2015	9521025	12/13/2016	In Force	System and Method for Detecting Ternary Sequences

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MP5894	MP5894	US	14/746,472	6/22/2015	9391624	7/12/2016	In Force	METHOD AND APPARATUS FOR AVOIDING DEAD ZONE EFFECTS IN DI
MP5897	MP5897	US	14/749,492	6/24/2015	9431052	8/30/2016	In Force	TWO DIMENSIONAL MAGNETIC RECORDING SYSTEMS, DEVICES AND
MP5897	MP5897C1	US	15/236,023	8/12/2016	9738221	8/8/2017	In Force	Two Dimensional Magnetic Recording System, Devices and Methods
MP5897	MP5897WO	WO	PCT/IB2015/054	6/25/2015			Pending	TWO DIMENSIONAL MAGNETIC RECORDING SYSTEMS, DEVICES AND
MP5898	MP5898	US	14/728,918	6/2/2015	10001830	6/19/2018	In Force	Input-Output Device Management Using Dynamic Clock Frequency
MP5898	MP5898CN	CN	201510330567.3	6/15/2015			Pending	INPUT-OUTPUT DEVICE MANAGEMENT USING DYNAMIC CLOCK FRE
MP5902	MP5902	US	14/962,803	12/8/2015	9746331	8/29/2017	In Force	METHOD AND APPARATUS FOR MAP MATCHING
MP5903	MP5903	US	14/733,296	6/8/2015	9960872	5/1/2018	In Force	Systems And Methods For Performing A Soft-Block Of A Queue Base
MP5904	MP5904	US	14/733,370	6/8/2015	9882823	1/30/2018	In Force	Systems And Methods For Blocking Transmission Of A Frame In A Net
MP5904	MP5904WO	WO	PCT/US2015/03	6/9/2015			Pending	Common-Source Power Amplifiers
MP5904	MP5904WOCN	CN	201580031181.1	6/9/2015			Pending	SYSTEMS AND METHODS FOR BLOCKING TRANSMISSION OF A FRAM
MP5904	MP5904WOEP	EP	15731784.3	6/9/2015	3155737	3/20/2019	In Force	Systems and Methods for Blocking Transmission of a Frame in a Netw
MP5904	MP5904WOEPDE	DE	15731784.3	6/9/2015	602015026747	3/20/2019	In Force	Systems and Methods for Blocking Transmission of a Frame in a Netw
MP5904	MP5904WOEPFR	FR	15731784.3	6/9/2015	3155737	3/20/2019	In Force	Systems and Methods for Blocking Transmission of a Frame in a Netw
MP5904	MP5904WOEPGB	GB	15731784.3	6/9/2015	3155737	3/20/2019	In Force	Systems and Methods for Blocking Transmission of a Frame in a Netw
MP5904	MP5904WOPJP	JP	2016-571730	6/9/2015	6575772	8/30/2019	In Force	Systems And Methods For Blocking Transmission Of A Frame In A Net
MP5906	MP5906	US	14/797,393	7/13/2015	9424880	8/23/2016	In Force	METHOD AND APPARATUS FOR DETERMINING READ-HEAD DEVIATIO
MP5906	MP5906.C1C1	US	15/861,249	1/3/2018	10147450	12/4/2018	In Force	METHOD AND APPARATUS FOR DETERMINING READ-HEAD DEVIATIO
MP5906	MP5906.C1	US	15/215,052	7/20/2016	9875762	1/23/2018	In Force	METHOD AND APPARATUS FOR DETERMINING READ-HEAD DEVIATIO
MP5906	MP5906C1C1C1	US	16/173,928	10/29/2018	10388313	8/20/2019	In Force	METHOD AND APPARATUS FOR DETERMINING READ-HEAD DEVIATIO
MP5906	MP5906WO	WO	PCT/US2015/04	7/13/2015			Pending	METHOD AND APPARATUS FOR DETERMINING READ-HEAD DEVIATIO
MP5907	MP5907	US	14/795,649	7/9/2015	9577707	2/21/2017	In Force	METHOD AND DEVICE FOR STABILIZING IMPEDANCE ON A POWER-LI
MP5910	MP5910	US	14/799,930	7/15/2015	10425908	9/24/2019	In Force	Methods and Apparatus for Enabling Spontaneous Location Determin
MP5910	MP5910WO	WO	PCT/IB2015/001	7/15/2015			Pending	Methods and Apparatus for Enabling Spontaneous Location Determin
MP5911	MP5911	US	14/812,978	7/29/2015	9525460	12/20/2016	In Force	MECHANISM FOR XDSL-PLC INTERFERENCE MITIGATION
MP5920	MP5920	US	14/843,419	9/2/2015	9887543	2/6/2018	In Force	METHOD AND APPARATUS FOR WAVE DETECTION
MP5921	MP5921	US	14/814,203	7/30/2015	9979507	5/22/2018	In Force	METHODS AND NETWORK DEVICE FOR GENERATING COMMUNICATI
MP5921	MP5921C1	US	15/985,238	5/21/2018	10256941	4/9/2019	In Force	Power Saving in a Communication Network for a Motor Vehicle
MP5921	MP5921C1C1	US	16/362,219	3/22/2019			Pending	Power Saving in a Twisted Wire Pair Communication Network
MP5926	MP5926CN	CN	201510363944.3	6/26/2015			Pending	METHOD, APPARATUS AND USER EQUIPMENT FOR SCALING CLOCK F
MP5931	MP5931	US	15/233,578	8/10/2016	10332483	6/25/2019	In Force	SYSTEMS AND METHODS FOR PROVIDING INTELLIGENT DISPLAY BRI
MP5934	MP5934	US	14/835,372	8/25/2015	9922000	3/20/2018	In Force	PACKET BUFFER WITH DYNAMIC BYPASS
MP5935	MP5935	US	14/830,543	8/19/2015	9977751	5/22/2018	In Force	METHOD AND APPARATUS FOR ARBITRATING ACCESS TO SHARED RE
MP5937	MP5937	US	14/837,289	8/27/2015	9743233	8/22/2017	In Force	METHOD AND APPARATUS FOR GEO-FENCE MONITORING

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MP5940	MP5940	US	14/822,599	8/10/2015	9681073	6/13/2017	In Force	METHOD AND APPARATUS FOR COMPENSATION OF VEILING GLARE I
MP5942	MP5942	US	14/962,670	12/8/2015	9877157	1/23/2018	In Force	AUTONOMOUS IN-DEVICE GNSS AUGMENTATION SYSTEM
MP5942	MP5942CN	CN	201510939094.1	12/15/2015			Pending	AUTONOMOUS IN-DEVICE GNSS AUGMENTATION SYSTEM
MP5944	MP5944	US	15/089,534	2/22/2016	9866682	10/31/2017	In Force	MULTILEVEL CLASS-D AMPLIFIERS
MP5947	MP5947	US	14/833,782	8/24/2015	9602058	3/21/2017	In Force	METHOD AND APPARATUS FOR ENVELOPE TRACKING CALIBRATION
MP5951	MP5951	US	15/212,248	9/21/2016	10275249	4/30/2019	In Force	METHOD AND APPARATUS FOR PREDICTING END OF LOOP
MP5955	MP5955	US	15/233,335	8/10/2016	9748878	8/29/2017	In Force	Adaptive Brake timing for a Brushless Motor
MP5956	MP5956	US	15/199,541	6/30/2016	10164548	12/25/2018	In Force	Delay Control For A Power Rectifier
MP5956E	MP5956EP	EP	16178334.5	7/7/2016			Pending	Power Rectifier
MP5957	MP5957WO	WO	PCT/US2015/04	8/19/2015			Pending	STORAGE DEVICE CONTROLLER ARCHITECTURE
MP5959	MP5959	US	14/836,263	8/26/2015	9704228	7/11/2017	In Force	System and Method for Fractional Pixel Dilation and Erosion
MP5962	MP5962	US	14/834,708	8/25/2015	9553564	1/24/2017	In Force	FILTER FOR DATA RATE CONVERSION USING FEEDBACK WITH A DIFFE
MP5965	MP5965CN	CN	201610399507.1	6/7/2016			Pending	A METHOD, APPARATUS AND USER DEVICE FOR COMMUNICATION
MP5967	MP5967	US	14/814,165	8/12/2015	9456848	9/20/2016	In Force	Method And Apparatus For Adjusting A Phase Of A Transmitted Sign
MP5968	MP5968	US	14/823,647	8/11/2015	9621144	4/11/2017	In Force	CLOCK GATED FLIP-FLOP
MP5968	MP5968C1	US	15/464,600	3/21/2017	9876486	1/23/2018	In Force	CLOCK GATED FLIP-FLOP
MP5968	MP5968WO	WO	PCT/182015/056	8/18/2015			Pending	EFFICIENT STANDARD FLIP-FLOP
MP5968	MP5968WOON	CN	201580045543.1	8/18/2015			Pending	CLOCK GATED FLIP-FLOP
MP5968	MP5968WOEP	EP	15835527.1	8/18/2015			Pending	Clock Gated Flip-Flop
MP5974	MP5974	US	14/836,973	8/27/2015	9660743	5/23/2017	In Force	CHANNEL ESTIMATION BY SEARCHING OVER CHANNEL RESPONSE CA
MP5975	MP5975	US	14/834,718	8/25/2015	9485120	11/1/2016	In Force	METHOD AND APPARATUS FOR SIGNAL DETECTION AND BASELINE
MP5979	MP5979	US	14/831,531	8/20/2015	9558811	1/31/2017	In Force	DISTURB-PROOF STATIC RAM CELLS
MP5980	MP5980	US	14/843,142	9/2/2015	9414202	8/9/2016	In Force	SYSTEM AND METHOD FOR PROVIDING SHORT-RANGE, LOW-ENERG
MP5982	MP5982	US	14/835,446	8/25/2015	9536590	1/3/2017	In Force	SYSTEM AND METHOD OF MEMORY ELECTRICAL REPAIR
MP5982	MP5982C1	US	15/379,567	12/15/2016	9830957	11/28/2017	In Force	SYSTEM AND METHOD OF MEMORY ELECTRICAL REPAIR
MP5984	MP5984	US	14/843,934	9/2/2015	9929759	3/27/2018	In Force	INTEGRATED DUPLEXER
MP5985	MP5985	US	14/838,902	8/28/2015	9838225	12/5/2017	In Force	CHANNEL ESTIMATION USING DATA FRAMES
MP5988	MP5988	US	14/829,052	8/18/2015	9940945	4/10/2018	In Force	Method And Apparatus For Eliminating Music Noise Via A Nonlinear
MP5988	MP5988WO	WO	PCT/US2015/04	8/26/2015			Pending	Method And Apparatus For Eliminating Music Noise Via A Nonlinear
MP5988	MP5988WOON	CN	201580047301.1	8/26/2015			Pending	METHOD AND APPARATUS FOR ELIMINATING MUSIC NOISE VIA A N
MP5988	MP5988WOEP	EP	15766266.9	8/26/2015			Pending	Method and Apparatus for Eliminating Music Noise Via a Nonlinear A
MP5991	MP5991	US	15/153,666	5/12/2016	10075410	9/11/2018	In Force	APPARATUS AND METHODS FOR ASSIGNING INTERNETWORK ADDRE
MP5991J	MP5991JP	JP	2016-098163	5/16/2016			Pending	APPARATUS AND METHODS FOR ASSIGNING INTERNETWORK ADDRE
MP5994	MP5994	US	14/991,700	1/8/2016			Pending	UPLINK RESOURCE ALLOCATION IN A WLAN

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MP5995	MP5995	US	15/012,066	2/1/2016	10153786	12/11/2018	In Force	ITERATIVE DECODER WITH DYNAMICALLY-VARIABLE PERFORMANCE
MP5999	MP5999	US	15/013,002	2/2/2016	10349364	7/9/2019	In Force	System and Method for Providing Communication Coexistence Among
MP6002	MP6002	US	14/838,222	8/27/2015	9369122	6/14/2016	In Force	Low Phase Noise Technique for a Crystal Oscillator Circuit
MP6002	MP6002WQ	WO	PCT/US2015/04	8/27/2015			Pending	Low Phase Noise Technique for a Crystal Oscillator Circuit
MP6002	MP6002WQCN	CN	201580047547.3	8/27/2015			Pending	LOW PHASE NOISE TECHNIQUE FOR A CRYSTAL OSCILLATOR CIRCUIT
MP6002	MP6002WQEP	EP	15760583.3	8/27/2015			Pending	Low Phase Noise Technique for a Crystal Oscillator Circuit
MP6002	MP6002WQJP	JP	2017-509616	8/27/2015	6436599	11/22/2018	In Force	LOW PHASE NOISE TECHNIQUE FOR A CRYSTAL OSCILLATOR CIRCUIT
MP6013	MP6013	US	14/831,063	8/20/2015	9996461	6/12/2018	In Force	DYNAMIC DIFFERENTIAL COMPRESSION FOR STORAGE DEVICE LOOK
MP6018	MP6018	US	14/863,042	9/23/2015	10230425	3/12/2019	In Force	TRANSMISSION POWER LEVEL CONFIGURATION FOR CROSSTALK REDU
MP6019	MP6019	US	14/855,829	9/16/2015	9755665	9/5/2017	In Force	SYSTEMS AND METHODS FOR AN ITERATIVE DECODING SCHEME
MP6019	MP6019C1	US	15/693,709	9/1/2017	10411735	9/10/2019	In Force	SYSTEMS AND METHODS FOR AN ITERATIVE DECODING SCHEME
MP6021	MP6021CN	CN	201510727910.7	10/30/2015			Pending	METHOD AND APPARATUS FOR DETERMINING POSITIONING ASSISTAN
MP6026	MP6026	US	14/877,504	10/7/2015	9577708	2/21/2017	In Force	SYSTEMS AND METHODS FOR A TWISTED PAIR TRANSCIVER WITH C
MP6026	MP6026C1	US	15/434,947	2/16/2017	10090062	6/26/2018	In Force	SYSTEMS AND METHODS FOR A TWISTED PAIR TRANSCIVER WITH C
MP6026	MP6026C1C1	US	16/002,584	6/7/2018	10484046	11/19/2019	In Force	SYSTEMS AND METHODS FOR A TWISTED PAIR TRANSCIVER WITH C
MP6028	MP6028	US	14/883,262	9/23/2015	9859928	1/2/2018	In Force	Method and Apparatus for Detecting EMI Signals Using a Common M
MP6036	MP6036	US	15/201,046	7/1/2016	9874889	1/23/2018	In Force	VOLTAGE REGULATOR
MP6037	MP6037	US	15/198,802	6/30/2016	9866111	1/9/2018	In Force	REGULATED CHARGE PUMP CIRCUIT
MP6040	MP6040	US	15/041,790	2/11/2016			Pending	DATA DEMODULATION USING BIT SEPARATING AND METRIC CROSS
MP6044	MP6044	US	15/073,150	3/17/2016	10310089	6/4/2019	In Force	METHOD AND APPARATUS FOR UPDATING AND PROVIDING SATELLI
MP6044	MP6044CN	CN	201610176659.7	3/24/2016			Pending	METHOD AND APPARATUS FOR UPDATING AND PROVIDING SATELLI
MP6055	MP6055	US	14/957,622	12/3/2015	9872299	1/16/2018	In Force	OPTIMIZED TRANSMIT-POWER ALLOCATION IN MULTI-CARRIER TRA
MP6057	MP6057	US	14/956,475	12/2/2015	956105	3/14/2017	In Force	TIME TRACKING SCHEMES FOR REDUCED ISI IN MULTI-TRANSMISSIO
MP6058	MP6058	US	15/001,937	1/20/2016	9846471	12/19/2017	In Force	Systems and Methods for Power Management in Devices
MP6060	MP6060	US	15/097,081	4/12/2016			Pending	SYSTEMS AND METHODS FOR PROVISIONING DEVICES FOR WLAN
MP6061	MP6061	US	15/364,503	11/30/2016			Pending	SYSTEMS AND METHODS FOR TRANSMITTING AN ACCESS REQUEST V
MP6061	MP6061CN	CN	201611091645.1	12/1/2016			Pending	SYSTEMS AND METHODS FOR TRANSMITTING AN ACCESS REQUEST V
MP6061E	MP6061EP	EP	16201663.8	12/1/2016			Pending	Systems and Methods for Transmitting an Access Request via a Flexib
MP6062	MP6062	US	15/013,150	2/2/2016	9634652	4/25/2017	In Force	METHOD AND APPARATUS FOR CONFIGURING DELAY LINES
MP6063	MP6063	US	15/198,798	6/30/2016	9705536	7/11/2017	In Force	Tunable Balun
MP6063	MP6063WQ	WO	PCT/IB2016/054	7/7/2016			Pending	Tunable Balun
MP6065	MP6065	US	15/041,774	2/11/2016	9727330	8/1/2017	In Force	MULTI-STREAM DEMODULATION SCHEMES WITH PROGRESSIVE OPT
MP6066	MP6066	US	14/997,821	11/10/2015	950138	11/22/2016	In Force	INTERFERENCE CANCELLATION
MP6066	MP6066WQ	WO	PCT/US2015/06	11/10/2015			Pending	INTERFERENCE CANCELLATION

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MP6072	MP6072	US	15/041,834	2/11/2016	9917670	3/13/2018	In Force	DC CORRECTION IN UPLINK MULTI-USER TRANSMISSION
MP6075	MP6075	US	15/098,904	4/14/2016	9864699	1/9/2018	In Force	METHOD AND APPARATUS FOR COMPRESSING LUT
MP6077	MP6077CN	CN	2016100061101	1/5/2016			Pending	NETWORK-ASSISTED POWER CONSUMPTION REDUCTION IN A WIREL
MP6078	MP6078	US	15/017,010	2/5/2016	9748940	8/29/2017	In Force	PULSE WIDTH MODULATION
MP6078	MP6078CN	CN	2016100850941	2/14/2016			Pending	PULSE WIDTH MODULATION
MP6078E	MP6078EP	EP	16154780.7	2/9/2016			Pending	Pulse Width Modulation
MP6087	MP6087	US	14/967,912	12/14/2015	9818047	11/14/2017	In Force	System and Method for Color Enhancement
MP6088	MP6088CN	CN	2016105506391	7/13/2016			Pending	DUAL CONNECTIVITY FOR USER EQUIPMENT HAVING ONE CARRIER
MP6091	MP6091	US	15/004,044	1/22/2016	9866745	10/31/2017	In Force	Systems and Methods for Low Pass Filter Mismatch Calibration
MP6091	MP6091CN	CN	2016100807931	2/4/2016			Pending	SYSTEMS AND METHODS FOR LOW PASS FILTER MISMATCH CALIBRA
MP6092	MP6092	US	14/968,466	12/14/2015	9639425	5/2/2017	In Force	Signature-Based Sleep Recovery Operation Flow
MP6092	MP6092RE	US	16/480,724	5/1/2019			Pending	Signature-Based Sleep Recovery Operation Flow
MP6093	MP6093	US	14/997,283	1/15/2016	9912267	3/6/2018	In Force	ROTOR LOCK DETECTION
MP6097	MP6097CN	CN	2016100040681	1/4/2016			Pending	DEVICE-TO-DEVICE (D2D) ASSISTANCE TO CELLULAR NETWORK COM
MP6098	MP6098	US	14/981,985	12/29/2015	9872305	1/16/2018	In Force	OPTIMIZED CELLULAR TRANSMISSION SCHEDULING BASED ON KNO
MP6098	MP6098CN	CN	2016100090693	1/6/2016			Pending	OPTIMIZED CELLULAR TRANSMISSION SCHEDULING BASED ON KNO
MP6101	MP6101	US	15/040,764	2/10/2016			Pending	OPTIMIZED BLOOM FILTER
MP6103	MP6103	US	15/097,080	4/12/2016	10070289	9/4/2018	In Force	Client Association for Provisioning Devices in a Communication Netw
MP6112	MP6112	US	14/939,652	11/12/2015			Pending	Method and Apparatus for Modeling Interactions with Hardware Co
MP6114	MP6114	US	14/924,283	10/27/2015	10175870	1/8/2019	In Force	PROTOTYPING APPARATUS WITH CONFIGURABLE PINS AND CORRES
MP6115	MP6115	US	14/924,357	10/27/2015	9652222	5/16/2017	In Force	PROTOTYPING APPARATUS WITH BUILT-IN PROGRAMMING FACILITY
MP6121	MP6121	US	14/938,488	11/11/2015	9977673	5/22/2018	In Force	SYSTEMS AND METHODS FOR PROVIDING A PLATFORM-SPECIFIC PO
MP6122	MP6122	US	14/870,540	9/30/2015	9836387	12/5/2017	In Force	System and Method for Implementing a Debugger as a Server
MP6123	MP6123	US	14/838,680	8/28/2015	9870468	1/16/2018	In Force	System and Method for Segregating Data in a Dynamic Programming
MP6124	MP6124	US	14/849,827	9/10/2015	9934141	4/3/2018	In Force	System and Method for Efficiently Managing a Containment Hierarchy
MP6126	MP6126	US	14/941,067	11/13/2015			Pending	Method and Apparatus for a Source Device Accessing Devices on a N
MP6131	MP6131	US	14/963,045	12/8/2015			Pending	METHODS AND DEVICES FOR DETERMINING CHANNEL STATE
MP6131	MP6131CI	US	15/337,474	10/28/2016			Pending	METHODS AND DEVICES FOR DETERMINING CHANNEL STATE
MP6136	MP6136	US	15/061,755	3/4/2016	9876719	1/23/2018	In Force	METHOD AND APPARATUS FOR LOAD BALANCING IN NETWORK SWI
MP6141	MP6141	US	15/137,815	4/25/2016	10265911	4/23/2019	In Force	Image-Based Monitoring and Feedback System for Three-Dimensiona
MP6142	MP6142	US	15/157,073	5/17/2016	10471665	11/12/2019	In force	THREE DIMENSIONAL (3D) PRINTING WITH STITCHING OF ADJACENT
MP6147	MP6147	US	15/200,942	7/1/2016	10416316	9/17/2019	In Force	METHOD AND APPARATUS FOR DETERMINING FRAME TIMING
MP6148	MP6148	US	15/211,009	7/15/2016	10228468	3/12/2019	In Force	METHOD AND APPARATUS FOR MULTIPATH DETECTION
MP6149	MP6149	US	15/226,201	8/2/2016	9754638	9/5/2017	In Force	SENSE AMPLIFIER

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MP6150	MP6150	US	15/154,275	5/13/2016	10379865	8/13/2019	In Force	SELECTION OF INSTRUCTIONS TO BE ISSUED
MP6151	MP6151	US	15/166,179	5/26/2016	10025717	7/17/2018	In Force	MULTI-DIMENSIONAL PREFETCHING
MP6158	MP6158	US	14/974,367	12/18/2015	9601477	3/21/2017	In Force	INTEGRATED CIRCUIT HAVING SPARE CIRCUIT CELLS
MP6158	MP6158C1	US	15/444,705	2/28/2017	10204835	2/12/2019	In Force	INTEGRATED CIRCUIT HAVING SPARE CIRCUIT CELLS
MP6158	MP6158CN	CN	201510958917.1	12/18/2015			Pending	INTEGRATED CIRCUIT HAVING SPARE CIRCUIT CELLS
MP6158	MP6158KR	KR	10-2015-018185	12/18/2015			Pending	INTEGRATED CIRCUIT WITH NO LEAKAGE SPARE CELLS
MP6158	MP6158TW	TW	104142828	12/18/2015			Pending	INTEGRATED CIRCUIT WITH NO LEAKAGE SPARE CELLS
MP6159	MP6159	US	15/044,690	2/16/2016	9515634	12/6/2016	In Force	SELECTING OF A PRE-FILTER IN A RECEIVER OF A COMMUNICATION
MP6160	MP6160	US	15/094,933	4/8/2016	9793838	10/17/2017	In Force	AUTO PHASE ADJUSTMENT SYSTEM AND METHOD FOR MOTOR COM
MP6162	MP6162	US	15/005,323	1/25/2016	9900691	2/20/2018	In Force	METHOD AND CIRCUITRY FOR PROTECTING AN ELECTROMECHANICA
MP6162	MP6162KR	KR	10-2016-002651	3/4/2016			Pending	METHOD AND CIRCUITRY FOR PROTECTING AN ELECTROMECHANICA
MP6168	MP6168	US	15/046,366	2/17/2016	9934152	4/3/2018	In Force	METHOD AND APPARATUS TO USE HARDWARE ALIAS DETECTION AN
MP6172	MP6172	US	15/056,135	2/29/2016	9659759	7/4/2017	In Force	METHOD AND DEVICE FOR DETECTING FALSE MOVEMENT OF A MOB
MP6174	MP6174	US	15/046,315	2/17/2016	10079918	9/18/2018	In Force	BLOCK CODING SCHEME FOR PHY DATA UNIT TRANSMISSION
MP6174	MP6174WO	WO	PCT/US2016/01	2/17/2016			Pending	BLOCK CODING SCHEME FOR PHY DATA UNIT TRANSMISSION
MP6174	MP6174WOCN	CN	20168001214.1	2/17/2016			Pending	BLOCK CODING SCHEME FOR PHY DATA UNIT TRANSMISSION
MP6174	MP6174WCEP	EP	16707335.2	2/17/2016	3259863	7/10/2019	In Force	Block Coding Scheme for Phy Data Unit Transmission
MP6174	MP6174WCEPDE	DE	16707335.2	2/17/2016	602016016636	7/10/2019	In Force	Block Coding Scheme for Phy Data Unit Transmission
MP6174	MP6174WCEPFR	FR	16707335.2	2/17/2016	3259863	7/10/2019	In Force	Block Coding Scheme for Phy Data Unit Transmission
MP6174	MP6174WCEPGB	GB	16707335.2	2/17/2016	3259863	7/10/2019	In Force	Block Coding Scheme for Phy Data Unit Transmission
MP6175	MP6175	US	15/200,783	7/1/2016	10141902	11/27/2018	In Force	APPARATUS FOR AND METHOD OF GENERATING OUTPUT SIGNAL BA
MP6175	MP6175CN	CN	201610538797.1	7/8/2016			Pending	APPARATUS FOR AND METHOD OF GENERATING OUTPUT SIGNAL BA
MP6179	MP6179	US	15/073,538	3/17/2016	9946648	4/17/2018	In Force	CACHE COHERENCY PROTOCOL-BASED PREFETCHING METHODS AND
MP6181	MP6181	US	15/193,459	6/27/2016	10172092	1/1/2019	In Force	Systems and Methods for Providing a Sleep Clock on a Wireless Com
MP6184	MP6184	US	15/078,416	3/23/2016	9620228	4/11/2017	In Force	Monotonically Increasing Persistent Counters
MP6187	MP6187	US	14/988,506	1/5/2016	9921994	3/20/2018	In Force	Dynamic Credit Control in Multi-Traffic Class Communication System
MP6194	MP6194	US	15/047,426	2/18/2016	9972602	5/15/2018	In Force	METHOD AND APPARATUS FOR INTERCONNECTING STACKED DIES U
MP6194	MP6194TW	TW	105105185	2/22/2016			Pending	STACKING DIE METHOD WITH FAOUT PROCESS
MP6194	MP6194WO	WO	PCT/US2016/01	2/19/2016			Pending	METHOD AND APPARATUS FOR INTERCONNECTING STACKED DIES U
MP6201	MP6201	US	15/074,764	3/18/2016	9842051	12/12/2017	In Force	MANAGING ALIASING IN A VIRTUALLY INDEXED PHYSICALLY TAGGED
MP6202	MP6202CN	CN	201610274718.1	4/28/2016			Pending	WIRELESS COMMUNICATION METHOD AND APPARATUS AND USER E
MP6203	MP6203	US	15/238,879	8/17/2016	9875101	1/23/2018	In Force	INDUCTION VARIABLE IDENTIFICATION
MP6204	MP6204	US	15/223,136	8/10/2016	10103901	10/16/2018	In Force	Virtual Controller Area Network
MP6204	MP6204 C1	US	16/160,619	10/15/2018			Pending	Virtual Controller Area Network

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MP6204	MP6204TW	TW	105126128	8/16/2016	1623213	5/1/2018	In Force	Virtual Controller Area Network
MP6204	MP6204WO	WO	PCT/US2016/04	8/10/2016			Pending	Virtual Controller Area Network
MP6205	MP6205	US	14/972,776	12/17/2015	9866425	1/9/2018	In Force	SYSTEMS AND METHODS FOR OPERATIONS, ADMINISTRATION AND
MP6205	MP6205C1	US	15/864,228	1/8/2018	10039864	10/8/2019	In Force	SYSTEMS AND METHODS FOR OPERATIONS, ADMINISTRATION AND
MP6205	MP6205C1C1	US	16/594,680	10/7/2019			Pending	SYSTEMS AND METHODS FOR OPERATIONS, ADMINISTRATION AND
MP6207	MP6207	US	15/019,937	2/9/2016	9667145	5/30/2017	In Force	Fast Transient Response for Switching Regulators
MP6207	MP6207C1	US	15/019,946	2/9/2016	9729057	8/8/2017	In Force	Fast Transient Response for Switching Regulators
MP6207	MP6207C2	US	15/019,949	2/9/2016	9667146	5/30/2017	In Force	Fast Transient Response for Switching Regulators
MP6207	MP6207C3	US	15/019,950	2/9/2016	9941791	4/10/2018	In Force	Fast Transient Response for Switching Regulators
MP6211	MP6211	US	15/157,903	5/18/2016	9953678	4/24/2018	In Force	Servo Gray Code Quality Detector
MP6212	MP6212	US	10/752,013	1/7/2004	6879124	4/12/2005	In Force	Method To Detect The True Zero-Crossing Points Of The Phase Back
MP6213	MP6213	US	12/063,461	8/10/2005	7968392	3/15/2011	In Force	Wireless Transportation Protocol
MP6213	MP6213CN	CN	200580051304	8/10/2005	ZL2005800513	11/3/2010	In Force	WIRELESS TRANSPORTATION PROTOCOL
MP6215	MP6215SGP	JP	2010510267	5/9/2008	5957649	7/1/2016	In Force	Low Profile Spindle Motor
MP6215	MP6215SGUS	US	12/601,782	5/9/2008	8946952	2/3/2015	In Force	Low Profile Spindle Motor
MP6217	MP6217	US	14/342,111	8/31/2012	9202512	12/1/2015	In Force	Data Storage Device
MP6217	MP6217CN	CN	201280054156	8/31/2012	ZL2012800541	1/2/2018	In Force	DATA STORAGE DEVICE
MP6218	MP6218	US	13/943,635	7/16/2013	9197144	11/24/2015	In Force	Method And System For Controlling A Motor
MP6218	MP6218SG	SG	2013054366	7/16/2013	196731	5/12/2015	In Force	Method And System For Controlling A Motor
MP6219	MP6219D1	US	14/494,499	9/23/2014	9007713	4/14/2015	In Force	Medium, Method Of Fabricating A Medium, Recording System And M
MP6219	MP6219D1C1	US	14/684,731	4/13/2015	9299377	3/29/2016	In Force	Storage Medium For Storing Data And Servo Information
MP6220	MP6220	US	13/683,174	11/21/2012	9336829	5/10/2016	In Force	Data Recording Medium, Method For Generating A Reference Clock S
MP6221	MP6221	US	13/684,116	11/21/2012	9129649	9/8/2015	In Force	Data Storage Device And A Method For Providing A Synchronization S
MP6222	MP6222C1	US	14/580,911	12/23/2014	9324369	4/26/2016	In Force	Data Recording Medium And Method For Generating A Reference Clo
MP6223	MP6223	US	14/359,251	11/21/2012	9111574	8/18/2015	In Force	Data Storage Device And Method For Generating A Reference Clock S
MP6225	MP6225	US	13/684,033	11/21/2012	9177598	11/3/2015	In Force	Device, Method Of Fabricating A Media And Method Of Servo Writin
MP6226	MP6226	US	13/684,104	11/21/2012	8786968	7/22/2014	In Force	Data Storage Device Including A Recording Channel A Detector, And
MP6227	MP6227	US	14/358,646	11/21/2012	9190106	11/17/2015	In Force	Method Of Processing Address Information For A Data Storage Devic
MP6227	MP6227C1	US	14/932,351	11/4/2015	9406326	8/2/2016	In Force	METHODS FOR STORING AND ACCESSING ADDRESS INFORMATION I
MP6229	MP6229	US	13/943,726	7/16/2013	9082418	7/14/2015	In Force	Methods For Reading Data From A Storage Medium Using A Reader A
MP6229	MP6229SG	SG	2013054317	7/16/2013	196730	1/29/2016	In Force	Methods For Reading Data From A Storage Medium Using A Reader A
MP6229	MP6229SOC1	US	14/797,698	7/13/2015	9257135	2/9/2016	In Force	Systems And Methods For Reading Data From A Storage Medium
MP6230	MP6230	US	13/946,621	7/19/2013	9269394	2/23/2016	In Force	Methods For Reading Data From A Storage Medium Using A Reader A
MP6230	MP6230SG	SG	2013055314	7/19/2013	196740	2/5/2016	In Force	Methods For Reading Data From A Storage Medium Using A Reader A

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MP6231	MP6231	US	13/942,862	7/16/2013	9368130	6/14/2016	In Force	Data Storage System, Method Of Writing To Storage In The Data Stor
MP6231	MP6231SG	SG	2013054424	7/16/2013	196732	4/25/2016	In Force	Data Storage System, Method Of Writing To Storage In The Data Stor
MP6234	MP6234	US	13/773,942	2/22/2013	9268705	2/23/2016	In Force	Data Storage Device And Method Of Managing A Cache In A Data Sto
MP6234	MP6234C1	US	15/088,065	2/19/2016	9524238	12/20/2016	In Force	Systems And Methods For Managing Cache Of A Data Storage Device
MP6234	MP6234D1SG	SG	102015051480	2/22/2013	102015051480	11/21/2016	In Force	Data Storage Device And Method Of Managing A Cache In A Data Sto
MP6234	MP6234SG	SG	2013013396	2/22/2013			Pending	Data Storage Device And Method Of Managing A Cache In A Data Sto
MP6234	MP6234SOC1C1	US	15/363,325	11/29/2016	10061704	8/28/2018	In Force	Systems And Methods For Managing Cache Of A Data Storage Device
MP6235	MP6235	US	14/378,778	2/18/2013	9111562	8/18/2015	In Force	Recording Medium And A Method Of Writing Servo Information On
MP6235	MP6235C1	US	14/827,822	8/17/2015	9275654	3/1/2016	In Force	Method And Apparatus For Writing Servo Information On A Recordi
MP6236	MP6236	US	13/946,648	7/19/2013	9251831	2/2/2016	In Force	Recording Medium And Method Of Forming The Same
MP6237	MP6237	US	13/946,804	7/19/2013	9653123	5/16/2017	In Force	Direct Data Connectors For A Sealed Device And Methods For Formin
MP6238	MP6238	US	13/881,735	5/10/2013	9070416	6/30/2015	In Force	Methods And Apparatus For Hard Disk Drive System Operation Monit
MP6238	MP6238C1	US	14/753,370	6/29/2015	9343083	5/17/2016	In Force	METHOD AND APPARATUS FOR DETERMINING RELATIVE HEAD-TO-DI
MP6239	MP6239	US	13/946,988	7/19/2013	9070410	6/30/2015	In Force	Recording Media, Data Storage Devices, And Methods For Determini
MP6239	MP6239C1	US	14/747,366	6/23/2015	9257140	2/9/2016	In Force	Systems And Methods For Determining A Position Error Of A Read/W
MP6239	MP6239SG	SG	2013055611	7/19/2013	196742	2/12/2016	In Force	Recording Media, Data Storage Devices, And Methods For Determini
MP6240	MP6240WOUS	US	14/416,554	1/22/2015	9318139	4/19/2016	In Force	RECORDING MEDIUM, A DATA STORAGE APPARATUS AND A METHO
MP6241	MP6241WOUS	US	14/417,032	7/30/2013	9800658	10/24/2017	In Force	Servers And Methods For Controlling A Server
MP6242	MP6242WO CN	CN	201380065129	10/14/2013	ZL2013800651	6/9/2017	In Force	AXIAL FIELD OF MOTOR AND METHOD OF ASSEMBLING HARD DISK D
MP6242	MP6242WOUS	US	14/434,691	10/14/2013	9472234	10/18/2016	In Force	Hard Disk Drive Spindle Motor Including A Base And A Base Insert For
MP6243	MP6243	US	14/260,536	4/24/2014	9177595	11/3/2015	In Force	Magnetic Disk Drive Device, Magnetic Disk Assembly And Methods O
MP6245	MP6245	US	14/056,330	10/17/2013	9268709	2/23/2016	In Force	Storage Controllers And Storage Control Methods
MP6246	MP6246	US	14/058,156	10/18/2013	10126987	11/13/2018	In Force	Storage Devices And Methods For Controlling A Storage Device
MP6247	MP6247	US	14/082,784	11/18/2013	9437230	9/6/2016	In Force	Design And Method Of Operation Of Micro-Millactuators And Micro-
MP6248	MP6248WO	WO	PCT/SG2014/00	7/30/2014			Pending	Write Interference Reduction When Reading While Writing To A Hard
MP6248	MP6248WOUS	US	14/903,783	7/30/2014	9626998	4/18/2017	In Force	Write Interference Reduction When Reading While Writing To A Hard
MP6249	MP6249WO	WO	PCT/SG2014/00	8/26/2014			Pending	Sensor Array For Reading Data From A Storage Medium, Storage Devi
MP6249	MP6249WOUS	US	14/913,129	8/26/2014	9711169	7/18/2017	In Force	Sensor Array For Reading Data From A Storage Medium, Storage Devi
MP6250	MP6250WO	WO	PCT/SG2014/00	6/25/2014			Pending	Read Assembly, Data Storage System, And Methods Of Using The Sa
MP6250	MP6250WOUS	US	14/900,023	6/25/2014	9626992	4/18/2017	In Force	Read Assembly, Data Storage System, And Methods Of Using The Sa
MP6251	MP6251WO	WO	PCT/SG2014/00	9/12/2014			Pending	Method Of Operating A Data Storage Apparatus And Data Storage Ap
MP6251	MP6251WOUS	US	14/914,813	9/12/2014	9685181	6/20/2017	In Force	Method Of Operating A Data Storage Apparatus And Data Storage Ap
MP6252	MP6252WO	WO	PCT/SG2014/00	8/25/2014			Pending	Motor Control System And Method Of Controlling A Motor
MP6252	MP6252WOUS	US	14/913,175	8/25/2014	9929684	3/27/2018	In Force	Motor Control System And Method Of Controlling A Motor

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MP6253	MP6253WO	WO	PCT/SG2014/00	9/16/2014			Pending	Magnetic Medium And Method Of Forming The Same
MP6253	MP6253WOUS	US	15/022,433	3/16/2016	10020015	7/10/2018	In Force	MAGNETIC MEDIUM HAVING MULTILAYERED SERVO LAYER AND ME
MP6254	MP6254WO	WO	PCT/SG2014/00	9/17/2014			Pending	Magnetic Disk, Method Of Track Following On A Magnetic Disk, And
MP6254	MP6254WOUS	US	15/022,483	9/17/2014	9704522	7/11/2017	In Force	Magnetic Disk, Method Of Track Following On A Magnetic Disk, And
MP6255	MP6255WO	WO	PCT/SG2014/00	9/17/2014			Pending	Method Of Writing Servo Information On A Storage Medium And Arr
MP6255	MP6255WOUS	US	15/022,493	9/17/2014	9672862	6/6/2017	In Force	Method Of Writing Servo Information On A Storage Medium And Arr
MP6256	MP6256SG	SG	10201400541V	3/11/2014			Pending	Nested Coil Configuration With Multi-Tier Magnets For HDD Axial Fil
MP6256	MP6256SGUS	US	14/644,900	3/11/2015	9275674	3/1/2016	In Force	Spindle Motor For Hard Disk Drive And Method Of Fabrication There
MP6257	MP6257	US	14/697,009	4/27/2015	9251857	2/2/2016	In Force	Method Of Predicting A Noise Component Associated With A Readab
MP6257	MP6257WOSG	SG	10201401883S	4/28/2014			Pending	Servo And Data-Dependent Noise Prediction
MP6258	MP6258	US	14/694,401	4/23/2015	9431053	8/30/2016	In Force	Run-Length Limited (RLL) Codes
MP6258	MP6258D1	US	15/248,620	8/26/2016	9852757	12/26/2017	In Force	Systems And Methods For Decoding Using Run-Length Limited (RLL)
MP6258	MP6258WOSG	SG	10201401781X	4/24/2014			Pending	DC-Free Constrained Codes For Disk Drive Systems With Dedicated Se
MP6261	MP6261	US	15/089,341	4/1/2016	9672853	6/6/2017	In Force	Digital Data Driven Position Error Signal for Magnetic Recording
MP6261	MP6261C1	US	15/614,447	6/5/2017	9837110	12/5/2017	In Force	Digital Data Driven Position Error Signal for Magnetic Recording
MP6265	MP6265	US	15/182,763	6/15/2016	10051437	8/14/2018	In Force	System And Method For Improving GNSS Receiver Tracking Loop Perf
MP6266	MP6266	US	15/171,216	6/2/2016	10109037	10/23/2018	In Force	Systems And Methods For Bayer Image Scaling Based On Edge Detect
MP6269	MP6269TW	TW	105117626	6/3/2016			Pending	SYSTEMS AND METHODS FOR INCREASING PACKING DENSITY IN A SE
MP6269	MP6269WO	WO	PCT/US2016/03	6/2/2016			Pending	SYSTEMS AND METHODS FOR INCREASING PACKING DENSITY IN A SE
MP6269	MP6269WOCN	CN	201680031750.1	6/2/2016			Pending	SYSTEMS AND METHODS FOR INCREASING PACKING DENSITY IN A SE
MP6272	MP6272	US	15/187,652	6/20/2016	9635515	4/25/2017	In Force	APPARATUS AND METHODS FOR GENERATING AN ACCURATE ESTIM
MP6272	MP6272C1	US	15/494,144	4/21/2017	10073169	9/11/2018	In Force	APPARATUS AND METHODS FOR GENERATING AN ACCURATE ESTIM
MP6280	MP6280	US	15/052,907	2/25/2016	9872335	1/16/2018	In Force	ITERATIVE RECEIVER WAKE-UP FOR LONG DRX PERIODS
MP6280	MP6280CN	CN	201610125788.1	3/4/2016			Pending	ITERATIVE RECEIVER WAKE-UP FOR LONG DRX PERIODS
MP6286	MP6286	US	15/168,640	5/31/2016	10164770	12/25/2018	In Force	Pipelined Data Cryptography Device and Method
MP6291	MP6291	US	15/173,137	6/3/2016			Pending	SYSTEMS AND METHODS FOR ITERATIVE CODING OF PRODUCT CODE
MP6292	MP6292TW	TW	105125825	8/12/2016			Pending	PACKAGE SOLUTION TO SERIALIZE DRAM INTERFACE
MP6292	MP6292WO	WO	PCT/US2016/04	8/8/2016			Pending	PACKAGING ARRANGEMENTS INCLUDING A SERIALIZING DRAM INTE
MP6298	MP6298	US	15/146,928	5/5/2016	10091079	10/2/2018	In Force	Apparatus And Method For Remotely Testing Memory-Mapped Devic
MP6298	MP6298C1	US	16/148,494	10/1/2018			Pending	SECURING REMOTE ETHERNET ACCESS TO A PROCESSOR AND A REGI
MP6298	MP6298WO	WO	PCT/IB2016/052	5/5/2016			Pending	Apparatus And Method For Remotely Testing Memory-Mapped Devic
MP6298	MP6298WOCF	CF	16725233.7	5/5/2016			Pending	Apparatus And Method For Remotely Testing Memory-Mapped Devic
MP6303	MP6303	US	15/172,115	6/2/2016	9917590	3/13/2018	In Force	DELAY LOCKED LOOP
MP6303	MP6303WO	WO	PCT/US2016/03	6/2/2016			Pending	DELAY LOCKED LOOP

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MP6303	MP6303WOEP	EP	16804478.2	6/2/2016			Pending	DELAY LOCKED LOOP
MP6306	MP6306	US	15/060,719	3/4/2016	10298344	5/21/2019	In Force	Systems And Methods For Indicating When Frames Egress A Phy Mod
MP6306	MP6306C1	US	16/416,954	5/20/2019			Pending	BRIDGES INCLUDING PHYSICAL LAYER DEVICES FOR INDICATING TRA
MP6308	MP6308	US	15/191,450	6/23/2016	10110046	10/23/2018	In Force	MOBILE TO MOBILE WIRELESS CHARGING
MP6328	MP6328	US	15/176,922	6/8/2016	9621337	4/11/2017	In Force	ADAPTIVE I/Q MISMATCH CALIBRATION
MP6328	MP6328WO	WO	PCT/IB2016/053	6/8/2016			Pending	ADAPTIVE I/Q MISMATCH CALIBRATION
MP6329	MP6329	US	15/166,188	5/26/2016	10051688	8/14/2018	In Force	Bridging Wireless Network Traffic
MP6333	MP6333	US	15/252,068	8/30/2016	9940946	4/10/2018	In Force	SHARP NOISE SUPPRESSION
MP6333	MP6333JP	JP	2016-166352	8/26/2016			Pending	SHARP NOISE SUPPRESSION
MP6334	MP6334	US	15/176,934	6/8/2016	10116359	10/30/2018	In Force	EXPLICIT BEAMFORMING IN A HIGH EFFICIENCY WIRELESS LOCAL AR
MP6334	MP6334D1	US	16/173,955	10/29/2018			Pending	EXPLICIT BEAMFORMING IN A HIGH EFFICIENCY WIRELESS LOCAL AR
MP6334	MP6334WO	WO	PCT/US2016/03	6/8/2016			Pending	EXPLICIT BEAMFORMING IN A HIGH EFFICIENCY WIRELESS LOCAL AR
MP6334	MP6334WOCN	CN	201680043435.1	6/8/2016			Pending	EXPLICIT BEAMFORMING IN A HIGH EFFICIENCY WIRELESS LOCAL AR
MP6334	MP6334WOEP	EP	16730660.4	6/8/2016			Pending	EXPLICIT BEAMFORMING IN A HIGH EFFICIENCY WIRELESS LOCAL AR
MP6334	MP6334WOJP	JP	2017-563216	6/8/2016			Pending	EXPLICIT BEAMFORMING IN A HIGH EFFICIENCY WIRELESS LOCAL AR
MP6334D	MP6334D1C1	US	16/700,562	12/2/2019			Pending	EXPLICIT BEAMFORMING IN A HIGH EFFICIENCY WIRELESS LOCAL AR
MP6337	MP6337	US	15/188,210	6/21/2016	10178635	1/8/2019	In Force	METHOD AND APPARATUS FOR PREVENTING CLUSTER FRAGMENTAT
MP6338	MP6338	US	15/170,184	6/1/2016	9576663	2/21/2017	In Force	MULTI-PORT MEMORY
MP6340	MP6340	US	15/259,167	9/8/2016	9780763	10/3/2017	In Force	Method And Apparatus For Offset Calibration Of A Multi-Mode High-
MP6342	MP6342	US	15/204,365	7/7/2016	9654132	5/16/2017	In Force	Hybrid Charge-Sharing Charge-Redistribution DAC For Successive App
MP6342	MP6342C1	US	15/210,051	7/14/2016	9660662	5/23/2017	In Force	Successive Approximation Sigma Delta Analog-To-Digital Converters
MP6342	MP6342C2	US	15/210,308	7/14/2016	9654130	5/16/2017	In Force	ANALOG-TO-DIGITAL CONVERTERS FOR SUCCESSIVE APPROXIMATIO
MP6342	MP6342C3	US	15/595,045	5/15/2017	9954549	4/24/2018	In Force	Charge-Sharing And Charge-Redistribution DAC And Method For Succ
MP6342	MP6342WO	WO	PCT/IB2016/054	7/8/2016			Pending	Hybrid Charge-Sharing Charge-Redistribution DAC For Successive App
MP6347	MP6347	US	15/136,466	4/22/2016	9946599	4/17/2018	In Force	SYSTEMS AND METHODS FOR ENHANCED DATA ACCESS TO A HARD
MP6348	MP6348	US	15/132,900	4/19/2016	10256944	4/9/2019	In Force	CONTROLLING FALSE PACKET ACCEPTANCE
MP6349	MP6349	US	15/135,428	4/21/2016	10219274	2/26/2019	In Force	CHANNEL BONDING OPERATIONS IN WIRELESS COMMUNICATIONS
MP6349	MP6349WO	WO	PCT/IB2016/052	4/22/2016			Pending	CHANNEL BONDING OPERATIONS IN WIRELESS COMMUNICATIONS
MP6349	MP6349WOCN	CN	201680022817.1	4/22/2016			Pending	CHANNEL BONDING OPERATIONS IN WIRELESS COMMUNICATIONS
MP6349	MP6349WOEP	EP	16720566.5	4/22/2016			Pending	Channel Bonding Operations In Wireless Communications
MP6353	MP6353	US	15/143,075	4/29/2016	10181966	1/15/2019	In Force	WIFI CLASSIFICATION BY PILOT SEQUENCES
MP6353	MP6353C1	US	15/265,614	9/14/2016	10382598	8/13/2019	In Force	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP6353	MP6353C1	US	16/538,079	8/12/2019			Pending	PHYSICAL LAYER FRAME FORMAT FOR WLAN
MP6355	MP6355	US	15/144,480	5/2/2016	9830976	11/28/2017	In Force	SYSTEMS AND METHODS FOR A HIGH PERFORMANCE MEMORY CELL

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MP6358	MP6358	US	15/048,135	2/19/2016			Pending	SYSTEMS AND METHODS FOR SECURED DATA TRANSFER VIA INTER-C
MP6358	MP6358WO	WO	PCT/US2016/01	2/19/2016			Pending	SYSTEMS AND METHODS FOR SECURED DATA TRANSFER VIA INTER-C
MP6358	MP6358WOCN	CN	20168003402.1	2/19/2016			Pending	SYSTEMS AND METHODS FOR SECURED DATA TRANSFER VIA INTER-C
MP6360	MP6360	US	15/165,713	5/26/2016	10097393	10/9/2018	In Force	Systems And Methods To Reduce Peak To Average Power Ratio For D
MP6360	MP6360C1	US	16/151,870	10/4/2018	10439854	10/8/2019	In Force	SYSTEMS AND METHODS TO REDUCE PEAK TO AVERAGE POWER RAT
MP6360	MP6360C1C1	US	16/571,406	9/16/2019			Pending	Systems And Methods To Reduce Peak To Average Power Ratio For D
MP6364	MP6364	US	15/184,342	6/16/2016	9965937	5/8/2018	In Force	Frequency Multipliers
MP6364	MP6364WO	WO	PCT/US2016/03	6/17/2016			Pending	Frequency Multipliers
MP6367	MP6367	US	15/619,143	6/9/2017	10268395	4/23/2019	In Force	SYSTEMS AND METHODS FOR COMMUNICATING ADDRESSABLE REQ
MP6368	MP6368	US	15/178,979	6/10/2016			Pending	Validating De-Authentication Requests
MP6371	MP6371	US	15/060,732	3/4/2016	10684559	9/25/2018	In Force	System And Method For Maintaining A Time Of Day In A Port Of A Ph
MP6371	MP6371C1	US	16/136,871	9/20/2018			Pending	MAINTAINING A TIME OF DAY IN PHYSICAL LAYER CIRCUIT OF A NET
MP6379	MP6379WOEPDE	DE	15731212.5	6/11/2015	602015039893	10/16/2019	In Force	Compressed Preamble for a Wireless Communication System
MP6379	MP6379WOEPFR	FR	15731212.5	6/11/2015	3155779	10/16/2019	In Force	Compressed Preamble for a Wireless Communication System
MP6379	MP6379WOEPGB	GB	15731212.5	6/11/2015	3155779	10/16/2019	In Force	Compressed Preamble for a Wireless Communication System
MP6390	MP6390D1	US	16/425,366	5/29/2019			Pending	MRAM STRUCTURE FOR EFFICIENT MANUFACTURABILITY
MP6392	MP6392	US	15/198,746	6/30/2016	9892558	1/30/2018	In Force	Power-On Reset Circuit
MP6400	MP6400	US	15/225,585	8/12/2016	10397386	8/27/2019	In Force	SYSTEM AND METHOD FOR PROVIDING TEST SCENARIO REPRODUCTI
MP6400	MP6400D1	US	16/510,419	7/12/2019			Pending	SYSTEM AND METHOD FOR PROVIDING TEST SCENARIO REPRODUCTI
MP6401	MP6401	US	15/246,977	8/25/2016	9774303	9/26/2017	In Force	LOW-NOISE AMPLIFIER FOR INTRA-BAND NON CONTIGUOUS CARRIE
MP6403	MP6403	US	15/164,364	5/25/2016	9838072	12/5/2017	In Force	Systems And Methods To Mitigate Electro-Magnetic Interference In S
MP6403	MP6403 C1	US	15/825,937	11/29/2017	10644406	8/7/2018	In Force	INTERFERENCE MITIGATION IN SINGLE TWISTED-PAIR COMMUNICAT
MP6404	MP6404	US	15/164,391	5/25/2016	9716529	7/25/2017	In Force	Systems And Methods To Adaptively Mitigate Electro-Magnetic Interf
MP6414	MP6414	US	15/219,164	7/25/2016	9844111	12/12/2017	In Force	DIMMABLE LED DRIVING SYSTEM AND METHOD FOR REDUCING FLIC
MP6415	MP6415	US	15/222,477	7/28/2016	10103944	10/16/2018	In Force	PURPOSELY CORRUPTED PACKET FOR CONNECTION INFORMATION
MP6415	MP6415WO	WO	PCT/IB2016/054	7/28/2016			Pending	PURPOSELY CORRUPTED PACKET FOR CONNECTION INFORMATION
MP6416	MP6416	US	15/220,406	7/27/2016	9871607	1/16/2018	In Force	PHASE CONTINUITY IN NARROW-BAND TRANSMISSION WITHIN A FR
MP6417	MP6417	US	15/221,438	7/27/2016	10159043	12/18/2018	In Force	TRAINING SEQUENCES IN WIRELESS COMMUNICATION SYSTEMS
MP6417	MP6417C1	US	16/221,755	12/17/2018			Pending	TRAINING SEQUENCES FOR HIGH EFFICIENCY WIRELESS LOCAL AREA
MP6418	MP6418	US	15/354,188	10/25/2016	10438881	10/8/2019	In Force	PACKAGING ARRANGEMENTS INCLUDING HIGH DENSITY INTERCONN
MP6418	MP6418TW	TW	105134851	10/27/2016			Pending	EMBEDDED HDI (HIGH DENSITY INTERCONNECT) SILICON BRIDGE IN
MP6418	MP6418WO	WO	PCT/US2016/05	10/26/2016			Pending	PACKAGING ARRANGEMENTS INCLUDING HIGH DENSITY INTERCONN
MP6422	MP6422	US	15/265,541	9/14/2016	10162547	12/25/2018	In Force	MEMORY EXPANSION IN A LINKING PROCESS
MP6423	MP6423	US	15/209,635	7/13/2016	9998167	6/12/2018	In Force	RECONFIGURABLE INTEGRATED RF FRONT-END FOR DUAL-BAND WL

PATENT

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MP6423	MP6423WO	WO	PCT/JP2016/054	7/13/2016			Pending	RECONFIGURABLE INTEGRATED RF FRONT-END FOR DUAL-BAND WIL
MP6427	MP6427	US	15/204,040	7/7/2016	9813034	11/7/2017	In Force	AMPLIFICATION CIRCUIT AND METHOD OF COMPENSATING FOR VOL
MP6427	MP6427WO	WO	PCT/JP2016/04	7/7/2016			Pending	AMPLIFICATION CIRCUIT AND METHOD OF COMPENSATING FOR VOL
MP6429	MP6429	US	15/220,898	7/27/2016	10339077	7/2/2019	In Force	SYSTEMS AND METHODS FOR IMPLEMENTING TOPOLOGY-BASED ID
MP6429	MP6429TW	TW	105124510	8/2/2016			Pending	SYSTEMS AND METHODS FOR IMPLEMENTING TOPOLOGY-BASED ID
MP6429	MP6429WO	WO	PCT/JP2016/04	7/28/2016			Pending	SYSTEMS AND METHODS FOR IMPLEMENTING TOPOLOGY-BASED ID
MP6430	MP6430	US	15/220,916	7/27/2016			Pending	SYSTEMS AND METHODS FOR AGGREGATING DATA PACKETS IN A M
MP6430	MP6430TW	TW	105124512	8/2/2016			Pending	SYSTEMS AND METHODS FOR AGGREGATING DATA PACKETS IN A M
MP6430	MP6430WO	WO	PCT/JP2016/04	7/28/2016			Pending	SYSTEMS AND METHODS FOR AGGREGATING DATA PACKETS IN A M
MP6431	MP6431	US	15/220,546	7/27/2016	10318453	6/11/2019	In Force	SYSTEMS AND METHODS FOR TRANSMITTING INTERRUPTS BETWEE
MP6431	MP6431TW	TW	105124513	8/2/2016			Pending	SYSTEMS AND METHODS FOR TRANSMITTING INTERRUPTS BETWEE
MP6431	MP6431WO	WO	PCT/JP2016/04	7/28/2016			Pending	SYSTEMS AND METHODS FOR TRANSMITTING INTERRUPTS BETWEE
MP6432	MP6432	US	15/220,923	7/27/2016	10474597	11/12/2019	In force	SYSTEMS AND METHODS FOR PERFORMING UNKNOWN ADDRESS DI
MP6432	MP6432TW	TW	105124514	8/2/2016			Pending	SYSTEMS AND METHODS FOR PERFORMING UNKNOWN ADDRESS DI
MP6432	MP6432WO	WO	PCT/JP2016/04	7/28/2016			Pending	SYSTEMS AND METHODS FOR PERFORMING UNKNOWN ADDRESS DI
MP6433	MP6433	US	15/220,684	7/27/2016	10198376	2/5/2019	In Force	METHODS AND APPARATUS FOR ACCELERATING LIST COMPARISON
MP6433	MP6433TW	TW	105124515	8/2/2016			Pending	METHODS AND APPARATUS FOR ACCELERATING LIST COMPARISON
MP6433	MP6433WO	WO	PCT/JP2016/04	7/28/2016			Pending	METHODS AND APPARATUS FOR ACCELERATING LIST COMPARISON
MP6436	MP6436	US	15/270,715	9/20/2016	10408855	9/10/2019	In Force	METHOD AND APPARATUS FOR EFFICIENTLY DETERMINING POSITIO
MP6437	MP6437	US	15/210,126	7/14/2016	10255231	4/9/2019	In Force	Apparatus and Methods for Managing Aggregate Integrated Circuit (I
MP6448	MP6448	US	15/214,822	7/20/2016	10162665	12/25/2018	In Force	Hypervisor Assisted Control Of CPU Access To Externally Managed Ph
MP6449	MP6449	US	15/370,796	12/6/2016	9740175	8/22/2017	In Force	ALL-DIGITAL PHASE LOCKED LOOP (ADPLL) INCLUDING A DIGITAL-TO-
MP6449	MP6449CN	CN	201710018793.1	1/10/2017			Pending	DIGITAL PHASE LOCKED LOOP AND ASSOCIATED CONTROLLING MET
MP6459	MP6459	US	15/236,230	8/12/2016	10201009	2/5/2019	In Force	METHODS AND APPARATUS FOR PROTECTING TRANSMISSIONS IN A
MP6460	MP6460	US	15/362,284	11/28/2016	10386495	8/20/2019	In Force	METHOD AND APPARATUS FOR DETECTING POOR DOPPLER MEASUR
MP6461	MP6461	US	15/236,252	8/12/2016	10069650	9/4/2018	In Force	ETHERNET AND OFDM MULTIPLEXING ON UTP CABLE
MP6461	MP6461WO	WO	PCT/JP2016/054	8/13/2016			Pending	ETHERNET AND OFDM MULTIPLEXING ON UTP CABLE
MP6462	MP6462	US	15/144,519	5/2/2016	10057615	6/26/2018	In Force	METHODS AND APPARATUS FOR PERFORMING FAST CACHING
MP6463	MP6463	US	15/248,423	8/26/2016	10365377	7/30/2019	In Force	Apparatus and Method for Mobility Mode State Detection
MP6464	MP6464	US	15/252,091	8/30/2016	10079031	9/18/2018	In Force	RESIDUAL NOISE SUPPRESSION
MP6464	MP6464JP	JP	2016-174112	9/6/2016			Pending	RESIDUAL NOISE SUPPRESSION
MP6465	MP6465CN	CN	201610664256.1	8/12/2016			Pending	FLEXIBLE INTERFACE FOR NAND FLASH MEMORY
MP6465	MP6465TW	TW	105125822	8/12/2016			Pending	FLEXIBLE INTERFACE FOR NAND FLASH MEMORY
MP6471	MP6471	US	15/260,790	9/9/2016	9912295	3/6/2018	In Force	POWER-EFFICIENT PROGRAMMABLE BROADBAND HIGH-PASS PREA

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MP6473	MP6473	US	15/258,437	7/9/2016	10299077	5/21/2019	In Force	APPARATUS AND METHOD FOR TRANSFORMING AND CLEANING POS
MP6474	MP6474	US	15/262,905	9/12/2016	10164745	12/25/2018	In Force	SYSTEMS AND METHODS FOR CROSS-CHANNEL SCHEDULING OF HIG
MP6474	MP6474.C1	US	16/226,177	12/19/2018			Pending	SYSTEMS AND METHODS FOR CROSS-CHANNEL SCHEDULING OF HIG
MP6474	MP6474WO	WO	PCT/US2016/05	9/12/2016			Pending	SYSTEMS AND METHODS FOR CROSS-CHANNEL SCHEDULING OF HIG
MP6474	MP6474WOCN	CN	201680052577.1	9/12/2016			Pending	SYSTEMS AND METHODS FOR CROSS-CHANNEL SCHEDULING OF HIG
MP6474	MP6474WOEP	EP	16770616.7	9/12/2016			Pending	SYSTEMS AND METHODS FOR CROSS-CHANNEL SCHEDULING OF HIG
MP6475	MP6475WO	WO	PCT/US2016/05	9/15/2016			Pending	METHOD AND APPARATUS FOR MEASURING SIGNAL-TO-QUANTIZA
MP6477	MP6477	US	15/298,141	10/19/2016	9866729	10/31/2017	In Force	CURRENT STEERING CELL WITH CODE-DEPENDENT NONLINEARITY C
MP6478	MP6478	US	15/362,573	11/28/2016			Pending	METHOD AND APPARATUS FOR POSITIONING
MP6479	MP6479	US	15/338,258	10/28/2016	10200002	2/5/2019	In Force	RF PEAK DETECTION METHOD USING A SEQUENTIAL BASEBAND SCH
MP6481	MP6481	US	15/278,004	9/27/2016	9877107	1/23/2018	In Force	PROCESSING AUDIO SIGNALS
MP6481	MP6481CN	CN	201610918225.1	10/20/2016			Pending	PROCESSING AUDIO SIGNALS
MP6481	MP6481EP	EP	16194562.1	10/19/2016			Pending	Active Equalisation of Audio Signals
MP6481	MP6481JP	JP	2016-202137	10/13/2016			Pending	PROCESSING AUDIO SIGNALS
MP6483	MP6483	US	15/286,720	10/6/2016	10055282	8/21/2018	In Force	Method and Apparatus of Feeding a Seed Value to a Processing Unit
MP6484	MP6484	US	15/297,028	10/18/2016	9935664	4/3/2018	In Force	Signal Detection and Power Measurement with Multi-Band Filter to
MP6484	MP6484.C1	US	15/943,275	4/2/2018	10090870	10/2/2018	In Force	Signal Detection and Power Measurement with Multi-Band Filter to
MP6486	MP6486	US	15/267,957	9/16/2016	9979449	5/22/2018	In Force	Systems and Methods for Detecting Data in a Received Multiple-Input
MP6486	MP6486WO	WO	PCT/US2016/05	9/16/2016			Pending	Systems and Methods for Detecting Data in a Received Multiple-Input
MP6487	MP6487	US	15/251,654	8/30/2016	9929884	3/27/2018	In Force	Systems and Methods for Detecting Data in a Received Multiple-Input
MP6487	MP6487WO	WO	PCT/US2016/001	8/30/2016			Pending	Systems and Methods for Detecting Data in a Received Multiple-Input
MP6490	MP6490	US	15/369,384	12/5/2016	10262973	4/16/2019	In Force	MODULAR CHIP WITH REDUNDANT INTERFACES
MP6493	MP6493	US	15/332,548	10/24/2016	10256961	4/9/2019	In Force	ACKNOWLEDGMENT OF UPLINK ORTHOGONAL-FREQUENCY DIVISIO
MP6493	MP6493.C1	US	16/378,170	4/8/2019			Pending	ACKNOWLEDGMENT OF UPLINK ORTHOGONAL-FREQUENCY DIVISIO
MP6493	MP6493WO	WO	PCT/US2016/05	10/24/2016			Pending	ACKNOWLEDGMENT OF UPLINK ORTHOGONAL-FREQUENCY DIVISIO
MP6495	MP6495	US	15/278,308	9/28/2016	9934150	4/3/2018	In Force	DATA CACHING CIRCUIT AND METHOD
MP6496	MP6496	US	15/363,957	11/29/2016	10270634	4/23/2019	In Force	CONTROL MESSAGE ROUTING STRUCTURE FOR A CONTROLLER AREA
MP6496	MP6496WO	WO	PCT/US2016/06	11/30/2016			Pending	CONTROL MESSAGE ROUTING STRUCTURE FOR A CONTROLLER AREA
MP6496	MP6496WOEP	EP	16816081.0	11/30/2016			Pending	ROUTING STRUCTURE OF CONTROLLER AREA NETWORK CONTROL M
MP6498	MP6498	US	15/363,955	11/29/2016	10129150	11/13/2018	In Force	SYSTEMS AND METHODS FOR IMPLEMENTING A SWITCHED CONTROL
MP6498	MP6498WO	WO	PCT/US2016/06	11/30/2016			Pending	SYSTEMS AND METHODS FOR IMPLEMENTING A SWITCHED CONTROL
MP6498	MP6498WOEP	EP	16816085.1	11/30/2016			Pending	SWITCHED CONTROLLER AREA NETWORK
MP6499	MP6499	US	15/363,964	11/29/2016	10212081	2/19/2019	In Force	SYSTEMS AND METHODS FOR IMPLEMENTING A TIME-STAMPED CO
MP6499	MP6499.C1	US	16/277,253	2/15/2019			Pending	SYSTEMS AND METHODS FOR IMPLEMENTING A TIME-STAMPED CO

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MP6499	MP6499WO	WO	PCT/US2016/06	11/30/2016			Pending	SYSTEMS AND METHODS FOR IMPLEMENTING A TIME-STAMPED CO
MP6499	MP6499WOEP	EP	16816086.9	11/30/2016			Pending	TIME-STAMPED CONTROLLER AREA NETWORK (CAN) BUS MES
MP6500	MP6500	US	15/900,236	2/20/2018			Pending	SYSTEMS AND METHODS FOR AN ERROR LOGGING MECHANISM AT
MP6500	MP6500WO	WO	PCT/US2018/01	2/20/2018			Pending	SYSTEMS AND METHODS FOR AN ERROR LOGGING MECHANISM AT
MP6500	MP6500WOCN	CN	201890000527.1	2/20/2018			Pending	SYSTEMS AND METHODS FOR AN ERROR LOGGING MECHANISM AT
MP6500	MP6500WOEE	DE	21.2018.000.159	2/20/2018			Pending	SYSTEMS AND METHODS FOR AN ERROR LOGGING MECHANISM AT
MP6500	MP6500WOJP	JP	2019-600098	2/20/2018			Pending	SYSTEMS AND METHODS FOR AN ERROR LOGGING MECHANISM AT
MP6501	MP6501	US	15/378,960	12/14/2016	10178144	1/8/2019	In Force	SCATTERING AUDIO STREAMS
MP6502	MP6502	US	15/366,902	12/1/2016	10204635	2/12/2019	In Force	DEVICE AND METHOD FOR PROCESSING MEDIA SAMPLES
MP6505	MP6505	US	15/285,295	10/17/2016	10274604	4/30/2019	In Force	SYSTEM, DEVICE, AND METHOD OF PRIORITIZING GENERATION OF P
MP6510	MP6510	US	15/398,456	1/4/2017	10203749	2/12/2019	In Force	METHOD AND APPARATUS FOR SAVING POWER, INCLUDING AT LEAS
MP6510	MP6510CN	CN	201710005489.1	1/4/2017			Pending	METHOD AND APPARATUS FOR SAVING POWER
MP6511	MP6511	US	15/369,063	11/11/2016	10255004	4/9/2019	In Force	Systems and Methods for Managing Address-Mapping Data in Memo
MP6511	MP6511CN	CN	201611017969.1	11/16/2016			Pending	SYSTEMS AND METHODS FOR MANAGING ADDRESS-MAPPING DATA
MP6514	MP6514	US	15/239,027	8/17/2016	10404996	9/3/2019	In Force	Systems and Methods for Using Multiple Frames to Adjust Local and
MP6515	MP6515	US	15/364,420	11/30/2016			Pending	APPARATUS AND METHOD FOR ACTIVATING CIRCUITS
MP6515	MP6515CN	CN	201611091172.1	12/1/2016			Pending	APPARATUS AND METHOD FOR ACTIVATING CIRCUITS
MP6516	MP6516	US	15/334,027	10/25/2016	10254337	4/9/2019	In Force	SYSTEM AND METHOD FOR ESTABLISHING A TRUSTED DIAGNOSIS/D
MP6516	MP6516WO	WO	PCT/IB2016/056	10/25/2016			Pending	SYSTEM AND METHOD FOR ESTABLISHING A TRUSTED DIAGNOSIS/D
MP6516	MP6516WO TW	TW	105134853	10/27/2016			Pending	SYSTEM AND METHOD FOR ESTABLISHING A TRUSTED DIAGNOSIS/D
MP6517	MP6517	US	15/297,989	10/19/2016	10193694	1/29/2019	In Force	METHOD AND APPARATUS FOR SECURELY CONFIGURING PARAMETE
MP6518	MP6518	US	15/285,320	10/4/2016			Pending	Methods and Apparatus for Secure Root Key Provisioning
MP6519	MP6519	US	15/371,029	12/6/2016			Pending	METHOD AND APPARATUS FOR DEMODULATING GNSS NAVIGATION
MP6520	MP6520	US	15/240,902	8/18/2016	10283094	5/7/2019	In Force	Run-length Compression and Decompression of Media Tiles
MP6522	MP6522	US	15/369,909	12/6/2016	10333525	6/25/2019	In Force	DIGITALLY-BASED TEMPERATURE COMPENSATION FOR A CRYSTAL O
MP6523	MP6523	US	15/286,829	10/18/2016	10856363	8/21/2018	In Force	Methods and Systems to Improve Yield in Multiple Chips Integration
MP6523	MP6523C1	US	16/045,083	7/25/2018	10431574	10/1/2019	In Force	Methods and Systems for Packaging Semiconductor Devices to Impro
MP6523	MP6523C1C1	US	16/571,673	9/16/2019			Pending	METHODS AND SYSTEMS FOR PACKAGING AN INTEGRATED CIRCUIT
MP6523	MP6523CN	CN	201610980755.1	11/8/2016			Pending	METHODS AND SYSTEMS TO IMPROVE YIELD IN MULTIPLE CHIPS INT
MP6523	MP6523TW	TW	105136722	11/10/2016			Pending	METHODS AND SYSTEMS TO IMPROVE YIELD IN MULTIPLE CHIPS INT
MP6524	MP6524	US	15/350,985	11/14/2016			Pending	EXPLICIT MULTUSER BEAMFORMING TRAINING IN A WIRELESS LOCA
MP6525	MP6525	US	15/335,093	10/26/2016	10073123	9/11/2018	In Force	HIGH-SPEED, LOW DRIFT, PRECISION PEAK DETECTION CIRCUIT AND
MP6526	MP6526	US	15/335,367	10/26/2016	10082819	9/25/2018	In Force	Switched-Capacitor Bandgap Reference Circuit Using Chopping Techn
MP6526	MP6526CN	CN	201610950160.1	10/26/2016			Pending	SWITCHED-CAPACITOR BANDGAP REFERENCE CIRCUIT USING CHOPPI

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MP6531	MP6531	US	15/353,544	11/16/2016	10320690	6/11/2019	In Force	METHODS AND APPARATUS FOR RANGE MEASUREMENT
MP6534	MP6534	US	15/367,431	12/2/2016	9946289	4/17/2018	In Force	BIAS CURRENT GENERATOR HAVING BLENDED TEMPERATURE RESPO
MP6536	MP6536WOEPDE	DE	16820413.9	12/7/2016	3387779	11/27/2019	In Force	TRIGGER-BASED SINGLE USER UPLINK TRANSMISSION
MP6536	MP6536WOEPFR	FR	16820413.9	12/7/2016	3387779	11/27/2019	In Force	TRIGGER-BASED SINGLE USER UPLINK TRANSMISSION
MP6536	MP6536WOEPGB	GB	16820413.9	12/7/2016	3387779	11/27/2019	In Force	TRIGGER-BASED SINGLE USER UPLINK TRANSMISSION
MP6537	MP6537	US	15/370,690	12/6/2016	10091580	10/2/2018	In Force	METHOD AND APPARATUS FOR TRACKING SOUND SOURCE MOVEME
MP6538	MP6538	US	15/374,066	12/9/2016	10034227	7/24/2018	In Force	Access Point Association Using Trigger-Based Uplink Single User Tra
MP6540	MP6540WO	WO	PCT/IB2016/057	11/30/2016			Pending	LOW POWER PULSED BANDGAP WITH HIGH ACCURACY IN SUBMICR
MP6546	MP6546	US	15/371,895	12/7/2016	10604081	6/19/2018	In Force	SYSTEMS AND METHODS FOR PROVIDING RESOURCE SIGNALING WIT
MP6546	MP6546.C1	US	16/010,895	6/18/2018	10405325	9/3/2019	In Force	SYSTEMS AND METHODS FOR PROVIDING RESOURCE SIGNALING WIT
MP6546	MP6546C1C1	US	16/557,186	8/30/2019			Pending	SYSTEMS AND METHODS FOR PROVIDING RESOURCE SIGNALING WIT
MP6546	MP6546WO	WO	PCT/US2016/06	12/8/2016			Pending	SYSTEMS AND METHODS FOR PROVIDING RESOURCE SIGNALING WIT
MP6546	MP6546WOCN	CN	201680073687.1	12/8/2016			Pending	SYSTEMS AND METHODS FOR PROVIDING RESOURCE SIGNALING WIT
MP6546	MP6546WOEP	EP	16822305.5	12/8/2016			Pending	SYSTEMS AND METHODS FOR PROVIDING RESOURCE SIGNALING WIT
MP6549	MP6549	US	15/371,783	12/7/2016	10095263	10/9/2018	In Force	APPARATUS AND METHOD FOR CALIBRATING SIGNAL SYNCHRONIZA
MP6551	MP6551	US	15/370,596	12/6/2016	10089175	10/2/2018	In Force	QUEUEING OF DECODING TASKS ACCORDING TO PRIORITY IN NAND F
MP6551	MP6551CN	CN	201611132290	12/9/2016			Pending	QUEUEING OF DECODING TASKS ACCORDING TO PRIORITY IN NAND F
MP6551	MP6551KR	KR	10-2016-016682	12/8/2016			Pending	MULTI-LEVEL ERROR CORRECTION SCHEME WITH CUT-IN-LINE FEAT
MP6551	MP6551TW	TW	105140915	12/9/2016			Pending	MULTI-LEVEL ERROR CORRECTION SCHEME WITH CUT-IN-LINE FEAT
MP6552	MP6552	US	15/366,820	12/1/2016			Pending	SYSTEMS AND METHODS FOR PERFORMING PHASE ERROR CORRECTI
MP6552	MP6552CN	CN	201611123256.1	12/8/2016			Pending	SYSTEMS AND METHODS FOR PERFORMING PHASE ERROR CORRECTI
MP6559	MP6559	US	15/250,270	8/29/2016	10055812	8/21/2018	In Force	SYSTEMS, APPARATUSES AND METHODS FOR IMAGE MESH WARPIN
MP6560	MP6560	US	15/379,027	12/14/2016	10187497	1/22/2019	In Force	SYSTEMS AND METHODS FOR PERFORMING DATA FRAME CONFIGUR
MP6561	MP6561	US	15/379,274	12/14/2016	10178673	1/8/2019	In Force	SOUNDING FOR UL OFDMA TRANSMISSION IN WLAN
MP6562	MP6562	US	15/378,276	12/14/2016			Pending	Security in Smart Configuration for WLAN Based IOT Device
MP6564	MP6564	US	15/375,661	12/12/2016			Pending	METHOD AND APPARATUS FOR STEREO VISION MATCHING INCLUDI
MP6565	MP6565	US	15/380,965	12/15/2016	9882747	1/30/2018	In Force	CHANNEL QUALITY INDICATOR FOR WIRELINE CHANNEL DEGRADATI
MP6565	MP6565.C1	US	15/878,334	1/23/2018	10177943	1/8/2019	In Force	CHANNEL QUALITY INDICATOR FOR WIRELINE CHANNEL DEGRADATI
MP6565	MP6565WO	WO	PCT/IB2016/057	12/16/2016			Pending	CHANNEL QUALITY INDICATOR FOR WIRELINE CHANNEL DEGRADATI
MP6565	MP6565WOCDE	DE	112016005802.1	12/16/2016			Pending	CHANNEL QUALITY INDICATOR FOR WIRELINE CHANNEL DEGRADATI
MP6567	MP6567	US	15/386,491	12/21/2016	10193476	1/29/2019	In Force	SYSTEMS AND METHODS FOR OPERATING A BRUSHLESS DC MOTOR
MP6568	MP6568	US	15/374,844	12/9/2016	10194385	1/29/2019	In Force	Determining multi-user or single user transmission for access point ac
MP6568	MP6568C1	US	16/241,881	1/7/2019			Pending	Single-User Acknowledgement Options for Wireless Communication I
MP6570	MP6570	US	15/187,537	6/20/2016	10231477	3/19/2019	In Force	THREE-DIMENSIONAL PRINTER FOR PRINTING EDIBLE PRINTING MAT

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MP6572	MP6572	US	15/400,435	1/6/2017	10148597	12/4/2018	In Force	SYSTEMS AND METHODS FOR PROVIDING A COMPATIBLE BACKPLAN
MP6572	MP6572C1	US	16/208,092	12/3/2018			Pending	SYSTEMS AND METHODS FOR PROVIDING A COMPATIBLE BACKPLAN
MP6574	MP6574	US	15/423,527	2/2/2017			Pending	NETWORK DEVICES FOR SCALABLE POINT TO MULTIPPOINT NETWORK
MP6574	MP6574WO	WO	PCT/IB2017/05C	2/2/2017			Pending	NETWORK DEVICES FOR SCALABLE POINT TO MULTIPPOINT NETWORK
MP6580	MP6580	US	15/453,600	3/8/2017			Pending	SYSTEMS AND METHODS FOR AN INDUCTOR STRUCTURE WITH ENH
MP6581	MP6581	US	15/411,032	1/20/2017			Pending	RECEIVER AND METHOD FOR OPERATION WITH DIFFERENT SATELLIT
MP6581	MP6581CN	CN	201710114942.1	2/28/2017			Pending	RECEIVER AND METHOD FOR OPERATION WITH DIFFERENT SATELLIT
MP6587	MP6587	US	15/453,567	3/8/2017	9935643	4/3/2018	In Force	Adaptive Charging Systems And Methods For A Successive-Approxim
MP6591	MP6591	US	15/429,627	2/22/2017	10182399	1/15/2019	In Force	RECEIVE OPERATION MODE REPORTING AND SUBSEQUENT FRAME
MP6595	MP6595	US	15/656,458	7/21/2017	9960018	2/20/2018	In Force	METHODS AND SYSTEMS FOR REDUCING TRANSIENT KICKBACK FRO
MP6599	MP6599	US	15/420,109	1/31/2017	10135409	11/20/2018	In Force	HIGH-EFFICIENCY RF DIGITAL POWER AMPLIFIER WITH JOINT DUTY-C
MP6600	MP6600	US	15/423,275	2/2/2017	10205586	2/12/2019	In Force	METHOD AND APPARATUS FOR NETWORK SYNCHRONIZATION
MP6602	MP6602	US	15/812,786	11/14/2017			Pending	SYSTEMS AND METHODS FOR SUPPRESSING OSCILLATOR-INDUCED SP
MP6604	MP6604	US	15/444,342	2/28/2017	10133493	11/20/2018	In Force	DRAM CONTROLLER WITH ADAPTIVE PRECHARGE POLICY
MP6604	MP6604CN	CN	201710117918.1	3/1/2017			Pending	DRAM CONTROLLER WITH ADAPTIVE PRECHARGE POLICY
MP6614	MP6614	US	15/486,192	4/12/2017			Pending	REPORTING BANDWIDTH CAPABILITY OF A BANDWIDTH-LIMITED CO
MP6614	MP6614WO	WO	PCT/US2017/02	4/12/2017			Pending	REPORTING BANDWIDTH CAPABILITY OF A BANDWIDTH-LIMITED CO
MP6617	MP6617	US	15/469,355	3/24/2017			Pending	SYSTEMS AND METHODS FOR AN INDUCTOR STRUCTURE HAVING AN
MP6623	MP6623	US	15/467,839	3/23/2017	10261707	4/16/2019	In Force	DECODER MEMORY SHARING
MP6629	MP6629	US	15/470,655	3/27/2017	10190862	1/29/2019	In Force	SYSTEMS AND METHODS FOR HIGH PRECISION CABLE LENGTH MEAS
MP6629	MP6629 C1	US	16/259,288	1/28/2019			Pending	SYSTEMS AND METHODS FOR HIGH PRECISION CABLE LENGTH MEAS
MP6629	MP6629WO	WO	PCT/US2017/02	3/27/2017			Pending	SYSTEMS AND METHODS FOR HIGH PRECISION CABLE LENGTH MEAS
MP6629	MP6629WOEP	EP	17717958.7	3/27/2017			Pending	SYSTEMS AND METHODS FOR HIGH PRECISION CABLE LENGTH MEAS
MP6629	MP6629WOKR	KR	10-2018-70247X	3/27/2017			Pending	SYSTEMS AND METHODS FOR HIGH PRECISION CABLE LENGTH MEAS
MP6633	MP6633	US	15/469,362	3/24/2017	10298370	5/21/2019	In Force	RESPONSE RULES OF TRIGGER FRAME
MP6633	MP6633C1	US	16/416,506	5/20/2019			Pending	RESPONSE RULES OF TRIGGER FRAME
MP6634	MP6634	US	15/469,274	3/24/2017	10440598	10/8/2019	In Force	METHODS AND APPARATUS FOR CLOCK DRIFT MITIGATION WITH SN
MP6634	MP6634WO	WO	PCT/US2017/02	3/24/2017			Pending	METHODS AND APPARATUS FOR CLOCK DRIFT MITIGATION WITH SN
MP6637	MP6637	US	15/479,005	4/4/2017			Pending	METHODS AND SYSTEMS FOR ACCESSING HOST MEMORY THROUGH
MP6637	MP6637WO	WO	PCT/US2017/02	4/4/2017			Pending	METHODS AND SYSTEMS FOR ACCESSING HOST MEMORY THROUGH
MP6637	MP6637WOEN	CN	201780022196.1	4/4/2017			Pending	METHODS AND SYSTEMS FOR ACCESSING HOST MEMORY THROUGH
MP6637	MP6637WOEP	EP	17718261.5	4/4/2017			Pending	METHODS AND SYSTEMS FOR ACCESSING HOST MEMORY THROUGH
MP6639	MP6639	US	15/486,154	4/12/2017	9967127	5/8/2018	In Force	INTEGER NON-UNIFORM CONSTELLATION FOR HIGH-ORDER QAM
MP6639	MP663911	US	15/627,163	6/19/2017			Pending	Integer Non-Uniform Constellation for High-Order QAM

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP6643	MP6643	US	15/488,281	4/14/2017	10484980	11/19/2019	In force	LINK LAYER SERVICE PLATFORM
MP6643	MP6643C1	US	16/687,009	11/18/2019			Pending	MEDIUM ACCESS CONTROL ENTITIES OPERATING IN RESPECTIVE NO
MP6644	MP6644	US	15/488,162	4/14/2017	10312730	2/19/2019	In Force	PARTIAL ASSOCIATION IDENTIFIER (AID) LENGTH CHANGE NOTIFICAT
MP6646	MP6646	US	15/487,812	4/14/2017	10356815	7/16/2019	In Force	CLEAR CHANNEL ASSESSMENT FOR CO-LOCATED WIRELESS NETWORK
MP6646	MP6646WO	WO	PCT/US2017/02	4/14/2017			Pending	CLEAR CHANNEL ASSESSMENT FOR CO-LOCATED WIRELESS NETWORK
MP6652	MP6652	US	15/598,107	5/17/2017	10257859	4/9/2019	In Force	METHOD AND APPARATUS FOR CLEAR CHANNEL ASSESSMENT
MP6657	MP6657	US	15/598,089	5/17/2017	10416887	9/17/2019	In Force	HYBRID STORAGE DEVICE AND SYSTEM
MP6673	MP6673	US	15/604,303	5/24/2017	10056147	8/21/2018	In Force	TWO-LEVEL STORAGE DEVICE WITH FASTER FRONT END
MP6681	MP6681	US	15/629,420	6/21/2017	10090969	10/2/2018	In Force	METHOD AND APPARATUS FOR TRANSMISSION
MP6681	MP6681WO	WO	PCT/US2017/03	6/21/2017			Pending	METHOD AND APPARATUS FOR TRANSMISSION
MP6681	MP6681WOCN	CN	201780038554.1	6/21/2017			Pending	METHOD AND APPARATUS FOR TRANSMISSION
MP6681	MP6681WODE	DE	112017003077.1	6/21/2017			Pending	METHOD AND APPARATUS FOR TRANSMISSION
MP6681	MP6681WOJP	JP	2018-566351	6/21/2017			Pending	METHOD AND APPARATUS FOR TRANSMISSION
MP6681	MP6681WOKR	KR	10-2019-700183	6/21/2017			Pending	METHOD AND APPARATUS FOR TRANSMISSION
MP6684	MP6684	US	15/629,427	6/21/2017	10250264	4/2/2019	In Force	MULTIPLYING DELAY-LOCKED LOOP USING SAMPLING TIME-TO-DIGI
MP6684	MP6684CN	CN	201710477077.1	6/21/2017			Pending	MULTIPLYING DELAY-LOCKED LOOP USING SAMPLING TIME-TO-DIGI
MP6685	MP6685CN	CN	201710475521.1	6/21/2017			Pending	ANALOG FRACTIONAL-N PHASE-LOCKED LOOP
MP6686	MP6686	US	15/626,133	6/18/2017	10083740	9/25/2018	In Force	RING OSCILLATOR BUILT FROM SRAM CELLS INTERCONNECTED VIA S
MP6686	MP6686CN	CN	201710475615.1	6/21/2017			Pending	RING OSCILLATOR BUILT FROM SRAM CELLS INTERCONNECTED VIA S
MP6686	MP6686KR	KR	10-2017-007867	6/21/2017			Pending	RING OSCILLATOR BUILT FROM SRAM CELLS INTERCONNECTED VIA S
MP6686	MP6686TW	TW	106120694	6/21/2017			Pending	SRAM speed characterization in advanced process nodes method and
POU920030011	POU920030011CN1	China	2.0041009094e-		21.2004100909	8/22/2007	Not Provide	Silicon Carrier For Optical Interconnect Modules
POU920030011	POU920030011US1	US	10/738,064	12/17/2003	7058247	6/6/2006		Silicon Carrier For Optical Interconnect Modules
POU920090152	POU920090152US1	US	12/698,258	2/2/2010	8149035	4/3/2012		Multi-Output PLL Output Shift
RAI920010005	RAI920010005CN1	China	2002803710		21.02803710.3	7/19/2006	Not Provide	Unlink Analog Architecture
RAI920010005	RAI920010005EP1	Europe	2716131.4		1352475	6/14/2006	Not Provide	Unlink Analog Architecture
RAI920010005	RAI920010005TW1	Taiwan	91100310		Nr-183610	12/9/2003	Not Provide	Unlink Analog Architecture
RAI920010005	RAI920010005US2	US	09/996,053	11/28/2001	6993107	1/31/2006		Analog Unidirectional Serial Link Architecture
ROC920000157	ROC920000157US1	US	09/617,558	7/17/2000	6766856	7/6/2004		Programmable Compensated Delay For Ddr Sdram Interface Using Pr
ROC920000263	ROC920000263TW	Taiwan	91104392		550594	9/1/2003	Not Provide	Clocked Memory Device that Includes a Programming Mechanism for
ROC920000263	ROC920000263US1	US	09/805,420	3/13/2001	6434082	8/13/2002		Clocked Memory Device That Includes A Programming Mechanism Fo
ROC920010006	ROC920010006US1	US	09/810,133	3/16/2001	6489850	12/3/2002		Crosstalk Suppression In Differential Ac Coupled Multichannel Ic Amp
ROC920010141	ROC920010141US1	US	09/977,423	10/15/2001	6558522	3/25/2003		Method And Ring Oscillator For Evaluating Dynamic Circuits
ROC920010356	ROC920010356US1	US	10/140,549	5/7/2002	6657886	12/2/2003		Split Local And Continuous Bitline For Fast Domino Read Sram

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ROC920010376	ROC920010376US1	US	10/306,200	11/27/2002	6774734	8/10/2004		Ring Oscillator Circuit For Edram/Dram Performance Monitoring
ROC920020055	ROC920020055US1	US	10/176,233	6/20/2002	6969159	6/23/2005		Method And Apparatus To Make A Semiconductor Chip Susceptible To
ROC920020138	ROC920020138US1	US	10/306,142	11/27/2002	6963516	11/8/2005		Dynamic Optimization Of Latency And Bandwidth On Dram Interfaces
ROC920040155	ROC920040155US1	US	10/970,522	10/21/2004	7055127	4/25/2006		Method And Sum Addressed Cell Encoder For Enhanced Compare An
ROC920040172	ROC920040172US1	US	11/128,069	5/12/2005	7389481	6/17/2008		Integrated Circuit Design Utilizing Array Of Functionally Interchangea
ROC920040271	ROC920040271CN1	China	2,0051011497e-		212005101149	2/1/2012	Not Provide	Using Electrically Programmable Fuses To Hide Architecture, Prevent
ROC920040271	ROC920040271TW	Taiwan	94142692		1397817	6/1/2013	Not Provide	Using Electrically Programmable Fuses To Hide Architecture, Prevent
ROC920040271	ROC920040271US1	US	11/016,219	12/17/2004	7442583	10/28/2008		Using Electrically Programmable Fuses To Hide Architecture, Prevent
ROC920040272	ROC920040272US1	US	11/016,220	12/17/2004	7268577	9/11/2007		Changing Chip Function Based On Fuse States
ROC920040374	ROC920040374US1	US	11/116,625	4/28/2005	7290185	10/30/2007		Method And Apparatus For Reducing Memory Errors
ROC920050092	ROC920050092US1	US	11/181,707	7/14/2005	7425855	9/16/2008		Set/Reset Latch With Minimum Single Event Upset
ROC920060016	ROC920060016US1	US	11/419,008	5/18/2006	7362154	4/22/2008		Radiation Hardened Latch
ROC920060016	ROC920060016US2	US	11/923,900	10/25/2007	7474134	1/6/2009		Radiation Hardened Programmable Phase Frequency Divider
ROC920060091	ROC920060091US1	US	11/533,408	9/20/2006	7474144	1/6/2009		Ratified Feedback Body Voltage Bias Generator
ROC920060091	ROC920060091US2	US	11/866,110	10/2/2007	8310298	11/13/2012		Ratified Feedback Body Voltage Bias Generator
ROC920060443	ROC920060443US1	US	11/560,428	11/16/2006	7460550	7/15/2008		Delay Mechanism For Unbalanced Read/Write Paths In Domino Sram
ROC920060498	ROC920060498US1	US	11/744,288	5/4/2007	7414878	8/19/2008		Method And Apparatus For Implementing Domino Sram Leakage Curr
ROC920070086	ROC920070086US1	US	11/782,808	7/25/2007	7480170	1/20/2009		Method And Apparatus For Implementing Domino Sram Leakage Curr
ROC920070086	ROC920070086US2	US	11/873,534	10/17/2007	7609542	10/27/2009		Implementing Enhanced Sram Read Performance Sort Ring Oscillator
ROC920070137	ROC920070137US1	US	11/969,449	1/4/2008	8140937	3/20/2012		Memory Initialization Time Reduction
ROC920070568	ROC920070568US1	US	12/366,005	2/5/2009	7812643	10/12/2010		Implementing Cnl Multiplexer Load Balancing
ROC920070581	ROC920070581US1	US	12/341,079	12/22/2008	7844843	11/30/2010		Implementing Power Savings In Hss Clock-Gating Circuit
ROC920080015	ROC920080015US1	US	12/560,242	1/27/2009	7911827	3/22/2011		Implementing Enhanced Sram Stability And Enhanced Chip Yield With
ROC920080088	ROC920080088US1	US	12/167,300	7/3/2008	7817481	10/19/2010		Column Selectable Self-Biasing Virtual Voltages For Sram Write Assist
ROC920080340	ROC920080340US1	US	12/401,730	3/11/2009	7830195	11/9/2010		Self-Test Design Methodology And Technique For Root-Gated Clockin
ROC920080023	ROC920090023DE1	Germany	2011111001181		1,1201110012	1/8/2015	Not Provide	Sampling signal displacement in the configurations with bidirectional
ROC920090023	ROC920090023US1	US	12/705,674	2/15/2010	8284621	10/9/2012		Strobe Offset In Bidirectional Memory Strobe Configurations
ROC920090073	ROC920090073US1	US	12/712,521	2/25/2010	8574982	11/5/2013		Implementing Edram Stacked Fet Structure
ROC920090073	ROC920090073US2	US	13/936,569	7/8/2013	9343464	5/17/2016		Implementing Edram Stacked Fet Structure
ROC920090102	ROC920090102US1	US	12/788,411	5/27/2010	8213249	7/3/2012		Implementing low Power Data Predicting Local Evaluation For Doubl
ROC920100009	ROC920100009US1	US	12/725,822	3/17/2010	8289798	10/16/2012		Voltage Regulator Bypass In Memory Device
ROC920100079	ROC920100079US1	US	12/841,384	7/22/2010	8316333	11/20/2012		Implementing Timing Pessimism Reduction For Parallel Clock Trees
ROC920100080	ROC920100080US1	US	12/841,403	7/22/2010	8271923	9/18/2012		Implementing Forward Tracing To Reduce Pessimism In Static Timing
ROC920100101	ROC920100101US1	US	12/858,881	8/18/2010	8237510	8/7/2012		Implementing Phase Locked Loop (Pll) With Enhanced Locking Capabi

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ROC920100243	ROC920100243US1	US	12/938,477	11/3/2010	8300450	10/30/2012		Implementing Physically Unclonable Function (PUF) Utilizing EDRAM M
ROC920100244	ROC920100244US1	US	12/956,343	11/30/2010	8686782	4/1/2014		Structure For A Frequency Adaptive Level Shifter Circuit
ROC920100255	ROC920100255US1	US	12/898,924	10/6/2010	8467230	6/18/2013		Data Security For Dynamic Random Access Memory Using Body Bias
ROC920100255	ROC920100255US2	US	13/760,190	2/6/2013	8711606	4/29/2014		Data Security For Dynamic Random Access Memory Using Body Bias
ROC920100277	ROC920100277US1	US	12/914,573	10/28/2010	8356264	1/15/2013		Implementing Enhanced Clock Tree Distributions To Decouple Across
ROC920100277	ROC920100277US2	US	13/666,484	11/1/2012	8631378	1/14/2014		Implementing Enhanced Clock Tree Distributions To Decouple Across
ROC920100295	ROC920100295US1	US	13/030,516	2/18/2011	8324933	12/4/2012		Implementing Dual Speed Level Shifter With Automatic Mode Contr
ROC920110034	ROC920110034US1	US	13/101,436	5/5/2011	8520429	8/27/2013		Data Dependent Stram Write Assist
ROC920110082	ROC920110082US2	US	13/827,690	3/14/2013	8826214	9/2/2014		Implementing Z Directional Macro Port Assignment
ROC920110132	ROC920110132US1	US	13/414,133	3/7/2012	8675427	3/18/2014		Implementing Rc And Coupling Delay Correction In Stram
ROC920110150	ROC920110150US1	US	13/284,239	10/28/2011	8415969	4/9/2013		Implementing Screening For Single Fet Compare Of Physically Unclon
ROC920110224	ROC920110224US1	US	13/309,068	12/1/2011	8451668	5/28/2013		Implementing Column Redundancy Steering For Memories With Wor
ROC920110227	ROC920110227US1	US	13/316,620	12/12/2011	8627162	1/7/2014		Implementing Enhanced Aperture Function Calibration For Logic Buil
ROC920110234	ROC920110234US1	US	13/343,938	1/5/2012	9061902	6/23/2015		Implementing Enhanced Hardware Assisted Dram Repair Using A Dat
ROC920120010	ROC920120010US1	US	13/368,918	2/8/2012	8659937	2/25/2014		Implementing Low Power Write Disabled Local Evaluation For Stram
ROC920120166	ROC920120166US1	US	13/658,226	10/23/2012	8797823	8/5/2014		Implementing SDRAM Having No Ras To Cas Delay In Write Operation
ROC920120225	ROC920120225US2	US	13/764,860	2/12/2013	9007858	4/14/2015		Stram Global Precharge, Discharge, And Sense
ROC920130044	ROC920130044US1	US	13/912,324	6/7/2013	8953365	2/10/2015		Capacitor Backup For Stram
ROC920130167	ROC920130167US1	US	14/060,665	10/23/2013	9064602	6/23/2015		Implementing Memory Device With Sub-Bank Architecture
ROC920130168	ROC920130168US1	US	14/077,559	11/12/2013	9087607	7/21/2015		Implementing Sense Amplifier For Sensing Local Write Driver With Bo
ROC920140005	ROC920140005US1	US	14/276,025	5/13/2014	9405468	8/2/2016		Stacked Memory Device Control
ROC920140005	ROC920140005US2	US	14/464,090	8/20/2014	9400602	7/26/2016		Stacked Memory Device Control
ROC920140066	ROC920140066US2	US	14/512,626	10/13/2014	9473141	10/18/2016		Receiving An I/O Signal In Multiple Voltage Domains
RP920020088	RP920020088US1	US	10/266,132	10/7/2002	6962413	11/22/2005		Method And System For Configuring Terminators In A Serial Commu
RP920020089	RP920020089US1	US	10/265,755	10/7/2002	7289572	10/30/2007		Method And System For Scalable Pre-Driver To Driver Interface
RP920030104	RP920030104US1	US	10/680,679	10/7/2003	7340660	3/4/2008		Method And System For Using Statistical Signatures For Testing High-
RP920030105	RP920030105US1	US	10/791,175	3/2/2004	7477713	1/13/2009		Method For Providing Automatic Adaptation To Frequency Offsets In
RP920040052	RP920040052US1	US	10/873,672	6/22/2004	7466135	7/29/2008		Reducing Power Consumption In Signal Detection
RP920040053	RP920040053US1	US	10/915,790	8/11/2004	7397876	7/8/2008		Method And Arrangements For Link Power Reduction
RP920040189	RP920040189US1	US	11/079,952	3/15/2005	7418032	8/26/2008		Altering Power Consumption In Communication Links Based On Means
RP920050081	RP920050081CN1	China	2,006,103,712e		ZL2006101371	12/16/2009	Not Provide	Source Series Terminated Serial Link Transmitter Having Ground Refe
RP920050081	RP920050081US1	US	11/263,138	10/27/2005	7307447	12/11/2007		Self Series Terminated Serial Link Transmitter Having Segmentation F
RP920050088	RP920050088CN1	China	2,006,103,711e		ZL2006101371	8/26/2009	Not Provide	Impedance Calibration For Source Series Terminated Serial Link Trans
RP920050088	RP920050088US1	US	11/262,101	10/28/2005	7368902	5/6/2008		Impedance Calibration For Source Series Terminated Serial Link Trans

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
RP5920050092	RP5920050092US1	US	11/458,712	7/20/2006	7465620	7/29/2008		Differential Amplifier And Method
RP5920050093	RP5920050093US1	US	11/531,714	9/14/2006	7391266	6/24/2008		Serial Link Output Stage Differential Amplifier And Method
RP5920050121	RP5920050121US1	US	11/417,626	5/4/2006	7332932	2/19/2008		Serial Link Receiver With Wide Input Voltage Range And Tolerance To
RP5920050194	RP5920050194US1	US	11/383,821	5/17/2006	7486114	2/3/2009		Signal Detector With Calibration Circuit Arrangement
RP5920090060	RP5920090060US1	US	12/543,301	8/18/2009	8255846	8/28/2012		Development Tool For Comparing Netlists
YOR919980423	YOR919980423US3	US	10/072,346	2/6/2002	7061821	6/13/2006		Address Wrap Function For Addressable Memory Devices
YOR920000587	YOR920000587US1	US	09/827,073	4/5/2001	6751151	6/15/2004		Ultra High-Speed Ddp-Sram Cache
YOR920040056	YOR920040056US2	US	11/502,196	8/10/2006	7388224	6/17/2008		Structure For Determining Thermal Cycle Reliability
YOR920040056	YOR920040056US4	US	14/729,446	6/3/2015	9443776	9/13/2016		Method And Structure For Determining Thermal Cycle Reliability
YOR920050329	YOR920050329JP1	Japan	2008-063543		5182802	1/25/2013	Not Provide %SER%Programmable Heavy-Ion Sensing Device For Accelerated DRA	
YOR920050329	YOR920050329US1	US	11/689,096	3/21/2007	7489308	3/3/2009		Programmable Heavy-Ion Sensing Device For Accelerated Dnam Soft
YOR920060860	YOR920060860DE1	Germa	08/18046.9		2140650	5/25/2011	Not Provide METHOD AND SYSTEM FOR RESILIENT PACKET TRACEBACK IN WIREL	
YOR920060860	YOR920060860EP1	Europe	08/18046.9		2140650	5/25/2011	Not Provide METHOD AND SYSTEM FOR RESILIENT PACKET TRACEBACK IN WIREL	
YOR920060860	YOR920060860JP1	Japan	2010-500227		4683383	2/18/2011	Not Provide METHOD AND SYSTEM FOR RESILIENT PACKET TRACEBACK IN WIREL	
YOR920060860	YOR920060860US1	US	11/694,388	3/30/2007	8695089	4/8/2014		Method And System For Resilient Packet Traceback In Wireless Mesh
YOR920070250	YOR920070250US1	US	11/761,610	6/12/2007	7973549	7/5/2011		Method And Apparatus For Calibrating Internal Pulses In An Integrate
YOR920070521	YOR920070521CN1	China	2.0081016177e-		212008101617	4/14/2010	Not Provide CMOS EPROM AND EEPROM DEVICES AND PROGRAMMABLE CMOS I	
YOR920070521	YOR920070521TW1	Taiwan	97137509		1394266	4/21/2013	Not Provide CMOS EPROM AND EEPROM DEVICES AND PROGRAMMABLE CMOS I	
YOR920070521	YOR920070521US1	US	11/935,143	11/5/2007	7700993	4/20/2010		Cmos Eeprom And Eeprom Devices And Programmable Cmos Inverters
YOR920070622	YOR920070622US1	US	12/019,913	1/25/2008	8421478	4/16/2013		Radio Frequency Integrated Circuitry With On-Chip Noise Source For S
YOR920070683	YOR920070683US1	US	12/194,571	8/20/2008	8135100	3/13/2012		Adaptive Clock And Equalization Control Systems And Methods For D
YOR920080076	YOR920080076US1	US	12/244,118	10/2/2008	8123088	2/28/2012		Dispensing Assembly With A Controlled Gas Environment
YOR920080077	YOR920080077US1	US	12/284,145	10/2/2008	8123089	2/28/2012		Dispensing Assembly With An Injector Controlled Gas Environment
YOR920080402	YOR920080402US1	US	12/498,175	7/6/2009	8189419	5/29/2012		Apparatus For Nonvolatile Multi-Programmable Electronic Fuse Syste
YOR920080514	YOR920080514US1	US	12/201,487	8/29/2008	8136045	2/28/2012		System And Method For Latency Reduction In Speculative Decision F
YOR920090051	YOR920090051CN1	China	2.0101020272e-		212010102027	7/18/2012	Not Provide DYNAMIC QUADRATURE CLOCK CORRECTION FOR A PHASE ROTATO	
YOR920090051	YOR920090051US1	US	12/492,419	6/26/2009	8139700	3/20/2012		Dynamic Quadrature Clock Correction For A Phase Rotator System
YOR920090649	YOR920090649US2	US	12/686,764	1/29/2010	8612974	4/2/2013		Global Synchronization Of Parallel Processors Using Clock Pulse Widt
YOR920110020	YOR920110020US1	US	13/173,434	6/30/2011	8599966	12/3/2013		Coupling System For Data Receivers
YOR920110020	YOR920110020US2	US	14/074,920	11/8/2013	8867668	10/21/2014		Coupling System For Data Receivers
YOR920120015	YOR920120015CN1	China	2.0131005111e-		2120131005111e-	3/30/2016	Not Provide CAPACITIVE LEVEL-SHIFTING CIRCUITS AND METHODS FOR ADDING	
YOR920120015	YOR920120015US1	US	13/399,054	2/17/2012	8704583	4/22/2014		Capacitive Level-Shifting Circuits And Methods For Adding Dc Offsets

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Exhibit B - 3A C I T O MAPL Group B

Family Ref No	IP Right ID	Country	Application No	Filing Date	Patent Number	Issue Date	Status	Title
AQUA.P175	AQUA.P1175CIP	US	14/730,083				Unknown	
AQUA.P176	AQUA.P176CIP1	US	14/504,318				Unknown	
AQUA.P176	AQUA.P176CIP1C1	US	15/175,516				Unknown	
AQUA.P189	AQUA.P189	US	11/373,928				Unknown	
AQUA.P193	AQUA.P193	US	11/725,528				Unknown	
AQUA.P194	AQUA.P194	US	12/077,672				Unknown	
AQUA.P196	AQUA.P196	US	12/384,298				Unknown	
AQUA.P197	AQUA.P197	US	12/765,097				Unknown	
AQUA.P198	AQUA.P198	US	12/789,728				Unknown	
AQUA.P199	AQUA.P199	US	12/818,549				Unknown	
AQUA.P200	AQUA.P200	US	12/839,254				Unknown	
AQUA.P201	AQUA.P201	US	12/857,080				Unknown	
AQUA.P203	AQUA.P203	US	12/960,474				Unknown	
AQUA.P204	AQUA.P204	US	13/235,283				Unknown	
AQUA.P222	AQUA.P222	US	15/364,030				Allowed	
AQUA.P222	AQUA.P222C2	US	16/365,516				Allowed	
AQUA.P223	AQUA.P223	US	15/276,259				Allowed	
ARC920030015	ARC920030015US2	US	11/778,641	7/16/2007	7562281	7/14/2009		Autonomic Parity Exchange
ARC920030040	ARC920030040US2	US	11/747,887	5/11/2007	8108750	1/31/2012		Raid 3+3
AUS920010302	AUS920010302US1	US	09/918,809	7/31/2001	6963629	11/8/2005		Adaptive Phase Locked Loop
AUS920030791	AUS920030791US2	US	12/173,093	7/15/2008	8219981	7/10/2012		Processor Dedicated Code Handling in A Multi-Processor Environment
AUS920050345	AUS920050345US4	US	11/838,341	8/14/2007	7558136	7/7/2009		Internally Asymmetric Methods And Circuits For Evaluating Static Memory
AUS920070816	AUS920070816US2	US	12/399,551	3/6/2009	7955418	8/9/2011		Method And Computer Program For Controlling A Storage Device Having Pe
AUS920070816	AUS920070816US3	US	13/115,149	5/25/2011	8208339	6/26/2012		Computer Program Product For Controlling A Storage Device Having Per-Ele
AUS920100248	AUS920100248US2	US	13/614,102	9/13/2012	8399981	3/19/2013		Ball Grid Array With Improved Single-Ended And Differential Signal Perform
BUR919940006	BUR919940006US3	US	08/942,515	10/2/1997	5946545	8/31/1999		
BUR919960079	BUR919960079US2	US	09/183,342	10/30/1998	6119241	9/12/2000		
BUR919970038	BUR919970038US2	US	09/296,807	4/23/1999	6184549	2/6/2001		
BUR919980148	BUR919980148US2	US	09/589,922	6/7/2000	6243283	6/5/2001		
BUR919990218	BUR919990218US3	US	10/946,552	9/21/2004	7096436	8/22/2006		Macro Design Techniques To Accommodate Chip Level Wiring And Circuit Pi
BUR919990240	BUR919990240US2	US	10/302,412	11/22/2002	6706621	3/16/2004		
BUR919990240	BUR919990240US3	US	10/625,635	7/23/2003	7138326	11/21/2006		Wafer Integrated Rigid Support Ring

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Family Ref No	IP Right ID	Country	Application No	Filing Date	Patent Number	Issue Date	Status	Title
BUR920010045	BUR920010045US3	US	11/680,371	2/28/2007	7457895	11/25/2008		Dynamic Memory Allocation Between Inbound And Outbound Buffers In A
BUR920010045	BUR920010045US4	US	12/183,533	7/31/2008	7739427	6/15/2010		Dynamic Memory Allocation Between Inbound And Outbound Buffers In A
BUR920010121	BUR920010121US3	US	12/194,570	8/20/2008	8122395	2/21/2012		On Chip Timing Adjustment In Multi-Channel Fast Data Transfer
BUR920010182	BUR920010182US2	US	10/664,238	6/9/2004	6922349	7/26/2005		Complementary Two Transistor Rom Cell
BUR920010192	BUR920010192US2	US	10/710,113	6/18/2004	7584077	9/1/2009		Physical Design Characterization System
BUR920020018	BUR920020018US2	US	11/410,829	4/24/2006	7248838	7/24/2007		Wireless Communication System Within A System On A Chip
BUR920020076	BUR920020076US4	US	12/911,379	10/25/2010	8184465	5/22/2012		Programmable Semiconductor Device
BUR920020076	BUR920020076US5	US	13/627,162	3/22/2012	8724365	5/13/2014		Programmable Semiconductor Device
BUR920020088	BUR920020088US3	US	11/875,004	10/19/2007	7440348	10/21/2008		Memory Array Having A Redundant Memory Element
BUR920020088	BUR920020088US5	US	11/875,011	10/19/2007	7725780	5/25/2010		Enabling Memory Redundancy During Testing
BUR920030075	BUR920030075US2	US	11/181,053	7/14/2005	7417453	8/26/2008		System And Method For Dynamically Executing A Function In A Programm
BUR920030077	BUR920030077US2	US	11/230,898	9/20/2005	7068105	6/27/2006		Low-Voltage Differential Amplifier
BUR920040024	BUR920040024US2	US	11/407,176	4/19/2006	7389478	6/17/2008		System And Method For Designing A Low Leakage Monotonic Cmos Logic Ci
BUR920040075	BUR920040075US2	US	11/383,353	5/15/2006	7444608	10/28/2008		Method And System For Evaluating Timing In An Integrated Circuit
BUR920040183	BUR920040183US2	US	12/136,359	6/10/2008	8051340	11/1/2011		System And Method For Balancing Delay Of Signal Communication Paths Th
BUR920050120	BUR920050120US2	US	12/169,668	7/9/2008	8135558	3/13/2012		Automated Simulation Testbench Generation For Serializer/Deserializer Dat
BUR920050127	BUR920050127US2	US	12/268,583	11/11/2008	8205124	6/19/2012		Microcontroller For Logic Built-In Self Test (lbist)
BUR920050127	BUR920050127US3	US	13/469,604	5/11/2012	8423847	4/16/2013		Microcontroller For Logic Built-In Self Test (lbist)
BUR920050210	BUR920050210US2	US	12/348,070	1/2/2009	8132129	3/6/2012		Method For Computing The Sensitivity Of A Vlsi Design To Both Random An
BUR920050293	BUR920050293US3	US	13/325,307	12/14/2011	8459140	7/30/2013		Dynamically Adjusting Pipelined Data Paths For Improved Power Managem
BUR920060087	BUR920060087US3	US	12/787,167	5/25/2010	8174329	5/8/2012		Power Management Architecture And Method Of Modulating Oscillator Fre
BUR920060127	BUR920060127US3	US	12/631,900	12/7/2009	7944734	5/17/2011		Integrating Nonvolatile Memory Capability Within Stram Devices
BUR920060129	BUR920060129US3	US	12/146,554	6/26/2008	7729159	6/1/2010		Apparatus For Improved Stram Device Performance Through Double Gate To
BUR920070067	BUR920070067US3	US	12/983,995	1/4/2011	8233302	7/31/2012		Content Addressable Memory With Concurrent Read And Search/Compare
BUR920070110	BUR920070110US2	US	12/350,312	1/8/2009	7782114	8/24/2010		Design Structure For A High-Speed Level Shifter
BUR920070120	BUR920070120US2	US	12/985,456	1/6/2011	8361598	1/29/2013		Substrate Anchor Structure And Method
BUR920070167	BUR920070167US2	US	13/358,105	1/25/2012	8423936	4/16/2013		Semiconductor Structure And Method Of Designing Semiconductor Structur
BUR920070189	BUR920070189US2	US	13/050,052	3/17/2011	8300452	10/30/2012		Structure And Method For Improving Storage Latch Susceptibility To Single
BUR920070213	BUR920070213US2	US	12/765,979	4/23/2010	7939390	5/10/2011		Gap Capacitors For Monitoring Stress In Solder Balls In Flip Chip Technology
BUR920070235	BUR920070235US2	US	13/404,485	2/24/2012	9230725	1/5/2016		Methods Of Designing An Inductor Having Opening Enclosed Within Conduc
BUR920070241	BUR920070241US2	US	12/343,686	12/24/2008	8097474	1/17/2012		Integrated Circuit Chip Design Flow Methodology Including Insertion Of On-
BUR920070276	BUR920070276US2	US	13/196,404	8/2/2011	8373267	2/12/2013		Electrostatic Discharge (Esd) Silicon Controlled Rectifier (Scr) Structure
BUR920080015	BUR920080015US2	US	12/961,026	8/23/2010	7940581	5/10/2011		Method Of Low Power Sensing In A Multi-Port Stram Using Pre-Discharged Bit
BUR920080016	BUR920080016US2	US	12/858,499	8/18/2010	7986571	7/26/2011		Low Power, Single-Ended Sensing In A Multi-Port Stram Using Pre-Discharge

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Family Ref No	IP Right ID	Country	Application No	Filing Date	Patent Number	Issue Date	Status	Title
BUR920090028	BUR920090028US2	US	13/366,804	2/6/2012	8437201	5/7/2013		Word-Line Level Shift Circuit
BUR920090083	BUR920090083US2	US	13/330,889	6/22/2012	8520458	8/27/2013		Phase Change Memory Cycle Timer And Method
BUR920130145	BUR920130145US1	US	15/048,256	2/19/2016	9530500	12/27/2016		Converting An Xy Team To A Value Team
BUR920150023	BUR920150023US1	US	15/621,529	6/13/2017	10295592	5/21/2019		Pre-Test Power-Optimized Bin Reassignment Following Selective Voltage Bi
CH920010044U	CH920010044US2	US	11/966,438	12/28/2007	7418069	8/26/2008		Clock Data Recovering System With External Early/Late Input
CH920040024U	CH920040024US2	US	12/116,151	5/6/2008	8054926	11/8/2011		Clock And Data Recovery System And Method For Clock And Data Recovery
END920010074	END920010074US2	US	11/251,745	10/17/2005	7351917	4/1/2008		Vents With Signal Image For Signal Return Path
END920030035	END920030035US5	US	11/857,632	9/19/2007	7378890	5/27/2008		Programmable Low-Power High-Frequency Divider
END920030035	END920030035US6	US	12/103,129	4/15/2008	7545191	6/9/2009		Method For Dividing A High-Frequency Signal
END920030108	END920030108US3	US	12/203,955	9/4/2008	8108811	1/31/2012		Validation Of Electrical Performance Of An Electronic Package Prior To Fabri
FIS919950077	FIS919950077US3	US	09/189,391	11/10/1998	6038192	3/14/2000		Copper To Aluminum Interlayer Interconnect Using Stud And Via Liner
FIS919950077	FIS919950077US5	US	09/190,628	11/12/1998	6075745	6/13/2000		Saving Content Addressable Memory Power Through Conditional Comparis
FIS919950077	FIS919950077US6	US	09/190,871	11/12/1998	6130854	10/10/2000		Unique Feature Design Enabling Structural Integrity For Advanced Low K Se
FIS919950077	FIS919950077US8	US	09/190,919	11/12/1998	6044031	3/28/2000		High Speed Fir Transmitter
FIS920000242	FIS920000242US2	US	10/844,533	5/13/2004	7037824	5/2/2006		Low Power Manager For Standby Operation Of A Memory System
FIS920000399	FIS920000399US2	US	10/353,119	1/28/2003	6711040	3/23/2004		Electrical Antifuse
FIS920010285	FIS920010285US2	US	10/437,208	5/13/2003	6815346	11/9/2004		Method Of Programming Electrical Antifuse
FIS920030003	FIS920030003US1	US	10/249,795	5/9/2003	6680681	1/20/2004		Stress Generating Shallow Trench Isolation Structure Having Dual Composi
FIS920030090	FIS920030090US2	US	11/205,565	8/17/2005	7023758	4/4/2006		Method For Quantifying The Manufacturing Complexity Of Electrical Design
FIS920060116	FIS920060116US2	US	12/555,241	9/8/2009	8115275	2/14/2012		Soft Error Rate Mitigation By Interconnect Structure
FIS920060116	FIS920060116US3	US	13/362,043	1/31/2012	8361887	1/29/2013		Electrically Programmable Fuse Using Anisometric Contacts And Fabrication
FIS920070278	FIS920070278US2	US	13/419,927	3/14/2012	8728905	5/20/2014		Semiconductor Device Contact Structure Having Stacked Nickel, Copper, An
FIS920070372	FIS920070372US2	US	12/914,367	10/28/2010	8635875	2/4/2014		Apparatus And Method For Transient Suppression In Synchronous Data Det
FIS920070383	FIS920070383US2	US	13/361,057	1/30/2012	8445374	5/21/2013		OUTPUT REGULATOR SYSTEM
FIS920080171	FIS920080171US2	US	13/420,724	3/15/2012	8639049	1/14/2014		Method And System For Detecting And Repairing Defective Memory Cells
FIS920140166	FIS920140166US1-	US	15/191,723	6/24/2016	9853006	12/26/2017		Method And Systems For Memory Testing And Test Data Reporting During
MP007	MP007RE	US	09/695,035	10/25/2000	RE37751	6/18/2002	In Force	METHOD AND SYSTEM FOR MEMORY TESTING AND TEST DATA REPORTING
MP0135	MP0135WIP1	JP	2004-520097	7/10/2003	4211656	3/6/2009	In Force	
MP0263	MP0263aaaa	US	13/862,991	4/15/2013	8762801	6/24/2014	In Force	
MP0263	MP0263aaC1	US	13/118,739	5/31/2011	8423841	4/16/2013	In Force	
MP0263	MP0263D1	US	11/679,133	2/26/2007	7734966	6/8/2010	In Force	
MP0315	MP0315	US	10/667,197					
MP0375	MP0375D1	US	11/751,593	5/21/2007	7640370	12/29/2009	In Force	
MP0375	MP0375D1C1	US	12/616,111	11/10/2009	8856391	10/7/2014	In Force	

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MP0452	MP0452	US	10/795,727					
MP0479	MP0479	US	10/801,197					
MP0626	MP0626C1CN	CN	200600570760	3/17/2006			Pending	WIRELESS AUDIO FOR ENTERTAINMENT CENTERS
MP0774	MP0774WO	WO	PCT/US1999/00	3/26/1999			Pending	Controller For A Disk Drive And Method For Writing Onto And Reading Fro
MP0775	MP0775DE	DE	99925959.1	5/27/1999	699265118	8/3/2005	In Force	Circuit And Method For Rapid Checking Of Error Correction Codes Using Cyc
MP0775	MP0775FR	FR	99925959.1	5/27/1999	1090462	8/3/2005	In Force	Circuit And Method For Rapid Checking Of Error Correction Codes Using Cyc
MP0775	MP0775GB	GB	99925959.1	5/27/1999	1090462	8/3/2005	In Force	Circuit And Method For Rapid Checking Of Error Correction Codes Using Cyc
MP0775	MP0775WO	WO	PCT/US1999/01	5/27/1999			Pending	Circuit And Method For A Rapid Checking Of Error Correction Codes Using C
MP0775	MP0775WO/DE	DE	99925959.1	5/27/1999	1090462	8/3/2005	In Force	CIRCUIT AND METHOD FOR RAPID CHECKING OF ERROR CORRECTION CODES
MP0775	MP0775WO/EP	EP	EP1999092595	5/27/1999	1090462	8/3/2005	In Force	Circuit And Method For Rapid Checking Of Error Correction Codes Using Cyc
MP0775	MP0775WO/FR	FR	99925959.1	5/27/1999	1090462	8/3/2005	In Force	CIRCUIT AND METHOD FOR RAPID CHECKING OF ERROR CORRECTION CODE
MP0775	MP0775WO/GB	GB	99925959.1	5/27/1999	1090462	8/3/2005	In Force	CIRCUIT AND METHOD FOR RAPID CHECKING OF ERROR CORRECTION CODE
MP0778	MP0778EP	EP	9183229	3/23/2000	1163573	8/1/2007	In Force	Arbitration Methods And Systems For Arbitrating Access To A Disk Controlle
MP0778	MP0778EP/DE	DE	00916322.9	3/23/2000	600357740	8/1/2007	In Force	Arbitration Methods And Systems For Arbitrating AccessTo A Disk Controlle
MP0778	MP0778EP/FR	FR	00916322.9	3/23/2000	1163573	8/1/2007	In Force	Arbitration Methods And Systems For Arbitrating AccessTo A Disk Controlle
MP0778	MP0778WO	WO	PCT/US2000/00	3/23/2000			Pending	Arbitration Methods And Systems For Arbitrating Access To A Disk Controlle
MP0778	MP0778WO/KR	KR	10-2001-70121	3/23/2000	619806	8/28/2006	In Force	ARBITRATION METHODS AND SYSTEMS FOR ARBITRATING ACCESS TO A DIS
MP0779	MP0779KR	KR	102001701575	6/1/2000	100699062	3/16/2007	In Force	Disk Drive Controller Circuit And Method For Skipping Defective And/Or Un
MP0779	MP0779WO	WO	PCT/US2000/01	6/1/2000			Pending	Disk Drive Controller Circuit And Method For Skipping Defective And/Or Un
MP0779	MP0779WO/KR	KR	10-2001-70157	6/1/2000	699062	3/16/2007	In Force	DISK DRIVE CONTROLLER CIRCUIT AND METHOD FOR SKIPPING DEFECTIVE
MP0780	MP0780WO	WO	PCT/US2001/02	7/17/2001			Pending	Disk Controller Configured To Perform Out Of Order Execution Of Write Op
MP0780	MP0780WO/JP	JP	2002-521285	7/17/2001	JP4579494	9/3/2010	In Force	DISK CONTROLLER CONFIGURED TO PERFORM OUT OF ORDER
MP0780	MP0780WO/KR	KR	102003700243	2/19/2003	697862	3/14/2007	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Write Op
MP0783	MP0783WO	WO	PCT/US2004/00	3/9/2004			Pending	Method And System For Embedded Disk Controllers
MP0786	MP0786WO	WO	PCT/US2000/01	5/5/2000			Pending	Systems And Methods For A Disk Controller Memory Architecture
MP0786	MP0786WO/DE	DE	00930436.1	5/5/2000	1188106	3/22/2006	In Force	Systems And Methods For A Disk Controller Memory Architecture
MP0786	MP0786WO/EP	EP	00930436.1	5/5/2000	1188106	3/22/2006	In Force	SYSTEMS AND METHODS FOR A DISK CONTROLLER MEMORY ARCHITECTUR
MP0786	MP0786WO/FR	FR	00930436.1	5/5/2000	1188106	3/22/2006	In Force	Systems And Methods For A Disk Controller Memory Architecture
MP0786	MP0786WO/GB	GB	00930436.1	5/5/2000	1188106	3/22/2006	In Force	Systems And Methods For A Disk Controller Memory Architecture
MP0800	MP0800WO	WO	PCT/US2005/02	7/13/2005			Pending	System And Method For Transmitting Data In Storage Controllers
MP0801	MP0801WO	WO	PCT/US2005/02	7/13/2005			Pending	System And Method For Transferring Data Using Storage Controllers
MP0802	MP0802WO	WO	PCT/US2005/02	7/13/2005			Pending	System And Method For Controlling Buffer Memory Overflow And Underflo
MP0803	MP0803WO	WO	PCT/US2005/02	7/13/2005			Pending	Dynamic WWMN Storage Module For Storage Controllers
MP0970	MP0970HK	HK	HK08103121.8	6/4/2002			Pending	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A THREADMILL BELT

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Family Ref No	IP Right ID	Country	Application No	Filing Date	Patent Number	Issue Date	Status	Title
MP1000	MP1000HK	HK	HK06112458.4	11/6/2005			Pending	WIRELESS PRINTER CONFIGURATION MODULE
MP1004	MP1004HK	HK	HK06112110.4	8/31/2005			Pending	LASER PRINT APPARATUS WITH DUAL HALFTONES
MP1005	MP1005HK	HK	HK06112112.2	8/31/2005			Pending	LASER PRINT APPARATUS WITH TONER EXPLOSION COMPENSATION
MP10057	MP10057	US	15/611,050	6/1/2017	10348380	7/9/2019	In Force	Methods and Apparatus for Singular Value Decomposition with Norm Sortin
MP1022	MP1022	US	11/582,067	10/17/2006	7720454	5/18/2010	In Force	Extremely Low If Architecture For In-Band On-Channel (IBOC) Radio
MP10226	MP10226PR	US	62/802,113	2/6/2019			Pending	Method and Apparatus for Extendable Hardware Queues
MP10289	MP10289PR	US	62/767,753	11/15/2018			Pending	LOW-POWER SERIALIZER WITH HALF-RATE CLOCKING FOR SERIAL COMMUN
MP10295	MP10295C1	US	0				Not Filed	Modular Memory-like Layout For FinFET Analog Designs
MP1031	MP1031	US	11/519,370	9/11/2006	8064542	11/22/2011	In Force	SYSTEMS AND METHODS FOR V/Q IMBALANCE CORRECTION AND CALIBRAT
MP10326	MP10326	US	16/049,732	7/30/2018			Pending	WAKEUP RADIO (WUR) PACKET PREAMBLE DESIGN
MP10383	MP10383C1	US	16/702,628	12/4/2019			Pending	Ethernet Transceiver with PHY-Level Signal-Loss Detector
MP10400	MP10400C1	US	0				Not Filed	TWO DIMENSIONAL MAGNETIC RECORDING (TDMR) OFF-TRACK PERFORM
MP10473	MP10473	US	16/690,803	11/21/2019			Pending	SERIAL MANAGEMENT INTERFACE WITH IMPROVED RELIABILITY
MP10473	MP10473PR	US	62/770,537	11/21/2018			Pending	Reliable SMI Access
MP10482	MP10482PR	US	62/771,879	11/27/2018			Pending	Latency optimization using a Smart Samplers Placement
MP10493	MP10493PR	US	62/814,165	3/5/2019			Pending	Flash Controller
MP10494	MP10494	US	16/583,541	9/26/2019			Pending	POLYGONAL BGA SEMICONDUCTOR PACKAGE
MP10494	MP10494C1	US	0				Not Filed	POLYGONAL BGA SEMICONDUCTOR PACKAGE
MP10496	MP10496	US	16/406,898	5/8/2019			Pending	WIFI BACKOFF TIMER
MP10499	MP10499	US	16/566,378	9/10/2019			Pending	METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA
MP10500	MP10500	US	16/583,639	9/26/2019			Pending	METHOD AND APPARATUS FOR TESTING A MULTI-DIE INTEGRATED CIRCUIT
MP10507	MP10507PR	US	62/770,047	11/20/2018			Pending	SELECT BANDWIDTH - REDUCE EMPTY Q NULL SELECT
MP10508	MP10508PR	US	62/771,890	11/27/2018			Pending	Effective Guaranteed Message Buffering
MP1066	MP1066PR	US	60/794,956					
MP11011	MP11011	US	16/525,105	7/29/2019			Pending	CONTROLLING PERFORMANCE OF A SOLID STATE DRIVE
MP11011	MP11011CN	CN	201910700563.	7/31/2019			Pending	CONTROLLING PERFORMANCE OF A SOLID STATE DRIVE
MP11011	MP11011EP	EP	19189312.2	7/31/2019			Pending	CONTROLLING PERFORMANCE OF A SOLID STATE DRIVE
MP11011	MP11011KR	KR	10-2019-00930	7/31/2019			Pending	CONTROLLING PERFORMANCE OF A SOLID STATE DRIVE
MP11016	MP11016PR	US	62/790,701	1/10/2019			Pending	EFFICIENT FREQUENCY DOMAIN IMPLEMENTATION FOR FEED FORWARD E
MP11022	MP11022	US	16/526,932	7/30/2019			Pending	Shared Memory Block Configuration
MP11029	MP11029	US	16/574,942	9/18/2019			Pending	HIGH EFFICIENCY LONG TRAINING FIELD SYMBOL TRANSMISSION FOR MUL
MP11029	MP11029WO	WO	PCT/US19/5171	9/18/2019			Pending	HIGH EFFICIENCY LONG TRAINING FIELD SYMBOL TRANSMISSION FOR MUL
MP11036	MP11036	US	16/526,808	7/30/2019			Pending	Wireless Local Area Network Management
MP11036	MP11036.2	US	16/526,753	7/30/2019			Pending	MEDIUM PROTECTION IN WIRELESS LOCAL AREA NETWORKS

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MP11036	MP11036PR4	US	62/792,306	1/14/2019			Pending	HIGH EFFICIENCY (HE) OPERATION AT 6GHz BAND
MP11036	MP11036WO	WO	PCT/US19/4423				Pending	HIGH EFFICIENCY (HE) OPERATION AT 6GHz BAND
MP11036	MP11036WO2	WO	PCT/US19/4423	7/30/2019			Pending	HIGH EFFICIENCY (HE) OPERATION AT 6GHz BAND
MP11039	MP11039	US	16/560,816	9/4/2019			Pending	CONFIGURABLE HASH-BASED LOOKUP IN NETWORK DEVICES
MP1103W	MP1103W/P3	JP	2009-521752	7/11/2007	5057590	8/10/2012	In Force	CONTROL SYSTEM FOR A ROTATING DATA STORAGE DEVICE AND DATA STO
MP11045	MP11045PR	US	62/790,708	1/10/2019			Pending	ARCHITECTURE FOR SUPPORTING MULTI-SPEED AND MULTI-PORT PACKETS
MP11051	MP11051	US	16/550,095	8/23/2019			Pending	PRIORITY FLOW CONTROL IN A DISTRIBUTED NETWORK SWITCH ARCHITECT
MP11059	MP11059PR	US	62/836,536	4/19/2019			Pending	OPERATIONS AND MANAGEMENT (OAM) IN ETHERNET NETWORKS FOR CO
MP11063	MP11063	US	16/574,229	9/18/2019			Pending	Mobile Storage System for Storing and Transferring Data Generated by Inter
MP11075	MP11075	US	16/585,930	9/27/2019			Pending	WLAN OPERATION USING MULTIPLE COMPONENT CHANNELS
MP11075	MP11075PR2	US	62/821,313	3/20/2019			Pending	FRAME EXCHANGE, BASIC SERVICE SET (BSS) OPERATION AND OPERATING
MP11075	MP11075WO	WO	PCT/US2019/05	9/27/2019			Pending	WLAN OPERATION USING MULTIPLE COMPONENT CHANNELS
MP11081	MP11081	US	16/697,361	11/27/2019			Pending	NETWORK SWITCH WITH ENDPOINT AND DIRECT MEMORY ACCESS CONTR
MP11081	MP11081PR	US	62/772,506	11/28/2018			Pending	PCI SWITCH CONCEPT FOR AUTOMOTIVE APPLICATIONS
MP11081	MP11081WO	WO	PCT/US2019/06	11/27/2019			Pending	NETWORK SWITCH WITH ENDPOINT AND DIRECT MEMORY ACCESS CONTR
MP11089	MP11089WO	WO	PCT/US2019/05	10/31/2019			Pending	PADDING FOR WAKEUP RADIO (WUR) PACKETS
MP11090	MP11090WO	WO	PCT/IB2019/05	10/26/2019			Pending	Artificial Intelligence-Enabled Management of Storage Media Access
MP11096	MP11096KR	KR	0	10/30/2019			Not Filed	Thermal Compensation for Laser in Heat Assisted Magnetic Recording
MP11096	MP11096TW	TW	108139298	10/30/2019			Pending	Thermal Compensation for Laser in Heat Assisted Magnetic Recording
MP11096	MP11096WO	WO	PCT/US2019/05	10/29/2019			Pending	Thermal Compensation for Laser in Heat Assisted Magnetic Recording
MP11099	MP11099	US	16/577,625	9/20/2019			Pending	OUT OF ORDER PACKET BUFFER SELECTION
MP11100	MP11100	US	16/577,545	9/20/2019			Pending	OUT OF ORDER PLACEMENT OF DATA IN NETWORK DEVICES
MP11101	MP11101	US	16/577,630	9/20/2019			Pending	NEGOTIATION OF ALIGNMENT MODE FOR OUT OF ORDER PLACEMENT OF D
MP11102	MP11102PR	US	62/808,053	2/20/2019			Pending	High density fractional bit Solid State Drives using codes set partitions
MP11103	MP11103PR	US	62/808,072	2/20/2019			Pending	High density packing of memory cells and diversity gain extraction in a solid
MP11104	MP11104	US	0				Not Filed	Secure WiFi
MP11105	MP11105PR	US	62/778,602	12/12/2018			Pending	Sector based association of STA with AP
MP11108	MP11108PR	US	62/767,999	11/15/2018			Pending	Feedforward IQ Imbalance Correction
MP11113	MP11113	US	0				Not Filed	Method of Using RAID to Recover Data Upon a Program Status Failure in No
MP11113	MP11113CN	CN	0				Not Filed	Method of Using RAID to Recover Data Upon a Program Status Failure in No
MP11113	MP11113EP	EP	0				Not Filed	Method of Using RAID to Recover Data Upon a Program Status Failure in No
MP11113	MP11113KR	KR	0				Not Filed	Method of Using RAID to Recover Data Upon a Program Status Failure in No
MP11113	MP11113PR	US	62/791,542	1/11/2019			Pending	Method of Using RAID to Recover Data Upon a Program Status Failure in No
MP11113	MP11113TW	TW	0				Not Filed	Method of Using RAID to Recover Data Upon a Program Status Failure in No

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Family Ref No	IP Right ID	Country	Application No	Filing Date	Patent Number	Issue Date	Status	Title
MP11119	MP11119PR	US	62/791,553	1/11/2019			Pending	Wide Input Common-Mode Folded-Cascode Amplifier with Adaptive Biasing
MP11120	MP11120CN	CN	201911136633	11/19/2019			Pending	HYBRID ARQ WITH VARYING MODULATION AND CODING
MP11120	MP11120EP	EP	19209668.3	11/18/2019			Pending	HYBRID ARQ WITH VARYING MODULATION AND CODING
MP11120	MP11120PR	US	62/770,082	11/20/2018			Pending	Hybrid ARQ (HARQ) for link adaptation in WLAN
MP11122	MP11122PR	US	62/771,901	11/27/2018			Pending	Fully Flexible Assignment of Interrupt Requests in a Multifunction PCIe Dev
MP11123	MP11123PR	US	62/775,210	12/4/2018			Pending	EHT Packet Format Designs: DCM Signaling
MP11124	MP11124PR	US	62/775,232	12/4/2018			Pending	EHT Packet Format Designs: Mashing & CRC generation
MP11125	MP11125	US	16/703,694	12/4/2019			Pending	Reducing Offset of a Differential Signal Output by a Capacitive Coupling Sta
MP11125	MP11125PR	US	62/775,781	12/5/2018			Pending	Circuits and methods for reducing dynamic offset of capacitive coupling wit
MP11125	MP11125SG	SG	10201911728V	12/5/2019			Pending	Reducing Offset of a Differential Signal Output by a Capacitive Coupling Sta
MP11127	MP11127	US	16/702,983	12/4/2019			Pending	Balanced Current Mirrors for Biasing a Magnetic Resistor in a Hard Disk Driv
MP11127	MP11127PR	US	62/775,795	12/5/2018			Pending	Speed-balanced current mirror for magnetic resistor bias in HDD preamp
MP11128	MP11128PR	US	62/770,086	11/20/2018			Pending	DIFFERENT ENCODING/SIGNALING SCHEMES FOR A SEQUENCE OF DATA BL
MP11128	MP11128WO	WO	PCT/IB2019/05	11/19/2019			Pending	SIGNALING OF ENCODING SCHEMES IN PACKETS TRANSMITTED OVER A WL
MP11131	MP11131CN	CN	0				Not Filed	METHOD TO TRANSMIT DATA TO PREAMP THROUGH DIFFERENTIAL INTERF
MP11131	MP11131EP	EP	0				Not Filed	METHOD TO TRANSMIT DATA TO PREAMP THROUGH DIFFERENTIAL INTERF
MP11131	MP11131KR	KR	0				Not Filed	METHOD TO TRANSMIT DATA TO PREAMP THROUGH DIFFERENTIAL INTERF
MP11131	MP11131PR	US	62/783,041	12/20/2018			Pending	METHOD TO TRANSMIT DATA TO PREAMP THROUGH DIFFERENTIAL INTERF
MP11132	MP11132PR	US	62/771,920	11/27/2018			Pending	Plastic mm-wave Waveguide with Plastic Foam as insulator
MP11133	MP11133PR	US	62/808,647	2/21/2019			Pending	Distributed Multiple Input Multiple Output (DMIMO) at MAC layer
MP11134	MP11134PR	US	62/808,703	2/21/2019			Pending	Dual use of solar cell as memory cell
MP11138	MP11138PR	US	62/780,088	12/14/2018			Pending	WiFi 11ax UL MULTI-USER MULTIPLE INPUT MULTIPLE OUTPUT (MU-MIMO)
MP11139	MP11139PR	US	62/768,007	11/15/2018			Pending	General Error Reporting and Handling
MP11142	MP11142PR	US	62/799,598	1/31/2019			Pending	REDUCED SECURITY RISK OF AUTHENTICATION BETWEEN THE HOST AND ST
MP11143	MP11143PR	US	62/782,200	12/19/2018			Pending	Breathing Rate Detection System using WiFi Signals
MP11144	MP11144CN	CN	0				Not Filed	ZONE SERVO SELF SERVO WRITE (SSW) WITH ZONED SPIRAL SERVO
MP11144	MP11144EP	EP	0				Not Filed	ZONE SERVO SELF SERVO WRITE (SSW) WITH ZONED SPIRAL SERVO
MP11144	MP11144KR	KR	0				Not Filed	ZONE SERVO SELF SERVO WRITE (SSW) WITH ZONED SPIRAL SERVO
MP11144	MP11144PR	US	62/782,211	12/19/2018			Pending	ZONE SERVO SELF SERVO WRITE (SSW) WITH ZONED SPIRAL SERVO
MP11144	MP11144TW	TW	0				Not Filed	ZONE SERVO SELF SERVO WRITE (SSW) WITH ZONED SPIRAL SERVO
MP11146	MP11146PR	US	62/803,204	2/8/2019			Pending	Exposed-die Heat-Sink design
MP11148	MP11148	US	0				Not Filed	Drive Health Management
MP11148	MP11148CN	CN	0				Not Filed	Drive Health Management
MP11148	MP11148EP	EP	0				Not Filed	Drive Health Management

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MP11148	MP11148KR	KR	0				Not Filed	Drive Health Management
MP11148	MP11148PR	US	62/799,608	1/31/2019			Pending	Drive Health Management
MP11148	MP11148TW	TW	0				Not Filed	Drive Health Management
MP11150	MP11150PR	US	62/808,740	2/21/2019			Pending	BASIC SERVICE SET (BSS) INFORMATION BROADCAST FOR ASSOCIATION
MP11151	MP11151PR	US	62/777,653	12/10/2018			Pending	PROTECTION ON MULTI-MASTER ACCESS TO THE SHARED RESOURCES OVE
MP11152	MP11152PR	US	62/808,743	2/21/2019			Pending	6 GHz BAND CHANNELIZATION
MP11153	MP11153PR	US	62/790,716	1/10/2019			Pending	Automatically track analog front end response by digital method in commu
MP11154	MP11154PR	US	62/783,052	12/20/2018			Pending	SHORT PREAMBLE DETECTION AND DOWNSHIFT
MP11155	MP11155PR	US	62/777,659	12/10/2018			Pending	BLOCKCHAIN BASED DECOMMISSIONING HDD/SSDs IN DATA CENTER
MP11155	MP11155PR2	US	62/829,537	4/4/2019			Pending	SELF-ENCRYPTION DRIVE (SED) ARCHITECTURE FOR DATA CENTER
MP11157	MP11157PR	US	62/808,750	2/21/2019			Pending	Method and apparatus to control and observe analog I/Os for testability
MP11158	MP11158PR	US	62/782,221	12/19/2018			Pending	PLACE & ROUTE BLOCKS FOR TS LOCATION FOR REUSE
MP11159	MP11159	US	0				Not Filed	SOLID-STATE DRIVE WITH INITIATOR MODE
MP11159	MP111592	US	0				Not Filed	NVMe-of Termination at the Drive Level
MP11159	MP111592CN	CN	0				Not Filed	NVMe-of Termination at the Drive Level
MP11159	MP111592EP	EP	0				Not Filed	NVMe-of Termination at the Drive Level
MP11159	MP111592KR	KR	0				Not Filed	NVMe-of Termination at the Drive Level
MP11159	MP111592TW	TW	0				Not Filed	NVMe-of Termination at the Drive Level
MP11159	MP11159CN	CN	0				Not Filed	SOLID-STATE DRIVE (SSD) INITIATOR MODE
MP11159	MP11159EP	EP	0				Not Filed	SOLID-STATE DRIVE (SSD) INITIATOR MODE
MP11159	MP11159KR	KR	0				Not Filed	SOLID-STATE DRIVE (SSD) INITIATOR MODE
MP11159	MP11159PR	US	62/783,060	12/20/2018			Pending	SOLID-STATE DRIVE (SSD) INITIATOR MODE
MP11159	MP11159PR2	US	62/818,557	3/14/2019			Pending	NVMe-of Termination at the Drive Level
MP11159	MP11159TW	TW	0				Not Filed	SOLID-STATE DRIVE (SSD) INITIATOR MODE
MP1115K	MP1115KR	KR	KR102034/2006	10/19/2006			Pending	PRINTER CONTROLLER APPARATUS IMPLEMENTED AS A SYSTEM IN A PACK
MP1115T	MP1115TW	TW	TW095138382	10/18/2006			Pending	PRINTER CONTROLLER APPARATUS IMPLEMENTED AS A SYSTEM IN A PACK
MP11160	MP11160PR	US	62/850,970	5/21/2019			Pending	Open Loop Feed-Forward Adaptive Bias Power Amplifier
MP11162	MP11162PR	US	62/798,231	1/29/2019			Pending	REDUCED AREA LOW LATENCY LATCH FIRST-IN FIRST OUT (FIFO)
MP11162	MP11162PR2	US	62/821,316	3/20/2019			Pending	REDUCED AREA LOW LATENCY LATCH FIRST-IN FIRST OUT (FIFO)
MP11163	MP11163CN	CN	0				Not Filed	forward timestamping
MP11163	MP11163PR	US	62/802,121	2/6/2019			Pending	forward timestamping
MP11165	MP11165PR	US	62/790,724	1/10/2019			Pending	HYBRID TERNARY CONTENT-ADDRESSABLE MEMORY (TCAM) EXACT MATCH
MP11167	MP11167PR	US	62/802,126	2/6/2019			Pending	USXGMII PCH SUPPORT
MP11169	MP11169PR	US	62/798,240	1/29/2019			Pending	PROGRAMMABLE HEADER ALTERATION

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Family Ref No	IP Right ID	Country	Application No	Filing Date	Patent Number	Issue Date	Status	Title
MP11170	MP11170PR	US	62/791,559	1/11/2019			Pending	LINK LIST CONTROLLER UNIT
MP11173	MP11173PR	US	62/821,813	3/21/2019			Pending	Fast Dynamic Frequency Scalling Phase Locked Loop (PLL) with Wide Range
MP11174	MP11174PR	US	62/823,497	3/25/2019			Pending	NEW PILOT DESIGN FOR NEXT GENERATION V2X (NGV)
MP11175	MP11175PR	US	62/831,596	4/9/2019			Pending	BASIC SERVICE SET (BSS) WITH MULTIPLE CHANNEL SEGMENTS OR >160MH
MP11175	MP11175PR2	US	62/945,749	5/9/2019			Pending	NEXT GENERATION WIRELESS 20MHz OPERATION
MP11178	MP11178PR	US	62/821,135	3/20/2019			Pending	Continuous time amplitude stabilization loop for crystal oscillators
MP11180	MP11180PR	US	62/803,220	2/8/2019			Pending	PACKET ACCLIMULATOR
MP11182	MP11182PR	US	62/803,234	2/8/2019			Pending	Operational User Privilege Decrease For Non-Compliant Users
MP11183	MP11183PR2	US	62/938,836	11/21/2019			Pending	FLOW ANALYSIS AND MANAGEMENT
MP11185	MP11185PR	US	62/810,215	2/25/2019			Pending	METHOD AND APPARATUS USING A LOGIC ANALYZER TO DEBUG AN INTEG
MP11186	MP11186PR	US	62/810,227	2/25/2019			Pending	METHOD AND APPARATUS FOR ACCELERATING MEMORY ACCESS
MP11187	MP11187PR	US	62/810,871	2/26/2019			Pending	Mapping cache method combining software and hardware
MP11194	MP11194HK	HK	HK07110528.3	9/19/2006			Pending	ENABLING INCREASED PRINT SPEED BY ELIMINATING NOZZLE FIRING SEQUE
MP11204	MP11204HK	HK	HK07111478.1	10/17/2006			Pending	SYSTEM AND METHOD FOR INDICATING POSITION OF A MOVEABLE MECHA
MP1120K	MP1120KR	KR	KR101564/2006	10/18/2006			Pending	SYSTEM AND METHOD FOR INDICATING POSITION OF A MOVEABLE MECHA
MP1133	MP1133	US	11/102,411	8/4/2005	7214955	8/5/2007	In Force	MEDIA RECOGNITION USING A SINGLE LIGHT DETECTOR
MP12022	MP12022WQUSC1	US	12/235,210	9/22/2008	7822055	10/26/2010	In Force	FIBRE CHANNEL CREDIT EXTENDER AND REPEATER
MP12024	MP12024	US	10/310,653	12/5/2002	7248580	7/24/2007	In Force	HARDWARE-ENFORCED LOOP-LEVEL HARD ZONING FOR FIBRE CHANNEL S
MP12123	MP12123PR	US	60/609,910					
MP12157	MP12157PR	US	60/669,655					
MP12186	MP12186	US	11/376,886					
MP12211	MP12211WOKR	KR	10-2008-70059	12/20/2006			Pending	METHOD AND DECODER FOR TAIL-BITING DECODING
MP12220	MP12220WOKR	KR	10-2008-70055	3/5/2007	1,00924061e+	10/21/2009	In Force	CHANNEL PROFILER AND METHOD OF PROFILING AN INCOMING SIGNAL
MP12225	MP12225WOKR	KR	10-2008-70055	5/2/2007	100973935	8/3/2010	In Force	METHOD AND APPARATUS FOR CORRECTING LINEAR ERROR PHASE OF AN
MP12237	MP12237	US	11/874,752					
MP12244	MP12244	US	11/949,755	12/3/2007	8555260	10/8/2013	In Force	DIRECT HARDWARE PROCESSING OF INTERNAL DATA STRUCTURE FIELDS
MP12249	MP12249WOIL	IL	207299	1/29/2008			Pending	SIGNAL PROCESSING UNIT AND METHOD AND CORRESPONDING TRANSCEI
MP12256	MP12256WOC	WO	PCT/CA2008/00	4/17/2008	DNA		Pending	SYSTEM AND METHOD FOR OPTIMIZING USE OF CHANNEL STATE INFORMA
MP12263	MP12263	US	12/176,750	7/21/2008	8619558	12/31/2013	In Force	MEMORY MANAGEMENT IN A NETWORK ADAPTER
MP12300	MP12300PR	US	61/224,570					
MP12317	MP12317PR	US	61/330,803					
MP12322	MP12322C1	US	12/822,583					
MP12329	MP12329C1	US	12/881,402					
MP12340	MP12340C1	US	12/974,431					

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MP12342	MP12342C1	US	13/012,711					
MP12400	MP12400PR	US	61/589,181					
MP12404	MP12404PR4	US	61/714,597					
MP12404	MP12404PRS	US	61/714,600					
MP1247W	MP1247WO	WO	PCT/US2007/01	8/14/2007	DNA		Pending	INTERRUPT HANDLING
MP12785	MP12785	US	15/455,070	3/9/2017	10402201	9/3/2019	In Force	Method and Apparatus for Detecting Memory Conflicts Using Distinguished
MP12794	MP12794	US	15/722,986	10/2/2017			Pending	METHOD AND APPARATUS FOR CACHE PRE-FETCH WITH OFFSET DIRECTIVE
MP12803	MP12803	US	16/186,313	11/9/2018			Pending	SYSTEMS AND METHODS FOR PROGRAMMABLE HARDWARE ARCHITECTURE
MP12803	MP12803-2	US	16/226,508	12/19/2018			Pending	SINGLE INSTRUCTION SET ARCHITECTURE (ISA) FORMAT FOR MULTIPLE ISA
MP12803	MP12803-3	US	16/226,534	12/19/2018			Pending	STREAMING ENGINE FOR MACHINE LEARNING ARCHITECTURE
MP12803	MP12803-4	US	16/226,539	12/19/2018			Pending	ARRAY-BASED INFERENCE ENGINE FOR MACHINE LEARNING
MP12803	MP12803-5	US	16/226,550	12/19/2018			Pending	ARCHITECTURE FOR DENSE OPERATIONS IN MACHINE LEARNING INFERENCE
MP12803	MP12803-6	US	16/226,559	12/19/2018			Pending	ARCHITECTURE FOR IRREGULAR OPERATIONS IN MACHINE LEARNING INFERENCE
MP12803	MP12803-7	US	16/226,564	12/19/2018			Pending	ARCHITECTURE OF CROSSBAR OF INFERENCE ENGINE
MP12827	MP12827	US	16/224,601	12/18/2018			Pending	SYSTEM AND METHOD FOR DROOP DETECTION
MP12828	MP12828	US	16/224,638	12/18/2018			Pending	SYSTEM AND METHOD FOR COMPENSATING FOR A DROOP EVENT
MP12828	MP12828-2	US	16/224,638	12/18/2018			Pending	SYSTEM AND METHOD FOR COMPENSATING FOR A DROOP EVENT
MP12829	MP12829	US	16/173,877	10/29/2018			Pending	METHODS AND APPARATUS FOR JOB SCHEDULING IN A PROGRAMMABLE
MP12831	MP12831	US	16/148,796	10/1/2018			Pending	METHODS AND APPARATUS FOR DYNAMIC ACKNOWLEDGEMENT LIST SELECTION
MP12832	MP12832	US	16/384,878	4/15/2019			Pending	METHODS AND APPARATUS FOR TWO-STAGE ACK/DTX DETECTION
MP12834	MP12834	US	16/400,992	5/1/2019			Pending	Methods and Apparatus for Symbol-to-Symbol Multiplexing of Control Data
MP12835	MP12835	US	16/236,455	12/29/2018			Pending	METHODS AND APPARATUS FOR SUB-BLOCK BASED ARCHITECTURE OF CHO
MP12836	MP12836	US	16/404,029	5/6/2019			Pending	Methods and Apparatus for Providing A Demapping System to Demap Uplink
MP12837	MP12837	US	16/421,917	5/24/2019			Pending	METHODS AND APPARATUS FOR PROVIDING A RESOURCE ELEMENT (RE) ID
MP12838	MP12838	US	16/427,069	5/30/2019			Pending	METHODS AND APPARATUS FOR DESCRAMBLING RECEIVED UPLINK TRANSMISSIONS
MP12839	MP12839	US	16/434,057	6/6/2019			Pending	METHODS AND APPARATUS FOR COMBINING RECEIVED UPLINK TRANSMISSIONS
MP12841	MP12841	US	16/457,863	6/28/2019			Pending	METHODS AND APPARATUS FOR DECODING RECEIVED UPLINK TRANSMISSIONS
MP12899	MP12899	US	15/595,308	5/15/2017	10402228	9/3/2019	In Force	Method and Apparatus for Assigning Processing Work in Parallel Computing
MP12899	MP12899D1	US	16/518,892	7/22/2019			Pending	Apparatus for Assigning Processing Work in Parallel Computing
MP12924	MP12924	US	16/110,957	8/23/2018			Pending	Methods And Apparatus For Simultaneous Multiprotocol Processing of Different
MP12925	MP12925	US	16/299,486	3/12/2019			Pending	Buffer Allocation with Memory-based Configuration
MP12930	MP12930	US	16/566,015	9/10/2019			Pending	METHOD OF CACHE PREFETCHING THAT INCREASES THE HIT RATE OF A NEXT
MP12933	MP12933PR	US	62/841,202	4/30/2019			Pending	Method and Apparatus for Compressing Incoming Data Stream Using A Co
MP12934	MP12934PR	US	62/841,207	4/30/2019			Pending	Method and Apparatus for Compressing Incoming Data Stream via Probabilistic

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MP12935	MP12935CN	CN	0				Not Filed	Discovery of Codeword Decoding Order in SIC Receiver Using Reinforcement
MP12935	MP12935PR	US	62/843,419	5/4/2019			Pending	Methods and Apparatus for Discovering Codeword Decoding Order in SIC R
MP12936	MP12936CN	CN	0				Not Filed	Methods and Apparatus for Providing an Adaptive Beam-Forming Antenna f
MP12936	MP12936DE	DE	0				Not Filed	Methods and Apparatus for Providing an Adaptive Beam-Forming Antenna f
MP12936	MP12936JP	JP	0				Not Filed	Methods and Apparatus for Providing an Adaptive Beam-Forming Antenna f
MP12936	MP12936KR	KR	0				Not Filed	Methods and Apparatus for Providing an Adaptive Beam-Forming Antenna f
MP12936	MP12936PR	US	62/946,661	5/11/2019			Pending	Methods and Apparatus for Providing an Adaptive Beam-Forming Antenna
MP12937	MP1293711	US	16/419,994	5/22/2019			Pending	ARCHITECTURE TO SUPPORT TANH AND SIGMOID OPERATIONS FOR INFERR
MP12938	MP1293811	US	16/420,028	5/22/2019			Pending	ARCHITECTURE FOR TABLE-BASED MATHEMATICAL OPERATIONS FOR INFERR
MP12939	MP1293911	US	16/420,103	5/22/2019			Pending	DATA TRANSMISSION BETWEEN MEMORY AND ON CHIP MEMORY OF INFE
MP12940	MP1294011	US	16/420,055	5/22/2019			Pending	ARCHITECTURE TO SUPPORT COLOR SCHEME-BASED SYNCHRONIZATION FO
MP12941	MP1294111	US	16/420,078	5/22/2019			Pending	ADDRESS INTERLEAVING FOR MACHINE LEARNING
MP12942	MP1294211	US	16/420,092	5/22/2019			Pending	ARCHITECTURE TO SUPPORT SYNCHRONIZATION BETWEEN CORE AND INFE
MP13000	MP13000PR	US	62/850,988	5/21/2019			Pending	SLEEP TIME COMPUTATION IN IEEE PS MODE
MP13001	MP13001PR	US	62/868,341	6/28/2019			Pending	Methods and Apparatus for Signal Bandwidth detection in WiFi Systems
MP13002	MP13002PR	US	62/837,093	4/22/2019			Pending	BLIND EQUALIZER SIGN ERROR LAST MEAN SQUARES (LMS) ALGORITHM
MP13003	MP13003PR	US	62/850,998	5/21/2019			Pending	Accelerate Key Access
MP13004	MP13004PR	US	62/816,747	3/11/2019			Pending	METHODS OF PORTING SYSTEM LEVEL TEST TO AUTOMATED TEST EQUIPM
MP13006	MP13006PR	US	62/852,580	5/24/2019			Pending	P-N TRANSISTORS SPEED CHARACTERIZATION METHOD AND IMPLEMENTA
MP13007	MP13007PR	US	62/837,100	4/22/2019			Pending	Send 5-2D Detector For Autophy 1000BASE-T1
MP13008	MP13008PR	US	62/816,759	3/11/2019			Pending	SOLID-STATE DRIVE (SSD) ERROR RECOVERY FLOW BASED ON REINFORCEM
MP13010	MP13010PR	US	62/816,760	3/11/2019			Pending	SELF SERVO WRITE PATTERN FREQUENCY MODIFICATION
MP13011	MP13011PR	US	62/832,741	4/11/2019			Pending	RESOURCE UNIT (RU) INDICATION FOR UPLINK (UL) MULTI-USER (MU) OPER
MP13012	MP13012PR	US	62/823,504	3/25/2019			Pending	BASIC SERVICE SET (BSS) WITH MULTIPLE CHANNEL SEGMENTS OR >160MH
MP13012	MP13012PR2	US	62/831,593	4/9/2019			Pending	Next Generation Wireless 20MHz Operation
MP13012	MP13012PR3	US	62/870,437	7/3/2019			Pending	BASIC SERVICE SET (BSS) WITH MULTIPLE CHANNEL SEGMENTS OR >160MH
MP13013	MP13013PR	US	62/821,933	3/21/2019			Pending	RESOURCE UNIT (RU) INDICATION FOR DOWNLINK (DL) MULTI-USER (MU)
MP13014	MP13014CN	CN	201910169178	3/6/2019			Pending	A METHOD AND AN APPARATUS FOR WAKING A PHYSICAL LAYER OF A FRS
MP13016	MP13016PR	US	62/852,597	5/24/2019			Pending	DUMMY DIE REPLACEMENT FOR PERIPHERAL DIE ON MCM PACKAGE
MP13017	MP13017PR	US	62/821,936	3/21/2019			Pending	ACCESS POINT (AP) COORDINATED ORTHOGONAL FREQUENCY MULTIPLE A
MP13017	MP13017PR2	US	62/837,106	4/22/2019			Pending	ACCESS POINT (AP) COORDINATED ORTHOGONAL FREQUENCY MULTIPLE A
MP13017	MP13017PR3	US	62/934,452	11/12/2019			Pending	ACCESS POINT (AP) COORDINATED ORTHOGONAL FREQUENCY MULTIPLE A
MP13019	MP13019PR	US	62/810,785	2/26/2019			Pending	Random Access Interleaved Sectors for Magnetic Recording
MP13020	MP13020PR	US	62/832,757	4/11/2019			Pending	EXTRA HIGH THROUGHPUT (EHT) AGGREGATED PLCP PROTOCOL DATA UNI

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MP13021	MP13021PR	US	62/849,043	5/16/2019			Pending	BASIC SERVICE SET (BSS) OPERATION WITH BAND AGGREGATION - CAPABILI
MP13021	MP13021PR2	US	62/877,207	7/22/2019			Pending	BASIC SERVICE SET (BSS) OPERATION WITH BAND AGGREGATION - CAPABILI
MP13025	MP13025PR	US	62/832,764	4/11/2019			Pending	EXTRA HIGH THROUGHPUT (EHT) AGGREGATED PLCP PROTOCOL DATA UNI
MP13026	MP13026PR	US	62/814,195	3/5/2019			Pending	ADAPTIVE SINGLE SOLID-STATE (SSD) CONTROLLER BANDWIDTH TO REDUC
MP13027	MP13027	US	16/578,478	9/23/2019			Pending	Multi-Protocol Frame Format
MP13028	MP13028PR	US	62/866,466	6/25/2019			Pending	SYNCHRONOUS WRITE PATTERN FREQUENCY MODIFICATION
MP13029	MP13029PR	US	62/832,768	4/11/2019			Pending	In-band sleep/wake-up command and status
MP13030	MP13030PR	US	62/816,765	3/11/2019			Pending	Full Duplex data transmission via TDM data transmission over channel runn
MP13032	MP13032PR	US	62/816,767	3/11/2019			Pending	ASYMMETRICAL AUTOMOTIVE ETHERNET PHY
MP13050	MP13050PR	US	62/934,436	11/12/2019			Pending	DATACENTER ON WHEELS
MP13051	MP13051	US	0				Not Filed	Peer-to-peer NVMe-of SSD Communication over Fabric Without Host/Com
MP13051	MP13051PR	US	62/818,566	3/14/2019			Pending	Peer-to-peer NVMe-of SSD Communication over Fabric Without Host/Com
MP13052	MP13052PR	US	62/818,621	3/14/2019			Pending	NVMe-of SOLID-STATE DRIVE (SSD)
MP13055	MP13055PR	US	62/866,285	6/25/2019			Pending	Machine Learning Switch for Real Time Automotive System Monitoring
MP13058	MP13058PR	US	62/854,812	5/30/2019			Pending	Qualification Tests of Channel state information (CSI) Reports in WLAN
MP13060	MP13060PR	US	62/860,192	6/11/2019			Pending	Ring Oscillator Based Temperature Sensor with Low Supply Sensitivity
MP13061	MP13061PR	US	62/854,820	5/30/2019			Pending	BASIC SERVICE SET (BSS) WITH MULTIPLE CHANNEL SEGMENTS: BACKOFF
MP13064	MP13064PR	US	62/934,442	11/12/2019			Pending	RAPID DETECTION OF 100 OR 1000 BASE-T1 ON SLAVE SIDE
MP13065	MP13065PR	US	62/869,906	7/2/2019			Pending	Multi frequency spiral track
MP13067	MP13067PR	US	62/852,612	5/24/2019			Pending	BASIC SERVICE SET (BSS) WITH BAND AGGREGATION - POWER SAVE AND T
MP13067	MP13067PR2	US	62/879,801	7/29/2019			Pending	BASIC SERVICE SET (BSS) WITH BAND AGGREGATION - POWER SAVE AND T
MP13067	MP13067PR3	US	62/886,812	8/14/2019			Pending	BASIC SERVICE SET (BSS) WITH BAND AGGREGATION - POWER SAVE AND T
MP13067	MP13067PR4	US	62/888,950	8/19/2019			Pending	BASIC SERVICE SET (BSS) WITH BAND AGGREGATION - POWER SAVE AND T
MP13067	MP13067PR5	US	62/897,155	9/6/2019			Pending	BASIC SERVICE SET (BSS) WITH BAND AGGREGATION - POWER SAVE AND T
MP13068	MP13068PR	US	62/854,834	5/30/2019			Pending	TRAFFIC SPECIFICATION ANNOUNCEMENT FOR TARGET WAKE TIME (TWT)
MP13069	MP13069PR	US	62/857,646	6/5/2019			Pending	VHTSIGB EQUALIZATION
MP13073	MP13073PR	US	62/857,656	6/5/2019			Pending	Split-Cascade Transistors Technique for 50% Duty Cycle Passive Mixer
MP13076	MP13076PR	US	62/852,620	5/24/2019			Pending	HIGH SPEED SUCCESSIVE APPROXIMATION REGISTER (SAR) ANALOG-TO-DIG
MP13077	MP13077PR	US	62/863,699	6/19/2019			Pending	MULTI-BAND OPERATION: SYNCHRONIZED AND UNSYNCHRONIZED
MP13079	MP13079PR	US	62/863,713	6/19/2019			Pending	MULTI-BAND OPERATION: SINGLE BAND STATION (STA) AND LEGACY STATI
MP13081	MP13081PR	US	62/853,635	5/28/2019			Pending	ACCESS POINT (AP) POWER SAVE: BASIC SERVICE SET (BSS) QUIET PERIOD
MP13082	MP13082PR	US	62/854,841	5/30/2019			Pending	METHOD AND APPARATUS FOR EFFICIENTLY INCREASING THE AVAILABILITY
MP13083	MP13083PR	US	62/869,916	7/2/2019			Pending	METHOD OF REAL-TIME INTEGRATED CIRCUIT (IC) CHIP ROBUSTNESS DETEC
MP13085	MP13085PR	US	62/857,661	6/5/2019			Pending	Gain-Boosted Transformer Coupled Common Gate Low-Noise Amplifier

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Family Ref No	IP Right ID	Country	Application No	Filing Date	Patent Number	Issue Date	Status	Title
MP13086	MP13086PR	US	62/857,666	6/5/2019			Pending	HYBRID ARQ (HARQ) TRANSMISSION ENABLER-ENCODER AND PREAMBLE D
MP13087	MP13087PR	US	62/859,553	6/10/2019			Pending	NULL DATA PACKET (NDP) POWER CONTROL FOR ERROR VECTOR MAGNITU
MP13089	MP13089PR	US	62/859,560	6/10/2019			Pending	BALL GRID ARRAY (BGA) PACKAGE UPPER CONNECTION TO MEMORY MOD
MP13091	MP13091PR	US	62/866,475	6/25/2019			Pending	ENCODING PROCESS AND PRE-FORWARD ERROR CORRECTION (FEC) PACHI
MP13092	MP13092PR	US	62/869,926	7/2/2019			Pending	Low Leakage Highly Reliable Continuous Diffusion Structures and Method o
MP13093	MP13093	US	16/537,109	8/9/2019			Pending	Methods and Apparatus for Distributing Baseband Signal Processing of Fifth
MP13093	MP13093PR	US	62/853,638	5/28/2019			Pending	Method and apparatus for baseband signal compression of 5G NR uplink
MP13094	MP13094PR	US	62/875,419	7/17/2019			Pending	DIGITALLY ASSISTED PROCESS AND TEMPERATURE AWARE ULTRA-FAST BA
MP13095	MP13095	US	16/536,828	8/9/2019			Pending	Methods and Apparatus for Distributed Baseband Signal Processing for Fit
MP13095	MP13095PR	US	62/849,029	5/16/2019			Pending	METHOD AND APPARATUS FOR A BASEBAND SIGNAL COMPRESSION OF 5G
MP13096	MP13096PR	US	62/875,432	7/17/2019			Pending	DOUBLE PARITY RAID IMPLEMENTATION IN SSD CONTROLLERS
MP13099	MP13099PR	US	62/879,781	7/29/2019			Pending	VERIFICATION SHADOW PIPE
MP13101	MP13101PR	US	62/879,789	7/29/2019			Pending	On-Chip High Density Capacitor, Structures and Method of Making the Sam
MP13102	MP13102PR	US	62/886,822	8/14/2019			Pending	HARDWARE BASED SUCCESSIVE INTERFERENCE CANCELLATION (SIC) USING
MP13105	MP13105PR	US	62/876,452	7/19/2019			Pending	IMPLEMENTING TRANSLATION LOOK-ASIDE BUFFER INVALIDATE (TLBI) AND
MP13106	MP13106PR	US	62/876,460	7/19/2019			Pending	Multi-level Correlated Prefetcher for an Out-of-Order Processor Core
MP13107	MP13107PR	US	62/876,468	7/19/2019			Pending	Using Backpressure Latency to Adjust Prefetch Lookup Distance in Out-of-O
MP13108	MP13108PR	US	62/876,492	7/19/2019			Pending	Vector Prefetcher in an Out-of-Order Core
MP13109	MP13109PR	US	62/876,499	7/19/2019			Pending	Long-range Schedules for an Out-of-Order Core
MP13110	MP13110PR	US	62/876,505	7/19/2019			Pending	SOFTWARE-DIRECTED OUT-OF-ORDER EXECUTED WITHOUT EXCEPTIONS
MP13111	MP13111PR	US	62/875,439	7/17/2019			Pending	FREQUENCY SCALING TO REDUCE BOOT TIME
MP13113	MP13113PR	US	62/879,795	7/29/2019			Pending	OBJECT-ORIENTED MEMORY
MP13115	MP13115PR	US	62/871,541	7/8/2019			Pending	METHOD FOR SYNCHRONIZING TIME REFERENCES ON MULTIPLE DEVICES
MP13117	MP13117PR	US	62/927,584	10/29/2019			Pending	BUND EQUALIZER FOR HIGH ORDER PULSE AMPLITUDE MODULATION (PA
MP13118	MP13118PR	US	62/893,070	8/28/2019			Pending	Pre-Code for A Two-Way Asymmetrical PHY over a single pair of cable
MP13124	MP13124PR	US	62/936,238	11/15/2019			Pending	SYSTEM AND TECHNIQUE FOR IMPROVING RADIATED IMMUNITY AND EMIS
MP13125	MP13125PR	US	62/936,244	11/15/2019			Pending	"LOOP INDUCTANCE NOISE CANCELLATION" TO SOLVE TRANSIENT ISSUES
MP13127	MP13127PR	US	62/943,682	12/4/2019			Pending	Apparatus of implementing 2-port read and 1-port write using 8T SRAM cell
MP13129	MP13129PR	US	62/943,690	12/4/2019			Pending	Using Machine Learning to Manipulate Memory Accesses to Alter Aspects s
MP13130	MP13130PR2	US	62/909,080	10/1/2019			Pending	MULTI-BAND OPERATION: AGGREGATE MAC PROTOCOL DATA UNIT (A-MP
MP13130	MP13130PR3	US	62/934,418	11/12/2019			Pending	MULTI-BAND OPERATION: AGGREGATE MAC PROTOCOL DATA UNIT (A-MP
MP13143	MP13143PR	US	62/927,588	10/29/2019			Pending	CROSS ENTROPY BASED ADAPTATION OF LINEAR FINITE IMPULSE RESPONS
MP13144	MP13144PR	US	62/927,591	10/29/2019			Pending	CROSS ENTROPY BASED ADAPTATION OF TARGET AND LINEAR FINITE IMPU
MP13145	MP13145PR	US	62/943,692	12/4/2019			Pending	CROSS ENTROPY BASED ADAPTATION OF NON-LINEAR FINITE IMPULSE RES

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MP13146	MP13146PR	US	62/943,699	12/4/2019			Pending	MODIFIED BRANCH-METRIC FOR NON-LINEAR FINAL IMPULSE RESPONSE (FI
MP13155	MP13155PR	US	62/916,105	10/16/2019			Pending	Ultra-Core CPU for High Performance Chips
MP13157	MP13157	US	16/708,085	12/9/2019			Pending	Self-Encryption Drive (SED)
MP13157	MP13157C1	US	16/708,203	12/9/2019			Pending	Self-Encryption Drive (SED)
MP13157	MP13157CN	CN	201911262133	12/10/2019			Pending	METHOD AND APPARATUS FOR DECOMMISSIONING A HARD DISK DRIVE
MP13157	MP13157EP	EP	19218955.7	12/10/2019			Pending	METHOD AND APPARATUS FOR DECOMMISSIONING A HARD DISK DRIVE
MP13157	MP13157KR	KR	0	12/10/2019			Not Filed	METHOD AND APPARATUS FOR DECOMMISSIONING A HARD DISK DRIVE
MP13157	MP13157PR	US	62/934,701	11/13/2019			Pending	METHOD AND APPARATUS FOR DECOMMISSIONING A HARD DISK DRIVE
MP13159	MP13159PR	US	62/916,113	10/16/2019			Pending	Shingled Magnetic Recording using data compression
MP13160	MP13160PR	US	62/912,495	10/8/2019			Pending	Methodology for Effective Interleaving in Random Access Interleaved Soluti
MP13166	MP13166	US	0				Not Filed	Method and apparatus for HW efficient implementation of int9 quantizatio
MP13168	MP13168	US	0				Not Filed	DCM port throttling for bandwidth
MP13169	MP13169	US	0				Not Filed	Mesh architecture for high bandwidth broadcast, multicast network
MP13171	MP13171	US	0				Not Filed	Mechanism to manage power to a power profile by throttling performance
MP13172	MP13172	US	0				Not Filed	Throttling mechanism for managing thermal and power -- pulse squashing
MP13183	MP13183	US	0				Not Filed	INSTRUCTION SCHEDULING ALGORITHM TO PREVENT SIDE-CHANNEL ATTA
MP13183	MP13183PR	US	62/944,243	12/5/2019			Pending	INSTRUCTION SCHEDULING ALGORITHM TO PREVENT SIDE-CHANNEL ATTA
MP13184	MP13184	US	0				Not Filed	DYNAMICALLY MARKING INSTRUCTIONS AS SENSITIVE TO ALLOW HARDWA
MP13184	MP13184PR	US	62/944,245	12/5/2019			Pending	DYNAMICALLY MARKING INSTRUCTIONS AS SENSITIVE TO ALLOW HARDWA
MP13185	MP13185	US	0				Not Filed	MICROARCHITECTURAL TAG FLOW FOR SENSITIVE INSTRUCTIONS TO PROT
MP13185	MP13185PR	US	62/944,251	12/5/2019			Pending	MICROARCHITECTURAL TAG FLOW FOR SENSITIVE INSTRUCTIONS TO PROT
MP13186	MP13186	US	0				Not Filed	USE OF RIGHT TO REPLACE TO PROTECT AGAINST PRIME-AND-PROBE SIDE-
MP13186	MP13186PR	US	62/944,263	12/5/2019			Pending	USE OF RIGHT TO REPLACE TO PROTECT AGAINST PRIME-AND-PROBE SIDE-
MP13187	MP13187	US	0				Not Filed	PREVENTING SIDE-CHANNEL ATTACKS BY INTRODUCING NOISY INSTRUCTIO
MP13187	MP13187PR	US	62/944,265	12/5/2019			Pending	PREVENTING SIDE-CHANNEL ATTACKS BY INTRODUCING NOISY INSTRUCTIO
MP13189	MP13189	US	0				Not Filed	MICROPARTITIONS TO PREVENT SPECULATION-BASED SECURITY ATTACKS
MP13189	MP13189PR	US	62/944,269	12/5/2019			Pending	MICROPARTITIONS TO PREVENT SPECULATION-BASED SECURITY ATTACKS
MP13194	MP13194PR	US	62/936,232	11/15/2019			Pending	CAR AS AN "OPEN PLATFORM"
MP13206	MP13206PR	US	62/944,274	12/5/2019			Pending	Using Differential Cache Block Size in the Processor Core and DDR channels
MP13208	MP13208PR	US	62/944,278	12/5/2019			Pending	Implementing last-level Cache Way Allocation with a Processor Core That
MP1433K	MP1433KR	KR	2004/010461	12/31/2002	10-647963	11/13/2006	In Force	ROBUST LOW-COMPLEXITY MULTI-ANTENNA ADAPTIVE MINIMUM MEAN S
MP1450	MP1450C1	US	11/470,662	9/7/2006	7715807	5/11/2010	In Force	WIRELESS COMMUNICATION DEVICE AND METHOD FOR COMMUNICATING
MP1477	MP1477	US	10/329,124					
MP1578W	MP1578WQIP	JP	2009-538416	11/27/2007	5153782	12/14/2012	In Force	SIMPLIFIED AUTO-CONFIGURATION AND SERVICE DISCOVERY IN AD-HOC N

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MP1753E	MP1753EP	EP	08731267.4	3/3/2009	EP2114686	5/16/2012	In Force	INK SUPPLY FOR A HAND-HELD INK JET PRINTER
MP1753E	MP1753EPOE	DE	8731267.4	3/3/2008	2114686	5/16/2012	In Force	INK SUPPLY FOR HAND-HELD INKJET PRINTER
MP1753E	MP1753EPFR	FR	8731267.4	3/3/2008	2114686	5/16/2012	In Force	INK SUPPLY FOR HAND-HELD INKJET PRINTER
MP1753E	MP1753EPGB	GB	8731267.4	3/3/2008	2114686	5/16/2012	In Force	INK SUPPLY FOR HAND-HELD INKJET PRINTER
MP1835J	MP1835JP	JP	2010-503188	4/9/2008			Pending	APPARATUSES, SYSTEMS, SOFTWARE AND METHODS FOR WIRELESS INTERA
MP1979W	MP1979WO	WO	PCT/US2008/06	6/16/2008			Pending	EXECUTING OPERATIONS IN CONNECTION WITH ONE MORE IMAGES WITH
MP2012W	MP2012WO	WO	PCT/US2008/06	7/3/2008			Pending	Location Aware Ad-Hoc Gaming
MP2204J	MP2204JP	JP	2002-214658	7/24/2002			Pending	BUILT-IN-SELF-TEST USING EMBEDDED MEMORY AND PROCESSOR IN AN AI
MP2204S	MP2204SG	SG	200201116-1	2/28/2002	94870	5/31/2004	In Force	BUILT-IN-SELF-TEST USING EMBEDDED MEMORY AND PROCESSOR IN AN AI
MP2205	MP2205GB	GB	325607	11/3/2003			Pending	A METHOD OF FIELD UPGRADEABLE BOOT CODE
MP2205J	MP2205JP	JP	2003-422115	12/19/2003			Pending	A METHOD OF FIELD UPGRADEABLE BOOT CODE
MP2325	MP2325H1C1	US	13/924,333	6/21/2013	9284130	3/22/2016	In Force	QUASI-CYCLOCLOW-DENSITY PARITY-CHECK (QC-LDPC) ENCODER
MP2522	MP2522PR	US	61/039,772					
MP2729W	MP2729WOBR	BR	P06909917-4	7/9/2009			Pending	UPLINK POWER CONTROL IN AGGREGATED SPECTRUM SYSTEMS
MP2744	MP2744C1	US	13/928,771	6/27/2013	9055296	6/9/2015	In Force	Processing Rasterized Data
MP3004	MP3004H1C1	US	14/089,501	11/25/2013	9122590	9/1/2015	In Force	Flash Memory Read Performance
MP3008	MP3008H1C1	US	14/462,821	8/19/2014	9695832	7/4/2017	In Force	Apparatuses For Controlling Operation Of A Motor Of A Fan Assembly Base
MP3189I	MP3189IWO	WO	PCT/IB2011/05	1/4/2011			Pending	SIGNALING OF DEDICATED REFERENCE SIGNAL (DRS) PRECODING GRANULA
MP3262W	MP3262WO	WO	PCT/US2010/03	5/19/2010			Pending	TRANSMIT ARCHITECTURE FOR WIRELESS MULTI-MODE APPLICATIONS
MP3751	MP3751	US	11/155,316	6/17/2005	8385434	2/26/2013	In Force	Process of Synchronization In The Time And Frequency Domain Of Multiple
MP3751	MP3751C1	US	13/775,299	2/25/2013	8837617	9/16/2014	In Force	Process Of Synchronization In The Time And Frequency Domain Of Multiple
MP3775	MP3775EWS	US	13/001,345	11/29/2011	9084833	7/28/2015	In Force	METHOD FOR SELECTIVELY SHARING A COMMUNICATION CHANNEL BETWE
MP3775	MP3775POC1	US	14/810,374	7/27/2015	9660892	5/23/2017	In Force	System For Selectively Unifying Overlapping Networks To Coordinate Comm
MP3778	MP3778ESUS	US	13/120,527	11/29/2011	8885689	11/11/2014	In Force	Communication Node Including Transceivers Transmitting On Different Typ
MP4132W	MP4132WO	WO	PCT/US2003/02	6/27/2003			Pending	Method And Apparatus For Channel Equalization
MP4132W	MP4132WOCN	CN	3819312.4	6/27/2003	Z1200303819	10/10/2007	In Force	METHOD AND APPARATUS FOR CHANNEL EQUALIZATION
MP4132W	MP4132WDEP	EP	03762110.9	6/27/2003	EP1540820	6/15/2005	In Force	Method and Apparatus for Channel Equalization
MP4132W	MP4132WDEPFR	FR	03762110.9	6/27/2003	1540820	1/20/2010	In Force	Method And Apparatus For Channel Equalization
MP4137A	MP4137A1WJ	JP	548675/2010	2/18/2009	JP5308460	7/5/2013	In Force	FREQUENCY DOMAIN ECHO & NEXT CANCELLATION
MP4139W	MP4139WO	WO	PCT/US2004/01	5/10/2004			Pending	Method And Apparatus For Equalization And Crosstalk Mitigation
MP4141E	MP4141EPOE	DE	08252001.6	6/10/2008	60200802657	8/7/2013	In Force	Sub-Channel Distortion Mitigation In Parallel Digital Systems
MP4141E	MP4141EPFR	FR	08252001.6	6/10/2008	2015454	8/7/2013	In Force	Sub-Channel Distortion Mitigation In Parallel Digital Systems
MP4141E	MP4141EPGB	GB	08252001.6	6/10/2008	2015454	8/7/2013	In Force	Sub-Channel Distortion Mitigation In Parallel Digital Systems
MP4146W	MP4146WO	WO	PCT/US2008/01	11/12/2008			Pending	Active Idle Communication System

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Family Ref No	IP Right ID	Country	Application No	Filing Date	Patent Number	Issue Date	Status	Title
MP4148W	MP4148WO	WO	PCT/US2009/00	8/12/2009			Pending	Method And Apparatus For Reducing Transmitter AC-Coupling Drop
MP4560S	MP4560SWOC	WO	10/521,198	7/9/2003	7644190	1/5/2010	In Force	METHOD AND APPARATUS FOR PIPELINED PROCESSING OF DATA PACKETS
MP4563	MP4563UWA	US	13/457,001	4/26/2012	8522246	8/27/2013	In Force	METHOD FOR PACKET FLOW CONTROL USING CREDIT PARAMETERS WITH A
MP4563	MP4563UWZ	US	13/960,954	8/7/2013	8997105	3/31/2015	In Force	METHOD FOR PACKET FLOW CONTROL USING CREDIT PARAMETERS WITH A
MP4566	MP4566WOUS	US	10/478,377	5/7/2004	7397798	7/8/2008	In Force	Method and Apparatus for Processing Blocks In A Pipeline
MP4568S	MP4568SWOC	WO	10/542,986	1/27/2004	7644256	1/5/2010	In Force	METHOD IN PIPELINED DATA PROCESSING
MP4572S	MP4572SWOC	WO	10/470,096	5/21/2001	7010673	3/7/2006	In Force	APPARATUS AND METHOD FOR PROCESSING PIPELINED DATA
MP4832	MP4832	US	14/180,990	2/14/2014	9408274	8/2/2016	In Force	Light Emitting Diodes Generating White Light
MP5208	MP5208PR	US	61/763,344					
MP5348	MP5348PR	US	61/832,067					
MP5424	MP542411	US	15/069,483	3/14/2016	9699545	7/4/2017	In Force	LOW-POWER METHOD AND CIRCUITRY OF DETERMINING HEADPHONE TYP
MP5483	MP5483	US	14/515,363	10/15/2014			Pending	METHODS AND NETWORK DEVICE FOR PERFORMING CUT-THROUGH
MP6083	MP6083	US	15/041,187	2/11/2016	10164727	12/25/2018	In Force	SHARED SCHEDULE TIME SLOT ALLOCATION
MP6212	MP6212JP	JP	2005001576	1/6/2005	4850418	10/28/2011	In Force	Method To Detect The True Zero-Crossing Points Of The Phase Back EMF Fo
MP6212	MP6212SG	SG	2004079448	12/27/2004	113040	4/30/2008	In Force	Method To Detect The True Zero-Crossing Points Of The Phase Back EMF Fo
MP6215	MP6215SG	SG	2009077983	11/23/2009	157108	7/31/2012	In Force	Low Profile Spindle Motor
MP6216	MP6216SG	SG	2010081453	11/4/2010	166343	1/31/2011	In Force	Low Profile Permanent Magnet Synchronous Motor With Segment Structur
MP6242	MP6242	WO	PCT/SG2013/00	10/14/2013			Pending	Axial Field Motor And Method Of Assembling
MP6244	MP6244JP	JP	2014091091	4/25/2014			Pending	Test System And Method For Coil Printed Circuit Board
MP6256	MP6256PRSG	SG	10201400541	3/11/2014			Pending	NESTED COIL CONFIGURATION WITH MULTI-TIER MAGNETS FOR HD ACIAL
MP6385	MP6385	US	15/986,728	2/1/2018			Pending	Methods and Apparatus for Partially Encrypted Firmware
POU920090152	POU920090152US2	US	13/413,825	3/7/2012	8289058	10/16/2012		Multi-Output Pll Output Shift
ROC920040272	ROC920040272US2	US	11/760,575	6/8/2007	7336095	2/26/2008		Changing Chip Function Based On Fuse States
ROC920050092	ROC920050092US2	US	11/972,233	1/10/2008	7486123	2/3/2009		Set/Reset Latch With Minimum Single Event Upset
ROC920050178	ROC920050178US3	US	11/928,135	10/30/2007	7611931	11/3/2009		Semiconductor Structures With Body Contacts And Fabrication Methods Th
ROC920070629	ROC920070629US2	US	12/258,704	10/27/2008	8139014	4/3/2012		I/O Driver For Integrated Circuit With Output Impedance Control
ROC920090023	ROC920090023US2	US	13/570,430	8/9/2012	8493801	7/23/2013		Strobe Offset In Bidirectional Memory Strobe Configurations
ROC920100244	ROC920100244US2	US	13/431,599	3/27/2012	8373486	2/12/2013		Structure For A Frequency Adaptive Level Shifter Circuit
ROC920100295	ROC920100295US2	US	13/602,164	9/2/2012	8751982	6/10/2014		Implementing Dual Speed Level Shifter With Automatic Mode Control
RP9320040053	RP9320040053US2	US	12/124,106	5/20/2008	8130887	3/6/2012		Method And Arrangements For Link Power Reduction
RP9320050093	RP9320050093US3	US	12/117,098	5/8/2008	7671678	3/2/2010		Serial Link Output Stage Differential Amplifier And Method
YOR919980423	YOR919980423US1	US	60/104,889	10/20/1998				
YOR920010028	YOR920010028US1	US	09/917,059	7/27/2001	6518827	2/11/2003		Sense Amplifier Threshold Compensation
YOR920040056	YOR920040056US3	US	12/128,260	5/28/2008	9287186	3/15/2016		Method And Structure For Determining Thermal Cycle Reliability

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YOR920050044	YOR920050044U53	US	11/872,085	10/15/2007	7405990	7/29/2008		Method And Apparatus For In-System Redundant Array Repair On Integrate
YOR920050044	YOR920050044U54	US	11/872,088	10/15/2007	7397709	7/8/2008		Method And Apparatus For In-System Redundant Array Repair On Integrate
YOR920050329	YOR920050329U52	US	12/134,347	6/6/2008	7675789	3/9/2010		Programmable Heavy-Ion Sensing Device For Accelerated Dram Soft Error D
YOR920060228	YOR920060228U52	US	12/030,260	2/13/2008	8054630	11/8/2011		Electronic Components On Trenched Substrates And Method Of Forming Sa
YOR920060228	YOR920060228U54	US	12/099,412	4/8/2008	7777301	8/17/2010		Electronic Components On Trenched Substrates And Method Of Forming Sa
YOR920060228	YOR920060228U56	US	12/783,787	5/20/2010	8659119	2/25/2014		Electronic Components On Trenched Substrates And Method Of Forming Sa
YOR920060228	YOR920060228U57	US	14/157,755	1/17/2014	9508789	11/29/2016		Electronic Components On Trenched Substrates And Method Of Forming Sa
YOR920070250	YOR920070250U52	US	12/543,215	8/18/2009	7944229	5/17/2011		Method And Apparatus For Calibrating Internal Pulses In An Integrated Circ

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