# PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

#### **CONVEYING PARTY DATA**

Name	Execution Date
CHAN H. YOO	08/24/2017
ASHOK PACHAMUTHU	08/24/2017

### **RECEIVING PARTY DATA**

Name:	MICRON TECHNOLOGY, INC.
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Internal Address:	P.O. BOX 6
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State/Country:	IDAHO
Postal Code:	83707-0006

### **PROPERTY NUMBERS Total: 1**

Property Type	Number
Application Number:	16933649

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ATTORNEY DOCKET NUMBER:	010829-9245.US01
NAME OF SUBMITTER:	SAM BEARD
SIGNATURE:	/Sam Beard/
DATE SIGNED:	07/20/2020

## **Total Attachments: 3**

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PATENT 506162269 REEL: 053256 FRAME: 0586

### ASSIGNMENT BY INVENTORS

This Assignment is by the following individuals (the "Assignors"):

- Chan H. Yoo, having a mailing address of 4516 S. Axiom Ave, Boise, ID, 83716; and
- Ashok Pachamuthu, having a mailing address of 3825 E. Wormwood Ct., Boise, ID, 83716.

The Assignors invented one or more certain inventions (the "Invention(s)") described in an application for Letters Patent of the United States titled SEMICONDUCTOR DEVICE HAVING LATERALLY OFFSET STACKED SEMICONDUCTOR DIES, naming the Assignors as inventors, and filed on \_\_\_\_\_\_\_\_ as U.S. Application No. \_\_\_\_\_\_ (the "Application"). The Assignors authorize the Assignee, identified below, or its representatives to insert the application number and filing date of the Application when known.

Micron Technology, Inc., a corporation of Delaware having its principal place of business at 8000 S. Federal Way, P.O. Box 6, Boise, Idaho 83707-0006 ("Assignee"), desires to acquire the entire right, title and interest in and to the Invention(s) and the Application, and in and to any patents (collectively, "Patents") that may be granted for the Invention(s) in the United States or in any foreign countries.

For valuable consideration, the receipt and sufficiency of which Assignors acknowledge, Assignors hereby sell, assign, and transfer to Assignee, its successors, legal representatives and assigns, the entire right, title and interest in and to: the Invention(s), the Application, and any Patents; any divisions, continuations, and continuations-in-part of the Application and any other application claiming priority rights from the Application; any reissues, reexaminations, or extensions of any and all Patents; the right to file foreign applications directly in the name of Assignee; and the right to claim priority rights deriving from the Application (collectively, the "Rights"). Assignors warrant that Assignors own the Rights, and that the Rights are unencumbered. Assignors also agree to not sign any writing or do any act conflicting with this assignment, and, without further compensation, sign all documents and do such additional acts as Assignee deems necessary or desirable to: perfect Assignee's enjoyment of the Rights; conduct proceedings regarding the Rights, including any litigation or interference proceedings; or perfect or defend title to the Rights. Assignors request the Commissioner of Patents to issue any Patent of the United States that may be issued on the Invention(s) to Assignee. This Assignment may be executed in counterparts.

10829-9216.US00/135845939.1

PATENT REEL: 053256 FRAME: 0587

Date: 8/24/2017	
	Chan H. Yoo
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Date:	8/24/2017		<u> </u>
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**RECORDED: 07/20/2020**