# 506166777 07/22/2020

# PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT6213514

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

#### **CONVEYING PARTY DATA**

Name	Execution Date				
CAVIUM INTERNATIONAL	12/31/2019				

### **RECEIVING PARTY DATA**

Name:	MARVELL ASIA PTE, LTD.
Street Address:	TAI SENG CENTRE, 3 IRVING ROAD #10-01
City:	SINGAPORE
State/Country:	SINGAPORE
Postal Code:	369522

# **PROPERTY NUMBERS Total: 1**

Property Type	Number								
Application Number:	11582067								

### **CORRESPONDENCE DATA**

**Fax Number:** (248)641-1270

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

**Phone:** 248-641-1600

Email: troymailroom@hdp.com, cburns@hdp.com, sstevens@hdp.com

Correspondent Name: HARNESS, DICKEY & PIERCE, P.L.C.

Address Line 1: P.O. BOX 828

Address Line 4: BLOOMFIELD HILLS, MICHIGAN 48303

ATTORNEY DOCKET NUMBER:	MP1022
NAME OF SUBMITTER:	CARLA M. BURNS
SIGNATURE:	/Carla M. Burns/
DATE SIGNED:	07/22/2020

#### **Total Attachments: 4**

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PATENT 506166777 REEL: 053282 FRAME: 0831

# ASSIGNMENT AND QUITCLAIM TRANSFER

WHEREAS, Cavium International, a corporation organized under the laws of Cayman Islands, and with offices at c/o Estera Trust (Cayman) Limited, PO Box 1350, Clifton House, 75 Fort Street, Grand Cayman KY1-1108, Cayman Islands (hereinafter "Assignor") owns the patents and patent applications listed in Exhibit A attached hereto and incorporated herein by this reference (hereinafter "Group A") and may have an ownership interest in some or all of the patents and patent applications listed in Exhibit B attached hereto and incorporated herein by this reference (hereinafter "Group B");

WHEREAS, Marvell Asia Pte, Ltd., a corporation organized under the laws of Singapore, and with offices at Tai Seng Centre, 3 Irving Road, #10-01 Singapore 369522, a limited liability corporation (hereinafter "Assignee"), desires to acquire the Assignor's ownership interest in, to and under the Group A and the Group B; and

WHEREAS, Assignor has executed on an effective date of December 31, 2019 (hereinafter "Effective Date") that certain Transfer and Assignment Agreement assigning, among other things, all interest in and to the Group A and transferring by quitclaim its ownership interest, if any, in and to the Group B to Assignee (the "Purchase Agreement").

NOW, THEREFORE, for consideration of one dollar (\$1.00) and other good and valuable consideration paid by Assignee to Assignor, the receipt and sufficiency of which hereby is acknowledged, Assignor does hereby sell, assign and transfer to Assignee its entire interest in and to the **Group A**, including all divisions, continuations, reexaminations, reissues, and foreign counterparts of the applications and patent registrations for the **Group A** (and the right to claim priority and the right to apply for any of the foregoing); including assignment of any and all provisional applications that are relied upon for priority; all rights to causes of action and remedies related thereto (including, without limitation, the right to sue for past, present or future infringement, misappropriation or violation of rights related to the foregoing); and any and all other rights and interests arising out of, in connection with or in relation to the **Group A**.

FURTHER, for consideration of one dollar (\$1.00) and other good and valuable consideration paid by Assignee to Assignor, the receipt and sufficiency of which hereby is acknowledged, Assignor does hereby quitclaim sell, assign and transfer to Assignee its ownership interest, if any, in and to the Group B, including all divisions, continuations, reexaminations, reissues, and foreign counterparts of the applications and patent registrations for the Group B (and the right to claim priority and the right to apply for any of the foregoing); including assignment of any and all provisional applications that are relied upon for priority; all rights to causes of action and remedies related thereto (including, without limitation, the right to sue for past, present or future infringement, misappropriation or violation of rights related to the foregoing); and any and all other rights and interests arising out of, in connection with or in relation to the Group B.

**FURTHER**, nothing contained herein shall be deemed to alter or amend the terms and provisions of the Purchase Agreement and in the event of any conflict between the terms and provisions of this Assignment and the Purchase Agreement, the terms and provisions of the Purchase Agreement shall be deemed to govern and be controlling in all circumstances. This Assignment is executed pursuant to the Purchase Agreement and is entitled to the benefits and subject to the

PATENT REEL: 053282 FRAME: 0832 provisions thereof and shall bind and inure to the benefit of the parties thereto and their respective successors and assigns.

FURTHER, Assignor hereby covenants and agrees to execute and deliver, at the request of Assignee, such further instruments of transfer and assignment and to take any other action as such Assignee may reasonably request to more effectively consummate the assignments contemplated by this Assignment. Specifically, Assignor agrees to, at Assignee's expense, execute, acknowledge and deliver such further documents, instruments, conveyances and assurances and take such further actions as may be reasonably required to register in the name of Assignee the assignment of any of the Patents in Group A and/or the Group B in any appropriate governmental agency or registrar.

IN WITNESS WHEREOF, Assignor and Assignee have executed and delivered this Patent Assignment by their duly authorized representatives at 11:58 pm PST on the Effective Date.

ASSIGNO	<u>R</u> :
	iternational, n organized under the laws of Cayman Islands
By:	
	Philip Anderson Director
ASSIGNE	<u>E</u> :
	sia Pte, Ltd., n organized under the laws of Singapore
Ву:	
	Steven Parker

Director

provisions thereof and shall bind and inure to the benefit of the parties thereto and their respective successors and assigns.

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IN WITNESS WHEREOF, Assignor and Assignee have executed and delivered this Patent Assignment by their duly authorized representatives at 11:58 pm PST on the Effective Date.

<u> </u>	<u>)K:</u>
Cavium I	nternational,
a corporati	on organized under the laws of Cayman Islands
Ву	
	Philip Anderson
	Director
ASSIGN	EE:
	Asia Pte, Ltd.,
a corporati	on organized under the laws of Singapore
By:	
	Steven Parker
	Director

PATENT REEL: 053282 FRAME: 0834

COUNTRY ASSAIRANCE NO. Filing Case Patient Number issue Dates  O WO PCT/US2005/02 7/13/2005  C WO PCT/US2005/02 7/13/2005  C HK HK06112118 6/4/2005  C HK HK06112110 4 8/31/2005  C HK HK06112111 2 8/31/2005  C HK HK06112112 2 8/31/2019  C US 62/767,753 11/15/2018  C US 11/582,057 9/11/2006 8084542 11/22/2011  US 11/690,393 11/171/2019  C US 62/770,537 11/21/2019  C US 62/770,537 11/21/2019  C US 62/770,537 11/21/2019  C US 62/770,537 11/21/2019  C US 62/770,547 11/20/2018  R US 62/770,647 11/20/2018  R US 62/770,647 11/20/2018  R US 62/770,439 9/26/2019  C US 16/583,539 9/26/2019  C US 16/583,539 9/26/2019  C US 62/770,647 11/20/2018  R US 62/770,649 11/27/2018																																		
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System And Method For Transferring Data Using Storage Controllers  System And Method For Controlling Buffer Memory Overflow And Underflo  Dynamic WWN Storage Module For Storage Controllers  PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A THREADMILL BELT  WIRELESS PRINTER CONFIGURATION MODULE  LASER PRINT APPARATUS WITH DUAL HALFTONES  LASER PRINT APPARATUS WITH TONER EXPLOSION COMPENSATION  Methods and Apparatus for Singular Value Decomposition with Norm Sorth  Extremely Low if Architecture For in-Band On-Channel (ISCC) Radio  Method and Apparatus for Extendable Hardware Queues  LOW-POWER SERIALIZER WITH HALF-HATE CLOCKING FOR SERIAL COMMU  Modular Memory-Like Layout for FinFET Analog Designs  SYSTEMS AND METHODS FOR 1/O IMBALANCE CORRECTION AND CALIBRATI  WAXEUP RADIO (WUR) PACKET PREAMBLE DESIGN  Ethernet Transcriver with PHY-Level Signal-Loss Detector  TWO DIMENSIONAL MAGNETIC RECORDING (TDMR) OFF-TRACK PERFORM  SERIAL MANAGEMENT INTERFACE WITH IMPROVED RELIABILITY  Reliable SMI ACCESS  Latency optimization using a Smart Samplers Placement  Flash Controlling BERFORMATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA  CONTROLLING PERFORMANCE OF A SOUD STATE DRIVE  CONTROLLING PERFORMANCE OF A SOUD STATE DRIVE  CONTR	Pending	Pending	Pending	Pending	Pending		Pending	Pending	Pending	Pending	Pending	Not Filed	Pending	Pending	Pending	Pending	Pending	Not Filed	Pending	Pending		Not Filed	Pending	Pending			Pending	Pending	Pending	Pending	Pending	Pending		
RECORDED: 07/22/2020 REEL: 053282 FRAME: 0835						07/				METHOD AND APPARATUS FOR TRANSMITTING SIGNALS OVER LONG DISTA	WIFI BACKOFF TIMER	POLYGONAL BGA SEMICONDUCTOR PACKAGE	POLYGONAL BGA SEMICONDUCTOR PACKAGE	Flach Controller	Latency optimization using a Smart Samplers Placement	Reliable SMI Access	SERIAL MANAGEMENT INTERFACE WITH IMPROVED RELIABILITY	TWO DIMENSIONAL MAGNETIC RECORDING (TOMR) OFF-TRACK PERFORM	Ethernet Transceiver with PHY-Level Signal-Loss Detector	WAKEUP RADIO (WUR) PACKET PREAMBLE DESIGN													System And Method For Transferring Data Using Storage Controllers	Tille