

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT6236650

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SHAO-LUN CHIEN	07/14/2020
PO-CHUN WANG	05/21/2020
HUI-ZHONG ZHUANG	06/03/2020
CHIH-LIANG CHEN	06/03/2020
LI-CHUN TIEN	06/15/2020
RECEIVING PARTY DATA	
Name:	TAIWAN SEMICONDUCTOR MANUFACTURING CO., LTD.
Street Address:	NO.8, LI-HSIN RD.6
Internal Address:	SCIENCE-BASED INDUSTRIAL PARK
City:	HSINCHU
State/Country:	TAIWAN
Postal Code:	300
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	16915954
CORRESPONDENCE DATA	
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<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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ATTORNEY DOCKET NUMBER:	T1516.10711US01
NAME OF SUBMITTER:	R. BURNS ISRAELSEN
SIGNATURE:	/R. Burns Israelsen/
DATE SIGNED:	08/05/2020
Total Attachments: 3	

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ASSIGNMENT

This assignment agreement is applicable to an invention entitled (Invention Title) _____
MULTI-BIT STRUCTURE

The PATENT RIGHTS referred to in this agreement are:

(check one) a patent application for this invention, executed by the ASSIGNOR(S) concurrently with this assignment.

U.S. patent application Serial No. 16/915,954, filed June 29, 2020

a U.S. patent application based on PCT International Application No. _____ filed on (date) _____ (U.S. patent application Serial No. _____, if known).

U.S. patent No. _____, issued _____.

The PATENT RIGHTS also include all divisions, reissues, continuations and extensions of the patents and patent applications identified above.

The PATENT RIGHTS assigned under this agreement are:

U.S. patent rights only.

The ASSIGNOR(S) referred to in this agreement is (or are) the inventor(s) whose signatures appear on page 2 of this Assignment and any Supplemental Sheet(s).

The ASSIGNEE referred to in this agreement is:

(Name of Assignee) TAIWAN SEMICONDUCTOR MANUFACTURING CO., LTD.

(Address) NO.8, LI-HSIN RD.6, SCIENCE-BASED INDUSTRIAL PARK, HSINCHU, TAIWAN 300, R.O.C.

The ASSIGNEE is:

(check one) An individual.

A Partnership.

A Corporation of TAIWAN, R.O.C. (specify state or country)

(other) _____

The ASSIGNOR(S), in consideration of \$10.00 paid by the ASSIGNEE, and other good and valuable consideration, receipt of which is acknowledged, hereby assign(s) the following rights to the ASSIGNEE, its successors and assigns:

the full and exclusive right to the invention;

the entire right, title and interest in and to the PATENT RIGHTS;

the right to sue and recover for any past infringement; and

the right to claim priority under 35 USC 119, 35 USC 120, or any other applicable provisions, based on any earlier patent applications for this invention.

THIS IS PAGE 2 OF AN ASSIGNMENT FROM THE INVENTOR(S) TO
ASSIGNEE: TAIWAN SEMICONDUCTOR MANUFACTURING CO., LTD.
INVENTION TITLE: MULTI-BIT STRUCTURE

As to all U.S. patent applications assigned under this agreement, the ASSIGNOR(S) hereby authorize(s) and request(s) the Commissioner of Patents and Trademark to issue all Letters Patent to the ASSIGNEE as the assignee of the entire right, title and interest, for the sole use and enjoyment of said ASSIGNEE, its successors and assigns.

Further, the ASSIGNOR(S) agree(s) to communicate to said ASSIGNEE, or its representatives, any fact known to the ASSIGNOR(S) with respect to said invention, and testify in any legal proceeding, sign all lawful papers, execute all divisional, continuation, substitute, renewal, reexamination and reissue applications, execute all necessary assignment papers to cause and all Letter Patent to be issued to said ASSIGNEE, make all rightful oaths and generally do everything necessary or desirable to aid said ASSIGNEE, its successors and assigns, to obtain and enforce proper protection for said invention.

The ASSIGNOR(S) authorize(s) the attorneys and agents who have the power of attorney in this application to check any appropriate boxes and to insert the Serial Number and filing date in this document after it has been executed.

<u>Shao-Lun CHIEN</u> Name of sole or first inventor	_____ Signature	_____ Date
<u>Po-Chun WANG</u> Name of second inventor, if any	<u>Po-Chun Wang</u> Signature	<u>2020/5/21</u> Date
<u>Hui-Zhong ZHUANG</u> Name of third inventor, if any	<u>Hui-Zhong Zhuang</u> Signature	<u>2020/6/3</u> Date
<u>Chih-Liang CHEN</u> Name of fourth inventor, if any	<u>Chih-Liang Chen</u> Signature	<u>2020/6/3</u> Date
<u>Li-Chun TIEN</u> Name of fifth inventor, if any	<u>Li-Chun Tien</u> Signature	<u>2020.6/15</u> Date

THIS IS PAGE 2 OF AN ASSIGNMENT FROM THE INVENTOR(S) TO
ASSIGNEE: TAIWAN SEMICONDUCTOR MANUFACTURING CO., LTD.
INVENTION TITLE: MULTI-BIT STRUCTURE

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The ASSIGNOR(S) authorize(s) the attorneys and agents who have the power of attorney in this application to check any appropriate boxes and to insert the Serial Number and filing date in this document after it has been executed.

Shao-Lun CHIEN

Name of sole or first inventor

Shao-Lun Chien 07/14/2020

Signature

Date

Po-Chun WANG

Name of second inventor, if any

Signature

Date

Hui-Zhong ZHUANG

Name of third inventor, if any

Signature

Date

Chih-Liang CHEN

Name of fourth inventor, if any

Signature

Date

Li-Chan TIEN

Name of fifth inventor, if any

Signature

Date