

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
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DIWAKAR CHOPPERLA	08/10/2020
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PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	16938798
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DATE SIGNED:	08/10/2020
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ASSIGNMENT

WHEREAS, we, Jinghui Zhu, a citizen of the United States of America, residing at, 1456 Chavez Way, San Jose, CA 95131, USA, and Diwakar Chopperla, a citizen of the United States of America, residing at 38660 Lexington St, Apt 506, Fremont, CA 94536, USA, has made an invention in:

"METHOD AND SYSTEM FOR ENHANCING PROGRAMMABILITY OF A FIELD-PROGRAMMABLE GATE ARRAY VIA A DUAL-MODE PORT"

for which we have executed application papers for a U.S. patent having an application serial No. 16/938,798; and


WHEREAS, Gowin Semiconductor Corporation, a Guangzhou (China) Corporation, having a place of business located at #1001, 10/F, Block A5, 234 Science Street, HuangPu, GuangZhou 510700, China, is desirous of acquiring the exclusive right, title and interest in and to said invention and in and to the Letters Patent to be granted and issued therefore in the United States of America and its territories and possessions, and all countries foreign thereto;

NOW, THEREFORE, for a valuable consideration, the receipt of which is hereby acknowledged, we, Jinghui Zhu and Diwakar Chopperla do sell, assign, transfer and set over unto the said Gowin Semiconductor Corporation, its successors and assigns the full and exclusive right, title and interest in and to said invention, and in and to any and all Letters Patent to be granted and issued therefore, not only for, to and in the United States of America, its territories and possessions, but also for, to and in all other countries including all priority rights under the International Convention; and we hereby authorize and request the Commissioner of Patents and Trademarks to issue said Letters Patent to said Gowin Semiconductor Corporation, its successors and assigns, in accordance with this Assignment. IN TESTIMONY WHEREOF, we have hereunto signed our names on the date indicated hereinafter:

Re: U.S. Patent Application entitled:

**METHOD AND SYSTEM FOR ENHANCING PROGRAMMABILITY OF A
FIELD-PROGRAMMABLE GATE ARRAY VIA A DUAL-MODE PORT**

1) Inventor: **Jinghui Zhu**

Signature:  Date: 08/06/2020
Jinghui Zhu

2) Inventor: **Diwakar Chopperla**

Signature: _____ Date: _____
Diwakar Chopperla

Re: U.S. Patent Application entitled:

**METHOD AND SYSTEM FOR ENHANCING PROGRAMMABILITY OF A
FIELD-PROGRAMMABLE GATE ARRAY VIA A DUAL-MODE PORT**

1) Inventor: Jinghui Zhu

Signature: _____ Date: _____
 Jinghui Zhu

2) Inventor: Diwakar Chopperla

Signature: Diwakar Chopperla Date: 08/10/2020
 Diwakar Chopperla