

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT6243972

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
MAGSIL CORPORATION	12/10/2013
RECEIVING PARTY DATA	
Name:	III HOLDINGS 1, LLC.
Street Address:	2711 CENTERVILLE RD
Internal Address:	SUITE 400
City:	WILMINGTON
State/Country:	DELAWARE
Postal Code:	19808
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	16989565
CORRESPONDENCE DATA	
Fax Number:	(312)775-8100
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	3127875800
Email:	emaxson@mcandrews-ip.com
Correspondent Name:	MCANDREWS, HELD & MALLOY, LTD.
Address Line 1:	500 WEST MADISON STRET
Address Line 2:	SUITE 3400
Address Line 4:	CHICAGO, ILLINOIS 60661
ATTORNEY DOCKET NUMBER:	28075US03
NAME OF SUBMITTER:	JEFFREY B. HUTER
SIGNATURE:	/Jeffrey B. Huter/
DATE SIGNED:	08/11/2020
Total Attachments: 7	
source=ASN MagSil to III Holdings 1 LLC Exhibit B (12-10-13)#page1.tif	
source=ASN MagSil to III Holdings 1 LLC Exhibit B (12-10-13)#page2.tif	
source=ASN MagSil to III Holdings 1 LLC Exhibit B (12-10-13)#page3.tif	
source=ASN MagSil to III Holdings 1 LLC Exhibit B (12-10-13)#page4.tif	

source=ASN MagSil to III Holdings 1 LLC Exhibit B (12-10-13)#page5.tif
source=ASN MagSil to III Holdings 1 LLC Exhibit B (12-10-13)#page6.tif
source=ASN MagSil to III Holdings 1 LLC Exhibit B (12-10-13)#page7.tif

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, MagSil Corporation, a California corporation, with an office at 5255 Stevens Creek Blvd., #188, Santa Clara, CA 95051 (“*Assignor*”), does hereby sell, assign, transfer, and convey unto III Holdings 1, LLC, a Delaware limited liability company, with an address at 2711 Centerville Rd, Suite 400, Wilmington, DE 19808 (“*Assignee*”), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the “*Patent Rights*”):

(a) the provisional patent applications, patent applications and patents listed in the table below (the “*Patents*”);

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
8320175	US	02/26/2010	MAGNETIC BOOSTER FOR MAGNETIC RANDOM ACCESS MEMORY Krishnakumar Mani
13/340452	US	12/29/2011	Magnetic Sidewalls for Write Lines in Field-Induced MRAM and Methods of Manufacturing them Krishnakumar Mani
12/202429	US	09/01/2008	High density magnetic memory based on nanotubes Krishnakumar Mani
7796421	US	05/08/2008	PROGRAMMABLE MAGNETIC READ ONLY MEMORY (MROM) Krishnakumar Mani
TW097117044	TW	05/08/2008	Programmable magnetic read only memory (MROM) Krishnakumar Mani
8400866	US	08/06/2010	Voltage boosting in MRAM current drivers Krishnakumar Mani

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
7787289	US	12/03/2007	MRAM DESIGN WITH LOCAL WRITE CONDUCTORS OF REDUCED CROSS-SECTIONAL AREA Krishnakumar Mani
7830704	US	06/06/2008	Compact magnetic random access memory cell with slotted bit line and method of manufacturing same Krishnakumar Mani
7944737	US	07/31/2008	Magnetic memory cell based on a magnetic tunnel junction (MTJ) with independent storage and read layers Krishnakumar Mani
12/706694	US	02/16/2010	Magnetic memory display driver system Krishnakumar Mani
8526221	US	10/11/2010	Semiconductor integrated circuit for low and high voltage operations Krishnakumar Mani
13/954899	US	07/30/2013	Semiconductor Integrated Circuit for Low and High Voltage Operations Krishnakumar Mani
13/369267	US	02/08/2012	Memory Cell Layout for Low Current Field-Induced MRAM Krishnakumar Mani
13/442829	US	04/09/2012	Counterbalanced-Switch MRAM Jannier Maximo Roiz Wilson

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
13/444805	US	04/11/2012	Self Contacting Bit Line to MRAM Cell Krishnakumar Mani
8369135	US	12/03/2010	Memory circuit with crossover zones of reduced line width conductors Krishnakumar Mani
12/960430	US	12/03/2010	Memory Circuit and Method of Forming the Same Using Reduced Mask Steps Krishnakumar Mani
13/153471	US	06/06/2011	Memory Circuit and Method for Dissipating External Magnetic Field Krishnakumar Mani
13/153472	US	06/06/2011	Integrated Circuit with Sensing Unit and Method for Using the Same Krishnakumar Mani
13/153473	US	06/06/2011	Memory Cell with Schottky Diode Krishnakumar Mani
13/153474	US	06/06/2011	Magnetic Enhancement Layer in Memory Cell Krishnakumar Mani
14/029778	US	09/19/2013	Magnetic Enhancement Layer in Memory Cell Krishnakumar Mani
13/214107	US	08/19/2011	Method of Etching MTJ Using CO Process Chemistries Krishnakumar Mani

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
13/311453	US	12/05/2011	Field Programming Method for Magnetic Memory Devices Krishnakumar Mani
13/155299	US	06/07/2011	Method for Fabricating a Circuit Krishnakumar Mani
13/208577	US	08/12/2011	Magnetic Memory Circuit with Stress Inducing Layer Krishnakumar Mani
13/311470	US	12/05/2011	MRAM with Metal Gate Write Conductors Krishnakumar Mani
13/424337	US	03/19/2012	Tool for Annealing of Magnetic Stacks Krishnakumar Mani
13/427373	US	03/22/2012	Methods for Manufacturing Carbon Ribbons for Magnetic Devices Krishnakumar Mani
7173847	US	11/04/2003	Magnetic storage cell Krish Mani
7239570	US	12/02/2003	Magnetic memory device and method for magnetic reading and writing Santosh Kumar
7394683	US	11/10/2004	Solid state magnetic memory system and method Santosh Kumar
TW094105368	TW	02/23/2005	Solid state magnetic memory system and method Santosh Kumar

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
8248845	US	01/31/2007	Magnetic storage cell Krish Mani
7649765	US	12/26/2007	Magnetic Memory Cell and Method of Fabricating Same Krishnakumar Mani
7894252	US	01/19/2010	Magnetic Memory Cell and Method of Fabricating Same Krishnakumar Mani

(b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, and/or (ii) for which any of the Patents directly or indirectly forms a basis for priority;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);

(d) all foreign patents, patent applications, and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

(e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents above and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;

(f) inventions, invention disclosures, and discoveries described in any of the Patents and/or any item in the foregoing categories (b) through (e) that (i) are included in any claim in the Patents and/or any item in the foregoing categories (b) through (e), (ii) are subject matter capable of being reduced to a patent claim in a reissue or reexamination proceeding brought on any of the Patents and/or any item in the foregoing categories (b) through (e), and/or (iii) could have been included as a claim in any of the Patents and/or any item in the foregoing categories (b) through (e);

(g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the

Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

- (1) damages,
- (2) injunctive relief, and
- (3) any other remedies of any kind

for past, current, and future infringement; and

(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

Assignor represents, warrants and covenants that:

(1) Assignor has the full power and authority, and has obtained all third party consents, approvals and/or other authorizations required to enter into this Agreement and to carry out its obligations hereunder, including the assignment of the Patent Rights to Assignee; and

(2) Assignor owns, and by this document assigns to Assignee, all right, title, and interest to the Patent Rights, including, without limitation, all right, title, and interest to sue for infringement of the Patent Rights. Assignor has obtained and properly recorded previously executed assignments for the Patent Rights as necessary to fully perfect its rights and title therein in accordance with governing law and regulations in each respective jurisdiction. The Patent Rights are free and clear of all liens, claims, mortgages, security interests or other encumbrances, and restrictions. There are no actions, suits, investigations, claims or proceedings threatened, pending or in progress relating in any way to the Patent Rights. There are no existing contracts, agreements, options, commitments, proposals, bids, offers, or rights with, to, or in any person to acquire any of the Patent Rights.

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

Assignor will, at the reasonable request of Assignee, do all things necessary, proper, or advisable, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining, perfecting, sustaining, and/or enforcing the Patent Rights.

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Santa Clara, CA on December 10, 2013.

ASSIGNOR:

MagSil Corporation

By: J. Kamdar
Name: JAY KAMDAR
Title: CEO
(Signature MUST be attested)

ATTESTATION OF SIGNATURE PURSUANT TO 28 U.S.C. § 1746

The undersigned witnessed the signature of Jay Kamdar to the above Assignment of Patent Rights on behalf of MagSil Corporation and makes the following statements:

1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.
2. Jay Kamdar is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me on December , 2013 to execute the above Assignment of Patent Rights on behalf of MagSil Corporation.
3. Jay Kamdar subscribed to the above Assignment of Patent Rights on behalf of MagSil Corporation.

I declare under penalty of perjury under the laws of the United States of America that the statements made in the three (3) numbered paragraphs immediately above are true and correct.

EXECUTED on 10/10/2013 (date)

Print Name: Rosanna Duff