

PATENT ASSIGNMENT COVER SHEET

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EPAS ID: PAT6246275

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SILANNA SEMICONDUCTOR NORTH AMERICA, INC.	08/11/2020
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PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	16879046
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NAME OF SUBMITTER:	PAUL G. STUART
SIGNATURE:	/Paul G. Stuart/
DATE SIGNED:	08/11/2020
Total Attachments: 1	
source=20200811_SILSP092_Assignment-Silanna_Semi_NA_to_Silanna_Asia#page1.tif	

PATENT ASSIGNMENT

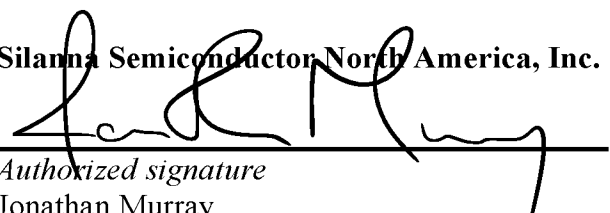
For good and valuable consideration, the receipt of which is hereby acknowledged, **Silanna Semiconductor North America, Inc.** ("Assignor"), having a registered office at 4795 Eastgate Mall, Suite 100, San Diego, CA 92121, USA, hereby assigns to **Silanna Asia Pte Ltd** ("Assignee"), having a registered office at 10 Collyer Quay, #10-01 Ocean Financial Centre, Singapore, 049315, all of its right, title and interest in and to the patent application listed below and the right to file non-provisionals, divisionals, continuations, continuations-in-part, substitute, continued prosecution, request for continued examination, renewals, reexaminations and reissues in the United States and foreign filing of the patent application listed below, including but not limited to the right to file, prosecute and issue patent applications and patents claiming priority to the patent application listed below for the invention set forth in the patent application listed below and, including without limitation, any and all claims and potential claims of past, present and future infringement of such patent application and the right to sue and collect for past, present and future damages and any other causes of action under such patent application ("Assigned Patent Rights"):

Serial No.	Filing Date	Title	Inventors
16/879,046	May 20, 2020	LDMOS Architecture and Method for Forming	David Snyder; Shanghai Larry Tu

Assignor also hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, letters patents, utility models or certificates of invention which may be granted upon the Assigned Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

IN WITNESS WHEREOF Assignor has executed this Patent Assignment this 11th day of August, 2020.

Silanna Semiconductor North America, Inc.



Authorized signature

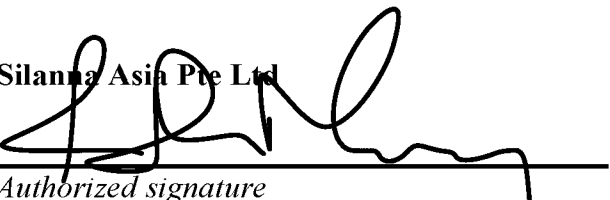
Jonathan Murray

Name (Printed)

Treasurer

Title

Silanna Asia Pte Ltd



Authorized signature

Jonathan Murray

Name (Printed)

Director

Title