506220879 08/25/2020

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT6267622

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date				
NUMONYX B.V.	11/22/2011				

RECEIVING PARTY DATA

Name:	MICRON TECHNOLOGY, INC.
Street Address:	8000 SOUTH FEDERAL WAY
City:	BOISE
State/Country:	IDAHO
Postal Code:	83707

PROPERTY NUMBERS Total: 2

Property Type	Number
Application Number:	13273182
Application Number:	16813638

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 312-456-8400

Email: GTIPMAIL@GTLAW.COM

Correspondent Name: GREENBERG TRAURIG, LLP (SV)

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Address Line 2: INTELLECTUAL PROPERTY DEPARTMENT

Address Line 4: CHICAGO, ILLINOIS 60601

ATTORNEY DOCKET NUMBER:	120426-096000RE, 096002RE
NAME OF SUBMITTER:	ALLISON HUNG
SIGNATURE:	/Allison Hung/
DATE SIGNED:	08/25/2020

Total Attachments: 59

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PATENT ASSIGNMENT AGREEMENT

WHEREAS, Numonyx B.V., a company organized under the laws of Netherlands, having a place of business at A-ONE Biz Center, Z.A. Vers la Piece, Rte. De l'Etraz, 1180 Rolle, Switzerland (hereinafter referred to as "Assignor") has rights in and to the following:

- 1. U.S. Patents and patent applications listed on Exhibit A, and
- 2. Foreign patents and patent applications listed on Exhibit B.

(said patents and patent applications listed in Exhibits A and B being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, Micron Technology, Inc. is a corporation having a principal place of business at 8000 S. Federal Way, Boise, Idaho 83707, United States (hereinafter referred to as "Assignee") is desirous of obtaining rights to SAID PATENT RIGHTS;

NOW, THERFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby sell, convey, transfer and assign to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all right, title and interest in and to SAID PATENT RIGHTS, together with any and all continuations, continuation-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

IN WTINESS WHEREOF, Assignor has caused this Assignment to be executed by its duly authorized officer effective as of the 22- day of Dovember, 2011.

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By:/	Viegre	1-10-	ر	Larrie	
Printed 1	Name:	Thomas	Ĺ.	Cans J.	-
Title:	150	· extrem			

Micron Technology, Inc.

Title: Chief Patent Coursel

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M & A FOR MINIIMIZING DATA TRANSFER TO MAIN MEMORY FROM A WRITE CACHE DURING A CACHE MISS	MICROPROCESSOR PROVIDING ENCODED INFORMATION ON BYTE ENABLE LINES INDICATING WHETHER READING CODE OR DATA, LOCATION OF CODE/DATA ON DATA LINES, AND BIT WIDTH OF CODE/DATA	NOVEL ARCHITECTURE FOR VIRTUAL GROUND HIGH-DENSITY EPROMS	LEAKAGE VERIFICATION FOR FLASH EPROM METHOD OF TRANSFERRING BURST DATA IN A MICROPROCESSOR METHOD OF TRANSFERRING BURST DATA IN A MICROPROCESSOR	METHOD AND APPARATUS FOR AUTOMATICALLY CONFIGURING SYSTEM MEMORY ADDRESS SPACE OF A COMPUTER SYSTEM HAVING A MEMORY SUBSYSTEM WITH INDETERMINISTIC NUMBER OF MEMORY UNITS OF INDETERMINISTIC SIZES DURING SYSTEM RESET	CREWICAL MODELLATION OF SHIN-ON GLASS FOR IMPROVED PERFORMANCE IN IC FABRICATION	TEST MODE ENABLE SCHEME FOR MEMORY	PORT EXPANDER ARCHITECTURE FOR MAPPING A FIRST SET OF ADDRESSES TO EXTERNAL MEMORY AND MAPPING A SECOND SET OF ADDRESSES TO AN I/O PORT	CIRCUIT INCLUDING A LATCHING UNIT FOR HOLDING INSTRUCTIONS DURING EXCEPTION FOR RE-EXECUTION FOLLOWING EXCEPTION HANDLING	SELF-BIASED, HIGH-GAIN DIFFERENTIAL AMPLIFIER WITH FEEDBACK	METHOD AND APPARATUS FOR CONDUCTING BUS TRANSACTIONS BETWEEN TWO CLOCK INDEPENDENT BUS AGENTS OF A COMPUTER SYSTEM USING A TRANSACTION BY TRANSACTION DETERMINISTIC REQUEST/RESPONSE PROTOCOL	MICROPROCESSOR BUS INTERFACE UNIT WHICH CHANGES CHEDULED DATA TRANSFER INDICATIONS UPON SINSING CHANGE IN ENABLE SIGNALS BEFORE RECEIVING READY SIGNAL	MICROCONTROLLER PERIPHERAL EXPANSION BUS FOR ACCESS TO INTERNAL SPEC- IAL FUNCTION REGISTERS	PROCESS FOR SIMULTANEOUSLY FABRICATING EEPROM CELL AND FLASH EPROM CELL	PROCESSOR CONTROLLED COMMAND PORT ARCHITECTURE FOR FLASH MEMORY	A CIRCUIT FOR PROVINDING A LOAD FOR THE CHARING OF AN EPROM CELL $\ 1/12/1988$	LEAKAGE VERIFICATION FOR FLASH EPROM	COUNTERS FOR QUEUING REUESTS AND REPLIES ON A PIPELINED PACKET BUS	
4/28/1994	10/8/1993	8/7/1989	3/14/1989 4/5/1989 11/24/1992	3/29/1995	3/8/1989	5/22/1991	6/12/1992	4/14/1993	1/2/1990	2/27/2995	8/1/1988	12/1/1992	11/21/1988	10/19/1990	. 1/12/1988	1/12/1988 2/17/1988	10/14/1988	12
08/234,587	08/133,771	07/390,159	07/323,310 07/333,980 07/981,943	08/412,607	07/320,763	07/707,241	07/898,190	08/047,696	07/462,879	08/394,560	07/227,078	07/984,280	07/274,420	07/601,012	07/144,569	07/144,567	07/257,857	Senalivo
11/14/1995	1/3/1995	2/12/1991	8/22/1989 7/14/1992 10/19/1993	4/30/1996	12/5/1989	12/31/1991	9/7/1993	5/30/1995	6/26/1990	10/3/1995	12/17/1991	5/31/1994	9/18/1990	6/22/1993	8/15/1989	10/17/1989 6/20/1989	9/17/1991	IssueDate
5,467,460	5,379,443	4,992,980	4,860,261 5,131,083 5,255,378	5,513,331	4,885,262	5,077,738	5,243,700	5,421,021	4,937,476	5,455,957	5,073,969	5,317,750	4,957,877	5,222,046	4,858,185	4,875,188 4,841,482	5,050,066	re Serfaino Issuebato Patentito Contro
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CIRCUITRY AND METHOD FOR DISCHARGING A DRAIN OF A CELL OF A NON-	APPARATUS AND METHOD FOR ACHIEVING VIA STEP COVERAGE SYMMETRY 12/27/1990	ARCHITECTURE FOR READING INFORMATION FROM A MEMORY ARRAY	COMMAND INTERFACE BETWEEN USER COMMANDS AND A MEMORY DEVICE	CIRCUITRY AND METHOD FOR PROGRAMMING AND ERASING A NONVOLATILE SEMICONDUCTOR MEMORY	NOVEL LITHOGRAPHIC TECHNIQUE FOR PATTERNING A SEMICONDUCTOR DEVICE	MICKOPROCESSOR PLL CLOCK CIRCUIT WITH SELECTABLE DELAYED FEEDBACK	SINGLE STEP SALICIDATION PROCESS	ANNEAL I'O DECKEASE MOISI URE ABSORBANCE OF INTERMETAL DIELECTRICS	TEOS INTERMETAL DIELECTRIC PRECLEAN FOR VIA FORMATION	HIGH VOLTAGE MOS TRANSISTOR FOR FLASH EEPROM APPLICATIONS HAVING A UNI-SIDED LIGHTLY DOPED DRAIN	RADIATION SHIELD FOR EPROM CELLS	HANDLE AND HANDLED IMPLEMENT	HIGH BANDWITH OUTPUT HERARCHICAL MEMORY STORE INCLUDING A CACHE, FETCH BUFFER AND ROM	HIGH CF4FLOW-REACTIVE ION ETCH FOR ALLIMINUM PATTERNING	METHOD OF MODIFYING A MICROINSTRUCTION WITH OPERANDS SPECIFIED BY AN INSTRUCTION HELD IN AN ALIAS REGISTER	BRANCH LOOK AHEAD ADDER FOR USE IN AN INSTRUCTION PIPELINE SEQUENCER WITH MULTIPLE INSTRUCTION DECODING	METHOOD FOR IMPROVING DEVICE SCALABILITY OF BURIED BIT LINE FLASH EPROM DEVICES HAVING SHORT REOXIDATION BEAKS AND SHALLOWER JUNCTIONS	APPARATUS FOR ISSUING INSTRUCTIONS AND REISSUING A PREVIOUS INSTRUCTIONS BY RECIRCULATING USING THE DELAY CIRCUIT	MEMORY DEVICE WITH A TEST MODE	SIX-WAY ACCESS PORTED RAM ARRAY CELL COMPUTER MEMORY WITH STATUS CELL	ETCHBACK PROCESS FOR TUNGSTEN UTILIZING A NF3/AR CHEMISTRY	SYSTEM FOR EXECUTING DIFFERENT CYCLE INSTRUCTIONS BY SELECTIVELY BYPASSING SCOREBOARD REGISTER AND CANCELING THE EXECUTION OF CONDITIONALLY ISSUED INSTRUCTION IF NEEDED RESOURCES ARE BUSY	APPARATUS AND METHOD FOR ALIGNMENT OF ADJACENT SURFACES
1 100 14004	Y 12/27/1990	5/9/1994	1/21/1994	2/24/1994	4/13/1993	5/29/1992	9/28/1992	6/7/1991	6/7/1991	7/2/1993	11/13/1990	12/27/1990	12/20/1990	5/8/1992	12/20/1990	10/26/1993	11/2/1990	11/12/1993	9/26/1990	2/28/1990 9/26/1990	4/17/1991	2/28/1990	8/6/1990
07/c00 t /7	07/635,562	08/239,939	08/185,449	08/201,044	08/046,477	07/890,937	07/951,945	07/712,116	07/712,119	08/087,140	07/612,452	07/635,868	07/630,534	07/883,067	07/630,497	08/141,685	07/609,192	08/150,784	07/589,391	07/486,408 07/589.388	07/686,683	07/486,407	07/563,560
100	6/30/1992	7/4/1995	10/31/1995	9/5/1995	4/2/1996	8/29/1995	2/15/1994	8/18/1992	6/15/1993	10/3/2000	9/17/1991	12/29/1992	5/17/1994	4/13/1993	6/22/1993	9/26/1995	4/7/1992	4/23/2002	1/5/1993	6/11/1991 8/4/1992	11/17/1992	2/9/1993	07/563,560 9/17/1991 5,048,195 US
	5,126,029	5,430,677	5,463,757	5,448,712	5,503,959	5,446,867	5,286,678	5,139,971	5,219,791	6,127,696	5,050,123	5,175,126	5,313,605	5,202,291	5,222,244	5,454,089	5,102,814	6,378,061	5,177,745	5,023,844	5,164,330	5,185,872	5,048,195
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NMXI-0066.00/US	NMXI-0065.00/US	NMXI-0064.00/US	NMXI-0063.00/US	NMXI-0062.00/US	NMX1-0061.00/US	NMX1-0060.00/US	NMX1-00S9.00/US		•		NMXI-0055.00/US	NMXI-0054.00/US		NMXI-0053.01/US	NMX1-0053.00/US	NMXI-0052.02/US	NMXI-0052.01/US	NMXI-0052.00/US	NWXI-0051.00/US	NMXI-0050.00/US	NMXI-0049,00/US	NMXI-0047.00/US	NMX1-0046.00/US
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METHOD AND APPARATUS FOR TESTING THE CONTINUITY OF STATIC RANDOM ACCESS MEMORY CELLS	PARTICLE FLUX SHADOWING FOR THREE-DIMENSIONAL TOPOGRAPHY SIMULATION	SURFACE SWEEPING METHOD FOR SURFACE MOVEMENT IN THREE DIMENSIONAL TOPOGRAPHY SIMULATION	COMPENSATED NOISE CONTROL	HIGH THROUGHPUT INTERLEVEL DIELECTRIC GAP FILLING PROCESS	HIGH SPEED NOR'ING INVERTING, MUX'ING AND LATCHING CIRCUIT WITH TEMPERATURE COMPENSATED OUTPUT NOISE CONTROL	CLOCK SWITCHING APPARATUS AND METHOD FOR COMPUTER SYSTEMS	LITHOGRAPHY USING A NEW PHASE-SHIFTING RETICLE	MULTI-QUADRANT CHARGE DOMAIN SYNAPSE CELL	PROCESS FOR FILLING SUBMICRON SPACES WITH DIELECTRIC	BUFFER CIRCUITRY FOR TRANSFERRING SIGNALS FROM TTL CIRCUITRY TO DUAL RANGE CMOS CIRCUITRY	CHARGE DOMAIN DIFFERENTIAL CONDUCTANCE SYNAPSE CELL FOR NEURAL NETWORKS	NOVEL PROCESS FOR FORMING A THIN OXIDE LAYER	PROCESS FOR FORMING A THIN OXIDE LAYER	A METHOD FOR FORMING AN ASYMMETRIC FLOATING GATE OVERLAP FOR IMPROVED DEVICE PERFORMANCE IN BURIED BIT-LINE DEVICES	ASYMMETRIC FLOATING GATE OVERLAP FOR IMPROVED DEVICE CHARACTERISTICS IN BURIED BIT-LINE DEVICES	ADVANCE MULTILAYER MOLDED PLASTIC PACKAGE USING MESIC TECHNOLOGY	ADVANCE MULTILAYER MOLDED PLASTIC PACKAGE USING MESIC TECHNOLOGY	ADVANCE MULTILAYER MOLDED PLASTIC PACKAGE USING MESIC TECHNOLOGY	METHOD FOR TESTING ERASE CHARACTERISTICS OF A FLASH MEMORY ARRAY	COMPOSITE POLISHING PAD FOR SEMICONDUCTOR PROCESS	A METHOD FOR PROGRAMMING A VIRTUAL GROUND EPROM CELL INCLUDING SLOW RAMPING OF THE COLUMN LINE VOLTAGE	HIGH-RESOLUTION SYNCHRONOUS DELAY LINE METHOD OF REPAIRING OVERERASED CELLS IN A FLASH MEMORY	METHOD AND APPARATUS FOR CONDUCTING BUS TRANSACTIONS BETWEEN TWO CLOCK INDEPENDENT BUS AGENTS OF A COMPUTER SYSTEM USING A TRANSACTION BY TRANSACTION DETERMINISTIC REQUEST/RESPONSE PROTOCCL AND BURST TRANSFER
12/31/1991	6/24/1992	11/5/1993	5/12/1992	6/28/1991	8/30/1991	12/30/1991	3/1/1995	8/20/1991	7/21/1992	12/23/1991	1/16/1992	12/17/1991	9/14/1995	11/10/1994	8/14/1991	2/6/1995	6/14/1994	8/18/1993	7/19/1994	7/9/1991	2/20/1991	3/24/1993 10/9/1991	5/26/1995
07/816,635	07/904,001	08/148,181	07/881,843	07/722,861	07/752,780	07/815,819	08/396,925	07/747,642	07/917,465	07/813,148	07/821,634	07/809,971	08/528,188	08/337,131	07/744,969	08/384,629	08/140,070	08/108,766	08/277,369	07/727,829	07/658,184	08/036,219 07/773,228	08/429,658
10/19/1993	1/25/1994	12/17/1996	3/1/1994	1/18/1994	12/1/1992	12/28/1993	5/27/1997	8/4/1992	12/14/1993	3/29/1994	10/13/1992	9/14/1993	9/14/1993	9/26/2000	2/22/1994	1/30/1996	9/17/1996		5/2/1995	5/25/1993	7/7/1992	11/15/1994 8/17/1993	11/28/1995
5,255,230	5,282,140	5,586,230	5,291,071	5,279,865	5,168,178	5,274,678	5,633,102	5,136,177	5,270,264	5,298,807	5,155,377	5,244,843	RE38,674	5,124,168	5,289,026	5,488,257	5,556,807		5,412,793	5,212,910	5,128,895	5,365,128 5,237,535	5,471,637
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NMXI-0067.00/US	Otility - ORG	MatterStatus	DELAY LINE LOOP FOR ON-CHIP CLOCK SYNTHESIS WITH ZERO SKEV	V AND 2/26/1993 08/023.673			5.410.263
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NMXI-0068.00/US	Utility - ORG	Issued	PROVIDING HIGH CURRENT CAPABILITY	6/10/1992	07/896,195	4/5/1994	5,301,097
NMXI-0069.00/US	utility - CON	Issued	RANDOM ACCESS MEMORY WITH DIGITAL SIGNALS RUNNING OVER THE SMALL SIGNAL REGION OF THE ARRAY	3/9/1994	08/208,800	10/10/1995	5,457,648
NMXI-0070.00/US	Utility - ORG	Issued	STATIC PLA WITH STATE MACHINE CONTROLLED BY MODEL OF BOTH MINTERM AND AN DUTPUT TIME	4/29/1992	07/875,661	12/28/1993	5,274,281
NMXI-0071.00/US	Utility - ORG	Issued	DETECTING VALID DATA FROM A TWISTED PAIR MEDIUM	7/31/1992	07/923,853	12/13/1994	5,373,508
NMXI-0072.00/U\$	Utility - ORG	Issued	BIAS CIRCUITRY FOR CONTENT ADDRESSABLE MEMORY CELLS OF A FLOATING GATE NONVOLATILE MEMORY	3/31/1992	07/861,093	11/30/1993	5,267,213
			APPARATUS AND METHOD FOR MINIMIZING VERIFY TIME IN A				
NMX1-0073.00/US	Utility - CON	Issued	SEMICONDUCTOR MEMORY BY CONSTANTLY CHARGING N-WELL CAPACITANCE	12/22/1994	08/361,872	4/9/1996	5,506,803
NMXI-0074.00/US	Utility - ORG	Issued	PROTECTED ERASE VOLTAGE DISCHARGE TRANSISTOR IN A NONVOLATILE SEMICONDUCTOR MEMORY	12/3/1992	07/971,074	5/3/1994	5,309,012
NMX1-0075.00/US	Utility - ORG	{ssued	POLYIMIDE PROCESS FOR PROTECTING INTEGRATED CIRCUITS	6/5/1992	07/893,765	9/7/1993	5,242,864
MMX1-0076.00/US	Utility - ORG	Issued	METHOD FOR THE ANISOTROPIC ETCHING OF METAL FILMS IN THE	9/8/1992	07/941,412	9/27/1994	5,350,484
NMXI-0077.00/US	Utility - ORG	Issued	METHOD OF MANAGING DEFECTS IN FLASH DISK MEMORIES	10/30/1992	07/969,749	12/5/1995	5,473,753
NMXI-0077.01/US	Utility - DIV	Issued	METHOD OF MANAGING DEFECTS IN FLASH DISK MEMORIES	8/12/1996	08/700,676	1/11/2000	6,014,755
NMXI-0078.00/US	Utility - ORG	Issued	METHOD OF CONTROLLING CLEAN-UP OF A SOLID STATE MEMORY DISK STORING FLOATING SECTOR DATA	10/30/1992	07/969,763	12/26/1995	5,479,633
NMXI-0079.00/US	Utility - ORG	Issued	POWER MANAGEMENT SYSTEM FOR COMPONENTS USED IN BATTERY POWERED APPLICATIONS	2/19/1993	08/019,617	2/11/1997	5,603,036
NMX1-0080.00/US	Utility - ORG	Issued	PROCESS FOR ETCHING SILICON DIOXIDE LAYER WITHOUT MICRO MASKING EFFECT	6/12/1992	07/897,768	3/22/1994	5,296,094
NMXI-0081.00/US	Utility - CON	Issued	A METHOD AND APPARATUS FOR DYNAMICALLY ALLOCATING ACCESS TIME TO A RESOURCE SHARED BETWEEN A PERIPHERAL BUS AND A HOST BUS BY DYNAMICALLY CONTROLLING THE SIZE OF BURST DATA TRANSFERS ON THE PERIPHERAL BUS	2/14/1995	08/389,078	11/26/1996	5,579,530
NMXI-0082.00/US	Utility - ORG	Issued	GENERALLY-DIAGONAL MAPPING OF ADDRESS SPACE FOR ROW/COLUMN ORGANIZER MEMORIES	9/30/1993	08/130,023	2/6/1996	5,490,264
SO/00'E80D-IXWN	Utility - ORG	Issued	APPARATUS UTILIZING DUAL COMPARE LOGIC FOR SELF CHECKING OF FUNCTIONAL REDUNDANCY CHECK (FRC) LOGIC	1/30/1992	07/830,209	1/4/1994	5,276,690
NMXI-0084.00/US	Utility - DIV	Expired	METHOD FOR FORMING A TITANIUM NITRIDE BARRIER LAYER	4/14/1992	07/869,227	8/3/1993	5,232,871
NMXI-0085.00/US	Utility - ORG	Issued	PROGRAMMABLE SECONDARY CLOCK GENERATOR		07/948,215	10/26/1993	5,256,994
NWX1-0086.00/US	utility - ORG	Issued	PROGRAMMABLE DEDICATED TIMER OPERATED ON A CLOCK INDEPENDENT OF PROCESSOR TIMER		07/897,897	7/25/1995	5,437,021
NMXI-0087.00/US	Utility - ORG	Issued	GATE/SOURCE DISTURB PROTECTION FOR SIXTEEN-BIT FLASH EEPROM MEMORY ARRAYS	6/19/1992	07/901,110	5/31/1994	5,317,535
NWX1-0088.D0/US	Utility - ORG	Issued	HIGH-SPEED BIAS-STABILIZED CURRENT-MIRROR REFERENCING CIRCUIT FOR NON- VOLATILE MEMORIES	6/19/1992	07/901,395	2/22/1994	5,289,412
NWIXI-0089.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR IMPROVING DATA FAILURE RATE TESTING FOR MEMORY ARRAYS	10/30/1992	07/969,465	5/16/1995	5,416,782
NMXI-0090.00/US	Utility - CON	Issued	METHOD FOR WRITING TO A FLASH MEMORY ARRAY DURING ERASE SUSPEND INTERVALS	10/30/1993	08/145,732	8/23/1994	5.341.330

NMXI-0107.02/US	NMXI-0107.01/US	NMXI-0107.00/US	NMXI-0106.00/US	NMXI-0105.00/US	NMXI-0104.00/US	SN/007:E010-1XWN	NMX1-0102.00/US	NWX1-0101.00/US	NMXI-0100.00/US	SN/0016600-1XMN	NMX1-0098.00/US	NMX1-0097.00/US	NMXI-0096.00/US	NMX1-0096,00/US	NMXI-0095.00/US	NMXI-0094.00/US	NMXI-0093.00/US	Sn/0017600-1xwn	NMXI-0091.00/US	MTI-DcktNo
Utility - CON	Utility - CON	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - CON	Utility - ORG	Utility - CON	Utility - ORG	Utility - CON	Utility - CON	Utility - CON	Utility - ORG	Utility - CON	Utility - ORG	Utility - ORG	utility - ORG	MTI-DcktNo MatterType MatterStatus
Issued	ssued	issued	Issued	Issued	Issued	ssued	Issued	issued	Issued	Issued	Issued	Issued	Issued	Issued	Issued	Issued	Issued	Issued	Issued	MatterStatus
APPARATUS FOR CONCURRENT ISSUE OF INSTRUCTIONS IN PARALLEL FROM 8/15/1997 FIRST AND SECOND STORAGE AREA	BRANCH TARGET BUFFER WITH ADDRESS TAGS APPENDED TO STORED INSTRUCTIONS FOR FACILITATING BUFFER FLUSHING	DETECTION	CRITICAL PATH PREDICTION FOR DESIGN OF CIRCUITS	METHOD AND APPARATUS FOR EXECUTION OF OPERATIONS IN A FLASH MEMORY ARRAY	METHOD AND APPARATUS FOR PROVIDING A CONTEXT SWITCH IN RESPONSE TO AN INTERRUPT IN A COMPUTER PROCESS	METHOD FOR LOCATING SECTOR DATA IN A MEMORY DISK BY EXAMINING A PLURALITY OF HEADERS NEAR AN INITIAL POINTER	IMPROVED SYSTEM OF RESOLVING CONFLICTING DATA PROCESSING MEMORY REQUESTS	MULTIPLE LAYER PROGRAMMABLE LAYOUT FOR VERSION IDENTIFICATION	HIGH SPEED OR CIRCUIT CONFIGURATION	METHOD FOR ALLOCATING MEMORY IN A SOLID STATE MEMORY DISK	SYSTEM FOR AUTOMATICALLY COMPRESSING AND DECOMPRESSING DATA FOR SENDER AND RECIEVER PROCESSES UPON DETERMINATION OF A COMMON COMPRESSION/ DECOMPRESSION METHOD UNDERSTOOD BY BOTH SENDER AND RECIEVER PROCESSES	METHOD AND APPARATUS FOR DATA BUFFERING AND QUEUE MANAGEMENT OF DIGITAL MOTION VIDEO SIGNALS	METHOD AND APPARATUS FOR UPDATING FLASH MEMORY RESIDENT FIRMWARE THROUGH A STANDARD DISK DRIVE INTERFACE	METHOD AND APPARATUS FOR UPDATING FLASH MEMORY RESIDENT FIRMWARE THROUGH A STANDARD DISK DRIVE INTERFACE	FAST PARITY GENERATOR USING COMPLEMENT PASS-TRANSISTOR LOGIC	OPTIMIZED LOOKUP TABLE METHOD FOR CONVERTING YUV PIXEL VALUES TO RGB PIXEL VALUES	ELECTRICALLY CONDUCTIVE INTERPOSER AND ARRAY PACKAGE CONCEPT FOR INTERCONNECTING TO A CIRCUIT BOARD	POLISHING PAD CONDITIONING APPARATUS FOR WAFER PLANARIZATION PROCESS	NONVOLATILE MEMORY WITH A PROGRAMMABLE OUTPUT OF SELECTABLE WIDTH AN A METHOD FOR CONTROLLING THE NONVOLATILE MEMORY TO SWITCH BETWEEN DIFFERENT OUT PUT WIDTHS	Title
8/15/1997	1/23/1997	6/30/1993	12/29/1993	6/30/1993	6/30/1993	10/30/1992	9/30/1992	8/3/1994	9/1/1992	6/7/1995	10/15/1992	12/16/1994	11/9/1998	11/9/1998	11/23/1993	4/17/1995	12/2/1992	9/24/1992	3/17/1993	FileDate
08/912,048	08/787,983	08/085,637	08/174,729	08/086,186	08/085,409	07/969,780	07/954,743	08/285,495	07/938,934	08/485,661	07/961,475	08/357,544	09/189,000	09/189,000	08/156,427	08/425,231	07/984,335	07/950,812	08/032,686	SerialNo
6/12/2001	6/26/2001	10/31/1995	8/6/1996	4/15/1995	7/9/1996	11/28/1995	8/16/1994	10/17/1995	12/28/1993	7/9/1996	9/17/1996	9/12/1995	12/28/1999	12/28/1999	3/4/1997	5/7/2002	6/14/1994	6/8/1993	4/2/1996	IssueDate PatentNo
6,247,120	6,250,821	5,463,748	5,544,071	5,509,134	5,535,397	5,471,604	5,339,442	5,459,355	5,274,277	5,535,369	5,557,749	5,450,544	6,009,497	6,009,497	5,608,741	6,384,838	5,321,583	5,216,843		
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NMXI-0126.00/US Utility - ORG	NMXI-0125.02/US Utility - DIV	NMXI-0125.01/US Utility - DIV	NMXI-0125.00/US utility - ORG	NMXI-0124.00/US Utility - CON	NMXI-0123.00/US Utility - ORG	NMX1-0122.00/US Utility - ORG	NMXI-0121.00/US Utility - ORG		NMXI-0119.01/US Utility - CON	NMXI-0119.00/US Utility - ORG	NMX-DII8.00/US Utility - ORG		NMXI-0116.00/US Utility - CON	NMXI-0115.01/US Utility - CON	NMXI-0115.00/US Utility - ORG	NMXI-0114.00/US Utility - ORG	NMXI-0113.00/US Utility - ORG	NMXI-0112.00/US Utility - ORG	NMXI-0111.00/US Utility - CON	NMXI-0110.00/US Utility - ORG	NMXI-0109.00/US Utility - ORG	NMXI-0108.00/US Utility - CON
{ssued	Issued	issued	Issued	issued	issued	(ssued	Issued	Issued	Issued	Issued	Issued	Issued	Issued	issued	Abandoned	Issued	Issued	Issued	Issued	Issued	Issued	Issued
METHOD AND APPARATUS FOR CONTROLLING THE OUTPUT CURRENT PROVIDED BY A CHARGE PUMP CIRCUIT	METHOD AND APPARATUS FOR A TWO PHASE BOOTSTRAP CHARGE PUMP	SYSTEM HAVING MULTIPLE PHASE BOOSTED CHARGE PUMP WITH A PLURALITY OF STAGES	METHOD AND APPARATUS FOR A TWO PHASE BOOTSTRAP CHARGE PUMP	METHOD AND APPARATUS FOR CONTROLLING THE OUTPUT VOLTAGE PROVIDED BY A CHARGE PUMP CIRCUIT	METHOD AND APPARATUS FOR PROGRAMMING AND ERASING FLASH EEPROM MEMORY ARRAYS UTILIZING A CHARGE PUMP CIRCUIT	ADDRESSING MODES FOR A DYNAMIC SINGLE BIT PER CELL TO MULTIPLE BIT PER CELL MEMORY	WRITE VERIFY SCHEMES FOR FLASH MEMORY WITH MULTILEVEL CELLS	SENSING SCHEME FOR FLASH MEMORY WITH MULTILEVEL CELLS	SPECIAL TEST MODES FOR A PAGE BUFFER SHARED RESOURCE IN A MEMORY DEVICE	MEMORY DEVICE	NOVEL SLURRIES FOR CHEMICAL MECHANICAL POLISHING	THIN, HIGH LEAD COUNT PACKAGE	METHOD AND APPARATUS FOR INITIATING COMMUNICATION VIA PAGING A MOBIL COMPUTING DEVICE	METHOD AND APPARATUS FOR ANALYZING DIGITAL VIDEO IMAGES BY MERGING DISPLACEMENT VECTORS	METHOD AND APPARATUS FOR ANALYZING DIGITAL VIDEO IMAGES BY MERGING DISPLACEMENT VECTORS	A ROUND ROBIN SCHEDULER USING A SCHEDULER CARRY OPERATION FOR ARBITRATION	PARALLEL MULTISTAGE SYNCHRONIZATION METHOD AND APPARATUS	METHOD AND CIRCUITRY FOR ENABLING AND PERMANENTLY DISABLING TEST MODE ACCESS IN A FLASH MEMORY DEVICE	APPARATUS AND A METHOD FOR IMPROVING THE PROGRAM AND ERASE PERFORMANCEOF A FLASH EEPROM MEMORY ARRAY	NONVOLATILE MEMORY CARD WITH AN ADDRESS TABLE AND AN ADDRESS TRANSLA TION LOGIC FOR MAPPING OUT DEFECTIVE BLOCKS WITHIN THE MEMORY CARD	METHOD AND APPARATUS FOR PROVIDING PRECISE FAULT TRACING IN A SUPERSCALAR MICROPROCESSOR	NONVOLATILE MEMORY WITH BLOCKS AND CIRCUITRY FOR SELECTIVELY PROTECTING THE BLOCKS FROM MEMORY OPERATIONS
9/10/1993	5/24/1994	5/20/1994	9/10/1993	6/19/1996	9/10/1993	6/2/1994	6/2/1994	4/10/1997	9/25/1996	6/30/1993	11/3/1993	2/1/1993	3 2/9/1996	6/10/1994	12/31/1992	12/30/1992	12/21/1993	12/30/1993	10/27/1994	s 6/30/1993	6/30/1993	12/19/1994
08/119,425	08/248,419	08/246,779	08/119,423	08/491,792	08/119,719	08/252,920	08/252,747	08/827,670	08/719,583	08/085,542	08/146,923	08/011,944	08/599,032	08/258,613	07/999,148	07/998,555	08/171,554	08/175,599	08/329,773	08/086,043	08/085,508	08/358,978
8/15/1995	7/11/1995	6/4/1996	6/6/1995	9/3/1996	5/9/1995	5/7/1996	7/23/1996	5/5/1998	11/10/1998	4/22/1997	8/23/1994	3/1/1994	12/3/1996	10/22/1996		11/22/1994	1/30/1996	6/11/1996	10/3/1995	6/4/1996	5/12/1998	4/30/1996
5,442,586	5,432,469	5,524,266	5,422,586	5,553,030	5,414,669	5,515,317	5,539,690	5,748,546	5,835,927	5,623,620	5,340,370	5,290,735	5,581,594	5,568,569		5,367,679	5,488,639	5,526,311	5,455,800	5,524,231	5,752,013	5,513,136
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Utility - ORG	Utility - CON	Utility - CON	Utility - CON	Utility - ORG	Utility - DIV	Utility - ORG	Utility - CON	Utility - ORG	Utility - ORG Utility - ORG	Utility - CON	Utility - ORG	Utility - CON	Utility - CON	Utility - DIV	utility - CIP	Utility - CON	Utility - ORG	Utility - ORG	Utility - DIV
Issued	ssued	Issued	Issued	Issued	Issued	Issued	Issued	issued	Issued Issued	Issued	issued	Issued	Issued	Issued	issued	ssued	Issued	ssued	Issued
METHOD FOR CONTROLLING DATAFLOW BETWEEN A PLURALITY OF CIRCULAR BUFFERS	APPARATUS FOR ENCODING SIGNALS USING A CONFIGURABLE TRANSFORM CIRCUIT	METHOD AND APPARATUS FOR INITIALIZING A COMPUTER SYSTEM HAVING CENTRAL AND DISTRIBUTED ADDRESS DECODE MEMORY BUS RESOURCES	METHOD AND SYSTEM FOR UPDATING FILES OF A PLURALITY OF STORAGE DEVICES THROUGH PROPAGATION OF FILES OVER A NETWORK	ANALOG CHROMA KEYING ON COLOR DATA	CIRCUITRY AND METHOD FOR PROGRAMMING AND ERASING A NON- VOLATILE SEMICONDUCTOR MEMORY	ARCHITECTURE AND METHOD FOR PROVIDING GUARANTEED ACCESS FOR A RETRYING BUS MASTER TO A DATA TRANSFER BRIDGE CONNECTING TWO BUSES IN A COMPUTER SYSTEM	METHOD AND APPARATUS FOR REDIRECTING REGISTER ACCESS REQUESTS WHEREIN THE REGISTER SET IS SEPARATE FROM A CENTRAL PROCESSING UNIT	BINMOS DRIVER CIRCUIT WITH INTEGRATED VOLTAGE SUPPLY CONVERSION 5/16/1994	CMOS VOLTAGE REFERENCE CIRCUIT CPU CLOCK CONTROL UNIT	METHOD AND APPARATUS FOR PROGRAMMING EMBEDDED MEMORIES OF A VARIETY OF INTEGRATED CIRCUITS USING THE IEEE TEST ACCESS PORT	METHOD OF PIPELINING SEQUENTIAL WRITES IN A FLASH MEMORY	METHOD AND CIRCUITRY FOR STORING DISCRETE AMOUNTS OF CHARGE IN A SINGLE MEMORY ELEMENT	APPARATUS FOR OPTIMIZING OPERATING PARAMETERS OF AN INTEGRATED CIRCUIT PACKAGE HAVING A VOLTAGE REGULATOR MOUNTED 10/31/1996	METHOD OF OPTIMIZING OPERATING PARAMETERS OF AN INTEGRATED CIRCUIT PACKAGE HAVING A VOLTAGE REGULATOR MOUNTED THEREON	ME HOU AND APPARATUS FOR SEQUENTIAL PROGRAMMING OF A FLASH EEPROM MEMORY ARRAY	ZERO STANDBY CURRENT POWER-UP RESET CIRCUIT	TRENCH ISOLATION WITH PLANAR TOPOGRAPHY AND METHOD OF FABRICATION	METHOD AND APPARATUS FOR SEQUENTIAL PROGRAMMING OF THE BITS IN A WORD OF A FLASH EEPROM MEMORY ARRAY	METHOD AND APPARATUS FOR CONTROLLING THE OUTPUT CURRENT PROVIDED BY A CHARGE PUMP CIRCUIT
5/26/1993	10/7/1994	1/31/1995	10/12/1995	11/19/1993	7/29/1993	2/24/1994	9/19/1996	5/16/1994	11/29/1993 12/29/1993	12/19/1995	6/30/1993	8/13/1997	10/31/1996	6/6/1995	6/2/1994	1/24/1996	6/29/1993	9/10/1993	3/14/1995
08/067,858	08/319,779	08/384,472	08/542,039	08/154,955	08/100,508	08/201,817	08/710,572	08/242,790	08/158,418 08/176,944	08/575,178	08/085,969	08/910,866	08/741,526	08/468,972	08/252,850	08/590,726	08/084,795	08/119,520	08/403,633
8/29/1995	6/20/1995	12/31/1996	8/13/1996	3/14/1995	4/30/1996	2/15/2000	9/9/1997	7/25/1995	7/18/1995 8/13/1996	1/21/1997	5/21/1996	4/6/1999	4/15/1997	9/17/1996	7/4/1995	10/29/1996	10/25/1994	7/16/1996	10/3/1995
5,446,839	5,426,462	5,590,289	5,546,539	5,398,075	5,518,333	6,026,455	5,666,556	5,436,585	5,434,534 5,546,568	5,596,734	5,519,847	5,892,710	5,621,245	5,556,811	5,430,674	5,570,050	5,358,891	5,537,350	5,455,794
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NMX1-0168.00/US	NMXI-0167.01/US	NMXI-0167.00/US	NMXI-0166.00/US	NMXI-0165.00/US	NMXI-0164.01/US	NMXI-0164.00/US	NMXI-0163.01/US	NMX1-0163.00/US	NMXI-0162.00/US		NIMXI-0160.00/US	NMXI-0159.01/US	NMX1-0159.00/US	NMXI-0158.00/US	NMXI-0157.00/US	NMXI-0156.00/US	NMXI-0155.00/US	NMXI-0154.00/US		NMXI-0153,00/US	NMXI-0152.00/US	NMXI-0151.00/US		NMXI-0150.00/US	NMXI-0149.00/US	NMXI-0148.00/US	NMXI-0147.00/US	NMXI-0146.00/US
Utility - ORG	Utility - CON	Utility - CON	Utility - ORG	Utility - DRG	Utility - DIV	Utility - CON	Utility - CON	Utility - CON	Utility - ORG	Utility - ORG	Utility - ORG	Utility - CON	Utility - ORG	Utility - CON	Utility - CON	Utility - ORG	Utility - ORG	Otility - ORG		Utility - ORG	Utility - ORG	Utility - CON		Utility - ORG	Utility - CON	Utility - CON	Utility - ORG	Utility - ORG
Issued	Issued	issued	Issued	Issued	ssued	Issued	ssued	Issued	Issued	issued	Issued	issued	issued	lssued	Issued	Issued	Issued	Lapsed		Issued	Issued	kssued		lssued	Issued	Issued	Issued	Issued
IC SOCKET PERMITTING CHECKING CONNECTED STATE BETWEEN IC SOCKET AND PRINTED WIRING BOARD	FAST INTERNAL REFERENCE CELL TRIMMING FOR FLASH EEPROM MEMORY	FAST INTERNAL REFERENCE CELL TRIMMING FOR FLASH EEPROM MEMORY	METHOD FOR PRECONDITIONING A NONVOLATILE MEMORY ARRAY	SYNCHRONOUS ADDRESS LATCHING FOR MEMORY ARRAYS	PIPELINED READ ARCHITECTURE FOR MEMORY	PIPELINED READ ARCHITECTURE FOR MEMORY	SYNCHRONOUS INTERFACE FOR A NONVOLATILE MEMORY	FLASH MEMORY INCLUDING A MODE REGISTER FOR INDICATING SYCHRONOUS OR ASYNCHRONOUS MODE OF OPERATION	BIT MAP ADDRESSING SCHEMES FOR FLASH MEMORY	LOW CAPACITANCE CONTENT-ADDRESSABLE MEMORY CELL	CACHE MEMORY UTILIZING PSEUDO-STATIC FOUR TRANSISTOR MEMORY CELL	LEAD FRAME AND SEMICONDUCTOR DEVICE USING SAME	LEAD FRAME AND SEMICONDUCTOR DEVICE USING SAME	METHOD AND APPARATUS FOR DECODING AND RECODING OF ADDRESSES	ENCODING AND DECODING VIDEO SIGNALS USING ADAPTIVE FILTER SWITCHING CRITERIA	LOW IMPACT COLLISION DETECTION METHOD	DUAL ROW SELECTION USING MULTIPLEXED TRI-LEVEL DECODER	FOR PROCESSING DATA USING LOADABLE MICROCODE RUNNING ON A PROCESSING DATA USING LOADABLE MICROCODE RUNNING ON A	DRIVER, COMPUTER-IMPLEMENTED PROCESS, AND COMPUTER SYSTEM	THROTTLING CIRCUIT FOR A DATA TRANSFER SYSTEM	SINGLE STAGE SENSING APPARATUS FOR A CONTENT ADDRESSABLE	RECEIVING ADDRESS DETERMINES ADDRESS CORRESPONDING TO RESOURCES BASED ON SELECT AND READY SIGNALS BY THAT PARTICULAR BESOURCES	SYSTEM UTILIZING MULTIPLE ADDRESS DECODE RESOURCES AND DECODER	METHOD AND APPARATUS FOR SENSING THE STATE OF FLOATING GATE MEMORY CELLS RY APPLYING A VARIABLE GATE VOLTAGE	METHOD FOR SYNCHRONIZING CLOCKS UPON RESET	METHOD AND APPARATUS FOR INTERFACING MEMORY DEVICES OPERATING AT DIFFERENT SPEEDS TO A COMPUTER SYSTEM BUS	TAB TAPE	AN ELECTRICANL PACKAGE THAT HAS A DIE COUPLED TO A LEAD FRANKE OF A DIELECTRIC TAPE AND A HEAT SINK THAT PROVIDES BOTH AN ELECTRICAL AND A THERMAL PATH BETWEEN THE DIE AND THE LEAD FRAME
11/18/1994	2/9/1996	1/31/1996	6/27/1994	6/3/1994	9/18/1996	12/20/1995	6/14/2000	6/18/1997	6/2/1994	6/15/1994	6/1/1994	12/3/1992	12/9/1991	12/5/1995	6/29/1994	3/23/1994	9/30/1994	11/12/1993		9/15/1994	3/11/1994	10/19/1995		9/29/1994	7/11/1994	2/28/1997	12/14/1993	2/25/1994
08/342,379	08/599,123	08/594,415	08/266,132	08/253,842	08/724,270	08/575,339	09/595,327	08/897,499	08/253,902	08/260,044	08/252,320	07/984,841	07/803,724	08/567,405	08/268,270	08/216,668	08/316,546	08/152,348		08/307.507	08/212,350	08/545,374		08/315,292	08/272,879	08/807,212	08/167,531	08/201,895
9/23/1997	3/4/1997	3/4/1997	7/16/1996	3/5/1996	11/4/1997	1/7/1997	5/13/2003	2/15/2000	3/5/1996	2/5/1996	12/12/1995	3/1/1994	8/17/1993	3/11/1997	1/30/1996	10/31/1995	5/14/1996	4/23/1996		7/18/1905	6/27/1995	9/16/1997		4/16/1996	4/23/1996	2/24/1998	12/5/1995	8/22/1995
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5,669,783	5,608,669	5,608,679	5,537,357	5,497,355	5,684,752	5,592,435	6,564,285	6,025,465	5,497,354	5,490,102	5,475,633	5,291,060	5.237.202	5,611,054	5,488,570	5,463,658	5,517,138	5,511,195	1	s 434 899	3,565	5,668,949		5,508,958	5,510,740	5,721,882	5,473,190	5,444,602

NMXI-0192.00/US Utilit	utilit	NMXI-0190.01/US Utilit	NMXI-0190.00/US Utilit	NMXI-0189.00/US Utilit	NMXI-0188.00/US Utilit	NMXI-0187.01/US Utilit	NMXI-0187.00/US Utilit		NMXI-0185.00/US utilit	NMXI-0183.00/US Utilit NMXI-0184.00/US Utilit	NMXI-0182.00/US Utilit	NMXI-0181.00/US Utilit				NMXI-0176.00/US Utilit	NMXI-0175.00/US Utilit	NMXI-0173.00/US Utilit	NMXI-0172.00/US บนให	48140 Sn/00'1210-1XwN	NMXI-0170.00/US Utility	NMX1-0169.00/US Utilit	MTI-DcktNo Matterfype MatterStatus Max. O168.01/US Utility - DIV Issued
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Issued	Issued	issued	lssued	lssued	Issued	ssued	Issued	Issued	Issued	Issued Issued	Issued	Issued	Issued	tssued	Issued	Issued	Expired	Issued	Issued	Issued	Issued	Issued	MatterStatus Issued
DATA CONSISTENCY ACROSS A BUS TRANSACTIONS THAT IMPOSE	RENDERING AN INTER VERSUS INTRA VIDEO ENCODING DECISION BASED UPON A VERTICAL GRADIENT MEASURE OF TARGET VIDEO FRAMES	HI-JACK HINGED CONNECTION ADAPTER FOR INPUT/OUTPUT CARDS	HI-JACX HINGED CONNECTION ADAPTER FOR INPUT/OUTPUT CARDS	METHOD AND APPARATUS FOR PREVENTING OVER-ERASURE OF FLASH EEPROM MEMORY DEVICES	METHOD AND APPARATUS FOR ROUTING IN REDUCED SWITCH MATRICES TO PROVIDE ONE HUNDRED PERCENT COVERAGE	APPARATUS AND SEMICONDUCTOR COMPONENT FOR ASSURING TEST FLOW COMPLIANCE	TEST FLOW ASSURANCE USING MEMORY IMPRINTING	PROCESS, APPARATUS AND SYSTEM FOR TRANSFORMING SIGNALS USING PSEUDO SIMD PROCESSING	PROCESS, APPARATOS AND SYSTEM FOR SELECTING QUANTIZATION LEVELS FOR ENCODING VIDEO SIGNALS	POLYSILICON POLISH FOR PATTERNING IMPROVEMENT COMPUTER SYSTEM WITH A MEMORY IDENTIFICATION SCHEME	ERROR MANAGEMENT PROCESSES FOR FLASH EEPROM MEMORY ARRAYS	CLOCKING SCHEME FOR LATCHING OF A DOMINO OUTPUT	SYSTEMS	DRAIN BIAS MULTIPLEXING FOR MULTIPLE BIT FLASH CELL I/O CONNECTOR FOR ADD IN PRINTED CIRCUIT CARDS FOR COMPUTER	WITHOUT FILE REALLOCATION BEFORE ERASURE	METHOD AND APPARATUS FOR COOPERATIVE MESSAGING	HEAD DRUM GROUND SYSTEM FOR USE IN A VIDEO CASSETTE RECORDER	METHOD AND APPARATUS FOR PROVIDING AN ULTRA LOW POWER REGULATED NEGATIVE CHARGE PUMP	PROGRAMMABLE HIGH WATERMARK IN STACK FRAME CACHE USING SECOND REGION AS A STORAGE IF FIRST REGION IS FULL AND AN EVENT HAVING A PREDETERMINED MINIMUM PRIORITY	METHOD AND APPARATUS FOR INTERACTIVE BUILT-IN-SELF-TESTING WITH USER- PROGRAMMABLE TEST PATTERNS	PARALLEL FLEXIBLE TRANSMISSION CABLE	DISPLAYING CALLER IDENTIFICATION INFORMATION IN A COMPUTER SYSTEM	
6/10/1007	9/8/1994	9/30/1996	11/1/1994	6/28/1994	7/25/1994	2/2/1996	9/27/1994	4/28/1994	4/28/1994	9/2/1997 5/7/1996	12/27/1994	10/19/1994 1/4/1995	7/15/1994	6/2/1994	3/17/1997	4/18/1995	1/5/1989	3/23/1994	9/28/2994	8/15/1996	6/27/1994	6/29/1994	3/20/1996
	08/302,595	08/724,625	08/333,139	08/267,472	08/279,954	08/595,796	08/312,831	08/234,976	08/234,978	08/944,041 08/643,990	08/364,546	08/325,879 08/368,335	08/275,599	08/252,684	08/819,688	08/423,306	08/294,529	08/217,095	08/314,418	08/698,465	08/266,150	08/268,265	SerralNo 08/618,686
	2/4/1997	7/7/1998	6/10/1997	2/6/1996	11/5/1996	2/18/1997	7/23/1996	9/24/1996	7/2/1996	6/8/1999 2/18/1997	12/12/1995	6/11/1996 9/26/1995	12/19/1995	1/16/1996	9/15/1998	4/15/1997	6/16/1992	7/2/1996	6/3/1997	5/30/2000	9/17/1996	8/13/1996	IssueDate 12/23/1997
	5,600,375	5,775,951	5,637,018	5,490,109	5,572,198	5,603,412	5,538,141	5,559,722	5,532,940	5,911,111 5,604,880	5,475,693	5,525,913 5,453,708	5,477,418	5,485,422	5,809,558	5,621,798	5,123,097	5,532,915	5,636,362	6,070,252	5,557,075	5,546,447	S,699,612
	SU	ß	SU	S	ĽS	Sn	SZU	L)S	เร	SU S	SO	ខ្ល	ß	S	S.	æ	SU	S	S	Sn	SO	SN	Country

Sn	5,671,179	9/23/1997	08/764,666	12/11/1996	LOW POWER PULSE GENERATOR FOR SMART VOLTAGE FLASH EEPROM	Issued	Utility - CON	NMXI-0215.00/US
US	5,594,360	1/14/1997	08/607,521	2/27/1996	COW CURREN REDUCED AREA PROGRAMMING VOLFAGE DETECTOR FOR FLASH MEMORY	Issued	Utility - CON	NMXI-0214.00/US
r s	5,557,330 5,802,213	9/1/1998	08/324,923	10/31/1994	ENCODING VIDEO SIGNALS USING LOCAL QUANTIZATION LEVELS	issued	Utility - ORG	NMXI-0213.00/US
	5,589,829	12/31/1996	08/329,340	10/26/1994	DECODING VARIABLE-LENGTH ENCODED SIGNALS	Issued	Utility - ORG	NMXI-0211.00/US NMXI-0212.00/US
ÇŞ	5,595,526	1/21/1997	08/347,813	11/30/1994	METHOD AND APPARATUS FOR ENDPOINT DETECTION IN A CHEMICAL/MECHANICAL PROCESS FOR POLISHING A SUBSTRATE	issued	Utility - ORG	NMXI-0210.00/US
SI	5,572,465	11/5/1996	08/451,037	5/25/1995	POWER SUPPLY CONFIGURED SENSING SCHEME FOR FLASH EEPROM	Issued	Utility - ORG	NMXI-0209.00/US
	5,781,473	7/14/1998	08/720,944	10/4/1996	VARIABLE STAGE CHARGE PUMP	Issued	Othety - DIV	1816/X3-0208.03/US
	5,732,039	3/24/1998	08/720,876	10/3/1996	VARIABLE STAGE CHARGE PUMP	tssued	Utility - DIV	NMXJ-0208.02/US
Su	5,602,794 5,767,735	2/11/1997 6/16/1998	08/537,233 08/720,943	9/29/1995 10/3/1996	VARIABLE STAGE CHARGE PUMP VARIABLE STAGE CHARGE PUMP	Issued	Utility - DIV	NMXI-0208.01/US
US	5,943,693	8/24/1999	08/412,555	3/29/1995	ALGORITHMIC ARRAY MAPPING TO DECREASE DEFECT SENSITIVITY OF MEMORY DEVICES	Issued	Utility - ORG	NMXI-0207.00/US
us	5,537,069	7/16/1996	08/413,951	3/30/1995	APPARALUS AND METHOD FOR SELECTING A TAP RANGE IN A DIGITAL DELAY LINE	Issued	Utility - ORG	NMXI-0206.00/US
SU	6,012,082	1/4/2000	08/935,742	9/23/1997	CPU-CYCLE STEALING FOR MULTI-TASKING OPERATING SYSTEM	Abandoned	Utility - CON	NMXI-0205.00/US
SU	5,663,918	9/2/1997	08/575,177	12/19/1995	METHOD AND APPARATUS FOR DETECTING AND SELECTING VOLTAGE SUPPLIES FOR FLASH MEMORY	Issued	Utility - CON	NMXI-0204.01/US
US			08/326,702	10/19/1994	SUPPLIES FOR FLASH MEMORY	Abandoned	Utility - ORG	NMXI-0204.00/US
Sn	5,588,728	12/31/1996	08/580,489	12/29/1995	PORTABLE DISK DRIVE CARRIER AND CHASSIS	Issued	Utility - CON	NMXI-0203.01/US
Sn	5,602,902	2/11/1997	08/410,212	3/24/1995	FOUR WIRE MODEM SIGNAL SWITCHING FOR VOICE AND DATA APPLICATIONS	Issued	Utility - ORG	NMXI-0202.00/US
US	5,552,832	9/3/1996	08/329,524	10/26/1994	RUN-LENGTH ENCODING SEQUENCE FOR VIDEO SIGNALS	Issued	Utility - ORG	NMX1-0201.00/US
US	5,754,162	5/19/1998	08/253,374	6/3/1994	HORIZONTALLY SCALING A IMAGE SIGNALS USING SELECTED WEIGHT FACTORS	issued	Utility - CON	NMXI-0200.00/US
เร	5,694,149	12/2/1997	08/253,737	6/3/1994	VERTICALLY SCALING IMAGE SIGNALS USING DIGITAL DIFFERENTIAL ACCUMULATOR PROCESSING	Issued	Utility - CON	NMXI-0199.00/US
, US	5,784,045	7/21/1998	08/254,364	6/3/1994	HORIZONTALLY SCALING IMAGE SIGNALS USING DIGITAL DIFFERENTIAL ACCUMULATOR PROCESSING	Issued	Utility - CON	Sn/00'86t0-iximi
SO	5,867,721	2/2/1999	08/978,998	11/28/1997	SELECTING AN INTEGRATED CIRCUIT FROM DIFFERENT INTEGRATED CIRCUIT ARRAY CONFIGURATIONS	Issued	Utility - CON	NMXI-0197.00/US
US	5,495,453	2/27/1996	08/326,689	10/19/1994	LOW POWER VOLTAGE DETECTOR CIRCUIT INCLUDING A FLASH MEMORY CELL	Issued	Utility - ORG	NMXI-0196.00/US
rs.	5,643,000	7/1/1997	08/691,859	7/31/1996	METHOD AND APPARATUS FOR PROVIDING PROCESSOR FIELD UPGRADABILITY TO A MOTHERBOARD	Issued	Utility - CON	NWXI-0195.00/US
Sn	5,740,385	4/14/1998	08/358,359	12/19/1994	LOW LOAD HOST/PCI BUS BRIDGE	Issued	Utility - ORG	NMX1-0194.00/US
ξ			09/156,272	9/16/1998	PHOTOLITHOGRAPHY METHOD USING COHERENCE DISTANCE CONTROL	Abandoned	Otility - DIV	NMXI-0193.02/US
Sn	5,946,079	8/31/1999	08/962,138	10/31/1997	PHOTOLITHOGRAPHY METHOD USING COHERENCE DISTANCE CONTROL	Issued	Otility - DIV	NMXI-0193.01/US
Sn	5,801,821	9/1/1998	08/497,321	6/30/1995	PHOTOLITHOGRAPHY METHOD USING COHERENCE DISTANCE CONTROL	Issued	utility - ORG	Sn/00'86t0-1XWN
IlssueDate PatentNo Country	PatentNo	IssueDate	SerialNo	FileDate	Title	MatterStatus	MatterType :	MII-DCHNo

Exhibit A - US Applications and Issued Patents MIT-Ocktivo MatterType MatterStatus Effablies Title

NMXI-0239.00/US	NMXI-0238.00/US	NMXI-0237.00/US	NMXI-0236.00/US	NMX1-0235.00/US	NMXI-0234.00/US	NMXI-0233.00/US	NMXI-0232.00/US	NMXI-0231.00/US	NMXI-0230.00/US	NMXI-0229.01/US	NMXI-0229.00/US	NMXI-0228.00/US	NMXI-0227.00/US	NMXI-0226.00/US	NMXI-0225.01/US	NMXi-0225.00/US	NMXI-0224.01/US	NMXI-0223.00/US	SOUTH STATES	SO/00222001XINN	NMX1-0221.01/US	NMXI-0221.00/US	NMXI-0220.00/US	NMXI-0219.00/US	NMX3-0218.00/US	NMXI-0217.00/US	NMXI-0216.00/US
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MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS	METHOD AND APPARATUS FOR CONNECTING A TELEPHONE TO A VOICE CAPABLE MODEM	METHODS AND APPARATUS FOR CORRELATING STUCK-AT FAULT TEST COVERAGE AND CURRENT LEAKAGE FAULT TEST COVERAGE	NEGATIVE VOLTAGE SWITCH ARCHITECTURE FOR A NONVOLATILE MEMORY 7/18/1997	NO HANDLE ZIP SOCKET	HOST-EXECUTED SHARED APPLICATION	LOW POWER PROGRAMMABLE LOGIC ARRAYS	APPARATUS AND METHOD FOR COMPUTATION OF STICKY BIT IN A MULTI- STAGE SHIFTER USED FOR FLOATING POINT ARITHMETIC	MEMORY ARRAYS WITH INTEGRATED BIT LINE VOLTAGE STABIJIZATION CIRCUITRY	METHOU AND APPARATUS FOR IMPROVING POWER UP TIME OF FLASH EEPROM MEMORY ARRAYS	METHOD AND APPARATUS FOR CLEANING A TEST PROBE	METHOD AND APPAKATOS FOR SUKUBBING THE BOND PADS OF AN INTEGRATED CIRCUIT DURING WAFER SORT	INSTRUCTION DEPENDENCY CHAIN INDENTIFIER	INTELLIGENT START FOR MOTION ESTIMATION SEARCH	METHOD AND APPARATUS FOR ELIMINATING VISUAL ARTIFACTS CAUSED BY DIFFUSING ERRORS IN A DECIMATED VIDEO SIGNAL	USE OF RSSI INDICATION FOR IMPROVED DATA TRANSMISSION OVER AMPS NETWORK	USE OF RSSI INDICATION FOR IMPROVED DATA TRANSMISSION OVER AMPS NETWORK	NEGATIVE VOLTAGE SWITCHING CIRCUIT	WIDTH IMPLEMENTATIONS OF VLIW ARCHITECTURES	METHOD AND ARRAPATING FOR ACCOMMISSION DISCRESS THE METHOD AND ARRAPATING MULTIPLE SEMICONDUCTOR DIES	PACKAGE HOUSING MULTIPLE SEMICONDUCTOR DIES	A METHOD TO PREVENT INTRUSIONS INTO ELECTRONIC CIRCUITRY	INTEGRATED CIRCUIT ASSEMBLY PREVENTING INTRUSIONS INTO ELECTRONIC CIRCUITRY	A BRANCH PREDICTOR USING MULTIPLE PREDICTION HEURISTICS AND A HEURISTIC IDENTIFIER IN THE BRANCH INSTRUCTION	MEMORY UNIT WITH BIT LINE DISCHARGER	METHOD AND APPARATUS FOR COMPENSATING FOR SUBTHRESHOLD CURRENT IN A STATIC RAIM	DEBUGGER FOR DEBUGGING TASKS IN AN OPERATING SYSTEM VIRTUAL DEVICE DRIVER	SMART BATTERY PROVIDING BATTERY LIFE AND RECHARGE TIME PREDICTION
12/20/1995	4/9/1997	9/29/1995	7/18/1997	11/13/1995	12/1/1995	8/15/1995	6/28/1996	8/8/1997	6/2/1995	6/13/1997	6/13/1997	9/6/1995	5/12/1997	1/2/1996	3/19/1997	3/31/1995	9/30/1996	9/20/1995	1/21/1997	3/13/1995	1/20/1998	8/25/1997	4/28/1995	5/5/1995	3/14/1995	11/8/1996	12/15/1994
08/575,295	08/831,578	08/536,112	08/895,613	08/558,032	08/565,953	08/515,248	08/672,678	08/907,754	08/458,805	08/874,388	08/874,821	08/524,065	08/854,770	08/582,200	08/820,129	08/414,914	08/723,666	08/530,076	08/781,358	08/402,933	896,800/60	08/917,988	08/431,016	08/437,090	08/403,407	08/748,551	12/15/1994 08/356,905 10/15/1996
10/20/1998	6/16/1998	1/27/1998	11/2/1999	2/11/1997	12/21/1999	11/19/1996	6/23/1998	12/1/1998	6/17/1997	10/5/1999	6/1/1999	1/20/1998	10/6/1998	7/7/1998	6/6/2000	3/18/1997	12/23/1997	12/8/1998	8/11/1998	2/17/1998	8/27/2002	6/2/1998	11/11/1997	5/6/1997	5/14/1996	3/30/1999	10/15/1996
5,824,571	5,768,351	5,712,857	5,978,263	5,602,719	6,006,282	5,575,636	5,771,183	5,844,852	5,640,083	5,961,728	5,909,121	5,710,902	5,818,969	5,777,624	6,073,030	5,612,991	5,701,272	5,848,288	5,793,101	5,719,436	6,438,825	5,761,054	5,687,360	5,627,788	5,517,452	5,889,988	5,565,759 US
SA	S	S	Sn	SO	SS	S	SO	S	S.	SO	SO	S.	ç	SU	SO	S	SS	SJ	S	SU	Sn	SN	S	US	S	S	su

5,778,097 6 080 022		000 00 100 11	1				MANUSE AND THE
,778,097	-	08/672.643	6/28/1996	MULTIVOLTAGE KEYED ELECTRICAL CONNECTOR	Issued	Utility - ORG	NMXI-0263.00/US
		08/610,582	3/7/1996	TABLE-DRIVEN BI-DIRECTIONAL MOTION ESTIMATION USING SCRATCH AREA AND OFFSET VALUES	Issued	Utility - ORG	NMX)-0262.00/U\$
5,706,218	1/6/1998	08/648,553	S/15/1996	RANDOM NUMBER GENERATOR	Issued	Utility - ORG	NMXF-0261.00/US
5,721,927	2/24/1998 5	08/689,357	8/7/1996	METHOU FOR VERIFYING CONTIGUITY OF A BINARY TRANSLATED BLOCK OF INSTRUCTIONS BY ATTACHING A COMPARE AND/OR BRANCH INSTRUCTION TO DECISE SCORE BLOCK OF INSTRUCTIONS	Issued	Utility - ORG	NMXI-0260.00/US
5,612,916	3/18/1997	08/707,925	9/10/1996	MEMORY BYPASS MODE	Issued	Utility - DIV	NMXI-0259.01/US
5,592,425	1/7/1997 5	08/576,705	12/20/1995	METHOD AND APPARATUS FOR TESTING A MEMORY WHERE DATA IS PASSED THROUGH THE MEMORY FOR COMPARISON WITH DATE READ	Issued	Utility - ORG	NMX1-0259.00/US
5,757,217	5/26/1998	08/672,412	6/28/1996	SLOW SPEED DRIVER WITH CONTROLLED SLEW RATE	Issued	Utility - ORG	NMXI-0258.00/US
5,773,895	6/30/1998	08/627,058	4/3/1995	ANCHOR PROVISIONS TO PREVENT MOLD DELAMINATION IN AN OVERMOLDED PLASTIC ARRAY PACKAGE	issued	Utility - ORG	NMXI-0257.00/US
5,736,777	4/7/1998 5	08/581,436	12/29/1995	METHOD AND APPARATUS FOR FAST SELF-DESTRUCTION OF A CMOS INTEGRATED CIRCUIT	Issued	Utility - ORG	NMXI-0256.00/US
5,784,263	7/21/1998 5	08/584,330	1/8/1996	CONNECTOR WITH ATTACHABLE DAUGHTER CARD RETENTION SYSTEM	fssued	Otility - ORG	NMXI-0255.00/US
5,917,239	6/29/1999 5	08/835,228	4/7/1997	RECESSED OR RAISED CHARACTERS ON A CERAMIC LID	Issued	Otility - CON	NMXI-0254,00/US
5,594,686	1/14/1997 5	08/578,175	12/29/1995	METHOD AND APPARATUS FOR PROTECTING DATA STORED IN FLASH MEMORY	Issued	Utility - ORG	NMXI-0253.00/US
5,787,454	7/28/1998 5	08/578,964	12/27/1995	REORDER BUFFER WITH INERLEAVING MECHANISM FOR ACCESSING A MULTI-PORTER CIRCULAR MEMORY ARRAY	Issued	Utility - ORG	NMXI-0252.00/US
5,904,560	5/18/1999 5	08/623,672	3/29/1996	CLEANING STEP WHICH IMPROVES ELECTROMIGRATION PERFORMANCE OF INTERLAYER CONNECTION IN INTEGRATED CIRCUITS	issued	Utility - ORG	NMXI-0250.00/US
5,625,303	4/29/1997 5	08/534,485	9/27/1995	MULTIPLEXER HAVING A PLURALITY OF INTERNAL DATA PATHS THAT OPERATE AT DIFFERENT SPEEDS	ssued	Utility - ORG	NMXI-0249.00/US
5,794,007	8/11/1998 5	08/553,017	11/3/1995	SYSTEM AND METHOD FOR PROGRAMMING PROGRAMMABLE ELECTRONIC COMPONENTS USING BOARD-LEVEL AUTOMATED TEST EQUIPMENT	ssued	utility - ORG	NMX1-0248.00/US
5,735,701	4/7/1998 5	08/570,239	12/11/1995	LOCKING POWER CABLE INTERFACE GUARD	Issued	Utility - ORG	NMXI-0247.00/US
6,449,735	9/10/2002 6	08/672,983	7/1/1996	METHOD AND APPARATUS FOR PROVIDING IMPROVED DIAGNOSTIC FUNCTIONS IN A COMPUTER SYSTEM	Issued	Utility - ORG	NMXI-0245.00/US
5,764,548		08/537,260	9/29/1995	FAST FLOATING-POINT TO INTEGER CONVERSION	Issued	Utility - ORG	NMXI-0245-00/US
5,691,883	11/25/1997 5	08/578,984	12/27/1995	MULTIPLE INTAKE DUCT MICROPROCESSOR COOLING SYSTEM	Issued	Utility - ORG	NMX1-0244,00/US
5,712,578	1/27/1998 5	08/579,095	12/27/1995	PROTOCOL PLA ARCHITECTURE HAVING IMPROVED CLOCK SIGNAL TO OUTPUT TIMING USING A TYPE-I DOMINO AND PLANE	!ssued	Utility - ORG	NMX1-0243.00/US
5,918,025	6/29/1999 5	08/775,807	12/31/1996	METHOD AND APPARATUS FOR CONVERTING A FIVE WIRE ARBITRATION/BUFFER MANAGEMENT PROTOCOL INTO A TWO WIRE	Issued	Utility - ORG	NMXI-0242.00/US
5,640,519	6/17/1997 5	08/528,914	9/15/1995	METHOD AND APPARATUS TO IMPROVE LATENCY EXPERIENCED BY AN AGENT UNDER A ROUND ROBIN ARBITRATION SCHEME	Issued	utility - ORG	NMXI-0241.00/US
5,602,502	2/11/1997 5	08/536,853	9/29/1995	PREDETERMINED LEVEL	Issued	Utility - ORG	NMXI-0240.00/US

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NMXI-0265.00/US	Utility - ORG	Issued	ROLLING THE TEMPERATURE OF	6/18/1996	08/665,405			ß
NMXI-0266.00/US	Utility - ORG	issued	AN INTEGRATED CIRCUIT UNDER TEST	6/18/1996	08/668,045	12/8/1998	5,847,366	S
NMXI-0267.00/US	Utility - ORG	ssued	CLADOING OF AN INTERCONNECT FOR IMPROVED ELECTROMIGRATION PERFORMANCE	6/28/1996	08/673,277	6/1/1999	5,909,635	US I T
NMXI-0268.00/US	Utility - CON	Issued	MEMORY TEST MODE FOR WORLDUNE RESISTIVE DEFECTS	2/23/1998	09/030,522	7/4/2000	6,085,341	S EN
NMXI-0269.00/US	Utility - ORG	Issued	A SYSTEM AND METHOD FOR ENSURING INTEGRITY OF AUDIO METHOD AND APPARATUS FOR TRACKING ERASE CYCLES UTILIZING ACTIVE	10/25/1996	08/738,086	8/31/1999	5,946,396	⊱ ATF
NMX1-0270.00/US	Utility - ORG	Issued	AND INACTIVE WEAR BAR BLOCKS HAVING FIRST AND SECOND COUNT	12/20/1996	08/770,958	10/5/1999	5,963,970	us D
NMXI-0271.00/US	Utility - ORG	issued	COMPARATOR UTILIZING REDUNDANCY	7/17/1996	08/682,108	3/9/1999	5,881,076	S
NMXI-0272.00/US	Utility - ORG	Issued	METHOD FOR EXTRACTING A RESISTOR NETWORK FROM AN INTEGRATED CIRCUIT POLYGON LAYOUT	7/12/1996	08/679,197	8/24/1999	5,943,487	S
NMXI-0273.00/US	Utility - ORG	Abandoned	LEAKAGE TRACKING DEVICE SAMPLE FOR IDDQ MEASUREMENT AND DEFECT RESOLUTION	9/27/1996	08/721,685	3/30/1999	5,889,409	æ
NMXI-0274.00/US	Utility - ORG	Abandoned	DELTA IDDQ TESTING	6/27/1996	08/670,545	3/30/1999	5,889,408	υs
NMXI-0274.01/US	Utility - DIV	Issued	METHOD TO PERFORM IDDO, TESTING IN THE PRESENCE OF HIGH BACKGROUND LEAKAGE CURRENT	9/29/1998	09/162,912	5/29/2001	6,239,605	S
NMX)-0274,02/US	Utility - DIV	Issued	METHOD TO PERFROM IODQ TESTING IN THE PRESENCE OF HIGH BACKGROUND LEAKAGE CURRENT	9/29/1998	09/163,592	5/29/2001	6,239,606	SO
NMXI-0275.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR DETECTING THE VOLTAGE ON THE VCC PIN	9/30/1996	08/723,270	10/13/1998	5,822,246	S
NMX1-0276.00/US	Utility - ORG	Issued	METHOD FOR IMPROVED DATA RETENTION IN A NONVOLATILE WRITEABLE MEMORY BY SENSING AND REPROGRAMMING CELL VOLTAGE LEVELS	12/20/1996	08/771,445	11/10/1998	5,835,413	SS
NMXI-0277.00/US	Utility - ORG	Issued	A SENSE AMPLIFIER COMPRISING A PREAMPLIFIER AND A DIFFERENTIAL INPUT LATCH FOR FLASH MEMORIES	9/16/1997	08/931,247	10/26/1999	5,973,957	S
NMXI-0278.00/US	Utility - ORG	Issued	NEURAL-FLASH ANALOG-TO-DIGITAL CONVERTER USING WEIGHTED CURRENT SIMILATION	2/26/1999	09/259,650	3/6/2001	6,198,421	S
NMXI-0279.00/US	Utility - ORG	Issued	DATA SPECULATABLE PROCESSOR HAVING REPLAY ARCHITECTURE APPARATUS FOR PROTECTING GATE ELECTRODES OF TARGET TRANSISITORS	11/13/1996	08/746,547	10/12/1999	5,966,544	SU
NMXI-0280.00/US	Utility - ORG	Issued	IN THE GATE ARRAY FROM GATE CHARGING BY EMPLOYING FREE TRANSISTORS IN THE GATE ARRAY METHOD AND APPARATUS FOR PROTECTING GATE ELECTRODES OF TARGET	6/28/1996	08/672,411	8/11/1998	5,793,069	Ř
NMXI-0280.01/US	Utility - DIV	Issued	TRANSISTORS IN THE GATE ARRAY FROM GATE CHARGING BY EMPLOYING FREE TRANSISTORS IN THE GATE ARRAY	2/17/1998	09/024,830	9/5/2000	6,113,648	SS
NMXI-0281.00/US	utility - ORG	Issued	LATCHING MECHANISM FOR PULSED DOMINO LOGIC WITH INHERENT RACE MARGIN AND TIME BORROWING	8/12/1996	08/700,613	8/18/1998	5,796,282	ß
NMXI-0282.00/US	Utility - ORG	Issued	METHOD AND APPARTUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN	12/31/1996	08/775,796	4/27/1999	5,898,610	ß
NMXI-0283.00/US	Utility - ORG	Issued	ORGANIC SUBSTRATE (PCB) SUP PLANE ""STRESS DEFLECTOR"" FOR FLIP CHIP DEVICES	9/26/1996	08/721,268	9/8/1998	5,804,771	ᅜ
NMXI-0283.01/US	Utility - DIV	Issued	ORGANIC SUBSTRATE (PCB) SUP PLANE ""STRESS DEFLECTOR"" FOR FLIP CHIP DEVICES	2/27/1998	09/032,628	1/16/2001	6,173,489	દ્ધ
NMX1-0284.00/US	Otility - ORG	Issued	PROCESS FOR FORMING AN INTEGRATED STRUCTURE COMPRISING A SELF- ALIGNED VIA/CONTACT AND INTERCONNECT	10/31/1996	08/739,879	3/30/1999	5,888,897	SO
NMXI-0285.00/US	Utility - ORG	Issued	CIRCUIT BOARD RETENTION SYSTEM	8/16/1996	08/689,698	6/24/1997	5,642,263	S

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NMXI-0330.00/US L	NMXI-0329.00/US		NMXI-0327.00/US L	NMXI-0326.00/US U	NMXI-0325.00/US U	NMXI-0324.01/US U	NMXI-0324.00/US U	NMXI-0322.00/US U NMXI-0323.00/US U		NMXI-0320.00/US U	NMXI-0319.00/U\$ U	NMXI-0318.00/US U	NMXI-0317.00/US U	NMXI-0316.00/US U	NWX1-0315.00/US U	NMX1-0314.00/US U	NWXI-0313.00/US U	NMXI-0312.00/US U	NMXI-0311.00/US บ	NMXI-0310.00/US U	NWXI-0309.D0/US U	NWXI-0308.01/US U
Utility - CON	Utility - CIP	Utility - ORG	Utility - ORG	Utility - ORG	Utility - GRG	Utility - DIV	Utility - ORG	Utility - ORG Utility - ORG	Utility - ORG	Utility - ORG	utility - ORG	Utility - ORG	utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	utility - ORG	Utility - DIV
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BASED ON OPERATING ENVIRONMENT	STATIC, HIGH-SENSITIVITY, FUSE-BASED STORAGE CELL	FAST BICMOS ACTIVE-PIXEL SENSOR CELL WITH FAST NPN EMITTER-FOLLOWER READOUT	METHOD AND APPARATUS FOR USING FUNCTION CONTEXT TO IMPROVE BRANCH PREDICTION	METHOD AND APPARATUS FOR FINDING LOOP_LEVER PARALLELISM IN A POINTER BASED APPLICATION	MULTI-LAYER CODER/DECODER FOR PRODUCING QUANTIZATION ERROR SIGNAL SAMPLES	UNIQUE CHEMICAL MECHANICAL PLANARIZATION APPROACH WHICH UTILIZES MAGNETIC SLURRY FOR POLISH AND MAGNETIC FIELDS FOR PROCESS CONTROL	UTILIZES MAGNETIC SLURRY FOR POLISH AND MAGNETIC FIELDS FOR PROCESS CONTROL	ANTI-THEFT MECHANISM FOR MOBILE COMPUTERS PERIPHERAL DEVICE PREVENTING POST-SCAN MODIFICATION UNIQUE CHEMICAL MECHANICAL PLANARIZATION APPROACH WHICH	EMPLOYING A LOOK-UP SERVICE AND A CALLEE CONNECTION SERVICE TO ESTABLISH A NETWORK PHONE CALL BETWEEN A CALLER AND A CALLEE	BRANCH INSTRUCTION HANDLING IN A SELF-TIMED MARKING SYSTEM	APPARATUS AND METHOD FOR SELF-TIMED MARKING OF VARIABLE LENGTH INSTRUCTIONS HAVING LENGTH-AFFECTING PREFIX BYTES	EFFICIENT SELF-TIMED MARKING OF LENGTHY VARIABLE LENGTH INSTRUCTIONS	METHOD AND APPARATUS FOR CONNECTING EXPANSION BUSES TO A PERIPHERAL COMPONENT INTERCONNECT BUS	MERGING DUMMY STRUCTURE REPRESENTATIONS FOR IMPROVED DISTRIBUTION OF ARTIFACTS IN A SEMICONDUCTOR LAYER	COMPUTER PACKAGE WITH A POLYGONAL SHAPED MOTHERBOARD	VARIABLE LOADING APPARATUS FOR OUTPUT LOADING OF INTEGRATED CIRCUITS	SELF-CONFIGURING INTERFACE ARCHITECTURE ON FLASH MEMORIES	SELF-CONFIGURING OUTPUT BUFFER ON FLASH MEMORIES	SELF-CONTAINING INPUT BUFFER ON FLASH MEMORIES	DYNAMICALLY CONTROLLING THE NUMBER OF BOUNDARY-SCAN CELLS IN A BOUNDARY-SCAN PATH	METHOD AND APPARATUS FOR DELIVERING DATA	A METHOD OF FORMING A FIDUCIAL FOR ALIGNING AN INTEGRATED CIRCUIT DIE
5/7/1999	12/31/1997	1/6/1998	9/22/1997	1/13/1998	11/5/1997	7/13/1999	12/31/1997	9/9/1997 3/31/1997	3/31/1997	12/23/1997	12/23/1997	12/23/1997	1/2/1997	9/30/1997	9/19/1997	6/13/1997	4/11/1997	4/11/1997	4/11/1997	5/22/1997	12/31/1996	11/26/1997
09/307,324	09/002,174	09/003,477	08/934,964	09/006,276	08/965,637	09/352,634	09/001,509	08/927,370 08/829,594	08/829,608	08/996,756	08/997,462	08/997,461	08/778,192	08/941,599	08/933,405	08/874,496	08/834,026	08/834,032	08/834,029	08/862,006	08/801,508	08/978,535
7/29/2003	9/28/1999	10/2/2001	10/12/1999	8/7/2001	9/21/2004	9/4/2001	7/4/2000	11/21/2000 7/20/1999	2/20/2001	8/3/1999	9/7/1999	8/24/1999	9/14/1999	6/27/2000	5/11/1999	5/4/1999	8/3/1999	5/11/1999	9/30/2003	6/16/1998	11/16/1999	12/14/1999
6,601,179	5,959,445	6,297,492	5,964,870	6,272,676	6,795,501	6,284,091	6,083,839	6,151,678 5,926,550	6,192,044	5,931,944	5,948,096	5,941,982	5,951,667	6,081,272	5,903,432	5,900,770	5,933,026	5,903,500	6,628,552	5,768,289	5,987,504	6,001,703
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REFERENCE CIRCUIT	PORTABLE DIGITAL PHOTOGRAPH VIEWER PORTABLE DIGITAL PHOTOGRAPH VIEWER POLITAD VIDEN VEHE DECENTATION FOR THE PROPERTY OF THE P	OVERLAY COUNTER FOR ACCELERATED GRAPHICS PORT	METHOD AND APPARATUS FOR REDUCING THE POWER CONSUMPTION OF A VOLTAGE REGILLATOR	A CHARGE PUMP AVOIDING GAIN DEGRADATION OUE TO THE BODY EFFECT 1/12/1998	EFFICIENT COMPANDING ALGORITHM SUITABLE FOR COLOR IMAGING	METHOD AND APPARATUS FOR CHANGING THE FONT USED TO DISPLAY TEXT ENTERED INTO AN INFORMATION INPUT FIELD	QUAD FLAT PACK INTEGRATED CIRCUIT PACKAGE	QUAD FLAT PACK INTEGRATED CIRCUIT PACKAGE	QUAD FLAT PACK INTEGRATED CIRCUIT PACKAGE AD FLAT PACK INTEGRATED CIRCUIT PACKAGE	CIRCUIT AND METHOD FOR CONFIGURING AND REGISTERING A CRYPTOGRAPHIC DEVICE	SYSTEM FOR SPECULATIVE BRANCH TARGET PREDICITION HAVING A DYNAMIC PREDICITION HISTORY BUFFER AND A STATIC PREDICTION HISTORY BUFFER	LOW VOLTAGE PROGRAMMABLE COMPLEMENTARY INPUT STAGE SENSE AMPLIFIER	SUPPLYING STANDBY VOLTAGE TO MEMORY AND WAKEUP CIRCUITRY TO WAKE A COMPUTER FROM A LOW POWER MODE	SUPPLYING STANDBY VOLTAGE TO MEMORY AND WAKEUP CIRCUITRY TO WAKE A COMPUTER FROM A LOW POWER MODE:	COMPUTER SYSTEM INCLUDING RIGHT ANGLE PROCESSOR AND ADD-ON CARD CONNECTORS	ADVANCED NEAR IDEAL MIXER	VARIABLE DELAY CELL WITH A SELF-BIASING LOAD	METHOD AND APPARATUS EMPLOYING A DYNAMIC ENCRYPTION	THERMAL INTERFACE ATTACH MECHANISM FOR ELECTRICAL PACKAGES	TECHNIQUE FOR PERFORMING DMA INCLUDING ARBITRATION BETWEEN A CHAINED LOW PRIORITY DMA AND HIGH PRIORITY DMA OCCURING BETWEEN TWO LINKS IN THE CHAINED LOW PRIORITY	PLGA TEST HANDLER THROW MEASURING UNIT	METHOD AND APPARATUS FOR AUTOMATICALLY DETERMINING AND DYNAMICALLY UPDATING USER PREFERENCES IN AN ENTERTAINMENT CYCTEM.	METHOD AND APPARATUS FOR SEARCHING A MASS STORAGE MEDIUM
3/16/1998	1/7/1998 12/19/2002	1/30/1998	9/3/1998	1/12/1998	1/13/2000	12/30/1997	10/26/1998	10/26/1998	12/4/1997 10/26/1998	9/30/1997	11/26/1997	1/14/1998	1/15/1999	12/30/1997	9/24/1997	12/4/1998	1/14/1998	9/29/1997	12/15/1997	2/10/1998	2/9/1998	5/30/1997	11/6/1997
09/040,523	09/004,052 10/324,692	09/016,790	09/148,033	09/005,560	09/482,551	09/000,712	09/179,014	09/179,125	08/984,766 09/179.016	08/938,491	08/979,579	09/006,770	09/231,725	09/001,102	08/936,857	09/205,992	09/006.649	08/937,874	08/990,856	09/021,688	09/020,661	08/866,707	08/965,592
8/31/1999	3 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9	3/6/2001	10/23/2001	5/15/2001	2/26/2002		7/3/2001	11/2/1999	2/2/1999 9/7/1999	10/19/1999	11/2/1999	4/18/2000	6/13/2000	10/23/2001	11/2/1999	2/6/2001	11/30/1999	11/16/1999	10/12/1999	8/22/2000	9/7/1999	8/31/1999	College (1) and the control of the control of the college of the c
5,946,258		6.199.149	6,307,355	6,232,826	6,351,555		6,255,135	5,978,724	5,867,367 5,949,651	5,970,147	5,978,909	6,051,999	6,075,751	6,308,278	5,978,873	6,184,739	626 766 5	5,987,572	5,965,937	6,108,743	5,948,960	5,945,988	08/965,592 US
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NMXI-0354.00/US	Utility - ORG	Ŀ	HOT SHEAR APPARATUS AND METHOD FOR REMOVING A SEMICONDUCTOR CHIP FROM AN EXISTING PACKAGE	12/30/1997	09/001,021	2/29/2000	6,029,730	SU
NMXI-0355.00/US	Utility - ORG	Issued	ELECTRONIC CARTRIDGE WHICH ALLOWS DIFFERENTIAL THERMAL EXPANSION BETWEEN COMPONENTS OF THE CARTRIDGE	2/17/1998	09/024,860	4/13/1999	5,894,408	25
NMXI-0356.00/US	Utility - ORG	issued	INTERGRATED PROCESSOR SUBSTRATE PROTECTIVE ENCLOSURE FOR USE IN DESKTOP PC SYSTEMS	2/17/1998	09/024,921	7/4/2000	6,084,773	55
NMXI-0357.00/US	Utility - ORG	penss	RETENTION MECHANISM ASSEMBLY FOR PROCESSOR CARTRIDGES WITH CAPTURED SCREW FASTENERS	6/16/1998	09/098,086	3/27/2001	6,208,527	Si
NMX1-0358.00/US	Utility - ORG	Issued	FAN DUCT MODULE	1/29/1998	09/015,423	10/10/2000	6,130,819	SO
NMXI-0359.00/US	Utility - ORG	Issued	REMOTELY MONITORING EXECUTION OF A PROGRAM	4/29/1998	09/069,527	7/25/2000	6,094,530	SI
NMX1-0360.00/US	Utility - ORG	Issued	CHASSIS FOR ELECTRONIC COMPONENTS	5/28/1998	09/085,779	10/12/1999	5,964,513	S.
NMXI-0360.01/US	Utility - DIV	issued	CHASSIS FOR ELECTRONIC COMPONENTS	6/10/1999	09/329,425	2/15/2000	6,024,426	U\$
NMXI-0360.02/US	Utility - DIV	Issued	CHASSIS FOR ELECTRONIC COMPONENTS	6/10/1999	09/329,426	2/22/2000	6,027,191	S
NMXI-0361.00/US	Utility - ORG	Issued	CODEC WITH GENETIC ADAPTATION	4/30/1998	09/071,049	3/25/2003	6,539,366	SI
			SELECTING DESIGN POINTS ON PARAMETER FUNCTIONS HAVING FIRST SUM			•		i
NMXI-0362.00/US	Utility - ORG	Issued	OF CONSTRAINT SET AND SECOND SUM OF OPTIMIZING SET TO IMPROVE SECOND SUM WITHIN DESIGN CONSTRAINTS	9/4/1998	09/148,392	3/18/2008	7,346,479	S
NMX1-0363.00/US	Utility - ORG	ssued	METHOD AND APPARATUS FOR UPDATING A TIMER FROM MULTIPLE TIMING DOMAINS	6/26/1998	09/105,479	2/1/2000	6,021,505	S
NMXI-0364.00/US	Utility - ORG	ssued	AUTOMATIC TRANSFER OF IMAGE INFORMATION BETWEEN IMAGING DEVICE AND HOST SYSTEM	1/7/1998	09/003,732	7/3/2001	6,256,059	SU
NMXI-0365.00/US	Utility - ORG	Issued	SILICON WAFER TESTING RIG AND A METHOD FOR TESTING A SILICON WAFER WHEREIN THE SILICON WAFER IS BENT INTO A DOME SHAPE	5/29/1998	09/087,753	8/8/2000	6,100,709	S
NWXI-0366.00/US	Utility - ORG	ssued	THIN OXIDES OF SILICON	7/29/1998	09/124,562	4/24/2001	6,221,789	: S
NMX1-0367.00/US	Utility - ORG	Issued	COMPUTER PROCESSOR WITH A REPLAY SYSTEM	6/30/1998	09/106,857	12/19/2000	5,163,838	5
NMX1-0368.00/US	Utility - ORG	Issued	INTERPACE TO A MEMORY SYSTEM FOR A PROCESSOR HAVING A REPLAY SYSTEM	12/30/1999	09/475,029	12/16/2003	6,665,792	S
NMX1-0368.01/US	Utility - CON	Issued	INTERFACE TO A MEMORY SYSTEM FOR A PROCESSOR HAVING A REPLAY SYSTEM	10/23/2003	10/690,634	8/8/2006	7,089,409	S
NMXH-0369.00/US	Utility - ORG	ssued	HIGH SPEED ANALOG COMPENSATED INPUT BUFFER	1/23/1998	09/012,201	11/7/2000	6,144,218	ĸ
SO/00/03/03/NN	Utility - ORG	Abandoned	POSITION SENSITIVE DISPLAY CONTROLLER	5/14/1998	09/079,065			ន
NWIXI-0372.00/US	Utility - ORG	Issued	PARACLEL ACCESS TESTING OF A INFIMORY ARRAY BUS PROTOCOL FOR ATOMIC TRANSACTIONS	5/28/1998 1/26/1998	09/086,330	11/9/1999	5,982,684 5,987,552	도 5
NMXI-0373.00/US	Utility - CON	Abandoned	METHOD AND SYSTEM FOR BRANCH TARGET PREDICTION USING PATH INFORMATION	3/31/2003	10/404,384			St
NMXI-0374.00/US	Utility - ORG	Issued	UP-SAMPLING DECIMATED COLOR PLANE DATA	12/7/1999	09/457,058	6/17/2003	6,580,837	S
NMX1-0375.00/US	Utility - CIP	(ssued	VOLTAGE BLOCKING METHOD AND APPARATUS FOR A CHARGE PUMP WITH DIODE CONNECTED PULL-UP AND PULL-DOWN ON BOOT NODES	12/30/1999	09/475,459	4/16/2002	6,373,324	S
NMXI-0376.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE	5/7/1998	09/074,556	2/20/2001	6,192,079	ᅜ
NMXI-0378.00/US	Utility - CON	Issued	APPARATUS AND METHOD FOR IMAGER DEVICE COLOR CALIBRATION USING SPECTRAL LIGHT SOURCE	11/18/2005	11/283,375	6/3/2008	7,382,918	SU
NMX1-0379.00/US	Utility - ORG	Issued	MEMORY CARD WHICH IS THERMALLY CONTROLLED	1/5/1999	09/225,927	12/19/2000	6,163,474	ŝ
Sn/00:08ED-IXWN	Utility - CON	Abandoned	INSTRUCTION PIPELINE INSTRUCTION QUEUE FOR INSTRUCTION REISSUE	6/19/2003	10/601,172			S
NMXI-0381.00/US	utility - ORG	Issued	CART FOR TRANSFERRING OBJECTS	6/26/1998	09/105,191	8/15/2000	6,102,647	SU

NMXI-0408.00/US Utility - ORG	NMX1-0407.00/US Utility - ORG	พพระ-0406.00/ 05 อยู่แหู - มีท		NMXI-0404.00/US Utility - ORG	NMXI-0403.00/US Utility - ORG	NMXI-0402.00/US Utility - ORG	NMXI-0401.00/US Utility - ORG				NMXI-0399.00/US Dtility - ORG		NMXI-0397.00/US Utility - ORG	NMX1-0396.00/US Utility - ORG	NMXI-0395.00/US Utility - ORG	NMXI-0394.00/US Utility - ORG	NMXI-0393.00/US Utility - ORG	NMXI-0392.00/US Utility - CON		NMXI-0391.01/US Utility - CON		•	NMXI-0390.00/US Utility - CIP	NWXI-0389.01/US Utility - DIV	NMXI-0389.00/US Utility - ORG	NMXI-0388.00/US Utility - ORG	NMXI-0387.00/US Utility - ORG	NMXI-0386.00/US Utility - ORG	NMX1-0385.00/US Utility - DIV	NMXI-0384.00/US Utility - ORG	NMXI-0383.00/US Utility - ORG	NMXI-0382.00/US Utility - ORG
RG Issued	RG Issued	V Issued		RG Abandoned	RG Issued	RG Issued	RG Issued	NO ISSUED			RG issued		RG Issued	RG Issued	RG issued	RG Issued	RG Issued	DN Issued	ON Issued	ON Issued		4	P Issued	V Issued	RG Issued	RG issued	%G (ssued	₹G issued	V Abandoned	रिG Issued	RG Issued	Utility - ORG Issued
METHOD FOR DIRESCTING THE ROUTE OF A CELL TRANSMITTING A NETWORK	COMPENSATION	CARRIER FOR DISK DRIVE HOT SWAPPING	CORRECTING NON-UNIFORMITY IN DISPLAYS	CALIBRATING PROJECTION DISPLAYS	MAGNETIC INK ENCODING PEN	HOT PLUG STRUCTURE AND METHOD FOR ENGAGING/DISENGAGING PCI COMPLIANT CARD	QUANTUM MAGNETIC MEMORY	OPERATE ON ANOTHER OPERATING SYSTEM	ENABLING SOFTWARE DESIGNED FOR ONE OPERATING SYSTEM TO	SMALL OUTLINE MEMORY MODULE	COLOR INTERPOLATION FOR A FOUR COLOR MOSAIC PATTERN SMALL OUTLINE RAMBUS IN-LINE MEMORY MODULE	METHOD AND APPARATUS FOR SETTING THE COMMON MODE LEVEL OF A DIFFERENTIAL OUTPUT	SETTING THE COMMON MODE LEVEL OF A DIFFERENTIAL CHARGE PUMP OUTPUT	METHOD AND APPARATUS FOR THE GENERATION OF STATISTICALLY RANDOM NUMBERS	HOT PLUG CARTRIDGE ASSEMBLY	CYCLIC MULTICASTING OR ASYNCHRONOUS BROADCASTING OF COMPUTER FILES	ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK	SYSTEM FOR DATA DEPENDENT VOLTAGE BIAS LEVEL	CHEMICAL-MECHANICAL POLISHING SLURRY	CHEMICAL-MECHANICAL POLISHING SLURRY	CHEMICAL-MECHANICAL POLISHING SLURRY	FAN DUCT MODULE	FAN DUCT MODULE	IMPLEMENTING MICRO BGATM ASSEMBLY TECHNIQUES FOR SMALL DIE	IMPLEMENTING MICRO BGA ASSEMBLY TECHNIQUES FOR SMALL DIE	TACTILE TRACKING SYSTEMS AND METHODS	SYSTEM AND METHOD FOR AUTOMATICALLY IDENTIFYING AND ATTACHING RELATED DOCUMENTS	SYSTEM AND METHOD FOR CONTROLLING AN ELECTRONIC DEVICE	VOICE DRIVEN WIRELESS POINTER	METHOD AND APPARATUS FOR DETERMINING A NEXT ADDRESS WITHIN A BINARY SEARCH ALGORITHM	CORRECTING LENS DISTORTION	METHOD AND APPARATUS OF RESOLVING A DEADLOCK BY COLLAPSING WRITEBACKS TO A MEMORY
5/28/1999	4/26/1999		2/26/1999	2/26/1999	9/15/1999	12/31/1998	12/23/1998	10/8/1998	2/4/2000	2/4/2000	11/24/1998	9/19/2000	8/6/1999	3/25/1999	7/21/1999	R 12/17/1998	⁾ F 12/31/1998	3/3/2000	11/27/2000	2/15/2000	12/1/1998	5/5/2000	5/8/1998	8/30/1999	9/24/1998	12/18/1998	12/24/1998	2/18/1999	10/13/2004	12/28/1998	7/6/1998	12/9/1997
09/322,152	09/299,771	10/006,502	09/259,394	09/258,050	09/395.407	09/223,811	09/221,233	09/168,592	02/435/064	00/121/00	09/199,836	09/665,339	09/370,622	09/276,366	09/358,373	09/213,220	09/224,030	09/518,107	09/723,092	09/504,191	09/203,432	09/566,185	09/074,951	09/385,924	09/160,535	09/216,580	09/220,910	09/250,940	10/962,585	09/221,626	09/110,805	08/987,279
11/25/2003	12/26/2000	10/28/2003		i i i i i i i i i i i i i i i i i i i	3/27/2007	12/19/2000	5/9/2000	11/19/2002	10/24/2000	0000, VLJAS	7/6/2004	7/3/2001	2/6/2001	4/16/2002	2/27/2001	7/3/2001	8/7/2001	1/29/2002	2/12/2002	4/10/2001	6/20/2000	4/22/2003	5/16/2000	12/3/2002	12/2/2003	2/4/2003	11/13/2007	3/11/2008		11/6/2001	12/14/1999	10/17/2000
6,654,369	6,166,563	6,637,719		10000	7 197 174	6,162,073	6,061,265	6,484,309	6,137,709	602,100	6,759,546	6,255,873	6,184,732	6,374,278	6,195,259	6,256,673	6,272,540	6,342,848	6,346,144	6,214,098	6,077,337	6,552,898	6,064,571	6,489,557	6,655,022	6,515,586	7,296,060	7,342,612		6,314,506	6,002,525	6,134,635
ES.	ĸ	Sn	S S	ī (<u> </u>	SU	Sn	Sn	5	5 5	: S	Sn	SN	SI	સ	rs.	5 5	S.	SO	SO	Sn.	SO	SS	દ્ધ	U5	SU	æ	S	S	SC	Sn	sn

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NMXI-0409.00/US	Utility - ORG	Issued	FLIP-FLOP CIRCUIT	6/28/1999	09/340,417	1/30/2001	5,181,180	US
NMXI-0410.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR PERFORMING DISTRIBUTED SIMULATION UTILIZING A SIMULATION BACKPLANE	12/22/1999	09/470,875	1/15/2008	7,319,947	SO
NMXI-0411.00/US	Utility - ORG	Issued	METHOD AND APPARATUS TO SELECTIVELY ADVANCE A WRITE POINTER FOR A QUEUE BASED ON THE INDICATED VALIDITY OR INVALIDITY OF AN INSTRUCTION STORED WITHIN THE QUEUE	3/30/2000	09/539,734	12/12/2006	7,149,883	g NT
NMXI-0412.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR PROVIDING REDUNDANCY IN NON- VOLATILE MEMORY DEVICES ACTUOD FOR ACTUON COMPENSATED EPARAGE SATE LIBERARDING BASED	S/6/1999	09/306,322	6/6/2000	6,072,723	چ ATE
NMXI-0413.00/US	Utility - ORG	Issued	METHOD FOR MOTION COMPENSATED FRAME RATE UPSAMPLING BASED ON PIECEWISE AFFINE WARPING	4/21/1999	09/296,451	8/20/2002	6,438,275	ន P
NMXI-0414.00/US	Utility - DIV	Abandoned	FLOURINE DOPED SIO2 FILM AND METHOD OF FABRICATION	3/24/2003	10/396,659			S
NMXI-0415.00/US	Utility - ORG	Issued	PARTIAL UNDERFILL FOR FLIP-CHIP ELECTRONIC PACKAGES	12/29/1999	09/474,746	3/2/2004	6,700,209	S
NMXI-0415.01/US	Utility - DIV	Issued	PARTIAL UNDERFILL FOR FLIP-CHIP ELECTRONIC PACKAGES	10/23/2000	09/694,802	4/2/2002	6,365,441	S
NMX1-0416.00/US	Utility - ORG	Issued	CHARGE PUMP WITH GATED PUMPED OUTPOT DIOUS AT INTERMEDIATE STAGE	6/30/1999	09/346,483	11/21/2000	6,151,229	υs
NMX1-0417.00/US	Utility - ORG	Issued	GATE ENHANCEMENT CHARGE PUMP FOR LOW VOLTAGE POWER SUPPLY	11/11/1999	09/438,186	9/18/2001	6,292,048	S
NMXI-0418.00/US	Utility - ORG	Issued	MULTI-LINK EXTENSIONS AND BUNDLE SKEW MANAGEMENT	12/22/1999	09/470,189	11/11/2003	6,646,991 6,687,840	2 2
NMXI-0418.02/US	Utility - CON	Issued	MULTI-LINK EXTENSIONS AND BUNDLE SKEW MANAGEMENT	9/2/2003	10/654,252	7/1/2008	7,394,766	Ę
NMXI-0419.00/US	Utility - ORG	ssued	OPTICALLY SWITCHABLE INFRARED DETECTOR	3/26/1999	09/277,637	4/20/2004	6,724,442	도 :
NMXI-0420.00/US	Utility - ORG	Abandoned	CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS	3/3/1999	09/261,849			S
NIMXI-0420.01/US	Utility - DIV	lssued	CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS	6/5/2001	09/874,666	11/28/2006	7,141,448	S
NMXI-04Z1.00/US	Utility - ORG	Issued	TECHNIQUE FOR SYNCHRONIZING FAULTS IN A PROCESSOR HAVING A REPLAY SYSTEM	12/28/1999	09/472,839	9/30/2003	6,629,271	S
NMXI-0421.01/US	utility - CON	issued	TECHNIQUE FOR SYNCHRONIZING FAULTS IN A PROCESSOR HAVING A REPLAY SYSTEM	7/8/2003	10/614,215	1/2/2007	7,159,154	Sn
NMX1-0422.00/US	Utility - CON	Issued	ENHANCED VIRTUAL RENAMING SCHEME AND DEADLOCK PREVENTION THEREFOR	1/27/2003	10/351,444	5/26/2009	7,539,850	S
NMXI-0423.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR RECOVERY OF PHYSICAL REGISTERS DURING A MISPREDICTION CONDITION	12/29/2000	09/750,095	12/26/2006	7,155,599	ઝ
NMX1-0424.00/US	Utility - ORG	Pending	METHOD AND APPARATUS FOR PREDICTING BRANCHES USING A META PREDITOR	12/28/2000	09/749,405			ß
NMXI-0425.00/US NMXI-0426.00/US	Utility - ORG	Issued Expired	FIBRE METERING ARRANGEMENT DATA BIT TO CONSTELLATION SYMBOL MAPPER	11/13/1995 4/9/1991	08/849,031 07/683,359	9/8/1998 2/9/1993	5,802,674 5,185,763	នន
NMXI-0427.00/US	Utility - ORG	Issued	METHOD OF QUANTIZING SIGNAL SAMPLES OF AN IMAGE DURING SAME	2/18/2000	09/507,399	6/8/2004	6,748,118	æ
NMX1-0428.00/US	Utility - ORG	Issued	APPARATUS FOR FAST LOGIC TRANSFER OF DATA ACROSS ASYNCHRONOUS CLOCK DOMAINS	8/16/1999	09/374,798	1/9/2001	6,172,540	ß
NMX1-0429.00/US	Utility - ORG	Issued	PLASMA INDUCED DEPLETION OF FLUORINE FROM SURFACES OF FLUORINATED LOW-K DIELECTRIC MATERIALS	8/15/2000	09/639,625	1/25/2005	6,846,737	SR

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NMXI-0429.01/US	utility - DIV		ASMA INDUCED DEPLETION OF FLUORINE FROM SURFACES OF JORINATED LOW-K DIELECTRIC MATERIALS	12	10/050,463	7/15/2003	6,593,650	S
NMXI-0430.00/US	Utility - ORG	Abandoned	TRANSMITTING DATA AND COMMANDS OVER A BUS	7/6/1999	09/350,269			S
			A SYSTEM PROCESSING DATA PACKETS RECEIVED FROM REMOTE HOST TO					
NMX1-0431.00/US	Utility - DIV	Issued	CONTROL SYSTEM OPERATION ACCORDING TO ADJUSTABLE TIMER INTERRUPTS BASED ON DATA FLOW RATE	2/9/2006	11/351,991	4/10/2007	7,203,767	Ş
			A HOST COMPUTER USING BASIC INPUT AND OUTPUT SYSTEM TO PROCESS					
NMXI-0431.01/US	Utility - DIV	issued		2/9/2006	11/351,934	3/25/2008	7,349,991	S
	•	-	C HMER IN TERROPTS					ì
NMXI-0432.00/US	Utility - ORG	Issued	ELASTIC BUFFER	11/1/1999	09/432,050	7/15/2003	6,594,329	5
NMXI-0432.01/US	Utility - CIP	Issued	JABBER COUNTER MECHANISM FOR ELASTIC BUFFER OPERATION	2/9/2000	09/500,524	2/21/2006	7,003,059	S
NMXI-0433.00/US	utility - ORG	Issued	MICROSCOPE WITH INFRARED IMAGING	7/30/1999	09/364,142	4/25/2000	6,055,095	S
NMXI-0433.01/US	Utility - CON	Abandoned	MICROSCOPE WITH INFRARED IMAGING	3/14/2000	09/524,701			S
NMXI-0434.00/US	Utility - ORG	Pending		9/30/1999	09/409,522			ŝ
SUMM SEAU-IXMN	Urlliny - ORG	lection.	METHOD AND APPARATUS FOR TRANSFERRING WAFER CASSETTES IN	10/1/1999	09/410 400	4/2/2002	6 364 331	5
Television Consideration Conference	Odiny Ono	0	MICROELECTRONIC MANUFACTURING ENVIRONMENT	+5/ +/ +100	0.01 +1.0, +0.0	1/ 4/ 4004	0,007,00±	. 6
NMXI-0436.00/US	Utility - ORG	issued	PIXEL SPAN DEPTH BUFFER	11/18/1999	09/442,663	12/24/2002	5,498,605	· 5
NR/XI-0437.00/05	Other Asino	Sued	HELD FRAME MULION DESIGN FOR DIGITAL VIDEO DECODER	6661/67/71	09/4/4,183	5/6/2003	9,500,286	Ç
NMXI-0438.00/US	Utility - ORG	Issued	VOLTAGE TOLERANT HIGH DRIVE PULL-UP DRIVER FOR AN I/O BUFFER	12/29/1999	09/474,566	5/14/2002	6,388,475	SO
NMXI-0439.00/US	Utility - ORG	Abandoned	OBSCURING VIDEO SIGNALS FOR CONDITIONAL ACCESS	2/29/2000	09/515,556			SN
NMXI-0440.00/US NMXI-0441.00/US	Utility - ORG	issued	METHOD OF COMPRESSING A COLOR IMAGE STEREOSCOPIC IMAGE SENSOR	10/1/1999 9/10/1999	09/411,697 09/393,757	9/28/2004 8/22/2000	6,798,901 6,108,130	នន
NMXI-0442.00/US	Utility - ORG	Abandoned	CONTROLLING PROCESSOR-BASED SYSTEMS USING A DIGITAL CAMERA	10/29/1999	09/430,282	6/12/2007	7,231,083	SS
NMXI-0443.00/US	Utility - ORG	Issued	EXECUTING A TRANSLATED BLOCK OF INSTRUCTIONS AND BRANCHING TO CORRECTION CODE WHEN EXPECTED TOP OF STACK DOES NOT MATCH ACTUAL TOP OF STACK TO ADJUST STACK AT EXECUTION TIME TO CONTINUE EXECUTING WITHOUT RESTARTING TRANSLATING	9/29/2000	09/676,175	3/4/2008	7,340,592	S
NMXI-0444.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR ADAPTIVE HIERARCHICAL VISIBILITY IN A TILED THREE-DIMENSIONAL GRAPHICS ARCHITECTURE	11/2/1999	09/432,134	2/25/2003	6,525,726	S
NMXI-0445.00/US	utility - ORG	Issued	SELF-TUNING AMPLIFIER	11/5/1999	09/434,250	7/31/2001	6,268,774	S
NMXI-0446.00/US	utility - ORG	Issued	METHOD AND APPARATUS FOR RAPID INITIALIZATION OF CHARGE PUMP CIRCUITS	9/27/1999	09/406,329	7/3/2001	6,255,896	SS.
NMX1-0447.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR UNLOCKING A COMPUTER SYSTEM HARD DRIVE	11/21/2001	10/002,541	2/14/2006	7,000,109	S
NMX1-0448.00/US	utility - ORG	Issued	REAL-TIME PERFORMANCE ASSESSMENT OF LARGE AREA NETWORK USER EXPERIENCE	12/23/1999	09/471,964	4/21/2009	7,523,190	SS
NMXI-0449.00/US	Utility - ORG	Expired	METHOD AND APPARATUS FOR IDENTIFYING SPEECH IN TELEPHONE SIGNALS	5/15/1989	07/351,785	12/18/1990	4,979,214	æ
NMXI-0450.00/US	Utility - ORG	Expired	METHOD AND APPARATUS FOR FREQUENCY ANALYSIS OF TELEPHONE SIGNALS	5/15/1989	07/351,393	6/5/1990	4,932,062	SU
NMXI-0451.00/US	Utility - ORG	Expired	CONVERTER FOR IN-BAND ROUTING AND/OR ORIGINATION INFORMATION	10/31/1989	07/429,849	9/3/1991	5,046,088	Ř
NMXI-0452.00/US	Utility - ORG	Expired	AUTOMATIC CALL CENTER OVERFLOW RETRIEVAL SYSTEM	11/13/1990	07/612,572	2/9/1993	5,185,786	Ĉ

7,240,342 US 6,577,695 US 7,065,327 US 7,007,304 US 7,107,202 US 7,327,370 US 6,469,482 US	25	10/174,193		VOLTAGE GENERATION	,		
	72		6/17/2002	MEMORY CIRCUIT INCLUDING BOOSTER PUMP FOR PROGRAMMING	lssued	Utility - DIV	NMXI-0478.01/US
		09/607,483	^R 6/30/2000	INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS	Issued	Utility - ORG	NMXI-0478.00/US
	2/5/2008	11/174,662	7/6/2005	MEMORY CONTROLLER HUB INTERFACE	{ssued	Utility - CON	NMXI-0477.00/US
	9/12/2006 7,1	09/495,150	1/31/2000	METHOD AND APPARATUS FOR HARDWARE AND SOFTWARE CO- SIMULATION	Issued	Utility - ORG	NMX1-0476.00/US
	2/28/2006 7,0	09/665,826	9/20/2000	METHOD AND APPARATUS TO IMPROVE THE PROTECTION OF INFORMATION PRESENTED BY A COMPUTER	Issued	Utility - ORG	NMXI-0475.00/US
	6/20/2006 7,0	09/762,720	11/20/2001	SINGLE-CHIP CIMOS DIRECT-CONVERSION TRANSCEIVER	Issued	Utility - ORG	NMXI-0474.00/US
		60/099,723	9/10/1998	A SINGLE-CHIP CMOS DIRECT-CONVERSION TRANSCEIVER	Expired	Prov - ORG	NMX1-0474.00/PR
	6/10/2003 6,5	09/526,794	3/4/1997	EMULATING NARROW BAND PHASE-LOCKED LOOP BEHAVIOR ON A WIDE BAND PHASE-LOCKED LOOP	Issued	Utility - ORG	NMXI-0473.00/US
	7/3/2007 7,2	09/608,616	6/30/2000	USER TRANSPARENT CONTINUOUS COMPILATION	Issued	Utility - ORG	NMX1-0472.00/US
us		09/478,994	1/7/2000	SYSTEM AND METHOD FOR STALLING A DISPLAY CONTROLLER UPON REQUEST	Abandoned	Utility - ORG	NMX1-0471.00/US
6,741,978 US	5/25/2004 6,7	09/547,624	4/12/2000	ACCESSING FILE DATA STORED IN NON-VOLATILE RE-PROGRAMMABLE SEMICONDUCTOR MEMORIES	Issued	Utility - ORG	NMXJ-0470,00/US
7,180,523 US	2/20/2007 7,1	09/539,343	3/31/2000	TRIMMING SURFACES	Issued	Utility - ORG	NMXI-0469.00/US
SN		10/743,397	12/23/2003	SHIFTING AN INPUT SIGNAL FROM A HIGH-SPEED DOMAIN TO A LOWER-SPEED DOMAIN	Abandoned	Utility - CON	NMXI-0468.01/US
6,668,298 US	12/23/2003 6,6	09/474,750	12/29/1999	SHIFTING AN INPUT SIGNAL FROM A HIGH-SPEED DOMAIN TO A LOWER-SPEED DOMAIN	Issued	Utility - ORG	NMXI-0468.00/US
7,366,748 US	4/29/2008 7,3	09/609,496	6/30/2000	METHODS AND APPARATUS FOR FAST ARGUMENT REDUCTION IN A COMPUTING SYSTEM	Issued	Utility - ORG	NMXI-0467.00/US
7,516,312 US	4/7/2009 7,5	10/817,263	4/2/2004	PRESBYOPIC BRANCH TARGET PREFETCH METHOD AND APPARATUS	issued	Utility - CON	NMXI-0466.00/US
6,650,802 US	11/18/2003 6,6	09/470,574	12/22/1999	METHOD AND APPARATUS FOR SWITCHING AN OPTICAL BEAM	Issued	Utility - ORG	NMXI-0465.00/US
6,754,764 US	6/22/2004 6,7	09/468,965	12/21/1999	METHOD AND APPARATUS FOR MAINTAINING ORDER IN A PIPELINED PROCESS AND ITS APPLICATION	lssued	Utility - ORG	NNIXI-0464.00/US
6,671,771 US	12/30/2003 6,6	09/468,477	12/21/1999	HASH CAM HAVING A REDUCED WIDTH COMPARISON CIRCUITRY AND ITS APPLICATION	Issued	Utility - ORG	NMXI-0463-00/US
6,735,418 US	5/11/2004 6,7	09/527,314	3/16/2000	ANTENNA INTERFACE	Issued	Utility - ORG	NMX1-0462.00/US
SS	ما بيا يودوند اربيا اي	60/135,467	6/2/1999	ANTENNA INTERFACE	Expired	Prov - ORG	NMXI-0462.00/PR
. US		08/828,000	6/4/1997	METHOD AND APPARATUS FOR DETERMINING DIGITS DIALED USING A SELECTED ALGORITHM TELEPHONE NETHANDRY MITH MORMALLY DEE HOOK TELEPHONES	Issued	Utility - CON	NMXI-0460.00/US
5,764,759 US A	6/9/1998 5,7	08/319,159	10/6/1994	CALL PROCESSING USING PREVIOUSLY OBTAINED LINE CHARACTERISTICS	issued	Utility - DRG	NMXI-0459.00/US
5,809,110 US H	9/15/1998 5,8	08/594,411	1/31/1996	TECHNIQUE OF CONTROLLING TELEPHONE CALLS	Issued	Utility - ORG	NMX1-0458.00/US
5,745,786 US N	4/28/1998 5,7	08/188,555	1/27/1994	METHOD AND APPARATUS FOR ASSIGNING MEMORY SPACE ADDRESSES TO MEMORY ON COMPUTER INTERFACE BOARDS	Issued	Utility - ORG	NMXI-0457.00/US
SJ	6/10/1997 5,6	08/524,111	8/22/1995	VOICE DETECTION	Issued	Utility - CON	NMXI-0456.00/US
	Ų1	08/226,678	4/12/1994	RING TONE DETECTION FOR A TELEPHONE SYSTEM	§ssued	Utility - ORG	NMXI-0455.00/US
5,404,400 US		08/024,402	3/1/1993	OUTCALLING APPARATUS	Issued	Utility - ORG	NMXI-0454.00/US
5,218,636 US	6/8/1993 5,2	07/665.970	3/7/1991	DIAL PULSE DIGIT DETECTOR	Expired DIAL P	Utility - ORG	NMXI-0453.00/US

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NMXI-0499.01/US Utility - CON	NMXI-0499.00/LIS Utility - ORG	NMXI-0498.00/US Utility - ORG		NMXI-0496.00/US Utility - ORG	NMXI-0495.00/US Utility - ORG	NMXI-0494.00/US Utility - ORG	NMXI-B493.00/US Utility - ORG	NMXI-0492.00/US Utility - ORG	NMXI-0491.00/US Utility - ORG	NMX1-0490.00/US Utility - ORG	NMXI-0489.01/US Utility - DIV	NMXI-0489.00/US Utility - ORG	NMXI-D488.00/US Utility - DIV	NMXI-0487.00/US Utility - ORG	NMXI-0486.01/US Utility - CON	-	-	NMXI-0484.00/US Utility - ORG	NMXI-0483.00/US Utility - ORG	NMXI-0482.03/US Utility - CON	NMX1-0482.02/US Utility - DIV	NMXI-0482.01/US Utility - DIV	NMXI-0482.00/US Utility - ORG	NMX1-0481.00/US Utility - ORG	NMX1-0480.00/US Utility - ORG
ON issued	RG Issued	RG Issued	RG Issued	RG Issued	RG Abandoned	RG Issued	RG Abandoned	RG Issued	RG Issued	RG Issued	lV (ssued	RG Issued	N Abandoned	RG Issued	ON Abandoned		RG Issued	RG Issued	RG Issued	ON Issued	V Abandoned	V (ssued	RG Issued	RG Issued	RG Issued
METHOD AND APPARATUS FOR ALLOWING CONTINUOUS APPLICATION OF HIGH VOLTAGE TO A FLASH MEMORY DEVICE POWER PIN	METHOD AND APPARATUS FOR ALLOWING CONTINUOUS APPLICATION OF HIGH VOLTAGE TO A FLASH MEMORY DEVICE POWER PIN	MEMORY SUBSYSTEM EMPLOYING POOL OF REFRESH CANDIDATES	REMOTELY MANAGING AND CONTROLLING A CONSUMER APPLIANCE	METHOD AND APPARATUS FOR GRAPHICAL DEVICE MANAGEMENT USING A VIRTUAL CONSOLE	METHOD AND APPARATUS TO PROVIDE CLIENT-SIDE FILTERING IN A BROADCAST SYSTEM	METHOD AND APPARATUS FOR PROVIDING A SECURE-PRIVATE PARTITION ON A HARD DISK DRIVE OF A COMPUTER SYSTEM VIA (DE CONTROLLER	COMBINED PROPAGATE/GENERATE/PARTIAL SUM CIRCUIT FOR SINGLE-RAIL ADDERS	EFFICIENT IMPLEMENTATION OF N-POINT DCT, N-POINT IDCT, SA-DCT AND SA-DCT ALGORITHMS	METHOD FOR IMPROVING ACCESSIBILITY OF A COMPUTER DISPLAY	COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE	METHODS OF HEAT EXTRACTION FROM AN INTEGRATED CIRCUIT DIE	NOVEL SYSTEM AND METHOD OF HEAT EXTRACTION FROM AN INTEGRATED CIRCUIT DIE	DIE-IN-HEAT SPREADER MICROELECTRONIC PACKAGE	PROCESS FOR FORMING MICROELECTRONIC PACKAGES AND INTERMEDIATE STRUCTURES FORMED THEREWITH	CARRIER FREQUENCY ESTIMATOR FOR A SIGNAL RECEIVER	CARRIER FREQUENCY ESTIMATOR FOR A SIGNAL RECEIVER	VITERBI ACS UNIT WITH RENORMALIZATION	SHARED CREDIT ROUND ROBIN QUEUING	METHOD OF INVERSE QUANTIZED SIGNAL SAMPLES OF AN IMAGE DURING IMAGE DECOMPRESSION	DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK	DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK	DEVICE TO RECEIVE., BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK	DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK	METHOD AND APPARATUS FOR BRANCH TRACE MESSAGE SCHEME	METHOD AND DEVICE FOR QUANTIZING THE INPUT TO SOFT DECODERS
F 12/13/2002	F 12/29/2000	12/29/2000	5/31/2000	^{5 A} 6/30/2000	3/22/2000	6/30/2000	6/26/2000	9/30/2000	8/18/1997	5/1/1992	12/18/2001	6/29/2000	2/9/2004	TE 3/1/2000	9/30/1999	7/20/1998	3/29/1993	3/21/2000	3 2/18/2000 ·	5/10/2004	1/20/2004	1/20/2004	6/26/2000	2/14/2000	6/15/1998
10/318,464	09/751,514	09/753,005	09/584,520	09/607,592	09/533,024	09/608,117	09/603,800	09/676,556	09/912,380	07/877,409	10/025,434	09/606,893	10/774,952	09/516,652	09/409,791	09/119,479	08/038,323	09/541,048	09/507,213	10/842,752	10/761,395	10/761,394	09/603,957	09/503,879	09/103,683
4/6/2004	2/4/2003	4/2/2002	6/1/2010	5/16/2006		12/26/2006		5/8/2007	7/6/1999	5/30/1995	6/22/2004	2/26/2002		7/1/2003		12/14/1999	9/20/1994	1/27/2004	11/1/2005	11/11/2008		4/1/2008	8/17/2004	11/11/2003	4/4/2000
5,717,855	6,515,901	6,366,516	7,729,943	7,047,495		7,155,615		7,216,140	5,920,870	5,421,001	6,751,837	6,351,387		6,586,836		6,002,721	5,349,608	6,683,884	6,961,472	7,450,583		7,352,763	6,778,548	6,647,545	09/103,683 4/4/2000 6,047,035 US
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Sn	5,345,577	9/6/1994	07/981,329	11/24/1992	THE PROPERTY OF THE PROPERTY O	Section 1	Utility - DRG	NWXI-D525 OO/IK
ţ				11/34/1003	DRAM REFRESH CONTROLLER WITH IMPROVED BLIS APRITRATION SOLEMA	Expired	Utility - DIV	NMXI-0524.00/US
55	6,125,412	9/26/2000	08/046,109	4/9/1993	NS TO AND	Issued	Utility - ORG	NMXI-0523.00/US
S	5,276,833	1/4/1994	07/544,821	7/2/1990	DATA CACHE MANAGEMENT SYSTEM WITH TEST MODE USING INDEX REGISTERS AND CAS DISABLE AND POSTED WRITE DISABLE	Expired	Utility - ORG	NMXI-0522.00/US
S	5,280,590	1/18/1994	07/902,317	6/22/1992	LOGIC SUPPORT CHIP FOR AT-TYPE COMPUTER WITH IMPROVED BUS ARCHITECTURE	Expired	Utility - CON	NMXI-0521.01/US
SO.	5,125,080	6/23/1992	07/436,200	11/13/1989	LOGIC SUPPORT CHIP FOR AT-TYPE COMPUTER WITH IMPROVED BUS ARCHITECTURE	Expired	Utility - ORG	NMXI-0521.00/US
SO	6,421,432	7/16/2002	09/497,970	2/4/2000	METHOD AND APPARATUS FOR CONTROLLING A COMPUTER TO IMPLEMENT TELEPHONE FUNCTIONS WITH A DISPLAYED TELEPHONE OF VARIABLE SIZE	Issued	Utility - CON	NMXI-0820.01/US
SO	6,240,168	5/29/2001	09/182,828	10/29/1998	APPARATUS AND METHOD FOR CONNECTING AN IMPLEMENT TO A TRACTION VEHICLE	Issued	utility - ORG	NMXI-0520.00/US
S)	6,324,263	11/27/2001	09/144,511	8/31/1998	CALLER ID	Issued	Utility - CIP	NMXI-0519.00/US
ર સ	6,975,720	12/13/2005	09/523,935 09/611,938	3/10/2000 7/7/2000	METHOD OF PROCESSING AN INBOUND CALL IN A CALL CENTER DYNAMICALLY LOCATING ICONS ON A VIDEO DISPLAY SCREEN	Abandoned	utility - ORG	NMXI-0518.00/US
S)	6,304,645	10/16/2001	09/518,966	3/4/2000	CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORMS	Issued	Utility - ORG	NWIXI-0516.00/US
Sn	6,418,201	7/9/2002	09/510,311	2/22/2000	RESOURCE ALLOCATION	Issued	Utility - ORG	NMXI-0515.00/US
SU.	6,853,723	2/8/2005	09/417,161	10/12/1999	HIGH IMPEDANCE POLARITY DETECTOR	Issued	Utility - ORG	SO/ON'T CO-IXIAIN
Ç.	6,931,121	8/16/2005	09/362,425	7/28/1999	DUAL PERSONALITY ANALOG PORT	Issued	Utility - ORG	NMXI-0513.00/US
SO	6,643,367	11/4/2003	09/301,855	4/29/1999	DYNAMIC AND SCHEDULED COMPUTER TELEPHONY RESOURCE ALLOCATION	Issued	Utility - ORG	NMXI-0512.00/US
æ	6,246,334	6/12/2001	09/299,162	4/26/1999	BOARD ID DISPLAY SYSTEM	Issued	Utility - ORG	NMXI-0511.00/US
Sn	6,595,798	7/22/2003	09/280,171	3/29/1999	TEST FIXTURE FOR TESTING A MULTI-PORT VOICE PROCESSING CARD AND THE LIKE	Issued	Utility - ORG	NMXI-0510.00/US
SU	7,123,712	10/17/2006	09/277,286	3/26/1999	COMPUTER TELEPHONY SERVER WITH IMPROVED FLEXIBILITY	Issued	Utility - ORG	NMXI-0509.00/US
Ç:	6,400,709	6/4/2002	09/204,607	12/2/1998	DTMF DETECTION IN INTERNET TELEPHONY	Issued	Utility - ORG	NMX1-0508.00/US
S.	5,787,159	7/28/1998	08/607,796	2/27/1996	USE OF CALLER ID INFORMATION	Issued	utility - ORG	NMXI-0507.00/US
G.	6.377.576	4/23/2002	08/541,678	10/10/1995	TELEPHONE CALL SETUP PROCEDURE	Issued	Utility - ORG	NMXI-0506.00/US
Sn	5,506,885	4/9/1996	08/289,315	8/11/1994	METHOD OF ADAPTING PROCESSING PARAMETERS IN A CALL PROCESSING SYSTEM	Issued	Utility - ORG	SO/00:5050-1XIMN
SO	7,308,713	12/11/2007	09/721,785	11/22/2000	LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS	Issued	Utility - ORG	NMXI-0504.00/US
Sn	6,486,791	11/26/2002	09/872,490	5/31/2001	DRIVING A VISUAL INDICATOR ARRAY IN AN ELECTRONIC SIGNALING SYSTEM	Issued	Utility - ORG	NMXI-0503.00/US
SS			09/733,289	12/8/2000	MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS	Abandoned	Utility - ORG	NMXI-0502.00/US
SO			10/299,135	11/19/2002	PROTECTIVE FILM FOR THE FABRICATION OF DIRECT BUILD-UP LAYERS ON AN ENCAPSULATED DIE PACKAGE	Abandoned	Utility - DIV	NWXI-0501.00/US
SO	7,348,496	3/25/2008	11/003,956	12/3/2004	CIXCUI BOARD WITH ORGANIC DELECTRIC CAYER	100000	Chind , Cin	interpol obdocooj ob

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NMXI-0576.00/US	MatterType Utility - ORG	MatterStatus	EXITIDIT AND ADDADATI IS COD INTERNACES INTACES INTACES INTO AND ADDADATI IS COD INTERNACES INTACES INTO AND AND ADDADATI IS COD INTERNACES INTO ADDADATI IS CODE INTERNACES INTO ADDADATI INTO ADDADA	FileDate	SerialNo	SerialNo Isuedate	PatentNo	Country
NMXI-0527.00/US	Utility - ORG	issued	CLOCK SPLITTER CIRCUIT TO GENERATE SYNCHRONIZED CLOCK AND INVERTED CLOCK	9/29/2000	09/676,313	5/7/2002	6,384,558	SU
NMX1-0528.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR A LOW LATENCY SOURCE-SYNCHRONOUS ADDRESS RECEIVER FOR A HOST SYSTEM BUS IN A MEMORY CONTROLLER	9/20/2000	09/665,922	6/8/2004	6,748,513	Sn
NMXI-0528.01/US	Utility - CON	Issued	METHOD AND APPARATUS FOR A LOW LATENCY SOURCE-SYNCHRONOUS ADDRESS RECEIVER FOR A HOST SYSTEM BUS IN A MEMORY CONTROLLER	3/30/2004	10/813,145	7/5/2005	6,915,407	รู
NMXI-0529.00/US	utility - ORG	Issued	RAY INTERSECTION REDUCTION USING DIRECTIONALLY CLASSIFIED TARGET LISTS	6/7/1999	09/327,015	12/3/2002	6,489,955	SS
NMXI-0530.00/US	Utility - ORG	Issued	STOCHASTIC SAMPLING WITH CONSTANT DENSITY IN OBJECT SPACE FOR ANISOTROPIC TEXTLINE MADDING	9/10/1999	09/393,477	6/4/2002	6,400,370	CZ.
NMXI-0531.00/US	Utility - ORG	Issued	3D RENDERING TEXTURE CACHING SCHEME	2/11/2000	09/502,994	5/23/2006	7,050,063	S
NMXI-0532.00/US	Utility - ORG	Issued	APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM	10/15/1996	08/731,635	12/29/1998	5,854,632	S
NMXI-0533.00/US	Utility - ORG	Issued	METHOD AND SYSTEM FOR SERVICING CACHE LINE IN RESPONSE TO PARTIAL CACHE LINE REQUEST	12/29/2000	09/752,846	12/24/2002	6,499,085	ᅜ
NMXI-0534.00/US	Utility - ORG	Abandoned	METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN	6/30/2000	09/608,637			ន
NMXI-0534.01/US	Utility - CON	Abandoned	METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN	5/5/2005	11/124,315			S
NMXI-0534.02/US	utility - DIV	Abandoned	ANTECEDENT STRENGTHENING TO PERFORM GENERALIZED TRAJECTORY EVALUATION	10/27/2006	11/553,905			SS
NMXI-0535.00/US	Utility - ORG	Issued	DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS	9/27/2000	09/670,850	3/12/2002	6,356,062	S
NMXI-0536.00/US	Utility - ORG	Issued	EXTENSIBLE BIOS ERROR LOG	12/13/2000	09/737,140	10/17/2006	7,124,412	SS
NMXI-0537.00/US	Utility - ORG	Issued	DIFFERENTIAL SIGNAL PATH FOR HIGH SPEED DATA TRANSMISSION IN FLASH MEMORY	12/29/2000	09/752,345	8/27/2002	6,442,069	us .
NMXI-0537.01/US	Utility - CIP	Issued	DIFFERENTIAL REDUNDANCY MULTIPLEXOR FOR FLASH MEMORY DEVICES	10/16/2001	09/982,246	6/3/2003	6,574,141	S
NMXI-0538.00/US	Utility - ORG	Issued	GATE ENHANCED TRI-CHANNEL POSITIVE CHARGE PUMP	12/29/2000	09/753,045	12/17/2002	6,496,055	SU
NMX1-0539.00/US	Utility - ORG	Issued	TRIPLE WELL NO BODY EFFECT NEGATIVE CHARGE PLIMP	10/18/2002	10/273,689	2/3/2004	6,686,793	당
NMX1-0540.00/US	Utility - ORG	Issued	NOVEL BI-VOLTAGE LEVELS SWITCHES	12/21/2000	09/745 070	9/1//2002	6,452,438	: S
NMXI-0541.00/US	Utility - ORG	Issued	SELF INITIALIZATION FOR CHARGE PUMPS	12/27/2000	09/752 247	4/1/2002	6,366,150	5
NMXI-0541.01/US	Utility - CON	Issued	SELF INITIALIZATION FOR CHARGE PUMPS	1/17/2002	10/051.696	%/2/2002 8/27/2002	6,366,158	i (5
NIMAXI 0543 00/US	onity - ore	SSUED	OUTPUT SWITCH FOR CHARGE PUMP RECONFIGURATION	12/26/2000	09/748,611	4/9/2002	6,369,642	CS SS
NWAT-JD45.DO/OS	Utarty - ORG	Issued	NEGATIVE OUTPUT VOLTAGE CHARGE PUMP AND METHOD THEREFOR	12/29/2000	09/753,351	3/19/2002	6,359,814	S
NMXI-0544.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR MATCHED-REFERENCE SENSING ARCHITECTURE FOR NON-VOLATILE MEMORIES	12/28/2000	09/752,714	2/4/2003	6,515,906	ß
NMXI-0544.01/US	Utility - CON	Issued	METHOD AND APPARATUS FOR MATCHED-REFERENCE SENSING ARCHITECTURE FOR NON-VOLATILE MEMORIES	12/26/2002	10/330,469	12/14/2004	6,831,862	S
NMXI-0545.01/US	Utility - DIV	Issued	CHARGING A CAPACITANCE OF A MEMORY CELL AND CHARGER CHARGING A CAPACITANCE OF A MEMORY CELL AND CHARGER	6/22/2001 5/23/2002	09/888,252	1/7/2003	6,504,760	5
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		MOC/95/15	PURGING GAS FROM A PHOTOLITHOGRAPHY ENCLOSURE BETWEEN A	issued	Utility - DIV	NMXI-0569.01/US
	•	12/29/2000	PURGING GAS FROM A PHOTOLITHOGRAPHY ENCLOSURE BETWEEN A MASK PROTECTIVE DEVICE AND A PATTERNED MASK	Issued	Utility - ORG	NMXI-0569.00/US
*/20/2004 8/31/2004		9/11/2003	EXPOSED AND EMBEDDED OVERLAY STRUCTURE	Abandoned	Utility - DIV	NMX1-0568.00/US
	09/709,000 12/30/2003	11/8/2000 12/21/2000	I/O DEVICE TESTING METHOD AND APPARATUS SPINDLE SLEEVE FOR COATER/DEVELOPER	Issued	Utility - ORG	NMX1-0567.00/US
	09/732,115	12/7/2000	CIRCUIT BOARD	Abandoned	Utility - ORG	NNAXI-0565.00/US
5/8/2007	10/767,324	1/29/2004	COHERENT LIGHT DESPECKLING APPRAPATIC CYCLE CANCELLOSING TO ANICOLOGICAL CONTROL CONTR	Abandoned	Utility - CON	NMX1-0564.00/US
10/7/2003	10/354,361	3/29/2003	METHOD OF FORMING A SEMICONDUCTOR DEVICE USING A CARBON DOPED OXIDE LAYER TO CONTROL THE CHEMICAL MECHANICAL POLISHING 1/29/2003 OF A DIELECTRIC LAYER	Íssued	Utility - DIV	NMXI-0563.01/US
4/15/2003	09/996,215	11/20/2001	DOPED OXIDE LAYER TO CONTROL THE CHEMICAL MECHANICAL POLISHING 11/20. OF A DIELECTRIC LAYER	{ssued	Utility - ORG	NMXI-0563.00/US
9/9/2003	09/730,206	12/4/2000	METHOD OF FORMING A CONTROLL OF THE STATE OF	Issued	Utility - ORG	NMXI-0562.00/U\$
3/21/2006	09/751,601	12/29/2000	MICROPROCESSOR	Issued	Utility - ORG	NMXI-0561.00/US
7/15/2003	09/749,610	12/28/2000	AUTO LATENCY TEST TOOL	Issued	utility - ORG	NMXI-0560.00/US
9/25/2007	09/717,579	11/21/2000	SERVICE LEVEL OF A DIGITAL TELEVISION BROADCAST SIGNAL	Issued	Utility - ORG	NMXI-0559.00/US
5/1/2007		12/19/2000	METHOD AND DRIVER FOR DETECTING GUTCHES	issued	OTHEY - ORG	CO/00/00/00/NAM
10/17/2006	09/741,599	12/19/2000	METHOD & APPARATUS FOR ADDING REAL-TIME PRIMITIVES	Issued	Utility - ORG	NMXI-0557.00/US
	11/321,632	12/28/2005	MECHANISM FOR HANDLING EXPLICIT WRITEBACK IN A CACHE COHERENT MULT-NODE ARCHITECTURE	Abandoned	Utility - CON	NMXI-0556.00/US
9/24/2002	09/750,110	12/29/2000	VOLTAGE LEVEL SHIFTER HAVING ZERO DC CURRENT AND STATE RETENTION IN DROWSY MODE	ssued	Utility - ORG	NMX1-0555.00/US
11/21/1995	08/129,899	9/30/1993	CIRCUIT AND METHOD FOR DIGITAL TO ANALOG SIGNAL CONVERSION	lssued	Utility - ORG	NMX1-0554.00/US
		11/27/2000	PROVIDING A VALID INITIAL FRAME	Abandoned	Utility - ORG	NMX1-0553.00/US
7/17/2007	09/756.579	1/8/2001	SHARING CLASSES BETWEEN PROGRAMS	Issued	Utility - ORG	NMXI-0552.00/US
5/5/2009		6/24/2002	CONTROLLING SNOOP ACTIVITIES USING TASK TABLE IN MULTIPROCESSOR SYSTEM	Issued	Utility - ORG	NMX1-0551.00/US
12/7/1999	09/009,561	1/20/1998	SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION.	Issued	utility - ORG	NMX1-0550.00/US
3/15/2005	09/714,382	11/15/2000	SYSTEM AND METHOD OF TRANSMITTING DATA FRAMES IN A MESH OF DATA SWITCHES	Issued	Utility - ORG	NMXI-0549.00/US
9/13/2005	10/877,634	6/25/2004	METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING	Issued	Utility - DIV	NMXI-0548.02/US
9/7/2004	10/078,106	2/19/2002	METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING	Issued	Utility - DIV	NMXI-0548.01/US
11/5/2002	09/823,463	3/30/2001	METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING	Issued	Utility - ORG	NMXI-0548.00/US
1/23/2007	09/675,286	9/29/2000	RUNTIME PREDICTION FRAMEWORK FOR CPU INTENSIVE APPLICATIONS	Issued	Utility - ORG	NMXI-0547.00/US
7/18/2000	08/966,771	11/7/1997	THREAD LIST EXPLICIT CACHING SYSTEM	Banssi	Othery - CON	MINIXI-U546.UI/US

Exhibit A - US Applications and Issued Patents MTI-Dckfilo | Mattertype | Matterstatus | Title | Title

	NMXI-0599.00/US	NMXI-0598.00/US	NMXI-0597.00/US	NMXI-0596.00/US	NMXI-0594.00/US	NMXI-0593.00/US	NMXI-0592.00/US	NMXI-0591.00/US	NMX1-0590.00/US	NMXI-0589.00/US	NMXI-0588.00/US	NMXI-0587.00/US	NMXI-0586.00/US	NMXI-0585.00/US	NMXI-0582.01/US NMXI-0584.00/PR	NMXI-0581.00/US	NMXI-0580.00/US	SO/00'6/50-lytown	NMXI-0578.00/US	NMXI-0578.00/PR	NMX1-0577.00/US	NMXI-0575.00/US	NMXI-0574.00/US	NMXI-0573.00/US	NMXI-0572.00/US	NMXI-0571.00/US	NMXI-0570.00/US
	Utility - ORG	Utility - ORG	Utility - CON	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	utility - ORG	Utility - ORG	Utility - CON Prov - ORG	utility - ORG ∪tility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Prov - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG
	Issued	issued	Abandoned	Issued	Issued	issued	Issued	Issued	Issued	Issued	Issued	Issued	issued	Abandoned	Issued Expired	Abandoned Issued	Abandoned	Issued	Issued	Expired	Abandoned	Issued	Issued	Issued	Issued	issued	Issued
	ENHANCED MEDIA GATEWAY CONTROL PROTOCOL	SYSTEM AND METHOD FOR PROTECTED MESSAGING	RECOGNIZING SIGNALS IN DESIGN SIAMILATION	MAICHING ROUTED CALL TO AGENT WORKSTATION	HOST TO INTERFACE WITH A NETWORK PROCESSOR	SYSTEMS AND METHODS FOR IMPLEMENTING POINTER MANAGEMENT	MULTI-PROTOCOL CONVERSION ASSISTANCE METHOD AND SYSTEM FOR A NETWORK ACCELERATOR	NETWORKS	CBR/VBR TRAFFIC SCHEDULER SYSTEM FOR MILITAL AVER BROADS AND DEGUSCONING IN COLUMN TO THE	SYSTEM AND METHOD FOR PERFORMING CUT-THROUGH FORWARDING IN AN ATM NETWORK SUPPORTING LAN EMULATION	SYSTEMS AND METHODS FOR ON-CHIP STORAGE OF VIRTUAL CONNECTION DESCRIPTORS	SYSTEMS AND METHODS FOR IMPLEMENTINGABR WITH GUARANTEED MCR	TWO-DIMENSIONAL QUEUING/DE-QUEUING METHODS AND SYSTEMS FOR IMPLEMENTING THE SAME	NETWORK ACCELERATOR SUBSYSTEM BASED ON SINGLE-CHIP NETWORK	METHOO FOR READING A STRUCTURAL PHASE-CHANGE MEMORY SINGLE-CHIP NETWORK PROCESSOR AND INTERFACE SYSTEM	AND REMAPPING IN A SECOND STAGE A PLURALITY OF INSTRUCTION GROUPS TO A PLURALITY OF FUNCTIONAL UNITS METHOD FOR READING A STRUCTURAL PHASE-CHANGE MEMORY	METHOD AND APPARATUS FOR SYNCHRONIZING NETWORK POLICIES METHOD AND APPARATUS FOR MAPPING IN ONE STAGE AND MERGING	KEYBOARD HOLDER	UNIVERSAL TELEPHONY INTERFACE POLARITY DETECTOR	UNIVERSAL TELEPHONY INTERFACE POLARITY DETECTOR	DATA PATH EVALUATION SYSTEM AND METHOD	PROGRAMMABLE DIGITAL PHASE LOCK LOOP	SYSTEM AND METHOD FOR SPLIT AUTOMATIC GAIN CONTROL	ELECTROMAGNETIC COUPLER SOCKET	CLAMP TO SECURE CARRIER TO DEVICE FOR ELECTROMAGNETIC COUPLER	INTERCONNECT MECHANICS FOR ELECTROMAGNETIC COUPLER	Utility - ORG Issued POST ETCH CLEAN SEQUENCE FOR MAKING A SEMICONDUCTOR DEVICE
	12/28/2000	11/21/2005	12/29/2000	12/29/2000	6/25/1999	6/25/1999	6/25/1999	6/25/1999	6/25/1999	6/25/1999	3/16/1999	6/17/1999	3/16/1999	6/25/1999	6/10/2003 6/27/1998	12/28/2000 8/2/2001	11/17/2000	3/12/2001	11/28/2001	8/29/2001	4/13/2001	12/29/2000	9/14/2001	12/29/2000	12/29/2000	11/15/2000	10/6/2000
	09/749,745	11/285,565	09/750,025	09/750,030	09/337,025	09/340,282	09/344,672	09/340,068	09/344,820	09/344,608	09/270,287	09/335,223	09/271,061	09/344,453	10/458,828	09/749,725	09/715,529	09/804,489	09/996,255	60/315,797	09/835,034	09/750,671	09/951,904	09/751 577	09/751,442	09/713,702	09/684,550 10/15/2002
	3/20/2007	***************************************	7/10/2007	9/14/2004	8/5/2003	7/23/2002	8/5/2003	9/23/2003	12/31/2002	4/27/2004	10/30/2001	12/2/2003	4/20/2004		3/16/2004	7 /o /2003		10/29/2002	1/9/2007	TO/ // 2005	coortion	9/3/2002	3/4/2008	2/18/7002	6/10/2003	12/24/2002	19
•	6,976,172 7,194,071		7,242,760	6,792,102	6,604,136	6,425,067	6,603,768	6,625,650	6,501,731	6,728,249	6,311,212	6,657,959	6,724,767		6,707,712	6 200		6,471,170	7,162,028	6,03L,339	•	6,445,230	7,340,069	000 000	6,576,847	6,498,305	6,465,358
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WII-DCKING	Matter Type	MatterType MatterStatus	Jide	FileDate	SerialNo		PateniNo	Country
NMXI-0600.00/US	Utility - ORG	Issued	DEVICE AND METHOD OF INTERFACING VOICE DATA BETWEEN A SWITCH AND A COMPUTER SYSTEM	4/13/2001	09/834,713	3/1/2005	6,862,341	S
NWXI-0601.00/US	Utility - ORG	Issued	COLOR FILTER ARRAY AND COLOR INTERPOLATION ALGORITHM	11/30/2000	09/727,038	7/12/2005	6,917,381	S
NMXI-0602.00/US	Utility - DIV	Abandoned Abandoned	PHYSICALLY INTERACTING WITH A PROCESSOR-BASED DISPLAY LOCATING A POSITION ON A DISPLAY SCREEN	12/7/2004	11/005,694			<i>સ</i> સ
NMXI-0604.00/US	Utility - ORG	Issued	METAL STRUCTURE FOR A PHASE-CHANGE MEMORY DEVICE	12/21/2000	09/745,835	5/27/2003	6.569.705	Ā S
NMXI-0604.01/US	Utility - DIV	Issued	METAL STRUCTURE FOR A PHASE-CHANGE MEMORY DEVICE	4/17/2003	10/418,530	9/28/2004	6,797,979	S 8
NMXI-0605.00/US	Utility - ORG	Issued	METHOD FOR SWITCHING DATA STREAMS	11/15/2000	09/712,185	1/11/2005	6,842,452	ES.
NMXI-0606.00/US	utility - ORG	Issued	REMOVING REDUNDANT INFORMATION IN HYBRID BRANCH PREDICTION	10/3/2001	09/970,485	4/10/2007	7,203,825	S.
NMXI-0607.00/US	Utility - ORG	issued	METHOD OF CREATING A PHOTONIC VIA USING FIBER OPTIC	12/19/2000	09/741,520	11/18/2003	6,650,823	S.
NMX1-0608.00/US	Utility - ORG	Issued	METHOD OF CREATING AN ANGLED WAVEGUIDE USING LITHOGRAPHIC TECHNIQUES	12/19/2000	09/741,237	2/21/2006	7,000,434	સ
NMXI-0609.00/US	Utility - ORG	ssued	MARKINGS FOR ALIGNING FIBER OPTIC BUNDLE	12/19/2000	09/741,642	10/21/2003	6,636,671	æ
NMXI-0609.01/05	Offity - DIV	Issued	MARKINGS FOR ALIGNING FIBER OPTIC BUNDLE	9/26/2003	10/672,329	8/9/2005	6,928,215	S
NMXI-0610.00/US	Utility - ORG	Issued	ALGNMENT OF OPTICAL FIBERS TO AN ETCHED ARRAY WAVEGUIDE	12/15/2000	09/738,800	9/30/2003	6,628,865	S.
NWXI-0611.00/US	Utility - ORG	Issued	ALIGNMENT OF FIBER OPTIC BUNDLE TO ARRAY WAVEGUIDE USING AN EPOXY	12/15/2000	09/738,799	3/16/2004	6,707,970	ĸ
NMXI-0612.00/US	Utility - ORG	issued	ALIGNMENT OF FIBER OPTIC BUNDLE TO ARRAY WAVEGUIDE USING PINS	12/15/2000	09/738,686	5/3/2005	6,886,989	દ્ધ
NMXI-0613.00/US	Utility - ORG	Ssued	METHOD FOR CENTERING A CORE OF A WAVE GUIDE AMPLIFIER	3/30/2001	09/822,380	2/17/2004	5,694,076	S
NMXI-0614.00/US	Utility - ORG	Issued	METHOD AND SYSTEM FOR DYNAMIC AND INTERACTIVE ROUTE FINDING	3/28/2001	09/818,993	11/19/2002	6,484,092	S
NMXI-0615.00/US	Utility - ORG	Issued	CLOCK NETWORK	12/12/2000	09/735,358	12/16/2008	7,466,180	S
SO/00'9T90-KININ	otility - ORG	Issued	PACKAGING AND ASSEMBLY METHOD FOR OPTICAL COUPLING NETHOD AND APPARATING FOR MILITIDITY ACCURATION.	6/26/2001	09/892,921	1/28/2003	6,512,861	S
NMXI-0617.00/US	Utility - ORG	Issued	DIMENSIONAL SEPARABLE SYMMETRIC FILTERING	11/20/2000	09/718,877	6/15/2004	6,751,640	SO
NMXI-0618.00/US	Utility - ORG	{ssued	HIGH-SPEED DIGITAL DISTRIBUTION SYSTEM	5/25/1999	09/318,287	9/10/2002	6,449,308	SU
NMXI-0619.00/US	Utility - ORG	issued	FREQUENCY	8/20/2001	09/933,547	4/5/2005	6,877,060	S
NMX1-0620.00/US	Utility - ORG	Issued	INITIALIZATION OF NEGATIVE CHARGE PUMP	3/15/2001	09/809,623	4/22/2003	6,552,600	S
NMX1-0621.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR SWITCHING AN OPTICAL BEAM IN A SEMICONDUCTOR SUBSTRATE	3/27/2001	09/819,160	8/5/2003	6,603,893	80
NMXI-0622.00/US	Utility - ORG	issued	INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (AWG) AND OPTICAL AMPLIFIER(S)	10/17/2001	09/982,227	5/23/2006	7,050,663	S
NMXI-0623.00/US	Utility - DIV	Issued	USE OF MEMBRANE PROPERTIES TO REDUCE RESIDUAL STRESS IN AN INTERLAYER REGION	9/10/2002	10/238,081	9/2/2003	6,614,065	ยร
NMXI-0623.01/US	Utility - CON	Issued	USE OF MEMBRANE PROPERTIES TO REDUCE RESIDUAL STRESS IN AN INTERLAYER REGION	9/18/2002	10/246,273	7/22/2003	6,596,614	S
NMXI-0624.00/US	utility - arg	Issued	APPARATUS AND METHOD FOR REMOVING PHOTOMASK CONTAMINATION AND CONTROLLING ELECTROSTATIC DISCHARGE	5/8/2001	09/850,766	5/11/2004	6,734,443	ß
NMXI-0625.00/US	Utility - ORG	lssued	APPARATUS FOR ENHANCED RATE CHEMICAL MECHANICAL POLISHING WITH ADJUSTABLE SELECTIVITY	12/22/2000	09/746,470	12/3/2002	6,488,571	દ્ર
NMXI-0625.01/US	Utility - DIV	Issued	APPARATUS FOR ENHANCED RATE CHEMCIAL MECHANICAL POLISHING WITH ADJUSTABLE SELECTIVITY	8/6/2002	10/213,776	6/14/2005	6,905,397	ß
NMXI-0626.D0/US	Utility - ORG	Issued	VIA-IN-PAD WITH OFF-CENTER GEOMETRY	12/29/2000	09/751,614	12/21/2004	6,833,615	æ
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ssued	Issued	ssued	Issued	Abandoned	ssued	Issued	ssued	Abandoned	Issued	Issued	Issued	Issued	Issued	Issued	issued	Issued	Issued	ssued	Abandoned	ssued	Issued	Issued	Issued	Issued	Issued	Abandoned	Expired	Issued	Issued	Issued	ssued	Issued
AUTOMATIC CODE GENERATION FOR EMBEDDED SYSTEMS	CACHE ARCHITECTURE WITH REDUNDANT SUB ARRAY METHOD AND SYSTEM FOR FORTHWARE MODIFY AND SYSTEM AND	PROCESSOR	DYNAMIC LOCAL DRIVE AND PRINTER SHARING	NETWORK COMMUNICATION CASING	A METHOD OF REDUCING RESIDUE DEPOSITION ONTO ASH CHAMBER BASE SURFACES	DIGITAL LEAKAGE COMPENSATION CIRCUIT	INTEGRATED CIRCUIT ANNEALING METHODS AND APPARATUS	FASTENER INSTALLATION TOOLS, SYSTEMS, AND METHODS OF USE	FASTENER INSTALLATION SYSTEMS	FASTENER INSTALLATION TOOL	FASTENER INSTALLATION TOOL	AC TESTING OF LEAKAGE CURRENT IN INTEGRATED CIRCUITS USING RC TIME CONSTANT	AC TESTING OF LEAKAGE CURRENT IN INTEGRATED CIRCUITS USING RC TIME CONSTANT	AC TESTING OF LEAKAGE CURRENT IN INTEGRATED CIRCUITS USING RC TIME CONSTANT	ON-DIE CACHE MEMORY WITH REPEATERS	METHOD AND APPARATUS FOR COMPRESSING CALORIE BURN CALCULATION DATA USING POLYNOMIAL COEFFICIENTS	METHOD AND APPARATUS FOR INTERSYSTEM CUT/COPY AND PASTE	LARGE FORMAT EMISSIVE DISPLAY	LARGE FORMAT EMISSIVE DISPLAY	MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION	DUAL-SWIPING INTERCONNECTION CLIP, AND HOOK AND SLOT ARRANGEMENT FOR PRINTED CIRCUIT BOARD (PCB) ATTACHMENT	KICKER FOR NON-VOLATILE MEMORY DRAIN BIAS	LOAD FOR NON-VOLATILE MEMORY DRAIN BIAS	MULTIPLIERLESS PYRAMID FILTER	METHOD AND APPARATUS FOR DETECTING AC REMOVAL	METHOD OF SCHEDULING MODULES ON A CAROUSEL	TIME VARYING FILTER WITH ZERO	INTERCONNECTING CIRCUIT MODULES TO A MOTHERBOARD USING AN EDGE CONNECTOR WITH CONDUCTIVE POLYMER CONTACTS	PORTABLE DOWNLOAD UNIT INCLUDING A MEMORY CHIP-TO-CD BURNER	CONNECTOR ASSEMBLY WITH DECOUPLING CAPACITORS	CONNECTOR ASSEMBLY WITH DECOLIPTING CAPACITORS	METHODS OF FABRICATING A VIA-IN-PAD WITH OFF-CENTER GEOMETRY
9/17/2001	6/27/2001	6/27/2001	7/27/2001	9/28/2001	3/26/2001	3/29/2001	4/25/2001	4/3/2006	9/5/2003	7/28/2003	3/29/2001	10/19/2005	7/12/2004	4/19/2001	6/27/2001	1/30/2001	3/30/2001	1/5/2006	5/2/2001	1/2/2002	3/30/2001	12/29/2000	12/29/2000	1/3/2001	3/9/2001	6/29/2001	9/7/2001	5/4/2001	10/23/2001	8/25/2003	5/15/2003	11/22/2004
09/953,277	09/894,638	09/894,513	09/917,320	00/05/ 500	09/818,177	09/823,191	09/842.389	11/396.769	10/655,822	10/628.597	09/871 247	11/253,377	10/889,417	09/838,730	09/893,779	09/773,174	09/823,215	11/325,961	09/847.447	10/039.095	09/821,114	09/752,550	09/752,535	09/754.684	09/802,451	09/895.113	60/317.802	09/849,506	09/999,784	10/647,396	00/858 77A	10/994,995
3/7/2006	12/9/2003	10/7/2003	1/15/2008		5/20/2003	5/28/2002	1/6/2004	1	5/23/2006	8/5/2008	9/73/2003	3/25/2008	11/22/2005	8/17/2004	11/11/2003	2/10/2004	11/21/2006	2/2/2010	1	1/13/2004	4/8/2003	6/1/2004	5/27/2003	12/9/2003	8/3/2004			10/8/2002	9/9/2008	5/31/2005	enoc/arte	4/24/2007
7,010,780	6,662,271	6,631,444	7,320,033		6,564,811	6,396,305	6.675.057	.) 4 14 14 14	7,048,073	7,407,020	5 537 803	7,348,790	6,967,496	6,777,970	6,647,455	6,689,057	7,139,842	7,654,878		6.677.233	6,544,047	6,744,671	6.570.789	6.662.209	6.772.366			6,461,169	7,424,208	6,898,852	6 621 787	7,208,348 US
C.	C.S	S	5 5	ì	St	5 5	- - 3	ž č	<u> </u>	<u>.</u> 5	ō	rs L	S	Ç	S.	S	S	CS 55	<u> </u>	5	Su	S :	5 1	5	<u> </u>	F 6	Ē	S	S	US C	ā	SS.

	SS SS	6,725,247	4/20/2004	11/032,828 09/846,609	4/30/2001	TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE	issued	Utility - ORG	NMX1-0679.00/US
	US VS	6,873,380	3/29/2005	09/904,269	7/12/2001 7/12/2001	PROVIDING OPTICAL ELEMENTS OVER EMISSIVE DISPLAYS PROVIDING OPTICAL ELEMENTS OVER EMISSIVE DISPLAYS PROVIDING OPTICAL ELEMENTS OVER EMISSIVE DISPLAYS	Issued	Utility - ORG	NMXI-0678.00/US
	SG	7,572,666	8/11/2009	10/438,146	5/13/2003	ASSEMBLING DISDI AV MODIN OF WEEN SLEEL KUDE AND PROGRAMMING ASSEMBLING DISDI AV MODIN OF	Issued Abandoned	Utility - CON	NMXI-0676.01/US
	Sn	6,605,527	8/12/2003	09/896,531	6/30/2001	ELEMENT	125040	Concy - Coo	
	SR	6,770,531	8/3/2004	09/896,008	6/30/2001	ADHESIYE MATERIAL FOR PROGRAMMABLE DEVICE REDUCED AREA INTERSECTION BETWEEN ELECTRODE AND PROGRAMMING	Issued	Othry - ORG	NMX)-0675.00/US
	SD	7,064,344	6/20/2006	10/418,548	4/17/2003	BARRIER MATERIAL ENCAPSULATION OF PROGRAMMABLE MATERIAL	Issued	Utility - DIV	NMXI-0674.01/US
	SO	6,642,102	11/4/2003	09/896,530	6/30/2001	BARRIER MATERIAL ENCAPSULATION OF PROGRAMMABLE MATERIAL	ssued	Calley - ORG	141V1X1-0074.00/US
	Sn	6,514,805	2/4/2003	09/896,532	6/30/2001	TRENCH SIDEWALL PROFILE FOR DEVICE ISOLATION	ssued	Other ORG	NIMYL-0673,00/05
	S	7,120,904	10/10/2006	09/552,292	4/19/2000	DATA-FLOW METHOD FOR OPTIMIZING EXCEPTION-HANDLING INSTRUCTIONS IN PROGRAMS	Issued	Utility - ORG	NMXI-0671.00/US
	Sn	6,370,685	4/9/2002	09/226,804	1/6/1999	IN PROGRAMS	Issued	Utility - ORG	NMX1-0670.00/US
	SU	7,308,563	12/11/2007	09/968,278	9/28/2001	DUAL-TARGET BLOCK REGISTER ALLOCATION	ssued	Utility - ORG	NMXI-0669.00/US
	Sn	6,633,972	10/14/2003	09/878,062	6/7/2001	METHOD AND APPARATUS FOR UTILIZING STATIC QUEUES IN PROCESSOR STAGING	Issued	Utility - ORG	NMXI-0668.00/US
	Sn	6,671,172	12/30/2003	09/950,100	9/10/2001	ELECTRONIC ASSEMBLIES WITH HIGH CAPACITY CURVED FIN HEAT SINKS	Issued	Utility - ORG	NMXI-0667.00/US
	us us	6,889,237	5/3/2005	09/817,711 09/823,212	3/26/2001 3/30/2001	TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE	Abandoned Issued	Utility - ORG	NWXI-0665.00/US
	SU	7,392,277	6/24/2008	09/893,868	6/29/2001	CASCADED DOMINO FOUR-TO-TWO REDUCER CIRCUIT AND METHOD	Issued	Utility - ORG	NMXI-0664.00/US
	Cs 2	7,428,568	9/23/2008	09/956,903	9/21/2001	SYMMETRIC CASCADED DOMINO CARRY GENERATE CIRCUIT	Issued	Utility - ORG	NMXI-0663.00/US
	C C	7,325,025	1/29/2008	10/020,447	12/18/2001	LOOK-AHEAD CARRY ADDER CIRCUIT	Issued	Utility - ORG	NMXI-0662.00/US
		7.216.201	5/8/2007	11/327,454	1/9/2006	PARALLEL CACHELETS	issued	Utility - CON	NMXI-0661.01/US
	: C	7 434 576	8/0/2/9/8	09/893.573	6/27/2001	PARALLEL CACHELETS	ssued	Utility - ORG	NMXI-0661.00/US
	8.	7,352,209	4/1/2008	11/411,647	4/26/2006	VOLTAGE-LEVEL CONVERTER PARALLEL CACHELETS	Issued	Prov - ORG	NMXI-0660.00/US NMXI-0661.00/PR
	SU			09/817,719	3/26/2001	ENABLING MANUAL ADJUSTMENT OF POINTING DEVICE CURSOR SPEED	Abandoned	Utility - ORG	NMXI-0659.00/US
	SO	6,952,122 6,766,286	1.0/4/2005 7/20/2004	09/966,481 09/820,108	9/28/2001 3/28/2001	GENERATING PUISES FOR RESETTING INTEGRATED CIRCUITS PYRAMID FILTER	Issued	Utility - ORG	NMX1-0658.00/US
D/	S	6,661,264	12/9/2003	09/967,180	9/28/2001	SUPPLY VOLTAGE PREDETERMINED LEVEL AND A LOGIC PREDETERMINED INITIAL STATE	Issued	Utility - ORG	NMXI-0655.00/US
\TF	Sn	6,985,709	1/10/2006	09/887,595	6/22/2001	NOISE DEPENDENT FILTER RELEASING FUNCTIONAL BLOCKS IN RESPONSE TO A DETERMINATION OF A	Issued	Utility - ORG	NMX1-0655,00/US
: NI -	ᅜ	6,462,612	10/8/2002	09/894,151	6/28/2001	CHOPPER'S L'ABRILZED BANDGAP RÉFERÈNCE CIRCUIT TO CANCEL OFFSET VARIATION	Issued	Utility - ORG	NMXI-0654.00/US
г	S.	6,650,589	11/18/2003	09/997,809	11/29/2001	LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY	Issued	Utility - ORG	NMXI-0653.00/US
	US	7,010,706	3///2006	09/834,408	4/13/2001	DECOUPLING THE POWER POTENTIAL	130000	Conty Cito	
	į	1		200	d that there is	APPARATUS HAVING A FIRST CIRCUIT SUPPLYING A POWER POTENTIAL TO A	SSIGN	IIIII ORG	NVXI-0852 dn/ijs
	Countr	Patentino		SerialNo	हाeDate	10-6.34	MII Dekino / Maite/Type / Matterstatus	MatterType	MTI-DektNo

NMXI-0701.00/US Utility - DIV NMXI-0702.00/US Utility - ORG NMXI-0702.01/US Utility - CON NMXI-0703.00/US Utility - ORG	NMXI-0696.00/US Utility - ORG NMXI-0698.00/US Utility - CON NMXI-0698.00/US Utility - CON NMXI-0699.00/US Utility - ORG			NMXI-0685.00/US Utility - ORG NMXI-0685.01/US Utility - CON NMXI-0686.00/US Utility - ORG NMXI-0686.01/US Utility - CON NMXI-0686.02/US Utility - CON NMXI-0686.02/US Utility - CON	NMXI-0680.00/US Utility - CON NMXI-0681.00/US Utility - CON NMXI-0682.00/US Utility - CON NMXI-0683.00/US Utility - COR NMXI-0683.00/US Utility - ORG NMXI-0683.01/US Utility - CIP NMXI-0684.00/US Utility - CIP
Abandoned Issued Abandoned Abandoned	Issued Issued Issued Issued Issued Abandoned	Abandoned Issued Issued	Issued Issued Issued Issued	issued issued issued issued issued issued issued	Utility - ORG Issued SYS Utility - CON Issued PAK Utility - CON Issued SYS Utility - CON Issued SYS Utility - CON Issued SYS Utility - CIP Issued PUI Utility - CIP Issued BES Utility - CIP Issued APR
METHOD AND APPARATUS FOR ACCESSING THREAD-PRIVATIZED GLOBAL STORAGE OBJECTS REPAIRABLE TILED DISPLAYS REPAIRABLE TILED DISPLAYS PARAMETRIC REPRESENTATION METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN	BEAM-ADDRESSED MICROMIRROR DIRECT VIEW DISPLAY CHARGE CONTROLLED MIRROR WITH IMPROVED FRAME TIME UTILIZATION AND METHOD OF ADDRESSING THE SAME SAWC PHASE DETECTION METHOD AND APPARATUS PCB-TO-CHASSIS MOUNTING SCHEMES METHOD AND APPARATUS TO EMULATE EXTERNAL IO INTERCONNECTION	MECHANISM TO IMPROVE AUTHENTICATION FOR REMOTE MANAGEMENT OF A COMPUTER SYSTEM VARIABLE LEVEL MEMORY VARIABLE LEVEL MEMORY VARIABLE LEVEL MEMORY MEMBRANE-ACTUATED CHARGE CONTROLLED MIRROR (CCM) PROJECTION DISPLAY	BYPASSABLE AMPLIFIER MOUNTING BRACKET FOR PCS AND OTHER ANTENNAS COMMON BOOT ENVIRONMENT FOR A MODULAR SERVER SYSTEM SUCCESSIVE TEMPLATE GENERATION USING MINIMAL RANDOM ACCESS MEMORY BANDWIDTH METHOD AND APPARATUS TO IMPROVE THE PROTECTION OF INFORMATION PRESENTED BY A COMPUTER MECHANISM TO IMPROVE AUTHENTICATION FOR REMOTE MANAGEMENT OF A COMPUTER SYSTEM	MOBILE STATION LOCATING SYSTEM AND METHOD MOBILE STATION LOCATING SYSTEM AND METHOD COMMUNICATION CONTROL FOR A USER OF A CENTRAL COMMUNICATION CENTER COMMUNICATION CONTROL FOR A USER OF A CENTRAL COMMUNICATION CENTER COMMUNICATION CONTROL FOR A USER OF A CENTRAL COMMUNICATION CENTER SPREAD SPECTRUM CODES FOR USE IN COMMUNICATION	TEM FOR SHARING CHANNELS BY INTERLEAVING FUTS THOM AND APPARATUS FOR REORDERING MEMORY REQUESTS FO THOD AND SYSTEM FOR USING INTERNAL FIFO RAM TO IMPROVE THOD AND SYSTEM FOR USING INTERNAL FIFO RAM TO IMPROVE TEM BOOT TIMES SE POSITION MODULATION WITH SPREAD SPECTRUM ST-OF-M PULSE POSITION MODULATION DETECTOR TARATUS FOR RECEIVING AND CORRELATING A SPREAD SPECTRUM NAL
	10/28/1998 N 10/15/1998 11/4/1992 8/21/2001 10/16/2001	-	5/18/1998 1/14/2000 11/20/2001 5/28/2001 6/29/2001		6/25/2001 6/25/2001 R 5/24/2005 7/29/2005 11/1/1993 6/6/1995 5/7/1999
11/437,352 11/437,352 09/906,342 11/334,729 10/309,472	09/179,933 09/172,615 07/971,689 09/934,871	11/188,420 09/955,282 10/666,988 09/208,041	08/821,511 09/080,812 09/483,215 10/086,410 09/894,755 09/895,738 10/002,337	08/893,938 09/282,281 08/823,234 09/354,926 09/819,460	9864,136 09/894,136 11/137,700 11/192,025 08/146,490 08/468,802 09/307,646
10/2/2007	2/29/2000 2/22/2000 1/4/1994 12/10/2002	11/4/2003 3/22/2005 2/29/2000	8/28/2001 10/24/2000 3/26/2002 6/7/2005 7/22/2003 7/4/2006 8/15/2006	5/4/1999 10/23/2001 10/26/1999 7/3/2001 5/15/2004	2/3/2009 4/1/2008 4/1/2008 7/8/2008 8/5/1997 9/9/1997 11/13/2001
7,277,066	5,031,656 5,028,696 5,276,704 6,493,233	5,643,169 6,870,767 6,031,657	6,282,228 6,137,354 6,361,007 6,904,482 6,598,133 7,073,070 7,093,124	5,901,358 6,308,073 5,974,310 6,256,492 6,751,456	7,486,685 7,353,349 7,358,383 7,358,383 5,654,978 5,656,379 6,317,452
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NMXI-0722.00/US	SO/00.17/0-01/05	NMXI-0720.00/US	NWXI-0719.00/US	SO/707/02/18	NMXI-0/15.00/US	NMXI-0715.00/US	NMXI-0714.00/US	NMXI-0713.00/US	NMXI-0712.03/US	NMXI-0712.02/US	NMXI-0712.01/U\$	NMXI-0712.00/US	NMXI-0711.00/US	NMXI-0709.00/US	NMXI-0708.00/US	NWXI-0707_01/US	NWX1-0707.00/US	NWXI-0706.00/US	NMX1-0705.00/US	NMX1-0704.00/US
Utility - ORG	Utility - ORG	Utility - ORG	Unity - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG	Utility - CON	Utility - CON	Utility - CIP	Utility - CIP	Utility - DRG	utility - ore	Utility - ORG	Utility - ORG	Utility - DIV	Utility - ORG	Utility - ORG	Utility - ORG	MTI-baktivo Mattertype
issued	ssued	Issued	Issued	ssued	sued	issued	Abandoned	Issued	Issued	!ssued	Issued	Issued	issued	Issued	Issued	Issued	Issued	lssued	Issued	MatterStatus
DISPLAY FOR COLOR BLIND USE	PROBABALISTIC NETWORKS FOR DETECTING SIGNAL CONTENT	SOFTWARE CALL CONTROL AGENT	ANSWERING SYSTEM	PROPERTY MANAGEMENT SYSTEM PROTOCOL AUTO-DETECTION INTELLIGENT CORWARDED TELEPHONE CALL HANDLING WITH A CALL	SOLDERING STATION	LOCKED CONTENT ADDRESSABLE MEMORY FOR EFFICIENT ACCESS METHODS AND APPARATISES LIGING A FIRES OF EVENING INTO A TOP A PROPERTY OF THE PROPERT	PROTOCOL DRIVER APPLICATION PROGRAMMING INTERFACE FOR OPERATING SYSTEMS	RECOVERY OF BIT-ROTATED FRAMES DURING FACSIMILE TRANSMISSIONS IN A GLOBAL SYSTEM FOR MOBILE COMMUNICATIONS (SSM) NETWORK	WALLET DETACHABLY RETAINING A PERSONAL INFORMATION DEVICE AND RECEIVING A WAKE SIGNAL AND A STAY AWAKE SIGNAL FROM THE PERSONAL INFORMATION DEVICE	PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROGRAMMING DATA FROM A COMPUTER TO THE PERSONAL INFORMATION DEVICE VIA THE PCMCIA PORT OR THROUGH A DOCKING STATION WITH BAUD RATE CONVERSION MEANS	A WALLET DETACHABLY RETAINING A PERSONAL INFORMATION DEVICE WHICH SUPPLIES A WAKE SIGNAL TO THE WALLET TO TURN ON AN INPUT DEVICE OF THE WALLET	PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROGRAMMING DATA FROM A COMPUTER TO THE PERSONAL INFORMATION DEVICE VIA THE PCMCIA PORT OR THROUGH A DOCKING STATION WITH BAUD RATE CONVERSION MEANS	A ZERO INSERTION FUNCE CONNECTOR FOR SUBSTRATES WITH EDGE CONTACTS	SLURRY FOR POLISHING A BARRIER LAYER ELECTRONIC ASSEMBLIES WITH FILLED NO-FLOW UNDERFILL	GLOBAL I/O TIMING ADJUSTMENT USING CALIBRATED DELAY ELEMENTS	WRITE-ONCE POLYMER MEMORY WITH E-BEAM WRITING AND READING	WRITE-ONCE POLYMER MEMORY WITH E-BEAM WRITING AND READING	STOPPING REPLAY TORNADOES	DIGITAL UPDATE SCHEWE FOR ADAPTIVE IMPEDANCE CONTROL OF ON-DIE INPUT/OUTPUT CIRCUITS	AUTOMATIC SYMBOLIC INDEXING METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN
11/21/2001		1/4/2002 9/28/2001	9/28/2001	1/3/2002	11/25/2002	1/9/2002	1/31/2002	7/19/2002	4/12/2001	8/5/1999	11/11/1998	2/24/1998	9/28/2001	11/30/2001 10/26/2001	9/28/2001	10/28/2002	9/27/2001	12/31/2001	9/28/2001	12/3/2002
09/991,629	09/963,177	10/040,689 09/967,367	09/967,108	10/038,917	10/304,195	10/042,905	10/062,863	10/199,382	09/834,167	09/368,878	09/189,572	09/028,367	09/967,060	10/002,855 10/003,238	09/965,223	10/282,534	09/966,014	10/039,588	09/965,627	10/309,529
8/16/2005	11/14/2006	10/12/2004 2/8/2005	2/27/2007	9/18/2007	3/20/2007	8/17/2004		5/17/2005	1/18/2005	3/13/2001	4/24/2001	11/2/1999	1/6/2004	4/13/2004 1/29/2008	3/27/2007	11/4/2003	9/23/2003	10/4/2005	6/3/2003	12/18/2007
6,931,151	7,136,813	6,804,317 6,853,720	7,184,536	7,272,211	7,194,182	6,779,089		6,894,801	6,845,455	5,202,209	6,223,233	5,978,591	6,672,891	6,719,920 7,323,360	7,197,659	6,643,161	6,625,052	6,952,764	6,573,747	IssueDate:
US	ម្រ	≅ 55	US	เร	SU	SS	US	SN	ષ્ટ	જ્ઞ	รย	સ	US	รบ รบ	US .	US -	US .	Sn	SU	US Sn

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MIT-DickING MatterStatus NMXI-0723.00/US Utility - ORG Issued	Utility - ORG	MatterStat	METHOD AND APPARATUS FOR SOFTWARE SELECTION OF PROTECTED REGISTER SETTINGS	11/14/2001			6/7/2005
NMXI-0724.00/US	Utility - ORG	Issued	SYNCHRONIZING DATA OR SIGNAL TRANSFER ACROSS CLOCKED LOGIC DOMAINS	1/2/2002	브	10/038,956	0/038,956 1/9/2007
NMXI-0725.00/US	Utility - ORG	Issued	FLASH MEMORY PROGRAM AND ERASE OPERATIONS	11/13/2001	0	09/993,336	9/993,336 8/30/2005
NMXI-0726.00/US	utility - ORG	Issued	CHARGE PUMP RIPPLE REDUCTION	1/2/2002		10/038,499	
NMXI-0726.01/US	Utility ~ CIP	Issued	CHARGE PUMP RIPPLE REDUCTION	6/27/2002		10/185,419	
NMXI-0727.00/US	Utility - ORG	issued	VOLTAGE DRIVER FOR A MEMORY	8/31/2001	_	09/945,021	
NMXI-0728.00/US	Utility - ORG	Issued	PHASE CHANGE MATERIAL MEMORY DEVICE	9/7/2001	^	09/948,830	
NMXI-0728.01/US	utility - DIV	Issued	PHASE CHANGE MATERIAL MEMORY DEVICE	4/29/2003		10/426,380	
NMXI-0729.00/US	Utility - ORG	Issued	REDUCING SHUNTS IN MEMORIES WITH PHASE-CHANGE MATERIAL	9/17/2001		09/953,833	
NMXI-0729.01/US	Utility - DIV	Issued	REDUCING SHUNTS IN MEMORIES WITH PHASE-CHANGE MATERIAL	1/19/2005		11/038,336	
NWX1-0730.00/US	Utility - ORG	Expired	DOUBLE DENSITY BACKWARD AND FORWARD COMPATIBLE CARD EDGE CONNECTOR SYSTEM	8/14/1990		07/567,123	07/567,123 10/29/1991
NMXI-0731.00/US	utility - ORG	(ssued	MULTIPLE LAYER PHASE-CHANGE MEMORY	8/31/2001		09/945,331	09/945,331 1/14/2003
NMXI-0731.01/US	Otility - DIV	issued	MULTIPLE LAYER PHASE-CHANGE MEMORY	11/21/2002		10/302,421	
NMXI-0731.02/US	Utility - CON	issued	MULTIPLE LAYER PHRASE-CHANGE MEMORY	12/4/2002		10/309,633	
NMXI-0732.00/US	Utility - ORG	Issued	SIGNAL TO NOISE RATIO OPTIMIZATION FOR VIDEO COMPRESSION BIT- RATE CONTROL	9/5/2001		09/947,331	
NMXI-0733.00/US	utility - ORG	Issued	EMBEDDED PCB IDENTIFICATION	9/11/2001		09/954,402	09/954,402 1/8/2008
NMXI-0734.00/US	Utility - ORG	Issued	OPTOELECTRICAL PACKAGE	3/28/2002		10/109,313	
NMX1-0735.00/US	Utility - ORG	Issued	ULTRA-THIN POLARIZATION MODE CONVERTERS BASED ON LIQUID CRYSTAL MATERIALS			10/265,873	
NMXI-0735.01/US	Utility - CON	ssued	ULTRA-THIN POLARIZATION MODE CONVERTERS BASED ON LIQUID CRYSTAL MATERIALS	7/26/2005		11/189,543	11/189,543 2/6/2007
NMX1-0736.00/US	Utility - CIP	Issued	FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING	3/28/2002		10/109,811	10/109,811 11/23/2004
NMXI-0736.01/US	Utility - DIV	Issued	FILM BULK ACCUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING	9/16/2004		10/942,147	10/942,147 12/26/2006
NMX1-0737.00/US	Utility - ORG	issued	1 X N FANOUT WAVEGUIDE PHOTODETECTOR	12/7/2001		10/008,922	10/008,922 2/15/2005
NMXI-0737.01/US	Utility - DIV	Issued	1 X N FANOUT WAVEGUIDE PHOTODETECTOR	1/25/2005		11/042,537	
NMXI-0738.01/US	Utility - DIV	Abandoned	SOCKET WARPAGE REDUCTION APPARATUS	1/31/2008		12/023,521	12/023,521
NMXI-0739.00/US	Utility - ORG	Issued	EXTENSION MECHANISM AND METHOD FOR ASSEMBLING OVERHANGING COMPONENTS	9/28/2001		09/964,747	09/964,747 10/5/2004
NMXI-0739.01/US	Utility - DIV	issued	EXTENSION MECHANISM AND METHOD FOR ASSEMBLING OVERHANGING COMPONENTS	8/12/2004		10/518,081	10/918,081 7/12/2005
NMX1-0740.00/US	utility - ORG	Issued	METHOD AND STRUCTURE FOR IDENTIFYING LEAD-FREE SOLDER	9/28/2001		09/964,746	09/964,746 8/7/2007
NMXI-0742.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR SYSTEM MANAGEMENT APPLICATIONS USING A LOCAL CONTROLLER	3/22/2002		10/104,341	10/104,341 9/19/2006
NMXI-0742.01/US	Utility - DIV	Issued	METHOD AND APPARATUS FOR GENERATING TRAFFIC IN AN ELECTRONIC BRIDGE VIA A LOCAL CONTROLLER	8/3/2006		11/462,264	11/462,264 3/18/2008
NMXI-0743.00/US	Utility - ORG	Issued	SYSTEM AND METHOD TO SYNCHRONIZE TWO OR MORE OBJECT MANAGEMENT SYSTEMS	1/23/2002		10/055,528	10/055,528 7/5/2005
NMXI-0744.00/US	Utility - ORG	Issued	DIELECTRIC MATERIAL TREATMENT	9/23/2002		10/252,295	10/252,295 5/11/2004
NMXI-0745.00/US	Utility - ORG	Issued	MEMORY SYSTEM ORGANIZED INTO BLOCKS OF DIFFERENT SIZES AND ALLOCATION METHOD THEREFOR	9/17/2001		09/953,356	09/953,356 2/1/2005

STATESTANDER: STATESTANDER		SO			10/445,272	5/23/2003	USING MULTIPLE SIMULATION ENVIRONMENTS	Abandoned	Utility - ORG	NMXI-0769.00/US
		S	7,418,639	8/26/2008	10/202,624	7/22/2002	TEST INTERFACE, SYSTEM, AND METHOD FOR TESTING COMMUNICATIONS DEVICES WITH NON-DETERMINISTIC LATENCY	Issued	Utility - ORG	NMXI-0768.00/US
		S)	7,218,491	5/15/2007	10/329,058	12/23/2002	ELECTROSTATIC DISCHARGE PROTECTION UNIT INCLUDING EQUALIZATION	issued	Utility - ORG	NMXI-0767.00/US
ISSUARD COMMENTE STATES CONTROLLING STATES		CS CS	5,927,497 7,498,201	3/3/2009	11/167,495	6/27/2005	METHOD OF FORMING A MULTI-DIE SEMICONDUCTOR PACKAGE	lssued	Utility - DIV	NMXI-0766.01/US
Institute Inst		: :	7 00 4 4 4 7 4	0/0/2000	10/326/070	במחכ/ ביל/ פ	MULTI-DIE SEMICONDUCTOR PACKAGE	issued	Utility - ORG	NMXI-0766.00/US
State Stat		7	7 371 975	5/13/2008	10/322.902	12/18/2002	ELECTRONIC PACKAGES AND COMPONENTS THEREOF FORMED BY	Issued	Utility - ORG	NMXI-0765.00/US
STATE SMARTH-CHARLE STATE STAT		es es	6,703,299	3/9/2004	10/03 2 ,115 10/774,869	12/21/2001 2/9/2004	UNDERFILL PROCESS FOR FLIP-CHIP DEVICE UNDERFILL PROCESS FOR FLIP-CHIP DEVICE	Issued Abandoned	Utility - ORG Utility - DIV	NMXI-0764.00/US NMXI-0764.01/US
SECUPION SCALABLE RECEIVER STRUCTURE FOR EFFICIENT BIT SEQUENCE DECODNIC 6/20/2995 6/21/2070 6/285,903 6/20/2000 6,069.938 105		S	7,212,021	5/1/2007	10/097,136	3/12/2002	MANUFACTURING INTEGRATED CIRCUITS AND TESTING ON-DIE POWER SUPPLIES USING DISTRIBUTED PROGRAMMABLE DIGITAL CURRENT SINKS	Issued	Utility - ORG	NMX1-0763.00/US
STATE STAT		SN	6,934,953	8/23/2005	09/997,669	11/28/2001	DEFERRED PROCEDURE CALL IN INTERFACE DESCRIPTON LANGUAGE	Issued	Utility - ORG	NMX1-0762.00/US
SSURED SCALABLE RECEIVER STRUCTURE FOR EFFICIENT BIT SEQUENCE DECODING 670/1997 08/285.803 5/30/2000 6.069.928 US		SS	7,112,970	9/26/2006	10/820,693	4/7/2004	COAXIAL RADIO FREQUENCY ADAPTER AND METHOD	ssued	Otility - DIV	NIVIXI-0/61.02/US
SEUNIOR SCALABLE RECEVER STRUCTURE FOR EFFICIENT BY SEQUENCE DECODING 6/30/1997 06/885,803 5/30/2000 6,069-328 US SEUNIOR SCALABLE RECEVER STRUCTURE FOR EFFICIENT BY SEQUENCE DECODING 6/30/1997 06/885,803 5/30/2000 6,069-328 US SEUNIOR DIGITAL IMPARAMENT LEARNING SEQUENCE DEVICE DECODING 6/30/1997 06/958,944 4/20/2004 6,724,344 US SEUNIOR DIGITAL IMPARAMENT LEARNING SEQUENCE ON A MEMORY 5/24/2005 10/041,543 9/22/2009 7,593,385 US SEUNIOR DIGITAL IMPARAMENT LEARNING SEQUENCE ON A MEMORY 5/24/2005 10/041,543 9/22/2009 7,593,385 US SEUNIOR DIGITAL IMPARAMENT SEQUENCE ON A MEMORY 5/24/2005 10/041,543 9/22/2009 7,593,385 US SEUNIOR DIGITAL IMPARAMENT SEQUENCE ON A MEMORY 5/24/2005 10/041,543 9/22/2009 7,593,385 US SEUNIOR DIGITAL IMPARAMENT SEQUENCE ON A MEMORY 5/24/2005 10/041,543 9/22/2009 7,593,385 US SEUNIOR DIGITAL IMPARAMENT SEQUENCE IMPERONACE VARIATION OF DRAVER 10/19/2004 10/041,543 9/22/2009 7,593,385 US SEUNIOR DIGITAL IMPARAMENT SEQUENCE IMPERONACE VARIATION OF DRAVER 10/19/2004 10/041,543 9/22/2009 7,363,555 US SEUNIOR METHOD AND APPARATUS FOR CONTROLLING ACCESS TO MOBILE DEVICES 1/19/2003 10/125,274 5/10/2003 6,577,274 US SEUNIOR METHOD AND APPARATUS FOR CONTROLLING ACCESS TO MOBILE DEVICES 1/19/2003 10/124,639 9/26/2003 7,363,932 US SEUNIOR METHOD AND APPARATUS FOR CONTROLLING ACCESS TO MOBILE DEVICES 1/19/2003 10/124,639 9/26/2003 7,363,932 US SEUNIOR METHOD AND APPARATUS FOR CONTROLLING ACCESS TO MOBILE DEVICES 1/19/2003 10/124,639 9/26/2003 7,363,932 US SEUNIOR METHOD AND APPARATUS FOR CONTROLLING ACCESS TO MOBILE DEVICES 1/19/2003 10/124,639 9/26/2003 7,363,932 US SEUNIOR METHOD AND APPARATUS FOR CANTROLLING ACCESS TO MOBILE DEVICES 1/19/2003 10/124,639 9/26/2003 7,363,932 US SEUNIOR METHOD AND APPARATUS FOR CANTROLLING ACCESS TO MOBILE DEVICES 1/19/2003 10/124,639 9/26/2003 7,363,932 US SEUNIOR METHOD AND APPARATUS FOR CANTROLLING ACCESS TO MOBILE DEVICES 1/19/2003 10/124,639 9/26/2003 7,263,932 US SEUNIOR METHOD AND APPA		รูบ	6,863,564	3/8/2005	10/645,229	8/20/2003	COAXIAL RADIO FREQUENCY ADAPTER AND METHOD	ssued	Utility - DIV	NMXI-0761,01/US
Interestation Interestatio		Z 02	6,844,738	1/18/2005	10/007,672	12/10/2001	COAXIAL RADIO FREQUENCY ADAPTER AND METHOD	ssued	Utility - ORG	NMXI-0761.00/US
State Stat		. 5	cc1,80c,7	5/1//2009	12/362 445	1/29/2002	METHOD FOR PACKAGING A TAPE SUBSTRATE	Abandoned	Utility - DIV	NMXI-0760.01/US
SCALABLE RECEIVER STRUCTURE FOR EFFICIENT BIT SEQUENCE DECODING 6/30/1997 08/885.803 5/30/2000 6,069.928 US EXPINED SISURED SISURED SOCIAL IMPAIRMENT LEARNING SEQUENCE DECODING 6/30/1997 08/885.803 5/30/2000 6,069.928 US EXPINED SISURED DEVICE ARIO INTERFACE WITH A VARIABLE NUMBER OF CHANNELS 17/2002 10/041.543 9/22/2009 7,523,230 US ISSURED NITERFACE WITH A VARIABLE NUMBER OF CHANNELS 17/2002 10/041.543 9/22/2009 7,523,230 US ISSURED NITERFACE WITH A VARIABLE NUMBER OF CHANNELS 17/2002 10/041.543 9/22/2009 7,523,230 US ISSURED NITERFACE WITH A VARIABLE NUMBER OF CHANNELS 17/2002 10/041.543 9/22/2009 7,523,230 US ISSURED NITERFACE WITH A VARIABLE NUMBER OF CHANNELS 17/2002 10/041.543 9/22/2009 7,523,230 US ISSURED NITERFACE CHANNELS FOR ANNALOS COMPENSATION OF DRIVER 17/2002 10/041.543 9/22/2009 7,523,230 US ISSURED NITERFACE CHANNELS FOR CONTROLLING ACCESS TO MOBILE DEVICES 12/19/2001 10/328.472 8/10/2003 6,577,274 US ISSURED NITERFACE CHANNEL OF CONTROLLING ACCESS TO MOBILE DEVICES 12/19/2001 10/328.472 8/10/2003 6,577,274 US INTERFACE CHANNEL OF CONTROLLING ACCESS TO MOBILE DEVICES 12/19/2001 10/328.472 8/10/2008 7,209,209 US INTERFACE CHANNEL OF CONTROLLING ACCESS TO MOBILE DEVICES 12/19/2001 10/328.472 8/10/2008 7,209,209 US INTERFACE CHANNEL OF CELCILOTION FOR THE LATTICE 8/7/2002 10/234.630 4/25/2006 7,209,207 US INTERFACE CHANNEL OF CHANNEL OF THE LATTICE 8/7/2002 10/334.630 4/25/2008 7,209,207 US INTERFACE CHANNEL OF CHANNEL OF THE LATTICE 8/7/2002 10/334.630 4/25/2008 7,269,307 US INTERFACE CHANNEL OF CHANNEL OF THE LATTICE 8/7/2002 10/334.630 4/25/2008 7,269,307 US INTERFACE CHANNEL OF CHANNEL OF THE LATTICE 8/7/2002 10/334.630 4/25/2008 7,269,307 US INTERFACE CHANNEL OF CHANNEL OF THE LATTICE 8/7/2002 10/334.630 4/25/2008 7,269,307 US INTERFACE CHANNEL OF THE LATTICE 8/7/2002 10/334.630 4/25/2008 7,269,307 US INTERFACE CHANNEL OF THE LATTICE 8/7/2002 10/334.630 7/3/2008 7,269,307 US INTERFACE CHANNELS CHANNELS CHANNELS CHANNELS CHANNELS CHANNELS CHANNELS CHANNEL		: 8	1 100 ADD	2/17/2000	10/22007	8/26/2012	STAK NETWORK METHOD FOR PACKAGING A TAPE STRATE	(ssued	Utility - ORG	NMXI-0760,00/US
Inter-		<u> </u>	7 072 352	7/4/2006	10/080.093	2/21/2002	INVERSE MULTIPLEXING OF UNMANAGED TRAFFIC FLOWS OVER A MULTI-	Issued	Utility - ORG	NMXI-0759.00/US
Market-Osakus Market-Osakus Market-Osakus Market-Osakus Market-Osakus Market-Osakus Market-Osakus Market-Osakus Market-Osakus Market-Osakus Market-Osa		SU	5,982,988	1/3/2006	10/044,766	1/10/2002	PROGRAMMABLE FRAME SPLITTER	Issued	Utility - ORG	NMXI-0758-00/US
Inter- I		S	7,240,247	7/3/2007	10/133,625	4/29/2002	PROVIDING A CHAIN OF TOKENIZED ERROR AND STATE INFORMATION FOR A CALL STACK	issued	Utility - ORG	NMXI-0757.00/US
STATES		SU	5,560,317	5/6/2003	10/038,411	1/3/2002	RECEIVING CALLER IDENTIFICATION INFORMATION WITH A TELECOMMUNICATIONS DEVICE FOR THE DEAF	Issued	Utility - ORG	NMX1-0756.00/US
Marke Mark		ᅜ	7,426,392	9/16/2008	10/154,948	5/23/2002	SEARCH RECEIVER USING ADAPTIVE DETECTION THRESHOLDS	Issued	Utility - ORG	NMXI-0755.00/US
SERUED		S.	7,036,071	4/25/2006	10/214,630	8/7/2002	PRACTICAL CODING AND METRIC CALCULATION FOR THE LATTICE INTERFERED CHANNEL	Issued	Utility - ORG	NMXI-0754.00/US
Marke Markerstatus		ะร	6,747,598	5/8/2004	10/417,540		METHOD AND APPARATUS FOR CONTROLLING ACCESS TO MOBILE DEVICES	Issued	utility - DIV	NMXI-0753.01/US
		S	6,577,274	6/10/2003	10/028,472	12/19	METHOD AND APPARATUS FOR CONTROLLING ACCESS TO MOBILE DEVICES	Issued	Utility - ORG	NMXI-0753.00/US
Fighe Markerstatus Scalable receiver structure for efficient bit sequence decoding 6/30/1997 08/885,803 5/30/2000 6,069,928 us Expired Digital impairment learning sequence decoding 6/24/1999 60/140,705 Us Issued Pad and codec detection 6/24/1999 60/140,705 08/588,934 4/20/2004 6,724,814 Us Device and method for maximizing performance on a memory 5/24/2005 11/137,314 4/21/2009 7,523,230 Us Interface with a variable number of channels 1/7/2002 10/041,543 9/22/2009 7,593,318 Us Issued Method and apparatus for header updating 50 driver 1/7/2002 10/041,543 9/22/2008 7,363,595 Us Output Signal slew rate against device impedance variation of driver 2/31/2004 10/814,398 4/22/2008 7,363,595 Us		US.	7,000,126	2/14/2006	10/126,274	4/18/2002	METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER	Issued	Utility - ORG	NMXI-0752.00/US
	DV.	SI	7,363,595	4/22/2008	10/814,398	3/31/2004	METHOD AND APPARATUS FOR ANALOG COMPENSATION OF DRIVER OUTPUT SIGNAL SLEW RATE AGAINST DEVICE IMPEDANCE VARIATION	Issued	Utility - CON	NMXI-0751.00/US
Fire MarkerStatus Scalable receiver structure for efficient bit sequence decoding 6/30/1997 08/885,803 5/30/2000 6,069,928 us Expired Digital impairment learning sequence 6/24/1999 60/140,705 us Issued Pad and codec detection 6/22/2000 6/298,934 4/20/2004 6,724,814 us Device and method for maximizing performance on a memory 6/24/2005 11/137,314 4/21/2009 7,523,230 us	TE	SU	7,593,318	9/22/2009	10/041,543	1/7/2002	METHOD AND APPARATUS FOR HEADER UPDATING	Issued	Utility - ORG	MMXI-0750.00/US
Inte Markerstatus Friedric Scharko Parentro Country Issued SCALABLE RECEIVER STRUCTURE FOR EFFICIENT BIT SEQUENCE DECODING 6/30/1997 08/885,803 5/30/2000 6,069,928 US Expired DIGITAL IMPAIRMENT LEARNING SEQUENCE 6/24/1999 60/140,705 Issued PAD AND CODEC DETECTION 6/22/2000 09/598,934 4/20/2004 6,724,814 US	NIT	Sn	7,523,230	4/21/2009	11/137,314	5/24/2005	DEVICE AND METHOD FOR MAXIMIZING PERFORMANCE ON A MEMORY INTERFACE WITH A VARIABLE NUMBER OF CHANNELS	Issued	utility - DIV	NMXI-0749.00/US
ine Matterstatus Italie Italie Italie Italie IsiaBate Senalto Italie Ipatemityo Ipatemityo Ipatemityo IsiaBate IsiaBa	-	સ સ	6,724,814	4/20/2004	60/140,705 09/598,934	6/24/1999 6/22/2000	DIGITAL IMPAIRMENT LEARNING SEQUENCE PAD AND CODEC DETECTION	Expired	Prov - ORG Utility - ORG	NMXI-0747.00/PR NMXI-0748.00/US
Matterstatus Senanto IssueDate Patentino		Sn	6,069,928	5/30/2000	08/885,803	6/30/1997	SCALABLE RECEIVER STRUCTURE FOR EFFICIENT BIT SEQUENCE DECODING	ssued	Utility - ORG	NMXI-0746.00/US
Exhibit A - US Applications and Issued Patents		E	PatentNo		Sevial No. 100	ents Elebate	Exhil		MatterType	WIT-DEANO.

MTI-DektNo	MatterType	MatterStatus	C10	FileDate	SerialNo	IssueDate	PatentNG Country	Count
NMXI-0770.00/US	Utility - ORG	issued	KEHRESHING MEMOKY CELLS OF A PHASE CHANGE MATERIAL MEMOKY DEVICE	8/5/2002	10/212,630	7/27/2004	6,768,665	Sn
NMXI-0771.00/US	Utility - ORG	Issued	TUNABLE OPTICAL DISPERSION COMPENSATOR	4/17/2002	10/126,106	8/17/2004	6,776,493	US.
NMXI-0772.00/US	Utility - CON	(ssued	METHOD TO REALIZE FAST SILICON-ON-INSULATOR (SOI) OPTICAL DEVICE	4/25/2006	11/411,059	11/7/2006	7,133,586	ŞU
NMXI-0772.01/US	Utility - DIV	(ssued	METHOD TO REALIZE FAST SILICON-ON-INSULATOR (SOI) OPTICAL DEVICE	4/25/2006	11/410,591	11/7/2006	7,133,585	รม
NMXI-0773.00/US	Utility - CON	Issued	METHOD AND APPARATUS FOR WAFER LEVEL TESTING OF INTEGRATED OPTICAL WAVEGUIDE CIRCUITS	11/19/2004	10/994,021	6/10/2008	7,386,197	US.
NMXI-0774,00/US	utility - ORG	Abandoned	BIT ERROR RATE TEST SYSTEM FOR MULTI-SOURCE AGREEMENT	12/20/2001	10/027,053			ะร
NMXI-0775.00/US	Utility - ORG	Issued	ON-CHIP JITTER TESTING	12/26/2002	10/331,122	8/5/2008	7,409,621	ç
NMXI-0776.00/US	Utility - ORG	Issued	TESTING METHODOLOGY AND APPARATUS FOR INTERCONNECTS	12/16/2002	10/319,517	5/16/2006	7,047,458	S.
NMXI-0777.00/US	Utility - ORG	Abandoned	LCD CONTROLLER ARCHITECTURE FOR HANDLING FLUCTUATING BANDWIDTH CONDITIONS	1/31/2002	10/062,268			Ş
NMXI-0778.00/US	Utility - ORG	Issued	INVERSE MULTIPLEXING OF MANAGED TRAFFIC FLOWS OVER A MULTI-STAR NETWORK	.R 2/21/2002	10/080,329	10/30/2007	7,289,525	S
NMXI-0779.00/US	Utility - ORG	issued	WAFER-LEVEL TEST STRUCTURE FOR EDGE-EMITTING SEMICONDUCTOR LASERS	4/18/2002	10/126,348	2/20/2007	7,180,929	SS
NMXI-0779.01/US	Utility - DIV	Issued	WAFER-LEVEL TEST STRUCTURE FOR EDGE-EMITTING SEMICONDUCTOR LASERS	9/11/2003	10/659,898	10/17/2006	7,122,391	SS
NMXI-0780.00/US	Utility - ORG	Issued	SMALLER AND LOWER POWER STATIC MUX CIRCUITRY IN GENERATING MULTIPLIER PARTIAL PRODUCT SIGNALS	12/5/2003	10/728,395	12/11/2007	7,308,470	ᅜ
NMXI-0781.00/U5	Utility - ORG	Issued	ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT HAVING A RING OSCILLATOR TIMER CIRCUIT	4/24/2002	10/128,373	3/4/2008	7,339,770	SO
NMXI-0782.00/US	Utility - ORG	issued	COUNT VALUE TO ENABLE EFFICIENT POINTER EXCHANGE MECHANISMS IN	1 6/28/2002	10/186,564	10/5/2004	6,801,143	รถ
NMXI-0783.00/US	Utility - ORG	Issued	ASYNCHRONOUS FIFO'S RATE POLICING ALGORITHM FOR PACKET FLOWS	5/2/2002	10/137,753	12/26/2006	7,154,853	SU
NMXI-0784.00/US	Utility - ORG	issued	FLIP-CHIP PACKAGE INTEGRATING OPTICAL AND ELECTRICAL DEVICES AND COUPLING TO A WAVEGUIDE ON A BOARD	5/30/2002	10/158,250	6/22/2004	6,754,407	SN
NMXI-0784.01/US	Utility - CON	issued	FLIP-CHIP PACKAGE INTEGRATING OPTICAL AND ELECTRICAL DEVICES AND COUPLING TO A WAVEGUIDE ON A BOARD	5/6/2004	10/840,017	5/23/2006	7,049,704	Sn
NMXI-0785.00/US	Utility - ORG	Issued	APPARATUS AND METHODS FOR INTERCONNECTING COMPONENTS TO VIA- IN-PAD INTERCONNECTS	10/8/2002	10/267,158	12/7/2004	6,828,512	S
NMXI-0787.00/US	Utility - ORG	Issued	DYNAMIC COMPUTATION OF CHIPSET-SUPPORTED ACCELERATED GRAPHICS 3/21/2002 PORT APERTURE SIZES	3/21/2002	10/104,042	8/23/2005	6,933,944	Sn
NMXI-0787.01/US	Utility - CON	Issued	DYNAMIC COMPUTATION OF CHIPSET-SUPPORTED ACCELERATED GRAPHICS PORT APERTURE SIZES	6/29/2005	11/172,342	2/27/2007	7,184,050	SO
NMXI-0788.00/US	Utility - ORG	Issued	SLAVE I/O DRIVER CALIBRATION USING ERROR-NULLING MASTER REFERENCE	8/29/2002	10/231,863	3/20/2007	7,194,559	S.
NMX1-0789.00/US	Utility - ORG	Abandoned	LOW LATENCY MEMORY ACCESS METHOD USING UNIFIED QUEUE MECHANISM	9/23/2002	10/253,229			S
NMXI-0790.00/US	Utility - ORG	Issued	UPDATING STACK POINTER BASED ON INSTRUCTION BIT INDICATOR WITHOUT EXECUTING AN UPDATE MICROINSTRUCTION	9/30/2002	10/262,214	7/18/2006	7,080,236	SU
NMXI-0791.00/US	Utility - ORG	(SCIP)	CODE-BASED OPTICAL NETWORKS, METHODS, AND APPARATUS					

	S	7,161,908	1/9/2007	10/256,475	9/27/2002	CONTROL UNIT	Sucu	County : Cook	
	SU	7,324,516	1/29/2008	10/217,644	8/14/2002	DATA PACKET HEADER CONVERSION DETERMINISTIC BANDWIDTH THROTTLING TO EACH ITATE OPERATION OF A	ssued	Utility ORG	NIMYL-0818 00/03
	S	7,239,999	7/3/2007	10/200,328	7/23/2002	AUDIO	issue o	Chillip One	NIMXI-0817 00/145
	SS	7,231,468	6/12/2007	10/456,185	6/6/2003	FUTURE ACTIVITY LIST FOR PERIPHERAL BUS HOST CONTROLLER SPEED CONTROL PLAYBACK OF PARAMETRIC SPEECH ENCODED DIGITAL	issued issued	Officy - ONG	NMX1-0816 00/US
	S	7,289,557	10/30/2007	10/660,228	9/10/2003	MEAN SQUARE ALGORITHM	issued	THIRT DEC	NMXI-0815 DD/IIS
	ม	7,203,631	4/10/2007	10/334,113	12/31/2002	SYSTEM AND METHOD TO ANALYZE VLSI DESIGNS ADAPTIVE EQUALIZATION USING A CONDITIONAL INDICATE SIGNLESION I SACT	Issued	Utility - ORG	NWX: 0813.00/US
	SD ST	6,699,798	3/2/2004	10/131,017	4/24/2002	SUBSTRATES DATA LINK ANALYZER	Abandoned	Utility - ORG	NMXI-0812.00/US
	Sn	7,223,701	5/29/2007	10/236,203	9/6/2002	PROCESSING FOR GAP FILL PROMOTING ADHESION OF FLUOROPOLYMER FILMS TO SEMICONDUCTOR	issue o	College College	NAMYLOGI TOOLIG
	Ę	1,727,777	6/1/2010	10/160,641	5/31/2002	FORMING FERROELECTRIC POLYMER MEMORIES IN-SITU SEQUENTIAL HIGH DENSITY PLASMA DEPOSITION AND ETCH	Issued	Other ORG	NMX1-0809:00/US
	Sn			13/041,541	3/7/2011	EXECUTING APPLICATIONS FROM A SEMICONDUCTOR NONVOLATILE MEMORY	Pending	Utility - CON	NMXI-0808.01/US
	ŞU	7,904,897	3/8/2011	10/215,549	8/8/2002	EXECUTING APPLICATIONS FROM A SEMICONDUCTOR NONVOLATILE MEMORY	Issued	Utility - ORG	NMXI-0808.00/US
	SU	6,909,645	6/21/2005	10/196,401	7/16/2002	CLUSTER BASED REDUNDANCY SCHEME FOR SEMICONDUCTOR MEMORIES	Issued	Utility - ORG	NMXI-0807.00/US
	SU	7,272,623	9/18/2007	10/118,349	4/8/2002	METHODS AND APPARATUS FOR DETERMINING A FLOATING-POINT EXPONENT ASSOCIATED WITH AN UNDERFLOW CONDITION OR AN OVERFLOW CONDITION	ssued	Otility - ORG	NMXI-0806.00/US
	รถ	7,814,383	10/12/2010	11/251,664	10/17/2005	COMPACTING CIRCUIT RESPONSES	issued	Othey - CON	CO/1015080-191608
	SJ	7,185,253	2/27/2007	10/107,628	3/27/2002	COMPACTING CIRCUIT RESPONSES	Abandoned	Utility - DRG	NMXI-0805.00/US
	SU	6,862,714	3/1/2005	10/126,402	4/19/2002	ACCURATELY TUNING RESISTORS	ssued	Utility - ORG	NMXI-0804.00/US
	દ્ય	7,180,429	2/20/2007	10/076,817	2/14/2002	SLOW MOTION DETECTION SYSTEM	Issued	Utility - ORG	NMXI-0803.00/US
	SS	7,231,552	6/12/2007	10/280,458	10/24/2002	METHOD AND APPARATUS FOR INDEPENDENT CONTROL OF DEVICES UNDER TEST CONNECTED IN PARALLEL	Issued	Utility - ORG	NMXI-0802.00/US
	S.	6,765,681	7/20/2004	10/119,501	4/10/2002	MEASURING OPTICAL PHASE	Issued	Utility - ORG	S0/00'1080-18WN
	ଞ	7,456,868	11/25/2008	10/061,907	2/1/2002	DIGITAL CAMERA WITH ISO PICKUP SENSITIVITY ADJUSTMENT	Issued	Utility - ORG	NMXI-0800.00/US
	SI			10/112,194	3/29/2002	TELEPHONE CONFERENCE BRIDGE PROVIDED VIA A PLURALITY OF COMPUTER TELEPHONY RESOURCE ALGORITHMS	Abandoned	บถlity - ORG	NMX1-0799.00/US
	US	7,162,564	1/9/2007	10/190,851	7/9/2002	CONFIGURABLE MULTI-PORT MULTI-PROTOCOL NETWORK INTERFACE TO SUPPORT PACKET PROCESSING	Issued	Utility - ORG	NMX1-0798.00/US
	S	6,950,952	9/27/2005	10/211,465	8/1/2002	DYNAMIC POWER LEVEL CONTROL BASED ON A BOARD LATCH STATE	issued	Utility - ORG	NMXI-0797.00/US
P	SN	6,865,344	3/8/2005	09/712,822	11/13/2000	CODE-SWITCHED OPTICAL NETWORK	Issued	Utility · ORG	NMXI-0796.00/US
л Т	SS	6,904,201	6/7/2005	10/142,329	5/9/2002	METHODS	Issued	Utility - ORG	NMX1-0795.00/US
·_·	SO			11/145,291	6/3/2005	SEGMENTED COMPLEX DIFFRACTION GRATINGS	Abandoned	Utility - DIV	NMX1-0794.02/US
.,,	SO			11/144,583	6/3/2005	SEGMENTED COMPLEX DIFFRACTION GRATINGS	Abandoned	Utility - DIV	NMXI-0794.01/US
-	ទ			11/145,251	6/3/2005	SEGMENTED COMPLEX DIFFRACTION GRATINGS	Abandoned	Utility - DIV	NMXI-0794.00/US
	ī			60/702 061	A/10/2001	OPTICAL PHASE AND AMPLITUDE MEASUREMENT DEVICE	Expired	Prov - ORG	NMXI-0793.00/PR
	SC	6,807,600	10/19/2004	10/205,546	7/24/2002	Method, system, and program for memory based data transfer	Issued	Utility - ORG	NMX1-0792.00/US
3	Country	Patemolo	Issuepate	SerialNo	Date:	314 Same some constitution of the same some same some same some same same some same same same same same same same sa	MatterStatus	MatterType	MIT-Dekting

NMXI-0843.01/US	NMXI-0843.00/US	NMXI-0842.00/US	NMXI-0840.01/US	NN/X1-0840.00/US	SO/00'6E80-IXWN SO/00'8E80-IXWN	NMXI-0836,00/US	NMXI-0835.00/US	NMXI-0834.00/US	NMXI-0833.00/US	NMXI-0832.00/US	NMXI-0831.00/US	NWX1-0830.00/US	NMXI-0829.00/US	NMXI-0827.00/US	NMXI-0826.00/US	NMXI-0825.00/US	NMXI-0824.00/US	NMXI-0823.00/US	NMXI-0822.0Z/US	NMXI-0822.01/US	NMXI-0822.00/US	NMXI-0821.00/US	NMXI-0820.00/US	SU/0076180-IXWN
Utility - CON	Utility - ORG	Utility - ORG	Utility - CON	Utility - ORG	Utility - ORG	Utility - DIV	Utility - ORG	Utility - ORG	Utility - ORG	utility - org	Utility - DIV	Utility - ORG	Utility - ORG	utility - ORG	Utility - ORG	Utility - ORG	Utility - DIV	Utility - ORG	Utility - CON	Utility - DIV	Utility - ORG	Utility - ORG	Utility - ORG	Utility - ORG
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10/21/2004	12/19/2002	6/13/2005 4/8/2003	1/24/2006	11/5/2002	10/28/2002 12/12/2002 12/30/2002	9/10/2004	11/27/2002	10/25/2002	6/30/2003	9/30/2003	12/26/2006	12/20/2002	12/13/2002	10/30/2002	12/31/2002	12/31/2002	12/16/2005	7/31/2002	11/24/2008	8/12/2004	11/4/2002	10/30/2002	12/13/2002	12/31/2002
10/970,015	10/324,863	11/151,010 10/409,287	11/338,932	10/289,081	10/281,857 10/319,310 10/330,537	10/939,226	10/306,320	10/280,926	10/610,713	10/676,893	11/616,164	10/326,901	10/265,226	10/283,920	10/335,049	10/335,012	11/303,417	10/210,866	12/277,114	10/917,679	10/288,021	10/284,724	10/319,183	10/334,749
6/10/2008	12/14/2004	2/13/2007 8/30/2005		7/17/2007	10/30/2007 5/1/2007				3/24/2009	5/20/2008	1/5/2010	8/9/2005	8/14/2007 4/17/2007	6/26/2007	10/26/2004			5/29/2007	11/3/2009		3/10/2009	1/2/2007	9/18/2007	9/15/2004
7,386,643	6,832,268	7,177,502 6,937,794		7,245,945	7,289,945 7,211,501				7,509,548	7,376,922	7,642,660	6,927,346	7,257,538 7,205,562	7,287,102	6,810,443			7,223,660	7,611,828		7,501,230	7,158,631	7,271,403	6,792,179
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US	5,/8/,443	9/7/2004	10/442,834	3/20/2005	TO A COMPANY OF THE CHARGE SALE HOWEVERING STATE IN COMPANY STATE IN COMPANY STATE IN COMPANY STATE IN COMPANY			
i :				T Ino Inon	DOR DESIGN AND METHOD SOO DECITIONS VENTED OF MAN	Issued	Utility - ORG	NMXI-0869.D0/US
S.	7,162,614	1/9/2007	10/608,121	6/30/2003	ELIMINATION OF POTENTIAL RENAMING STALLS DUE TO USE OF PARTIAL RECIPTEDS	Issued	Utility - ORG	NMX1-0868.00/US
SI	7,143,279	11/28/2006	10/448,696	5/29/2003	OYNAMIC BIOS EXECUTION AND CONCURRENT UPDATE FOR A BLADE SERVER	Issued	Utility - ORG	NMXI-0867.00/US
SU	7,145,481	12/5/2006	10/676,888	9/30/2003	METHOD AND APPARATUS FOR TRUSTED KEYBOARD SCANNING	Issued	Utility - ORG	NMXI-0866.00/US
Sn	7,299,425	11/20/2007	10/606,868	6/26/2003	METHOD AND APPARATUS TO CREATE BYPASS LOGIC IN A DIGITAL CIRCUIT DESIGN	Issued	utility - ORG	NMXI-0865.00/US
સ			10/461,217	6/13/2003	METHOD AND APPARTUS TO CHARACTERIZE AN ELECTRONIC DEVICE	Abandoned	Utility - ORG	NMX1-0864.00/US
S.	7,176,122	2/13/2007	10/379,061	3/4/2003	DIELECTRIC WITH SIDEWALL PASSIVATING LAYER	Issued	Utility - ORG	NMXI-0863.00/US
ខ្ល	7,205,074	4/17/2007	10/334,841	12/31/2002	VENTING OF PELLICLE CAVITY FOR A MASK	Issued	Utility - ORG	NMXI-0862,00/US
ž			10/609 963	6/30/2003	POST-DEPOSITION MODIFICATION OF INTERLAYER DIFFECTRICS	Abandoned	Utility - ORG	NMXI-0861.00/US
S.	7,268,425	9/11/2007	10/382,680	3/5/2003	THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH	issued	Utility - ORG	NMXI-0860.00/US
េះ			10/427,377	5/1/2003	SMART CONTROL POINTS	Abandoned	Utility - ORG	NMXI-0859.00/US
SJ.	7,200,060	4/3/2007	10/620,469	7/15/2003	MEMORY DRIVER ARCHITECTURE AND ASSOCIATED METHODS	Issued	Utility - ORG	NMXI-0858.00/US
Sin	7,213,142	5/1/2007	10/421,287	4/22/2003	SYSTEM AND METHOD TO INITIALIZE REGISTERS WITH AN EEPROM STORED BOOT SEQUENCE	\ssued	Utility - ORG	NMXI-0857.00/US
US.	7,553,694	6/30/2009	11/100,885	4/4/2005	METHODS OF FORMING A HIGH CONDUCTIVITY DIAMOND FILM AND STRUCTURES FORMED THEREBY	lssued	Utility - DIV	NMXI-0856.01/US
S	7,417,255	8/26/2008	10/973,161	10/25/2004	METHODS OF FORMING A HIGH CONDUCTIVITY DIAMOND FILM AND STRUCTURES FORMED THEREBY	Issued	Utility - DIV	NMXI-0856.00/US
SN			10/453,115	6/2/2003	METHOD AND APPARATUS FOR PREFETCHING BASED UPON TYPE IDENTIFIER TAGS	Abandoned	Utility - ORG	NMX1-0855.00/US
Sn			10/435,347	\$/9/2003	APPARATUS AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING	Abandoned	Utility - ORG	NMX1-085a.00/US
SU			10/425,987	4/30/2003	LOW POWER ADDER	Abandoned	utility - ORG	NMXI-0853.00/US
N2			10/426,044	4/30/2003	LOW POWER ADDER CIRCUIT UTILIZING BOTH STATIC AND DYNAMIC LOGIC	Abandoned	Utility - ORG	NMXI-0852.00/US
S			12/100,380	4/9/2008	INTERCONNECTION DESIGNS AND MATERIALS HAVING IMPROVED STRENGTH AND FATIGUE LIFE	Pending	Utility - DIV	NMXI-0851.01/US
Sn	7,387,827	6/17/2008	10/427,168	4/30/2003	INTERCONNECTION DESIGNS AND MATERIALS HAVING IMPROVED STRENGTH AND FATIGUE LIFE	Issued	Utility - CIP	NWXI-0851.00/US
Ç,			10/336,629	1/3/2003	THERMAL MANAGEMENT FOR TELECOMMUNICATION DEVICES	Abandoned	Utility - ORG	NMXI-0850.00/US
รูบ			12/106,211	4/18/2008	METHODS TO PATTERN CONTACTS USING CHROMELESS PHASE SHIFT MASKS	Abandoned	Utility - DIV	NMXI-0849.01/US
SU	7,374,865	5/20/2008	10/304,303	11/25/2002	METHODS TO PATTERN CONTACTS USING CHROMELESS PHASE SHIFT MASKS	Issued	Utility - ORG	NMXI-0849.00/US
Sn			10/465,666	6/20/2003	SYSTEM AND PROCESS TO CONTROL ELECTROPLATING A METAL ONTO A SUBSTRATE	Abandoned	Utility - ORG	NMXI-0848.00/US
S.	7,240,260	7/3/2007	10/317,605	12/11/2002	STIMULUS GENERATION	Issued	Utility - ORG	NMXI-0847.00/US
SO	7,024,575	4/4/2D05	10/294,271	11/14/2002	APPARATUS AND METHOD FOR TIME SYNCHRONIZATION OF A PLURALITY OF MULTIMEDIA STREAMS	Issued	Utility - ORG	NMXI-0846.00/US
S.			10/278,535	10/23/2002	REMOVING FLUORINE-BASED PLASMA ETCH RESIDUES	Abandoned	Utility - ORG	NMXI-0845.00/US
SO	7,453,950	11/18/2008	10/369,933	2/18/2003	SYSTEM AND METHOD FOR EFFICIENTLY MODULATING DATA USING SYMBOLS HAVING MORE THAN ONE PULSE	Issued	Utility - ORG	NMXI-0844.00/US
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_ %			11/514,802	8/31/2006	COMMUNICATION REGISTERS FOR PROCESSING ELEMENTS	Abandoned	Utility - DIV	MMXI-0894,00/US
SS	7,307,295	12/11/2007	11/049,524	2/1/2005	METHOD AND AN APPARATUS FOR A HARD-CODED BIT VALUE CHANGEABLE IN ANY LAYER OF METAL	Issued	Utility - DIV	NMXI-0893.01/US
SS	7,326,973	2/5/2008	11/049,435	2/1/2005	METHOD AND AN APPARATUS FOR A HARD-CODED BIT VALUE CHANGEABLE IN ANY LAYER OF METAL	ssued	Utility - DIV	SU/00:E680-IXWN
Sn	7,210,079	4/24/2007	11/321,012	12/28/2005	APPARATUS AND METHOD FOR ADAPTING A LEVEL SENSITIVE DEVICE TO PRODUCE EDGE-TRIGGERED BEHAVIOR	issued	Utility - CON	NMXI-0892.01/US
SU	7,032,149	4/18/2006	10/449,332	5/30/2003	PRODUCE EDGE-TRIGGERED BEHAVIOR	issued	Utility - ORG	NMXI-0892.00/US
S IS	6,842,133	1/11/2005	10/603,859	4/30/2003	STROBE THROUGH DIFFERENTIAL SIGNATURE SUBARATIES AND ASSETUDO FOR ADMITTALE ASSETUDING	ssued	Utility - ORG	NMX1-0891.00/US
SU	7,029,978	4/18/2006	10/633,869	8/4/2003	CONTROLLING THE LOCATION OF CONDUCTION BREAKDOWN IN PHASE CHANGE MEMORIES DEVICE SYSTEM AND METHOD FOR CHANNEL SCANNING.	Issued	Utility - ORG	SO/0016880-1XWN
Sn			10/663,165	9/15/2003	MANAGEMENT	Abandoned	Other - Other	NIVIAI-USGS.UU/US
S			60/44//665	2/14/2005	ENHANCED GENERAL PACKET RADIO SERVICE (GPRS) MORILITY	1	37)	מאלי המספים החלילה
SN			10/457,092	6/6/2003	FUHANCED GORS MOBILITY MANAGEMENT	Expired	Prov - ORG	NWXI-0888.00/07
Su	7,395,301	7/1/2008	10/454,790	6/3/2003	METHOD AND PROCESS FOR DETERMINING A QUOTIENT	ssued	Utility - ORG	NWXI-0886.00/US
rs S	6,826,100	11/30/2004	10/404,408	3/31/2003	PUSH BUTTON MODE AUTOMATIC PATTERN SWITCHING FOR INTERCONNECT BUILT-IN SELF TEST	Issued	Utility - ORG	NMX1-0885.00/US
Sn	7,155,370	12/26/2006	10/393,223	3/20/2003	REUSABLE, BUILT-IN SELF-TEST METHODOLOGY FOR COMPUTER SYSTEMS	issued	Utility - ORG	NMXI-0884.00/US
S			10/610,855	6/30/2003	TOPOLOGY BASED REPLACEMENT ROUTING OF SIGNAL PATHS	Abandoned	Utility - ORG	NMX1-0883.00/US
US.	6,861,917	3/1/2005	10/444,311	5/22/2003	OSCILLATOR SYSTEM WITH SWITCHED-CAPACITOR NETWORK AND METHOD FOR GENERATING A PRECISION TIME REFERENCE	ssued	utility - ORG	NMXI-0882.00/US
US.	7,045,408	5/16/2006	10/443,152	5/21/2003	INTEGRATED CIRCUIT WITH IMPROVED CHANNEL STRESS PROPERTIES AND A METHOD FOR MAKING IT	ssued	Utility - ORG	NMXI-0881.00/US
SU			10/611,617	6/30/2003	METHOD, APPARATUS AND SYSTEM FOR CREATING EFFICIENT UPNP CONTROL POINTS	Abandoned	Utility - ORG	NMX1-0880.00/US
			11/561,684	11/20/2006	VARIABLE LATCH	Abandoned	Utility - DIV	NMXI-0879.01/US
SS	7,160,127	1/9/2007	10/391,893	3/18/2003	VARIABLE LATCH	Issued	Utility - ORG	NMXI-0879.00/US
SU			11/207,305	8/19/2005	MODIFIED ELECTROPLATING SOLUTION COMPONENTS IN A LOW-ACID ELECTROLYTE SOLUTION	Abandoned	Utility - DIV	NMX)-0878.01/US
SO	7,371,311	5/13/2008	10/682,276	10/8/2003	MODIFIED ELECTROPLATING SOLUTION COMPONENTS IN A LOW-ACID ELECTROLYTE SOLUTION	Issued	utility - ORG	NMXI-0878.00/US
Sh			10/682,275	10/8/2003	MODIFIED ELECTROPLATING SOLUTION COMPONENTS IN A HIGH-ACID ELECTROLYTE SOLUTION	Abandoned	Utility - ORG	NMXI-0877.00/US
સ	7,302,652	11/27/2007	10/404,937	3/31/2003	LEAKAGE CONTROL IN INTEGRATED CIRCUITS	Issued	Utility - ORG	NMXI-0876.00/US
SU	7,437,634	10/14/2008	10/436,775	5/13/2003	TEST SCAN CELLS	Issued	Utility - ORG	NMXI-0875.00/US
Sn	7,406,641	7/29/2008	10/612,293	6/30/2003	SELECTIVE CONTROL OF TEST-ACCESS PORTS IN INTEGRATED CIRCUITS	Issued	Utility - ORG	NMXI-0874.00/US
Sn	7,299,433	11/20/2007	10/458,537	6/9/2003	TIMING ANALYSIS APPARATUS, SYSTEMS, AND METHODS	Issued	Utility - ORG	NMXI-0873.00/US
SU	7,205,865	4/17/2007	10/402,125	3/28/2003	APPARATUS AND METHOD FOR COMBINING WRITES TO 1/O	Issued	Utility - ORG	NMXI-0872.00/US
S	6,958,450	10/25/2005	10/405,098	3/31/2003	CONNECTION APPARATUS, SYSTEMS, AND METHODS	Issued	Utility - ORG	NMXI-0871.00/US
SO	7,302,756	12/4/2007	10/612,281	6/30/2003	BOND FINGER ON VIA SUBSTRATE, PROCESS OF MAKING SAME, PACKAGE MADE THEREBY, AND METHOD OF ASSEMBLING SAME	Issued	Utility - ORG	NMX1-0870.00/US
SU			Tu) avu, 300	4002/11/0	The property of the control of the c			

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	w	PREDICTIVE FILTERING OF REGISTER CACHE ENTRY	Issued	Utility - ORG	NMXI-0928.00/US
10/799,928	3/12/2004	RIALS FOR ""SOFT"" POLYMER	Abandoned	Utility - ORG	NMX1-0927,00/US
10/746,975	12/24/2003	SECURE BOOTING AND PROVISIONING	\ssued	Utility - ORG	NMXI-0926.00/US
10/742,218	12/19/2003	MANUFACTURE FOR RUNNING)ssued	Utility - DRG	NMXI-0925.00/US
09/839,848 10/939,237	4/20/2001 9/10/2004	DYNAMICALLY RECONFIGURABLE OPTICAL SWITCHING SYSTEM CONTROLLED BREAKDOWN PHASE CHANGE MEMORY DEVICE	Issued Issued	Utility - ORG Utility - ORG	NMXI-0923.00/US
11/197,806	8/4/2005	ACTIVE PHASE CANCELLATION FOR INDUCTOR / CAPACITOR NETWORKS	Abandoned	Utility - DIV	NMXI-0921.00/US
10/666,077	9/19/2003	PRIORITIZED ADDRESS DECODER	Issued	Utility - ORG	NMXI-0920.00/US
10/692,436	10/22/2003	METHOD AND APPARATUS FOR LIMITING PORTS IN A REGISTER ALIAS TABLE. HAVING HIGH-BANDWIDTH AND LOW-BANDWIDTH STRUCTURES	Issued	Utility - ORG	Sn/00/6160-ixwn
10/677,081	9/30/2003	PASSING PARAMETERS BY IMPLICIT REFERENCE	Issued	Utility - ORG	NMXI-0918.00/US
10/641,614	8/15/2003	SYSTEM AND METHOD FOR MULTIPLE ENGINE PROCESSING INSTRUCTIONS	Abandoned	Utility - ORG	NMXI-0917.00/US
10/883,404	6/30/2004	RIZATION	Abandoned	Utility - ORG	NWXI-0916.00/US
10/661,738	9/11/2003	METHODS AND APPARATUSES FOR MANUFACTURING ULTRA THIN DEVICE LAYERS FOR INTEGRATED CIRCUIT DEVICES	Issued	Utility - ORG	NMXI-0915.00/US
10/739,268	12/19/2003		Issued	utility - ORG	NMX1-0914.00/US
10/749,271	12/30/2003	3 AN ADAPTIVE REPLAY LOOP IN A	Abandoned	Utility - ORG	NMXI-0913.00/US
10/611,380	6/30/2003	HIERARCHICAL REORDER BUFFERS FOR CONTROLLING SPECULATIVE EXECUTION IN A MULTI-CLUSTER SYSTEM	Abandoned	Utility - ORG	NMXI-0912.00/US
10/600,668	6/20/2003		Issued	Utility - ORG	NMXI-0911.00/US
11/063,963	2/23/2005	ECTURE FOR INTEGRATED CIRCUIT PACKAGE	issued	Utility - CON	NMXI-0910.00/US
10/611,215	6/30/2003	SONA	lscued	Utility - ORG	NMXI-0909.00/US
10/731,177	12/9/2003	WIRE-BONDED PACKAGE WITH ELECTRICALLY INSULATING WIRE ENCAPSULANT AND THERMALLY CONDUCTIVE OVERMOLD	issued	Utility - DIV	NMX1-0908.00/US
10/699,638	10/31/2003	SYMBOLIC BUFFER ALLOCATION IN LOCAL CACHE AT A NETWORK PROCESSING ELEMENT	Issued	Utility - ORG	Sn/0012060-1XMN
10/655,565	9/4/2003	SYSTEMS AND METHODS FOR USING HOLC CHANNELS SIMULTANEOUSLY PROCESS MULTIPLE HOLC CHANNELS	Issued	Utility - ORG	Sn/00°9060-1xwin
10/628,997	7/28/2003	NETWORK STATISTICS	Issued	utility - ORG	NWX1-0905.00/US
10/609,714	6/30/2003		Abandoned	Utility - ORG	NMX1-0904.00/US
10/456,076	6/6/2003		Issued	Utility - ORG	NMXI-0903.00/US
10/669,309	9/23/2003	LING	Issued	Utility - ORG	NMX1-0902.00/US
10/660,876	9/12/2003	ITER TELEPHONY INTEGRATION ACCESS	Abandoned	Utility - ORG	Sn/00/1060-1XWN
10/610,104	6/30/2003	APPARATUS, SYSTEM AND METHOD FOR PERFORMING TABLE MAINTENANCE	Abandoned	Utility - ORG	NMXI-0900.00/US
10/463,651	6/17/2003		Abandoned	Utility - ORG	Sn/00'6680-1XWN
10/428,286	5/2/2003		Abandoned	Utility - ORG	Sn/00'8680-1XWN
10/609,954	6/30/2003	OR UPDATING A SORTED LIST	Issued	Utility - ORG	NMXI-0897.00/US
10/723,413	11/26/2003		Abandoned	Utility - ORG	NWXI-0896.00/US
10/629,093	(140) (04)		Abandoned	Other ORG	NMXI-0895,00/05

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		10/954,947	9/29/2004	OVERHEAD PROCESSING AND GENERATION TECHNIQUES	Abandoned	Utility - ORG	NWXI-0958.00/US
7,323,707	1/29/2008	10/881,664	6/30/2004	INITIALIZING PHASE CHANGE MEMORIES	Issued	Utility - ORG	NMX1-0957.00/US
7 326 973	2/5/2008	10/880.988	6/30/2004	INTERCONNECT STRUCTURE IN INTEGRATED CIRCUITS	ssued	Utility - ORG	NWXI-0956.00/US
		10/663 107	rooc/ut/9	DETERMINING AVAILABLE BANDWIDTH IN A NETWORK	Abandoned	Utility - ORG	NMXI-0955.00/US
7,380,180	5/27/2008	10/893,015	7/16/2004	METHOD, SYSTEM, AND APPARATUS FOR TRACKING DEFECTIVE CACHE	Issued	Utility - ORG	NMX1-0954.00/US
		10/915,293	8/10/2004	STACKS, AND METHODS OF ASSEMBLING SAME	Abandoned	Utility - ORG	NMX1-0953.00/US
		11/053,786	2/9/2005	NON STROBE SENSING CIRCUIT	Abandoned	Utility - ORG	NMXI-0952.00/US
		11/095,950	3/31/2005	PROGRAMMABLE CURRENT LOAD SYSTEMS AND METHODS	Abandoned	Utility - ORG	NMXI-0951.00/US
		10/805,106	3/19/2004	METHOD, SYSTEM, AND PROGRAM FOR OPTIMIZING CODE	Abandoned	Utility - ORG	Sn/00'0560-1XWN
7,249,230	7/24/2007	10/874,998	6/23/2004	QUEUE STRUCTURE WITH VALIDITY VECTOR AND ORDER ARRAY	Issued	Utility - ORG	NMXI-0949.00/US
7,183,500	2/27/2007	10/883,610	6/30/2004	ELECTROMAGNETIC INTERFERENCE (EMI) FILTER WITH PASSIVE NOISE CANCELATION	issued	Utility - ORG	NMXI-0948.00/US
		10/815,904	3/31/2004	A METHOD AND APPARATUS FOR MULTIPROCESSOR EMBEDDED DEBUGGING SUPPORT VIA ATTACHMENT OF BREAKPOINT BIT FIELDS TO INSTRUCTIONS AND DEBUG REGISTER FIELD MANIPULATION	Abandoned	Utility - ORG	NMXI-0947.00/US
		10/868,703	6/15/2004	APPARALUS AND MELHOD OF DYNAMIC USAGE PROFILE ACQUISITION AND UTILIZATION IN A WIRELESS MOBILE DEVICE	Abandoned	Utility - ORG	NMXI-0946.00/US
7,415,577	8/19/2008	10/799,555	3/10/2004	METHOD AND APPARATUS TO WRITE BACK DATA	Issued	utility - org	NMXI-0945.00/US
7,867,818	1/11/2011	11/212,015	8/24/2005	METHODS AND APPARATUSES FOR PROVIDING STACKED-DIE DEVICES	Issued	Utility - DIV	NMXI-0944.01/US
7,187,068	3/6/2007	10/917,142	8/11/2004	METHODS AND APPARATUSES FOR PROVIDING STACKED-DIE DEVICES	Issued	Utility - ORG	NMXI-0944.00/US
		11/050,369	2/2/2005	TECHNIQUES FOR TRANSPOSITION OF A MATRIX ARRANGED IN A MEMORY AS MULTIPLE ITEMS PER WORD	Abandoned	Utility - ORG	NMXI-0943.00/US
7,400,041	7/15/2008	10/8/5,51/	4/26/2004	COMPLIANT MULTI-COMPOSITION INTERCONNECTS	issued	Utility - ORG	NMXI-0942.00/US
7,270,056	9/18/2007	10/930,251	8/31/2004	PRINT STRIPPER FOR ESD CONTROL	issued	Utility - ORG	NMXI-0940.00/US
		10/956,994	9/30/2004	METHOD, APPARATUS AND SYSTEM FOR DATA INTEGRITY OF STATE RETENTIVE ELEMENTS UNDER LOW POWER MODES	Abandoned	Utility - ORG	Sn/0016260-1XWN
7,441,268	10/21/2008	10/863,661	6/8/2004	METHOD AND APPARATUS TO MANAGE EXCEPTIONS IN NETWORK PROCESSORS	Issued	utility - ORG	NMXI-0938.00/US
7,797,492	9/14/2010	10/783,621	2/20/2004	METHOD AND APPARATUS FOR DEDICATING CACHE ENTRIES TO CERTAIN STREAMS FOR PERFORMANCE OPTIMIZATION	Issued	Utility - ORG	NMXI-0937.00/US
7,764,709	7/27/2010	10/885,821	7/7/2004	PRIORITIZATION OF NETWORK TRAFFIC	Issued	Utility - ORG	NMXI-0936.00/US
		10/805,947	3/22/2004	HYBRID BRANCH PREDICTION	Abandoned	Utility - ORG	NMXI-0935.00/US
7,289,382	10/30/2007	10/744,664	12/23/2003	REWRITABLE FUSE MEMORY	Issued	Utility - ORG	NMXI-0934.00/US
		10/992,250	11/17/2004	USB ON-THE-GO IMPLIMENTATION	Abandoned	Utility - ORG	NMXI-0933.00/US
7,606,951	10/20/2009	10/988,417	11/12/2004	MEMORY REUSE FOR MULTIPLE ENDPOINTS IN USB DEVICE	Issued	Utility - ORG	NMXI-0932.00/US
7,259,023	8/21/2007	10/939,145	9/10/2004	FORMING PHASE CHANGE MEMORY ARRAYS	tssued	Utility - ORG	NMXI-0931.00/US
7,173,817	2/6/2007	10/748,385	12/29/2003	FRONT SIDE HOT-SWAP CHASSIS MANAGEMENT MODULE	Issued	Utility - ORG	NMXI-0930.00/US
7,181,601	2/20/2007	10/731,567	12/8/2003	INSTRUCTIONS IN SPECULATIVE EXECUTION	Danssi	Othry - ORG	SO/0035260-1X1AIN
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SI	7,271,673	9/18/2007	11/171,860	6/30/2005	VOLTAGE CONTROLLED OSCILLATOR (VCO) TUNING	Issued	Utility - ORG	NMXI-1015.00/US
SD			13/229,455	9/9/2011	DIFFERENTIAL DELAY COMPENSATION	Pending	Utility - CON	NMXF-1014.01/US
SU	8,018,926	9/13/2011	11/093,907	3/30/2005	DIFFERENTIAL DELAY COMPENSATION	Issued	Utility - ORG	NMXI-1014.00/US
SU			21/170,110	6/30/2005	DEVICE, SYSTEM AND METHOD FOR WIRELESS COMMUNICATION AND CURSOR POINTING	Abandoned	Utility - ORG	NMXI-1013.00/US
Sn			11/361,228	2/24/2006	MOVEABLE LOCKED LINES IN A MULTI-LEVEL CACHE	Pending	utility - ORG	NMXI-1012.00/US
SN	7,581,065	8/25/2009	11/101,785	4/7/2005	LOW LOCALITY-OF-REFERENCE SUPPORT IN A MULTI-LEVEL CACHE HIERACHY	Issued	Utility - ORG	Sn/00'1'01'1'00'NWN
SO	7,328,313	2/5/2008	11/094,687	3/30/2005	USING RESERVE SIGNALS AND CONTROL BITS	Issued	Utility - ORG	NMXI-1010.00/US
SO			13/197,221	8/3/2011	LOCKING ENTRIES INTO TRANSLATION LOOKASIDE BUFFERS	Pending	Utility - CON	NMXI-1009.01/US
S			11/092,432	3/29/2005	LOCKING ENTRIES INTO TRANSLATION LOOKASIDE BUFFERS	Pending	Utility - ORG	NMXI-1009.00/US
Stu			11/069,924	3/1/2005	POWER MODE CHANGE VOLTAGE CONTROL IN COMPUTERIZED SYSTEM	Abandoned	Utility - ORG	NMXI-1008.00/US
Sn	7,603,575	10/13/2009	11/173,218	6/30/2005	FREQUENCY-DEPENDENT VOLTAGE CONTROL IN DIGITAL LOGIC	Issued	Utility - ORG	NMXI-1007.00/US
S SI			11/114,261 11/090,571	4/26/2005 3/25/2005	PULSED DC AND RF PHYSICAL VAPOR DEPOSITION CLUSTER TOOL METHOD AND APPARATUS FOR MONITORING PATH STATISTICS	Pending Abandoned	Utility - ORG	NWXI-1005.00/US
SS			11/241,339	9/30/2005	ERROR PROTECTION TECHNIQUES FOR FRAMES ON A WIRELESS NETWORK	Abandoned	Utility - ORG	NMXI-1004.00/US
ູນ	7,248,475	7/24/2007	11/140,769	5/31/2005	WIRELESS DEVICE ENCLOSURE USING PIEZOELECTRIC COOLING STRUCTURES 5/31/2005	Issued	Utility - ORG	NMX1-1003.00/US
સ	7,613,894	11/3/2009	11/218,371	9/2/2005	POWER LOSS RECOVERY IN NON-VOLATILE MEMORY	Issued	Utility - ORG	NMXI-1002.00/US
SI			10/989,539	11/16/2004	ADAPTIVE WIRELESS NETWORKS AND METHODS FOR COMMUNICATING MULTIMEDIA IN A PROACTIVE ENTERPRISE	Abandoned	Utility ~ ORG	NMXI-1000.00/US
Sn			11/024,164	12/28/2004	MECHANISM FOR PERFORMING RUNAHEAD EXECUTION IN A CENTRAL PROCESSING UNIT	Abandoned	Utility - ORG	NMXI-0999.00/US
SU			11/615,862	12/22/2006	MEMORY CELL WITH NANOCRYSTAL AS DISCRETE STORAGE ELEMENT	Abandoned	Utility - DIV	NMXI-0998.01/US
SO			10/997,609	11/23/2004	METHOD FOR FORMING A MEMORY DEVICE	Abandoned	Utility - ORG	Sn/00'8660-1XWN
S			11/012,316	12/15/2004	COMMUNICATING AN ADDRESS TO A MEMORY DEVICE	Abandoned	Utility - ORG	NMXI-0997.00/US
સ	1	1	13/041,379	3/5/2011	SECURE CONTROLLER FOR BLOCK ORIENTED STORAGE	Pending	utility - CON	Sn/10'9660-IXWN
દ્ધ	7,904,943	3/8/2011	11/027,913	12/28/2004	SECURE CONTROLLER FOR BLOCK ORIENTED STORAGE	Issued	Utility - ORG	NMX1-0996.00/US
E 5	6/5/714//	8/12/2008	11/02/,/84	12/30/2004	MENADRY MITH MODIFIARI E ADDRESS MAD	Abandoned	Other are	SO/00'S660-IXWN
; (S	1	2 (41)	11/169,537	6/29/2005	PIXEL DATA COMPRESSION FROM CONTROLLER TO DISPLAY	Abandoned	Utility - ORG	NMXI-0994.00/US
SU			11/024,956	12/29/2004	A WIRELESS COMMUNICATIONS DEVICE	Pending	Utility - ORG	SO/0018660-IXWN
US			11/018,958	12/21/2004	PROTECTED PROCESSING APPARATUS, SYSTEMS, AND METHODS	Abandoned	Utility - ORG	NMXI-0992.00/US
. CS	7,506,239	3/17/2009	11/022,212	12/23/2004	SCALABLE TRACEBACK TECHNIQUE FOR CHANNEL DECODER	Issued	Utility - ORG	NMXI-0991.00/US
S:			11/021,323	12/23/2004	TECHNIQUES TO MANAGE PARTITION PHYSICAL MEMORY	Abandoned	Utility - ORG	SN/00:0660-1XWN
Sn	7,529,958	5/5/2009	10/989,948	11/15/2004	PROGRAMMABLE POWER TRANSITION COUNTER	Issued	Utility - ORG	NMXI-0989.00/US
Sn	7,259,982	8/21/2007	11/029,981	1/5/2005	READING PHASE CHANGE MEMORIES TO REDUCE READ DISTURBS	Issued	Utility - ORG	Sn/00'8860-1XWN
SN	7,282,730	10/16/2007	11/037,850	1/18/2005	FORMING A CARBON LAYER BETWEEN PHASE CHANGE LAYERS OF A PHASE CHANGE MEWORY	Issued	Utility - ORG	NWXI-0987.00/US
us			11/103,188	4/11/2005	HEATING PHASE CHANGE MATERIAL	Pending	Utility - ORG	NMXI-0986.00/US
Sn	7,500,131	3/3/2009	10/935,902	9/7/2004	TRAINING PATTERN BASED DE-SKEW MECHANISM AND FRAME ALIGNMENT 9/7/2004	issued	Utility - ORG	SU/00.5860+IXWN
Country	PatentNo		SerialNo	fileDate	Tile	Matterstati	MII-DCktNo WatterType MatterStatus	MTI-DcktNo

NMXI-1042.00/US Utility - ORG	NMXI-1041.00/US Utility - ORG	NMXI-1040.00/US Utility - ORG	NMXI-1039.00/US Utility - DRG	NMXI-1038.00/US Utility - ORG	NMXI-1037.00/US Utility - ORG	NMXI-1036.00/US Utility - ORG	NMXI-1035.00/US Utility - ORG	NMXI-1034.00/US Utility - ORG	NMXI-1033.00/US Utility - ORG	NMXI-1032.00/US Utility - ORG	NMXI-1031.00/US Utility - ORG	NMXI-1030.00/US Utility - ORG				NMXI-1027.00/US Utility - ORG	NMXI-1026.00/US Utility - ORG	NMXI-1025.00/US Utility - ORG	NMXI-1024.00/US Utility - ORG	NMXI-1023.01/US Utility - DIV	NMXI-1023.00/US Utility - ORG	NMXI-1022.00/US Utility - ORG			NMXI-1019.01/US Utility - CON	NMXI-1019.00/US Utility - ORG	NMXI-1018.02/US Utility - DIV			NMXI-1017.00/US Utility - ORG	NMXI-1016.00/US Utility - ORG Abandoned
issued	ssued	Abandoned	Issued	Pending	issued	Abandoned	Abandoned	Abandoned	Abandoned	Abandoned	Ssued	lssued	Abandoned	Abandoned	Abandoned	Abandoned	Abandoned	Abandoned	Issued	Issued	Issued	Issued	135060	Abandoned	Pending	Issued	Allowed	Issued	Abandoned	Issued	Abandoned
FREQUENCY SYNTHESIZER TRANSCONDUCTANCE AMPLIFIER ARRANGEMENT	LOW VOLTAGE DOUBLE BALANCED MIXER	SYSTEM AND METHOD FOR REDUCING FALSE ALARM IN THE PRESENCE OF RANDOM SIGNALS	CURRENT MIRROR DRIVE CIRCUIT WITH HIGH BREAKDOWN VOLTAGE	ARCHITECTURE FOR VIRTUAL SECURITY MODULE	RADIO FREQUENCY AMPLIFIER	METHOD AND APPARATUS TO EFFICIENTLY CONFIGURE MULTI-ANTENNA EQUALIZERS	SECURITY MODULE HAVING ACCESS LIMITED BASED UPON SECURITY LEVEL OF CODE SEEKING ACCESS	LOW DELAY AND SMALL MEMORY FOOTPRINT PICTURE BUFFERING	ENCODING TECHNIQUES EMPLOYING NOISE-BASED ADAPTATION	FORMING OVONIC THRESHOLD SWITCHES WITH REDUCED DEPOSITION CHAMBER GAS PRESSURE	WIRELESS COMMUNICATIONS WITH AUXILIARY ANTENNAS	PROCESSOR MULTI-PARTITION SECURITY ARCHITECTURE	LOW-JITTER CLOCK DISTRIBUTION	METHOD AND APPARATUS FOR DISPLAYING ROTATED IMAGES	FORMING HEATERS FOR PHASE CHANGE MEMORIES	FORMING SEATERS FOR PHASE CHANGE MENAORIES	COMPOSITE ORGANIC INORGANIC NANOCLUSTERS AS CARRIERS AND IDENTIFIERS OF TESTER MOLECULES	ALTERNATING BETWEEN UPLINK AND DOWNLINK WITHIN EACH CHANNEL OF A FREQUENCY DIVISION MULTIPLEXING SYSTEM	METHOD AND APPARATUS FOR OPTIMIZING FLASH DEVICE ERASE DISTRIBUTION	STRUCTURED, ELECTRICALLY-FORMED FLOATING GATE FOR FLASH MEMORIES	STRUCTURED, ELECTRICALLY-FORMED FLOATING GATE FOR FLASH MEMORIES	MULTIPLE PARALLEL OSCILLATOR CIRCUITS	APPARATUS, SYSTEM, AND METHOD FOR OSCILLATOR NETWORK WITH	REDUCING OXIDATION OF PHASE-CHANGE MEMORY ELECTRODES	PERIPHERAL INTERFACE ALERT MESSAGE FOR DOWNSTREAM DEVICE	PERIPHERAL INTERFACE ALERT MESSAGE FOR DOWNSTREAM DEVICE	FABRICATING SUB-LITHOGRAPHIC CONTACTS	FABRICATING SUB-LITHOGRAPHIC CONTACTS	FABRICATING SUB-LITHOGRAPHIC CONTACTS	PHASE CHANGE MEMORY WITH U-SHAPED CHALCOGENIDE CELL	METHOD AND SYSTEM FOR TRANSFERRING MESSAGES TO A MOBILE STATION ACCORDING TO SPECIFIC PARAMETERS
4/22/1998	1/7/1998	3/31/2006	11/2/1994	9/29/2006	7/6/2000	3/30/2006	2/15/2006		3/31/2006	3/29/2006	3/30/2006	11/10/2005	11/30/2005	12/16/2005	3/6/2008	to it a iange	9/26/2006	9/12/2005	9/30/2005	3/25/2008	11/14/2005	6/29/2005	(/26/2005	6/28/2005	5/19/2011	6/20/2005	6/3/2010	10/6/2008	7/29/2005	7/20/2005	6/27/2005
09/064,276	09/003,908	11/396,329	08/333,264	11/529,987	09/611,720	11/394,533	11/354,676	11/292,569	11/394.929	11/392,135	11/394,994	11/271,778	11/291,204	11/303 117	12/074.813	11 /1/0 AGO	11/527,895	11/225,303	11/240,004	12/055,216	11/274,622	11/172,133	944,691/rr	11/168,780	13/111,673	11/156,838	12/792,992	12/287,073	11/193,952	11/185.488	05 11/166,143
5/9/2000	4/3/2001		11/21/1995		6/18/2002						7/21/2009	11/24/2009							9/16/2008	12/7/2010	5/20/2008	6/26/2007	5,24,2003			6/28/2011		7/13/2010	-	2/2/2010	
6,060,917	6.211.718		5,469,093		6.407.632						7,564,919	7.624.249							7,426,605	7,847,333	7,374,996	7,236,059	7,496,719			7,970,958		7,754,516		7.655,938	รม
; S; 5	<u> </u>	SU	Sn	ᅜ	ES	SU	US	LS 8	[]	SU	US :	5, 1	F 8	<u>.</u>	ž 8	ñ	SO	æ	ક્ષ	S	S	SN	Ş	S S	SO	SU	æ	ᅜ	당	SU	S

thibit A - US Applications and Issued Patents

			Exhibit A - US Applications and Issued Patents	tents				
NMXI-1044.00/US	Utility - ORG	Issued	RADIO FREQUENCY AMPLIBER WITH IMPROVED INTERMODULATION PERFORMANCE	1/24/2002	10/056,155	4/5/2005	6,876,843	Sn
NMXI-1045.00/US	Utility - ORG	issued	SELF-ALIGNED BIPOLAR JUNCTION TRANSISTORS	4/26/2006	11/411,311	1/25/2011	7,875,513	S
NMXI-1045.01/US	Utility - DIV	Pending	SELF-ALIGNED BIPOLAR JUNCTION TRANSISTORS	12/16/2010	12/969,652			SO
NMXI-1045.00/US	Utility - ORG	Issued	VOLTAGE CONTROLLED OSCILLATORS	8/20/2002	10/223,869	1/27/2004	6,683,509	υs
NMXI-1047.00/US	Utility - ORG	Issued	SIGNAL PROCESSING STAGE AND RADIO FREQUENCY TUNER	4/28/2003	10/424,899	9/14/2004	6,791,414	C.S.
NMXI-1048.00/US	Utility - ORG	Abandoned	ASYMMÉTRIC CHALCOGENIDE DEVICE	10/10/2006	11/545,234			LIS
NMXI-1049.00/US	Utility - ORG	Pending	READ WINDOW IN CHALCOGENIDE SEMICONDUCTOR MEMORIES	11/9/2006	11/595,055			S
SO/00:050T-1XWN	Utility - ORG	Abandoned	AUTHENTICATING DATA RETURNED FROM NON-VOLATILE MEMORY COMMANDS	9/28/2006	11/529,022			LS.
NMXI-1051.00/US	utility - ora	Abandoned	A PHASE CHANGE MEMORY CELL HAVING A TAPERED MICROTRENCH	11/30/2006	11/606,800			S
NMXI-1052.00/US NMXI-1052.01/US	Utility - ORG Utility - CON	Issued Pending	HIGH DENSITY NOR FLASH ARRAY ARCHITECTURE HIGH DENSITY NOR FLASH ARRAY ARCHITECTURE	6/28/2007 3/9/2011	11/823,518 13/043,677	3/22/2011	7,910,976	US US
NMXI-1053.00/US	Utility - NSPCT	Expired	OSCILLATOR NETWORK FOR RADIO RECEIVER	5/17/1989	07/335,087	8/7/1990	4,947,141	S
NMXI-1054.00/US	Utility - ORG	Issued	DIGITAL TLEPHONE SYSTEM WITH AUTOMATIC VOICE MAIL REDIRECTION	8/31/1998	09/144,186	4/1/2003	6,542,584	SO

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NMX1-0599.00/CN	NMXI-0583.D0/CN	NMX1-0550.00/CN	NMX1-0535.00/CN	NMXI-0516.00/CN	NMXI-0504.00/CN	NMXI-0502.00/CN	NMXI-0478.00/CN	NMXI-0420.00/CN	NMXI-0414.00/CN	NMXI-0392.00/CN	NMXI-0377.00/CN	NMX:-0251.00/CN	NMXi-0239.00/CN	NMXI-0683.00/CL NMXI-0204.00/CN	NMXI-0622.00/CA	NMXI-0550.00/CA NMXI-0599.00/CA	NMXI-0497,00/CA	NMXI-0490.00/CA	NMXI-0462.00/CA	NMX1-0393.00/CA	NMXI-0203.00/BR NMXI-0173.00/CA	NMXI-0341.00/BE	NMXI-0546.00/AU	NMXI-0490.00/AU
Utility - NSPCT	Utility - NSPCT	Utility - DRG	Utility - NSPCT	Utility - NSPCT	Utility - NSPCT	Utility - NSPCT	Utility - NSPCT	Utility - ORG	Utility - NSPCT	Utility ~ ORG	Utility - NSPCT	Utility - NSPCT	Utility - NSPCT	Utility - ORG Utility - NSPCT	Utility - NSPCT	Utility - ORG Utility - NSPCT	Utility - NSPCT	Utility - NSPCT	Utility - ORG	Utility - ORG	Utility - ORG Utility - ORG	Utility - ORG	Utility - ORG	MATERIANS MATERIANS CO NMXI-0490.00/AU Utility - NSPCT Abandoned INT
Issued	Abandoned	issued	ssued	Abandoned	Issued	Abandoned	Issued	issued	ssued	Issued	issued	Abandoned	Issued	Abandoned Issued	Abandoned	Abandoned Abandoned	Abandoned	Abandoned	Abandoned	Abandoned	Abandoned Abandoned	Expired	Lapsed	MatterStatus Abandoned
ENHANCED MEDIA GATEWAY CONTROL PROTOCOL	STSTEM AND METHOD FOR PROVIDING ACTION AND VERIFICATION SERVICES IN AN ENHANCED MEDIA GATEWAY	SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION	DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS	CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORMS	LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS	MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS	INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS	CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS	IMPROVED FLOURINE DOPED SIG2 FILM AND METHOD OF FABRICATION	CIRCUIT FOR DATA DEPENDENT VOLTAGE BIAS LEVEL	METHOD AND APPARATUS FOR PERFORMING VIDEO IMAGE DECODING	METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER	MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS	BEST-OF-M PULSE POSITION MODULATION DETECTOR ACCURATE VPP DETECTOR FOR FLASH MEMORY	WAVEGUIDE GRATING (ACW) AND OPTICAL AMPLIFIER(S)	SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION ENHANCED MEDIA GATEWAY CONTROL PROTOCOL	REMOTELY MANAGING AND CONTROLLING A CONSUMER APPLIANCE	COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE	ANTENNA INTERFACE	ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK	UNIVERSAL CARRIER FOR HOT SWAPPABLE DRIVER LOW POWER CHARGE PUMP FOR FLASH MEMORY	PORTABLE COMMUNICATION TERMINAL FOR REMOTE DATABASE QUERY	METHOD OF PRE-CACHING DATA UTILIZING THREAD LISTS AND MULTIMEDIA EDITING SYSTEM USING SUCH PRE-CACHING	MPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE TERFACE
12/21/2001	8/29/2003	12/27/2000	9/25/2001	1/24/2001	11/20/2001	6/10/2003	6/1/2001	2/8/2000	10/11/2000	11/5/1999	10/25/2000	9/29/1997	12/12/1996	6/5/1996 10/18/1995	10/17/2002	1/20/1999 8/27/2003	10/31/2002	1/27/1993	11/26/2001	6/18/2001	11/28/1995 3/16/1995	6/30/1987	9/12/1995	EliéPate ////////////////////////////////////
01822925.5	01822961.1	99804084.3	01816419.6	01806003.X	01819255.6	1820255.1	01814712.7	00804578.X	00816502.5	99815589.6	00819034.8	97181414.7	96199935.5	1000-96 95196684,7	2,463,627	2,318,450 2,432,906	2,408,276	2,132,108	2,371,945	2,355,881	PI9509921-2 2,185,333	87109409.0	37233/95	SenaiNo
11/12/2008	8/27/2008	2/25/2004	12/7/2005		10/22/2008		8/5/2009	3/2/2005	9/28/2005	8/4/2004	1/11/2006	5/28/2003	1/14/2004	5/21/2003		6/5/2007		3/30/1999			8/19/2003 6/1/2004	12/15/1993		
ZL01822925.5	ZL01822961.1	ZL99804084.3	ZL01816419.6		ZL01819255.6		ZL01814712.7	ZL00804578.X	ZL00816502.5	ZL 99815589.6	ZL00819034.8	Z197181414.7	ZL96199936.5	ZL95195684.7		2,318,450		2,132,108			PI9509921-2 2,185,333	0251296		SueDate
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	issued VARIABLE DELAY CELL WITH A SELF-BIASING LOAD 1/5/1999	Other creat	
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN CN CN 1/14/2009 .1 CN			NMXI-0337.00/DE
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN	SELF-CONFIGURING INTERFACE ARCHITECTURE ON FLASH MEMORIES 10/11/1999	Utility - ORG Iss	NMXI-0313.00/DE
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL2028184.0 CN 1/14/2009 ZL200510098094 CN C	INTEGRATED CIRCUIT PACKAGE 7/9/1997	Utility - ORG Iss	NMXI-0291.00/DE
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN CN CN CN CN 1/7/2009 ZL200480005928 CN CN 6/17/12009 ZL200480005928 CN CN 6/17/1993 4024930 DE 6/30/2004 695 33 487.5-08 DE 6/30/2004 695 33 487.5-08 DE 6/30/2004 695 33 487.5-08 DE 6/30/2004 6952925.9 DE 6/30/2003 69629925.9 DE 8/25/2004 69730430.2 DE	Abandoned METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK 5/21/1999	Utility - ORG Ab	NMXI-0251.00/DE
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN CN CN 1/1/2009 ZL200480005928 CN CN 1/7/2009 ZL200480005928 CN CN 6/17/12009 ZA200480005928 CN 6/17/1993 4024930 DE 8/9/2004 695 33 487.5-08 DE 6/30/2004 695 33 487.5-08 DE 6/30/2004 695 33 487.5-08 DE 6/30/2004 695 39 487.5-08 DE 6/30/2004 695 39 487.5-08 DE 6/30/2004 69629925.9 DE	Issued METHOD AND APPARATUS FOR PROVIDING IMPROVED DIAGNOSTIC 6/27/1997	Utility - EPPAT 185	NMXI-0246.00/DE
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL200510098094 CN 1/14/2009 .1 CN C	SSUED MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS 12/12/1996	Utility - NSPCT 155	NMX1-0239.00/DE
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN C	NEGATIVE VOLTAGE SWITCHING CIRCUIT	·	NMXI-0224.00/DE
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN CN CN 1/7/2009 Z1200480005928 CN CN CN 6/17/1993 A024930 DE 8/9/2004 695 33 487.5-08 DE	ISSUED PACKAGE HOUSING MULTIPLE SEMICONDUCTOR DIES 3/12/1996	Utility - EPPAT ISS	NMXI-0222.00/DE
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN CN CN CN 1/7/2009 ZL200480005928 CN CN 6/17/1993 4024930 DE	PORTABLE DISK DRIVE CARRIER AND CHASSIS		NMXI-0203.00/DE
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN CN CN 1/7/2009 Z1200480005928 CN 1/7/2009 3 CN	Abandoned A NOVEL ARCHITECTURE FOR FLASH VIRTUAL GROUND MEMORY ARRAY 8/5/1990	Utility - ORG Ab	NMXI-0018.00/DE
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN CN CN 1/7/2009 ZL200480005928 CN 2/7/2009 ZL200480005928 CN C	METHOD AND APPARATUS FOR EFFICIENT AND ARTIFACTS FREE 12/15/2006 DISPLAYING OF ROTATING IMAGES	Utility - ORG Ab	NMX1-1028.00/CN
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN CN CN CN 1/7/2009 ZL200480005928 CN 1/7/2009 3.3 CN	Pending RESONANT TUNNELING DEVICE USING METAL OXIDE SEMICONDUCTOR 10/28/2005 PROCESSING	Utility - ORG Pe	NMXI-0984.00/CN
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN CN CN CN CN CN 1/7/2009 ZL200480005928 CN	Abandoned ANNOTATING MEDIA CONTENT WITH USER-SPECIFIED INFORMATION 7/3/2006	Utility - NSPCT Ab	NMXI-0924.00/CN
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN CN CN CN CN	THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH 2/9/2004 EXTERNAL-CONNECTOR-SIDE DIE AND METHOD	Utility - NSPCT Issued	NMXI-0860.00/CN
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN CN CN	Pending SYMBOLS HAVING MORE THAN ONE PULSE - CHINA 1/8/2004	Utility - NSPCT Pe	NMXI-0844.00/CN
1/25/2006 ZL 02818839.X CN 6/22/2011 ZL 02828164.0 CN 1/14/2009 ZL 200510098094 CN .1 CN	Abandoned PRONUNCIATION NETWORK 12/22/2003	Otility - NSPCT Ab	NMXI-0839,00/CN
1/25/2006 ZL 02818839.X CN 5/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN .1 CN	Abandoned INVERSE MULTIPLEXING OF MANAGED TRAFFIC FLOWS OVER A MULTI- 2/5/2003	Utility - NSPCT Ab	NMXI-0778.00/CN
1/25/2006 ZL 02818839.X CN 5/22/2011 ZL02828164.0 CN 1/14/2009 ZL200510098094 CN	Abandoned METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF 12/16/2004 SYSTEM POWER	Utility - NSPCT Ab	NMXI-0752.00/CN
ZL02818839.X CN ZL02828164.0 CN	SYSTEM AND METHOD TO DETECT ERRORS AND PREDICT POTENTIAL 7/6/2005	Utility - ORG lssued	NMXI-0741.00/CN
ZL 02818839.X CN	FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING 12/17/2002	Utility - NSPCT Issued	NMX1-0736.00/CN
CZ CZ	Abandoned DIGITAL FRAME DETERMINATION AND APPARATUS 7/2/2004 Abandoned PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT 3/25/2004	Utility - NSPCT Ab: Utility - NSPCT Ab:	NMXI-0719.00/CN NMXI-0721.00/CN
3/28/2007 ZL02821250.9 CN	ed ELECTRONIC ASSEMBLY WITH FILLED NO-FLOW UNDERFILL METHODS OF 10/28/2002 MANUFACTURE	Utility - NSPCT Issued	NMXI-0710.00/CN
1/27/2010 ZL02822880.4 CN		ㅋ	NMXI-0694.00/CN
CN	g MOBILE STATION LOCATING SYSTEM AND METHOD		NMXI-0685.D0/CN
7/9/2008 ZL02813257.2 CN 11/12/2008 ZL02823658.0 CN	ed MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION 12/30/2002 11/20/2002	Utility - NSPCT Issued	NMXI-0637.00/CN
ZLD1821582.3	VIA-IN-PAD WITH OFF-CENTER GEOVALTRY AND METHODS OF MANUFACTURE	Utility - NSPCT Issued	NMXI-0626.00/CN
Issuebate Patentyo Country		Mīī-Dekulo // MatterType // MatterStatus	MIL-DektNo

Exhibit B - Foreign Applications and Patents

		Company of the amount of the contract of the	Exhibit B - Foreign Applications and Pa	A CONTROLL OF THE PROPERTY OF	\$200 September 1980 September 1985 S			
MILEDICINO E Willity - ORG Issued MI	Utility - ORG	issued Manterstates	ETHOD AND CIRCUIT FOR DATA DEPENDENT VOLTAGE BIAS LEVEL	5/14/2001	19983726.0-42	11/11/2010	13	30
NWXI-0393.00/DE	Utility - EPPAT	issued	ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK	12/30/1999	99967807.1	8/10/2005	1142202	õ
NMXI-0414.00/DE	Utility - NSPCT	ssued	IMPROVED FLOURINE DOPED SIO2 FILM AND METHOD OF FABRICATION	10/11/2000	10085212.2	11/20/2008	10085212	ЭG
NMXI-0473.00/DE	Utility - EPPAT	Issued	EMULATING NARROW BANK PHASE-LOCKED LOOP BEHAVIOR ON A WIDE BAND PHASE-LOCKED LOOP	3/4/1998	98911456.5	6/29/2005	968568	ЭĞ
NMXI-0478.00/DE	Utility - EPPAT	lssued	INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS	6/1/2001	01941818.5	12/15/2010	60143656.3	De
NMXI-0496.00/DE	Utility - EPPAT	Abandoned	METHOD AND APPARATUS FOR GRAPHICAL DEVICE MANAGEMENT USING A VIRTUAL CONSOLE	12/27/2002	01948405.4	9/17/2008	1370935	30
NMX1-0504.00/DE	Utility - EPPAT	Issued	UNK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS	11/20/2001	01995179.7	4/13/2011	1336286	ĕ
NMX1-0532.00/DE	Utility - EPPAT	Issued	APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM	10/15/1997	97308183.9	7/30/2003	69723798.2	맖
NMXI-0535.00/DE	Utility - EPPAT	Issued	DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS	9/25/2001	01975434.0	5/6/2009	1323226	30
NMXI-0546.00/DE NMXI-0550.00/DE	Utility - EPPAT Utility - EPPAT	Issued	THREAD LIST EXPLICT CACHING SYSTEM SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION	9/12/1995 1/20/1999	95935071.1 99904159.3	12/6/2006 1/14/2009	0781432 69940302.2	DE DE
NMXI-0622.00/DE	Utility - EPPAT	Issued	INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (ACW) AND OPTICAL AMPLIFICA(S)	10/17/2002	02795533.5	12/21/2005	1436648	DΕ
NMX1-0626.00/DE	Utility - EPPAT	Issued	VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE DITAL-DAMASCENE INTERCONNECTS WITHOUT AN ETCH STOP LAYER BY	11/28/2001	01273027.1	5/11/2011	60144621.6	유
NMXI-0672.00/DE	Utility - NSPCT	Issued	ACTERNATING IDS	9/27/2002	02768930.6	12/7/2005	1430525	2 2
NMXI-0679.00/DE	Utility - EPPAT	ssued	TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE	11/27/2003	02725662.7	6/22/2005	60204778.1	유
NMXI-0694.00/DE	Utility - EPPAT	Ahandoned	VARIABLE LEVEL MEMORY DIGITAL ERAMS DETERMINATION AND APPARATIS	8/6/2002	02757016.7 2798607.4	6/27/2007	1428221 60734644.4	2 P
NMX1-0786.00/DE	Utility - EPPAT	Issued	ESTABLISHING AN AD HOC NETWORK	4/24/2003	03724240.1	7/8/2009	1514386	DΕ
NMXI-0844.00/DE	Utility - EPPAT	Issued	SYSTEM AND METHOD OF EFFICIENTLY MODULATING DATA USING SYMBOLS HAVING MORE THAN ONE PULSE - EPO	1/8/2004	04700900.6	12/12/2007	1595343	몺
NMXI-0174.00/EP	Utility - ORG	Abandoned	MICROPROGRAMMED SYSTEMS SOFTWARE INSTRUCTION UNDO	5/11/1988	88107610.3	11/30/1994	292791	; m
NMXI-0222.00/EP	Utility - NSPCT	Issued	ONIVERSAL CARRIER FOR BUT SWAFFABLE DRIVER DUAL DIE PACKAGE FOR SECIAL APPLICATIONS	3/12/1996	96908756.8	9/8/200 4 6/30/2004	0815615	eg eg
NMXI-0325.00/EP	Utility - NSPCT	Abandoned	MULTI-LAYER CODER/DECODER FOR PRODUCING QUANTIZATION ERROR SIGNAL SAMPLES	6/2/2000	98946118.1			æ
NMXI-0337.00/EP	Utility - NSPCT	Issued	VARIABLE DELAY CELL WITH A SELF-BIASING LOAD	1/5/1999	99901331.1	4/23/2008	1048108	EM O
NMXI-0377.00/EP	Utility - NSPCT	Abandoned	METHOD AND APPARATUS FOR PERFORMING VIDEO IMAGE DECODING	5/31/2002	00989714.1			ΕĐ
NMXI-0393.00/EP	Utility - NSPCT	Issued	ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK	12/30/1999	99967807.1	8/10/2005	1142202	£.
NMXI-0462.00/EP	Utility - NSPCT	Abandoned	ANTENNA INTERFACE	10/30/2001	00932547.3			eg G
NMXI-0473.00/EP	Utility - NSPCT	Issued	EMULATING NARROW BAND PHASE-LOCKED LOOP BEHAVIOR ON A WIDE BAND PHASE-LOCKED LOOP	3/4/1998	98911466.5	6/29/2005	968568	æ
NMXI-0474.00/EP	Utility - NSPCT	Lapsed	SINGLE-CHIP CMOS DIRECT-CONVERSION TRANSCEIVER	3/16/2001	99946859.8	11/15/2006	1110330	贸
NMXI-0478.00/EP	Utility - NSPCT	Issued	FOR FLASH MEMORY AND OTHER APPLICATIONS	12/23/2002	01941818.5	12/15/2010	1297530	Ą

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NMXI-0490.00/EP	Utility - NSPCT	Abandoned	UTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE		93904673.6	12/20/1995		ED.
NMX1-0496.00/EP	Utility - NSPCT	Abandoned	METHOD AND APPARATUS FOR GRAPHICAL DEVICE MANAGEMENT USING A VIRTUAL CONSOLE	12/27/2002	01948405.4	9/17/2008	1370935	ra To
NMXI-0497.00/EP	Utility - NSPCT	Abandoned	REMIOTELY MANAGING AND CONTROLLING A CONSUMER APPLIANCE	5/17/2001	01935694.8			安
NMXI-0502.00/EP	Utility - NSPCT	Abandoned	MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS	6/9/2003	1992286.3			m
NMXI-0504.00/EP	Utility - NSPCT	Issued	LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS	11/20/2001	01995179.7	4/13/2011	1336286	£
NMXI-0532.00/EP	Utility - ORG	Issued	APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM	10/15/1997	97308183.9	7/30/2003	0837429	Ð
NMXI-0535.00/EP	Utility - NSPCT	Issued	DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS	9/25/2001	01975434.0	5/6/2009	1323226	ģ
NMXI-0546.00/EP NMXI-0550.00/EP	Utility - NSPCT Utility - NSPCT	lssued	THREAD LIST EXPLICIT CACHING SYSTEM SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION	9/12/1995 1/20/1999	95935071.1 99904159.3	12/6/2006 1/14/2009	0781432 1059621	8
NMXI-0583.00/EP	Utility - NSPCT	Abandoned	SYSTEM AND METHOD FOR PROVIDING AUTHENTICATION AND VERIFICATION SERVICES IN AN ENHANCED MEDIA GATEWAY	8/29/2003	1273026.3			Ē
NMXI-0622.00/EP	Utility - NSPCT	Issued	INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (ACW) AND OPTICAL AMPLIFIER(S)	10/17/2002	02795533.5	12/21/2005	1436648	Ð
NMXI-0626.00/EP	Utility - NSPCT	Issued	VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE	11/28/2001	01273027.1	5/11/2011	1346613	四
NMXI-0637.00/EP	Utility - NSPCT	Allowed	MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION THOSE DIMENSIONAL EVERAMINE FOR ARCHITECTURE	12/30/2002	02792542.9	6 (72 /700s	1005001	3 43
NMX1-0694.00/EP	Utility - NSPCT	Issued	VARIABLE LEVEL MEMORY	8/6/2002	02757016.7	6/27/2007	1428221	m i
NMX1-0710.00/EP	Utility - NSPCT	Abandoned	ELECTRONIC ASSEMBLY WITH FILLED NO-FLOW UNDERFILL METHODS OF MANUFACTURE	4/8/2004	02802225.9			æ
NMXI-0719,00/EP	Utility - NSPCT	Abandoned	DIGITAL FRAME DETERMINATION AND APPARATUS	6/24/2004	02798607.4	12/2/2009	1468514	æ
NMXI-0721.00/EP	Utility - NSPCT	Abandoned	PROBABIUSTIC NETWORKS FOR DETECTING SIGNAL CONTENT FILM BULK ACQUSTIC RESONATOR STRUCTURE AND METHOD OF	4/22/2004	02757625.5			æ
NMXI-0736.00/EP	Utility - NSPCT	Pending	PRODUCING IT	12/17/2002	02805206.6			æ
NMX1-0741.00/EP	Utility - NSPCT	pending	SYSTEM AND METHOD TO DETECT ERRORS AND PREDICT POTENTIAL FAILURES	6/30/2005	05768522.4			ā
NMX1-0778.00/EP	Utility - NSPCT	Abandoned	INVERSE MULTIPLEXING OF MANAGED TRAFFIC FLOWS OVER A MULTI- STAR NETWORK	2/5/2003	03717887.8	4/11/2007	1476994	ĘĘ
NMX1-0786,00/EP	Utility - NSPCT)ssued	ESTABLISHING AN AD HOC NETWORK	4/24/2003	03724240.1	7/8/2009	1514386	Ą
NMXI-0844.00/EP	Utility - NSPCT	tssued	SYSTEM AND METHOD OF EFFICIENTLY MODULATING DATA USING SYMBOLS HAVING MORE THAN ONE PULSE - EPO	1/8/2004	04700900.6	12/12/2007	1.595343	æ,
NMXI-0860.00/EP	Utility - NSPCT	Pending	THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR-SIDE DIE AND METHOD	2/9/2004	04709509.6			æ
NMX1-0924.00/EP	Utility - NSPCT	Abandoned	ANNOTATING MEDIA CONTENT WITH USER-SPECIFIED INFORMATION	10/27/2004	04796692.4			TP TP
NMXI-0393.00/FI	Utility ~ EPPAT	Abandoned	ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK	12/30/1999	99967807.1	8/10/2005	1142202	<u>m</u>
NMXI-0018.00/FR	Utility - ORG	Abandoned	A NOVEL ARCHITECTURE FOR FLASH VIRTUAL GROUND MEMORY ARRAY	5/30/1990	9006713	1/27/1995	9006713	72
NMXI-0050.00/FR NMXI-0224.00/FR	Utility - ORG Utility - EPPAT	Abandoned Abandoned	CONFORMING POLISHING PAD NEGATIVE VOLTAGE SWITCHING CIRCUIT	7/7/1992 6/7/1996	9208358 96918382.1	4/29/1994	9208358	G 33

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MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS	NEGATIVE VOLTAGE SWITCHING CIRCUIT	VARIABLE STAGE CHARGE PUMP	METHOD AND APPARATUS FOR DETECTING AND SELECTING VOLTAGE	PORT EXPANDER ARCHITECTURE FOR EPROM PORTABLE DISK DRIVE CARRIER AND CHASSIS	REPROGRAMMING DATA FROM A COMPUTER	VARIABLE LEVEL MEMORY	TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE	IN LEGRALLU OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (ACW) AND OPTICAL AMPLIFIER(S)	THREAD LIST EXPLICT CACHING SYSTEM	APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM	COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE	IMPROVED FLOURINE DOPED SIO2 FILM AND METHOD OF FABRICATION	METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE	PORTABLE COMMUNICATION TERMINAL FOR REMOTE DATABASE QUERY	INTEGRATED CIRCUIT PACKAGE	METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER	MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS	PORTABLE DISK DRIVE CARRIER AND CHASSIS PACKAGE HOUSING MULTIPLE SEMICONDUCTOR DIES	M&A FOR BACKING OUT OF A SOFTWARE INSTRUCT. AFTER EXEC. HAS BEGUN	CONFORMING POLISHING PAD	A NOVEL ARCHITECTURE FOR FLASH VIRTUAL GROUND MEMORY ARRAY	METHOD OF ACCESSING AN ELECTRICALLY PROGRAMMABLE MEMORY DEVICE	PORT EXPANDER ARCHITECTURE FOR EPROM	LOAD LINE FOR EPROM	TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE	INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (ACW) AND OPTICAL AMPLIFIER(S)	APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM	ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK
12/12/1396	9/21/1998	1/15/1000	2/26/1998	11/2/1989 3/17/1998	11/16/1999	8/6/2002	11/27/2003	10/17/2002	9/12/1995	10/15/1997	1/27/1993	10/11/2000	11/1/2000	6/30/1987	7/9/1997	9/29/1997	12/12/1996	11/28/1995 3/12/1996	5/11/1988	2/19/1992	3/5/1990	3/5/1990	11/2/1989	8/24/1988	11/27/2003	10/17/2002	10/15/1997	12/30/1999
99101098.1	98110789.7	00400045	981015020 2	97102060.5	0202551.8	02757016.7	02725662.7	02795533.5	95935071.1	97308183.9	93904673.6	0212404.8	0026721.1	87109409.0	5.6623066	9911695.6	9811429.1	9594294.2 969087568	881077610.3	9203649.0	9004859.6	9314259.4	8924745.6		02725662.7	02795533.5	97308183.9	999 99967807.1 8/10/2005 1142202 FR
8/17/2001	7/30/2004	7/13/2002	7/19/2002	3/27/1998 5/13/2005	3/31/2004	6/27/2007	6/22/2005	12/21/2005	12/6/2006	7/30/2003	12/20/1995	4/28/2004	12/4/2002	12/15/1993	8/15/2001	9/12/2001	12/13/2000	9/8/2004 6/30/2004	11/30/1994	11/30/1994	1/5/1994	1/26/1994	11/4/1992	11/13/1991	6/22/2005	12/21/2005	7/30/2003	8/10/2005
1016341	HK1015520 HK 10098978	UK1002730	HK1003232	1000475	2369702	1428221	1396081	1436648	0781432	0837429	638188	2373372	2353912	0251296	2333182	2340973	2323212	0794717 0815615	292791	2257382	2234834	2266796	2227582	2214380	1396081	1436648	0837429	1142202
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M&A FOR BACKING OUT OF A SOFTWARE INSTRUCTION AFTER EXECUTION HAS BEGUN	A NOVEL ARCHITECTURE FOR FLASH VIRTUAL GROUND MEMORY ARRAY	VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE	MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS	A METHOD AND SYSTEM FOR PROVIDING INTERNET SERVICES TO A CONSUMER THROUGH REMOTE MANAGEMENT AND CONTROL	CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS	METHOD AND ANYAWATOS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN	CONFORMING POLISHING PAD	THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR-SIDE DIE AND METHOD	SYMBOLS HAVING MORE THAN ONE PULSE	ESTABLISHING AN AD HOC NETWORK	FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING	DIGITAL FRAME DETERMINATION AND APPARATUS PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT	MANUFACTURE	SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION	APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM	CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORMS	LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS	MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS	INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS	IMPROVED FLOURINE DOPED SIO2 FILM AND METHOD OF FABRICATION	METHOD AND APPARATUS FOR PERFORMING VIDEO IMAGE DECODING	METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE	METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER	METHOD AND APPARATOS FOR PROVIDING MAPROVED DIAGNOSTIC FUNCTIONS IN A COMPUTER SYSTEM	Tite
5/11/1988	7/23/1990	5/27/2003	4/29/2003	5/17/2001	8/10/2001	12/11/1997	4/7/1992	12/19/2005	12/30/2005	4/24/2003	11/3/2004	10/27/2004 8/6/2004	11/28/2001	5/22/2001	5/23/1998	5/23/2003	11/20/2001	11/5/2003	12/23/2002	10/11/2000	5/31/2002	4/4/2001	9/29/1997	5/7/1999	Hiebate
88107610.3	21015 A/90	675/KOLNP/2003	540/KOLNP/2003	IN/PCT/2002/01358-C	IN/PCT/200101042/MU 4/17/2006	130563	921103	05111716.5	05112185.5	05102495.1	04108659.1	04108409,4 04105864.8	3109116.7	01103490.8	98194475.9	03103974.1	03106719.4	03107859.2	03104815.2	02107701.3	02108033.0	01102410.7	00105429.0	99102071	SelfaiNo "
11/30/1994	5/16/1994	10/14/2005		6/9/2006	4/17/2006	2/1/2004	11/21/1995			7/8/2009				9/11/2009	2/20/2004					12/10/2004		5/2/2003	5/31/2002	5/6/2005]IssueOate
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Exhibit B - Foreign Applications and Patents

J	Ġ.	4276946	3/13/2009	2003-536786	10/17/2002	WAVEGUIDE GRATING (ACW) AND OPTICAL AMPUFIER(S)	Issued	Utility - NSPCT	NMXI-0622.00/JP
	df			2007-290121	12/21/2001	ENHANCED MEDIA GATEWAY CONTROL PROTOCOL	Abandoned	Utility - NSPCT	NMXI-0599.01/JP
-	.			2002-555069	8/27/2003	ENHANCED MEDIA GATEWAY CONTROL PROTOCOL	Abandoned	Utility - NSPCT	NMXI-0599.00/JP
	٩ſ	4272429	3/6/2009	2002-554833	6/27/2003	SYSTEM AND METHOD FOR PROVIDING AUTHENTICATION AND VERIFICATION SERVICES IN AN ENHANCED MEDIA GATEWAY	Abandoned	Utility - NSPCT	NMXI-0583,00/JP
•	چ			195011/1997	7/4/1997	APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM	Abandoned	Utility - ORG	NMXI-0532.00/JP
*	۵ſ	3813873	6/9/2006	2001-565629	5/23/2003	CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORMS	Issued	Utility - NSPCT	NMXJ-0516.00/JP
v	5 0			2002-548782	6/9/2003	MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS	Abandoned	Utility - NSPCT	NMXI-0502.00/JP
-	¥9	3786419	3/31/2006	519231/93	1/27/1993	COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE	Issued	Otility - NSPCT	NMX1-0490.00/1P
Ť	φĮ			2000-603092	8/31/2001	CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS	Abandoned	Utility - ORG	NMXI-0420.00/JP
~	JP.	4348015	7/24/2009	2000-583167	4/19/2001	METHOD AND CIRCUIT FOR DATA DEPENDENT VOLTAGE BIAS LEVEL	Issued	Utility - ORG	NMXI-0392.00/JP
. u	je je gt	4188562	9/19/2008	2000-540619 2000-547785	1/5/1999 3/12/1999	VARIABLE DELAY CELL WITH A SELF-BIASING LOAD METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE	Abandoned	Utility - NSPCT	NMXI-0337.00/JP
	g.			2000-519556	9/21/1998	MULII-LAYEN CODER/DECODER FOR PRODUCING QUANTIZATION ERROR SIGNAL SAMPLES	Abandoned	Utility - ORG	NMXI-0325.00/JP
-	٩ſ			\$41666/98	3/12/1998	PERIPHERAL DEVICE PREVENTING POST-SCAN MODIFICATION	Abandoned	Utility - ORG	NMXI-0323.00/JP
**	ď٤			530079/98	12/11/1997	METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN	Abandoned	Utility - ORG	NMXI-0282.00/JP
-	Je	4463879	2/26/2010	523636/98	9/29/1997	METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER	Abandoned	Utility - ORG	NMXI-0251.00/JP
-	વે			501941/97	6/7/1996	NEGATIVE VOLTAGE SWITCHING CIRCUIT	Abandoned	Utility - NSPCT	NMXI-0224.00/JP
•	र्वे	3717937	9/9/2005	527776/96	3/12/1996	OUAL DIE PACKAGE FOR SECIAL APPLICATIONS	Issued	Utility - NSPCT	NMX1-0222.00/JP
. •	ਰ ਦ	3766101	2/3/2006	\$14005/96 \$13766/97	10/18/1995 9/30/1996	ACCURATE VPP DETECTOR FOR FLASH MEMORY VARIABLE STAGE CHARGE PUMP	Pending Issued	Utility - NSPCT	NMXI-0204.00/JP NMXI-0208.00/JP
-	٩	1865189	8/26/1994	121665/88	5/18/1988	METHOD AND APPARATUS FOR BACKING OUT OF A SOFTWARE INSTRUCTION AFTER EXECUTION HAS BEGUN	Abandoned	Utility - ORG	NMXI-0174.00/JP
-	ď	3688710	6/17/2005	524718/95	3/16/1995	METHOD AND APPARATUS FOR PROVIDING AN ULTRA LOW POWER REGULATED NEGATIVE CHARGE PUMP	Issued	Utility - ORG	NMXI-0173.00/JP
-	٩Į	3667786	4/15/2005	71270/94	3/17/1994	BUILD UP TYPE SOCKETABLE BGH	Issued	Utility - ORG	NMXI-0168.00/JP
	Φſ	3365565	11/1/2002	121605/1993	5/24/1993	CALCULATION METHOD FOR PARTICLE FLUX SHADOWING	issued	Utility - ORG	NMXI-0065.00/JP
	₹	3416894	4/11/2003	121723/1993	5/24/1993	SURFACE SWEEPING METHOD FOR 3-D TOPOGRAPHY SIMULATION	Issued	Utility - ORG	NMX1-0064.00/JP
TE	₩			514358/93	2/18/1993	ADVANCED MULTILAYER MOLDED PLASTIC PACKAGE USING MESIC TECHNOLOGY	Abandoned	Utility - DIV	NMXI-0052.00/JP
	약	3099209	8/18/2000	154196/92	5/22/1992	CONFORMING POLISHING PAD	Abandoned	Utility - ORG	NMXI-0050.00/JP
	۵í	317963	4/6/2001	324108/91	11/13/1991	UPROM DEVICE	Issued	Utility - ORG	NMXI-0034.00/JP
	مَرْ	3213820	7/27/2001	122983/92	4/17/1992	TUNGSTEN ETCH PROCESS	Issued	Utility - ORG	MMXI-0023.00/JP
-	п П	1053621	1/14/2009	999041593	1/20/1999	SYSTEM AND METHOD FOR AUTOMATIC PRA DETECTION	Issued	Utility - EPPAT	NMXI-0550.00/IT
•	Ŧ			97308183.9	10/27/2003	APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A	Lapsed	Utility - EPPAT	NMXI-0532.00/IT
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Exhibit B - Foreign Applications and Patents

3 3	0548910	1/25/2006	2003-7004397	3/5/2003	BUILD-UP LAYERS DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS	Issued	Utility - NSPCT	NMX1-0535.00/KR
S S			10-2003-700750s	6/5/2003	MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND	Abandoned	Utility - NSPCT	NMXI-0502.00/KR
S	0498675	6/22/2005	2005-7001212	1/21/2005	CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS	Issued	Utility - DIV	NMXI-0420.01/KR
∑	0522383	10/11/2005	2001-7011227	9/3/2001	CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS	Issued	Utility - ORG	NMXI-0420.00/KR
KR	0569749	4/4/2006	10-2001-7006012	4/19/2001	METHOD AND CIRCUIT FOR DATA DEPENDENT VOLTAGE BIAS LEVEL	Issued	Utility - ORG	NMXI-0392.00/KR
X X	0367110 430139	12/20/2002 4/22/2004	10-2000-7007725 2000-7012436	7/13/2000 11/7/2000	Variable delay cell with a self-bjasing load Position sensitive display controller	Issued Abandoned	Utility - NSPCT Utility - ORG	NMXI-0337.00/KR NMXI-0370.00/KR
KR.	387338	5/30/2003	1999-7009316	10/9/1999	SELF-CONFIGURING INTERFACE ARCHITECTURE ON FLASH MEMORIES	Issued	Utility - ORG	NMXI-0313.00/KR
KR.	488324	4/29/2005	1999-7001953	3/9/1999	INTEGRATED CIRCUIT PACKAGE	Issued	Utility - ORG	NMXI-0291.00/KR
K R	343029	6/21/2002	1999-7005947	6/29/1999	METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN	Issued	Utility - ORG	NMXI-0282.00/KR
%	387336	5/30/2003	1999-7004461	9/29/1997	METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER	Issued	Utility - NSPCT	NMXI-0251.00/KR
ХR	307895	8/24/2001	1998-704709	6/20/1998	MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS	Issued	Utility - NSPCT	NMXI-0239.00/KR
\$ \$ \$	214163 263275 292832	5/18/1999 5/15/2000 3/27/2001	92-4336 1996-705254 1997-709025	3/17/1992 9/23/1996 6/7/1996	CONFORMING POLISHING PAD LOW POWER CHARGE PUMP FOR FLASH MEMORY NEGATIVE VOLTAGE SWITCHING CIRCUIT	Issued Issued	Utility - ORG Utility - ORG Utility - NSPCT	NMXI-0050.00/KR NMXI-0173.00/KR NMXI-0224.00/KR
٩Į			2006-538272	10/27/2004	ANNOTATING MEDIA CONTENT WITH USER-SPECIFIED INFORMATION	Abandoned	Utility - NSPCT	NMXI-0924.00/JP
٩L			2004-501283	4/16/2004	APPARATUS AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING	Abandoned	Utility - NSPCT	NMXI-0854.00/JP
μ			2004-500440	4/24/2003	ESTABLISHING AN AD HOC NETWORK	Abandoned	Utility - NSPCT	NMXI-0786.00/JP
<u>م</u> م			2008-329618	12/25/2008	METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER	Abandoned	Utility - DIV	NMXI-0752.01/JP
JР			586733/2003	10/14/2004	METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER	Abandoned	Utility - NSPCT	NMXI-0752.00/IP
d۲	4152892	7/11/2008	2003-553708	12/17/2002	FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING	Issued	Utility - NSPCT	NMXI-0736.00/JP
₩			2001-516051	1/30/2002	PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROGRAMMING DATA FROM A COMPUTER TO THE PERSONAL INFORMATION DEVICE VIA THE PCMCIA PORT OR THROUGH A DOCKING STATION WITH BAUD RATE CONVERSION MEANS	Abandoned	Utility - ORG	NMXI-0712.00/JP
σ	4102198	3/28/2008	2002-586286	4/12/2002	TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE	Issued	Utility - NSPCT	NMXI-0679.00/JP
₽			2002-575882	9/26/2003	ENABLING MANUAL ADJUSTMENT OF POINTING DEVICE CURSOR SPEED	Abandoned	Utility - NSPCT	NMXI-0659.00/JP
₽			P2003-550231	11/20/2002	LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY	Abandoned	Utility - NSPCT	NMXI-0653.00/JP
ρ			93	6/30/2003	VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE	Abandoned	Utility - NSPCT	NMXI-0626.00/JP
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UNIVERSAL CARRIER FOR HOT SWAPPABLE DRIVER ACCURATE VPP DETECTOR FOR FLASH MEMORY VARIABLE STAGE CHARGE PUMP METHOD TO PREVENT INTRUSIONS INTO ELECTRONIC CIRCUITRY DUAL DIE PACKAGE FOR SECIAL APPLICATIONS	METHOD AND APPARATUS FOR PROGRAMMING AND ERASING FLASH EEPROM MEMORY ARRAYS UTILIZING A CHARGE PUMP CIRCUIT	CONFORMING POLISHING PAD	ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK	INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (ACW) AND OPTICAL AMPLIFIER(S)	TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE VARIABLE DELAY CELL WITH A SELF-BIASING LOAD VARIABLE STAGE CHARGE PUMP	ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK	APPARATUS AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING	FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING	DIGITAL LEAKAGE COMPENSATION CIRCUIT NOISE DEPENDENT FILTER	VIA-IN-PAD WITH OFF CENTER GEMETRY AND METHODS OF MANUFACTURE	MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS	SELF-CONFIGURING INTERFACE ARCHITECTURE ON FLASH MEMORIES	DUAL DIE PACKAGE FOR SECIAL APPLICATIONS AN INTEGRATED CIRCUIT PACKAGE	CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS	APPARATUS AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING	METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER	FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING	VARIABLE LEVEL MEMORY	LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE	VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE	INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (ACW) AND OPTICAL AMPUFIER(\$)
11/28/1995 10/18/1995 9/30/1996 2/15/1996 3/12/1996	9/6/1994	2/19/1992	12/30/1999	10/17/2002	4/12/2002 1/12/1999 9/30/1996	12/30/1999	5/7/2004	12/17/2002	1/18/2002 6/21/2002	12/10/2001	11/12/2001	4/29/1998	3/8/1996 9/9/1997	8/10/2001	4/16/2004	10/15/2004	6/17/2004	3/17/2004	11/20/2002 10/29/2003	6/28/2003	10/17/2002
9702174-5 9701544-0 9800988-9 9704857-4 9704782-3	9605497-8	9590755-6	99967807.1	2004114867	02725662.7 1-1999-00064 98108548	99967807.1	PI 20041684	PI20024718	PI 20020207 PI 2022322	PI20015604	PIZ0015198	9801913	9600858 9704164	PA/A/2001/008580	2005-7021223	10-2004-7016496	2004-7009478	2004-7003885	2004-7008174 10-2003-7014161	2009-7008832	2004-7005595
9/28/1998 9/28/1998 1/25/2000 5/25/1999 3/30/1999	5/7/2002	7/29/1995	8/10/2005	3/20/2006	6/22/2005 2/3/2004 8/20/2001	8/10/2005		11/28/2008	5/31/2006 12/31/2007	6/29/2007		4/29/2006	6/30/2006 5/31/2005				1/9/2007		9/28/2006 2/2/2006	7/14/2006	4/3/2007 0705865
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VANIAGUE DELAY CEIL, WITH A SELF-BIASING LUAD METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE	CHCUIT AND METHOD FOR CONTROLLING POWER AND PERFORMANCE BASED ON OPERATING ENVIRONMENT CONTROLLING TO THE CO	METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN	METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER	IIC BASED VBI DECODER	A METHOD TO PREVENT INTRUSION INTO ELECTRONIC CIRCUITRY	BUILD UP TYPE SOCKETABLE BGH	MANUFACTURE CONTORMING BOTISHING BOD. LITTLEY	VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE FI ECTRONIC ACCEMBLY WITH SELED NO. SI OW HINDERSELL METHODS OF	MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS	SYMBOLS HAVING MORE THAN ONE PULSE	ESTABLISHING AN AD HOC NETWORK	PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT	DIGITAL FRAME DETERMINATION AND APPARATUS	VARIABLE LEVEL MEMORY	MATERIAL DEPOSITION FROM A LIQUETED GAS SOLUTION LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY	VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE	DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS	APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM	MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS	INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS	EMULATING NARROW BAND PHASE-LOCKED LOOP BEHAVIOR ON A WIDE BAND PHASE-LOCKED LOOP	ANTENNA INTERFACE	IMPROVED FLOURINE DOPED SIO2 FILM AND METHOD OF FABRICATION	INTEGRATED CIRCUIT PACKAGE	METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN	METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER	METHOD AND APPARATUS FOR PROVIDING IMPROVED DIAGNOSTIC FUNCTIONS IN A COMPUTER SYSTEM	illu ra
1/13/1999 8/4/1999	5/2/1998	12/26/1997	10/15/1997	1/30/1997	12/28/1995	2/28/1996	10/24/2002	12/27/2001	12/3/2001	1/8/2004	4/24/2003	3/10/2004	6/28/2004	8/6/2002	12/30/2002	11/28/2001	9/25/2001	10/13/1997	5/29/2003	6/1/2001	3/4/1998	11/26/2001	10/11/2000	7/9/1997	12/11/1997	9/29/1997	6/27/1997	ate
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ADAPTIVE EQUALIZATION USING A CONDITIONAL UPDATE SIGN-SIGN LEAST MEAN SQUARE ALGORITHM	ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT HAVING A RING OSCILLATOR TIMER CIRCUIT	INVERSE MULTIPLEXING OF MANAGED TRAFFIC FLOWS OVER A MULTI- STAR NETWORK	METHOD, MACHINE-READABLE MEDIUM, APPARATUS AND SYSTEM FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER	SYSTEM AND METHOD TO DETECT ERRORS AND PREDICT POTENTIAL FAILURES	FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING	INTEGRATED OPTOELECTRICAL CIRCUIT PACKAGE WITH OPTICAL WAVEGUIDE INTERCONNECTS	PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT	DIGITAL FRAME DETERMINATION AND APPARATUS	TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE	ENABLING MANUAL ADJUSTMENT OF POINTING DEVICE CURSOR SPEED	LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY	A SECOND CIRCUIT UNDER A FIRST OPERATING MIDE OHTERWISE DECOUPLING THE POWER POTENTIAL	DIGITAL LEAKAGE COMPENSATION CIRCUIT APPARATUS HAVING A FIRST CIRCUIT SUPPLYING A POWER POTENTIAL TO	MATERIAL DEPOSITION FROM A LIQUEHED GAS SOLUTION	METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING	DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS	APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM	LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS	REMOTELY MANAGING AND CONTROLLING A CONSUMER APPLIANCE	INDUCTIVE CHARGE PURP CIRCUIT FOR PROVIDING VOLLAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS	ANTENNA INTERFACE SINGLE-CHIP COMOS DIRECT-CONVERSION TRANSCEIVER	IMPROVED FLOURINE DOPED SIG2 FILM AND METHOD OF FABRICATION	METHOD AND APPARATUS FOR WRITING HOPS INTO A BUFFER QUEUE INCLUDING OVERWRITING ENTRIES THAT ARE INDICATED TO CONTAIN INVALID HOPS	PPARATUS FOR PERFORMING VIDEO IMAGE DECODING
9/9/2004	2/24/2003	2/21/2003	4/17/2003	7/4/2005	12/13/2002	3/11/2003	9/23/2002	8/28/2002 1/3/2003	3/12/2002	1/15/2002	11/20/2002	3/7/2002	3/26/2002	12/31/2002	3/5/2002	9/27/2001	6/27/1997	11/21/2001	5/30/2001	5/24/2001	5/23/2000 12/7/1999	11/16/2000	2/21/2001	12/5/2000
93127272	092103764	92103629	92108929	94122531	91136074	92105182	091121741	92100083	091104602	91100508	091132888	91104263	091105913	91138032	091104064	090123932	86109133	090128857	90113072	90112533	89109929 88115667	089124269	090103910	089125882
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Completed	Expired	Expired	Expired	Expired	Expired	Expired	Expired	Expired	Expired	Abandoned	Completed	Completed	Completed	Completed	Expired	Pending	Pending	Issued	Issued	Abandoned	Abandoned	Issued	Abandoned	Issued	Issued	Issued	Issued
IMPROVED FLOURING DOPED SION FILM AND METHOD OF FARRICATION	INCLUDING OVERWRITING ENTRIES THAT ARE INDICATED TO CONTAIN INVALID UOPS	METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE	POSITION SENSITIVE DISPLAY CONTROLLER	CIRCUIT AND METHOD FOR CONTROLLING POWER AND PERFORMANCE BASED ON OPERATING ENVIRONMENT	MULTI-LAYER CODER/DECODER	PERIPHERAL DEVICE PREVENTING POST-SCAN MODIFICATION	FIDUCIAL FOR ALIGNING AN INTEGRATED CIRCUIT DIE	INTEGRATED CIRCUIT PACKAGE	METHOD FOR IMPROVED DATA RETENTION IN A NONVOLATILE WRITEABLE MEMORY BY SENSING AND REPROGRAMMING CELL VOLTAGE	METHOD AND APPARATUS FOR FAST SELF DESTRUCTION OF A CMOS INTERGRATED CIRCUIT	METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER	MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS	PACKAGE HOUSING MULTIPLE SEMICONDUCTOR DIES	PORTABLE DISK ORIVE CARRIER AND CHASSIS	ANNEAL TO DECREASE MOISTURE ABSORBANCE OF INTERMETAL DIELECTRICS	LOW DELAY AND SMALL MEMORY FOOTPRINT PICTURE BUFFERING	METHOD AND APPARATUS FOR EFFICIENT AND ARTIFACTS FREE DISPLAYING OF ROTATING IMAGES	RESONANT TUNNELING TRANSISTOR USING CONVENTIONAL MOSFET PROCESSING	TECHNIQUES FOR TRANSMITTING AND RECEIVING TRAFFIC OVER ADVANCED SWITCHING COMPATIBLE SWITCH FABRICS	REMOTE STATUS FORWARDING IN A DISTRIBUTED CROSS-CONNECTION SYSTEM	CASCADED CONNECTION MATRICES IN A DISTRIBUTED CROSS- CONNECTION SYSTEM	NOVEL STACKED PACKAGE THROUGH SOLDER BALL WITH STENCIL- PRINTED ENCAPSULANT	ANNOTATING MEDIA CONTENT WITH USER-SPECIFIED INFORMATION	TECHNIQUES TO TEST SIGNAL PROPAGATION MEDIA	STROBE THROUGH DIFFERENTIAL SIGNALING IN MSL	TECHNIQUES TO PERFORM LINE QUALIFICATION	HERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH XTERNAL-CONNECTOR-SIDE DIE AND METHOD
10/11/2000	3/13/2001	3/12/1999	4/13/1999	4/17/1998	9/21/1998	3/12/1998	12/11/1997	7/9/1997	10/10/1997	12/27/1996	9/29/1997	12/12/1996	3/12/1996	11/28/1995	5/18/1992	12/1/2006	12/12/2006	10/28/2005	12/28/2005	12/16/2005	12/16/2005	7/27/2005	10/28/2004	5/27/2004	4/19/2004	5/27/2004	2/11/2004
	PCT/US01/08104	PCT/US99/05436	PCT/US99/08034	PCT/US98/07759	PCT/US98/19536	PCT/US98/05010	PCT/US97/22690	PCT/US97/11890	PCT/US97/18694	PCT/US96/20808	PCT/US97/17491	PCT/US96/19808	PCT/US96/03331	PCT/US95/15443	PCT/US92/0414S	95144673	95146490	094137898	94147137	94144719	94144718	94125491	93132733	93115086	93110830	93115087	04 93103221
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	hibit B - Foreign Applications and Patents

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WALLET FOR PERSONAL INFORMATION DEVICE	RAANITEACTURE NAANITEACTURE NAANIT	VARIABLE LEVEL MEMORY	COMMUNICATION CONTROL FOR A USER OF A CENTRAL COMMUNICATION CENTER	BEST-OF-M PULSE POSITION MODULATION DETECTOR	ENABLING MANUAL ADJUSTMENT OF POINTING DEVICE CURSOR SPEED	A SECOND CIRCUIT UNDER A FIRST OPERATING MIDE OHTERWISE DECOUPLING THE POWER POTENTIAL	MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION DIGITAL LEAKAGE COMPENSATION CIRCUIT	WA-IN-PAU WITH OFFICEN ER GEOMETRY AND METHODS OF MANUFACTURE	CBR/VBR TRAFFIC SCHEDULER ENHANCED MEDIA GATEWAY CONTROL PROTOCOL	SYSTEMS AND METHODS FOR IMPLEMENTING WITH GUARANTEED MCR	VERIFICATION SERVICES IN AN ENHANCED MEDIA GATEWAY	SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION	METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING	CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORM	LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS	MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS	REMOTELY MANAGING AND CONTROLLING A CONSUMER APPLIANCE	METHOD AND APPARATUS FOR GRAPHICAL DEVICE MANAGEMENT USING A VIRTUAL CONSOLE	COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE	CARRIER FREQUENCY ESTIMATOR FOR A SIGNAL RECEIVER	VITERBLACS UNIT WITH RENORMALIZATION	BAND PHASE-LOCKED LOOP	ANTENNA INTERFACE EMULATING NARROW BAND PHASE-LOCKED LOOP BEHAVIOR ON A WIDE	ONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT ACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS
11/11/1999	10/28/2002	8/6/2002	3/13/1998	5/13/1996	1/25/2002	4/5/2002	12/30/2002 2/7/2002	11/28/2001	6/25/1999 12/21/2001	6/25/1999	11/20/2001	1/20/1999	3/21/2002	1/24/2001	11/20/2001	11/9/2001	\$/17/2001	6/15/2001	1/27/1993	5/26/1999	7/15/1994	3/4/1998	5/18/2000	2/8/2000
DCT /1 1500 / 755 75	PCT/US02/34516	PCT/US02/25092	PCT/US98/04864	PCT/US96/06811	PCT/US02/02110	PCT/US02/10816	PCT/US02/41705 PCT/US02/03633	PCT/US01/44721	PCT/USS9/14268 PCT/USO1/49779	PCT/US99/14500	PCT/US01/44675	PCT/US99/01202	PCT/US02/08751	PCT/US01/02323	PCT/US01/43615	PCT/US01/49898	PCT/US01/16135	PCT/US01/19280	PCT/U\$93/00732	PCT/US99/10464	PCT/US94/07957	PCT/US98/041/5	PCT/US00/13606	PCT/US00/03242
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REPROSENMENT DE PRESENTATION AND REPROCESSAND THE PRESENDANT THE PRESENTATION PROJECT NATIFICATION CONTROLL THROUGH A DOCKING STATION MATERIAL PRAME DETERMINATION AND APPARATUS SERVICE PROJECT NATIFICATION CONTROLL THROUGH A DOCKING STATION WITH BAULD AND CONTROL THROUGH A DOCKING STATION WITH BAULD AND CONTROL THROUGH AS SERVICE PROTECTION CONTROL TO SERVICE PROJECT SERVICE AND METHOD TO DETECT ERROBS AND PREDICT POTENTIAL 4/36/2003 PCT/US03/23838 PCT/US03/23839 PCT	REPROJUNAL INPURMATION DEVICE VIA THE POLYCUL POLYCON DEVINDANCE REPROJUNG INFORMATION DEVICE VIA THE POLYCUL PORT OR THROUGH A DOCKING STATION WITH BAUD RATE CONVESSION MEANS DIGITAL FRAME DETERMINATION AND APPARATUS DIGITAL FRAME DETERMINATION AND APPARATUS PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT NATEGORIES OF TORECTRICAL CIRCUIT PACKAGE WITH OPTICAL WAYEGUDE INTERCONNECTOS SYSTEM AND METHOD TO DETECT ERRORS AND PREDICT POTIENTIAL FALLURES MAETHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT HAVING A RING OSCILLATOR TIMER CIRCUIT STARLISHING AND AD CO NETWORK ADAPTIVE EQUALIZATION USING A CONDITIONAL UPDATE SIGN-SIGN LEAST MEAN SQUARE ALGORITHM PROCUNICATION NETWORK APPARATUS AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING THERMALLY ENHANCED ELECTRONIC FUP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR-SIDE BIE AND METHOD TECHNIQUES TO TEST SIGNAL PROPAGATION MEDIA PROCESSING TECHNIQUES TO TEST SIGNAL PROPAGATION MEDIA METHODS AND APPARATUSES FOR PROVIDING STACKED-DIE DEVICES CASCADED CONNECTION MATRICES IN A DISTRIBUTED CROSS-CONNECTION SYSTEM TECHNIQUES FOR TRANSMITTING AND RETECTION GENERAL CONNECTION SYSTEM BAYLANCED SWITCHING COMPATIBLE SWITCH FABRICS APPARATOR SONTAL MEMORY FOODPRINT PICTURE BUFFERING APVANCED TOA CHASSIS WITH A MOYABLE FAN TRAY METHOD AND APPARATUS EOR PRICIENT AND ARTIFACTS FREE DISPLAYING OF ROTATIONS (MAGGES LOWN DELLY AND SMALL MEMORY FOOTPRINT PICTURE BUFFERING 11/27/2006 11/27/2006 11/27/2006 11/27/2006 11/27/2006 11/27/2006 11/27/2006 11/27/2006 11/27/2006 11/27/2006 11/27/2006 11/27/2006 11/27/2006 11/27/2006 11/27/2006 11/27/2006 11/27/2006
NG 11/16/1999 F 12/27/2002 F 9/5/2002 F 9/5/2002 F 9/5/2003 F 6/30/2005 F 6/30/2003 F 10- 2/5/2003 F 4/24/2003 F 4/24/2003 F 12/22/2003 F 12/22/2003 F 12/22/2004 F 12/5/2004 F 12/5/2004 F 12/5/2004 F 12/5/2004 F 12/5/2005 F 12/15/2005 F 11/17/2005 F 11/17/2005 F 11/17/2006 F 11/17/2006 F 11/17/2006 F 11/30/2006 F	PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROCARMINING DATA FROM A COMPUTER OF THE RESIGNAL 11/16/1999 INFORMATION DEVICE VIA THE PERSONAL THE PERSONAL STATION WITH BAUD BATE CONVERSION MEANS DIGITAL FRAME DETERMINATION AND APPARATUS DIGITAL FRAME DETERMINATION AND APPARATUS PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT PROBABILISTIC NETWORK SIGNAL PROBACTION IN CONSIDERATION OF SYSTEM POWER PROPERSONAL PROPERCY PROPERTY PRESENTATION IN CONSIDERATION OF SYSTEM POWER PROPERSONAL SULVAME CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER PROPERSONAL SULVAME AND PREDICT POTENTIAL ESTABLISHING AND DETECT ROORS AND PREDICT POTENTIAL ESTABLISHING AND AND HOLD CHARGE PROTECTION CIRCUIT HAVING A RING DISCIPLIANCE CONNECTORS TO PROVIDE MULTITHREADED COMPUTER PROCESSING PROVINCION SYSTEM PROPERSONAL SULVAME SIGNALING PROVINCION SYSTEM PROPERSONAL SULVAMENTITING AND RECEIVING STACKED-DIE DEVICES CASCADED CONNECTION MATRICES IN A DISTRIBUTED CROSS-CONNECTION PROVINCION SYSTEM PROPERSONAL PROVINCED SYSTEM PROVESSING PROVINCION SYSTEM PROVESSING PROVESSING PROVESSING PROVINCION SYSTEM PROVESSING PROVESSING PROVINCION SYSTEM PROVINCION SYSTEM PROVESSING PROVINCION SYSTEM PROVINCI
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REPROGRAMMING DATA FROM A COMPUTER TO THE PERSONAL INFORMATION DEVICE VIA THE PERSONAL INFORMATION WITH BAUD PARIE CONVERSION MEANS DIGITAL FRAME DETERMINATION AND APPARATUS PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT INTEGRATED OPTOELECTRICAL CIRCLIT PACKAGE WITH OPTICAL WAVEGUIDE INTERCONNECTS SYSTEM AND METHOD TO DETECT ERRORS AND PREDICT POTENTIAL FAILURES METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER INMER CIRCUIT PRESENTATION IN CONSIDERATION OF SYSTEM POWER INMER CIRCUIT ESTABLISHING AN AD HOC NETWORK ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT HAVING A RING OSCILLATOR TIMBER CIRCUIT ESTABLISHING AN ADHOC NETWORK ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT HAVING A RING OSCILLATOR TIMBER CIRCUIT ESTABLISHING AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING THERMALLY ENHANCED ELECTRONIC FUD-CHIP PACKAGING WITH ESTABLISHING AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING THERMALLY ENHANCED ELECTRONIC FUD-CHIP PACKAGING WITH EXTERNAL-CONNECTION-SIDE DIE AND METHOD TECHNIQUES TO TEST SIGNAL PROPAGATION MEDIA METHODS AND APPARATUSES FOR PROVIDING STACKED-DIE DEVICES CASCADED CONNECTION MATRICES IN A DISTRIBUTED CROSS-CONNECTION SYSTEM TECHNIQUES TO TEST SIGNAL PROPAGATION MEDIA METHODS AND APPARATUSES FOR PROVIDING STACKED-DIE DEVICES CASCADED CONNECTION MATRICES IN A DISTRIBUTED CROSS-CONNECTION SYSTEM REMOTE STATUS FORWARDING IN A DISTRIBUTED CROSS-CONNECTION FERENCES IN A DISTRIBUTED CROSS-CONNECTION SYSTEM REMOTE STATUS FORWARDING IN A DISTRIBUTED CROSS-CONNECTION PROCESSING APPARACIUS SFOR TRANSMITTING AND RECEIVING TRAFFIC OVER ADD SMALL MEMORY FOOTPRINT PICTURE BUFFERING DISPANANCE OF ROTATING INAGES! LOW DELAY AND SMALL NEMORY FOOTPRINT PICTURE BUFFERING DISPANANCE FOR VIRTUAL SECURITY MODULE	ted
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