

PATENT ASSIGNMENT COVER SHEET

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Stylesheet Version v1.2

EPAS ID: PAT6333346

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
AMIT K SRIVASTAVA	03/03/2020
MAJID SHUSHTARIAN	03/03/2020
ANAND K ENAMANDRAM	06/01/2020
JARED W HAVICAN	06/01/2020
JEFFREY A PIHLMAN	03/25/2020
MICHAEL J KARAS	03/04/2020
RAMAMURTHY KRITHIVAS	08/25/2020
CHRISTINE WATNIK	03/10/2020
RECEIVING PARTY DATA	
Name:	INTEL CORPORATION
Street Address:	2200 MISSION COLLEGE BLVD
City:	SANTA CLARA
State/Country:	CALIFORNIA
Postal Code:	95054
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	16790648
CORRESPONDENCE DATA	
Fax Number:	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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Email:	alan@patentlylegal.com
Correspondent Name:	ALAN BURNETT
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ATTORNEY DOCKET NUMBER:	AC4628-US
NAME OF SUBMITTER:	R. ALAN BURNETT
SIGNATURE:	/R. Alan Burnett/

DATE SIGNED:	10/02/2020
Total Attachments: 12 source=AC4628-US_Intel_Assignments_All#page1.tif source=AC4628-US_Intel_Assignments_All#page2.tif source=AC4628-US_Intel_Assignments_All#page3.tif source=AC4628-US_Intel_Assignments_All#page4.tif source=AC4628-US_Intel_Assignments_All#page5.tif source=AC4628-US_Intel_Assignments_All#page6.tif source=AC4628-US_Intel_Assignments_All#page7.tif source=AC4628-US_Intel_Assignments_All#page8.tif source=AC4628-US_Intel_Assignments_All#page9.tif source=AC4628-US_Intel_Assignments_All#page10.tif source=AC4628-US_Intel_Assignments_All#page11.tif source=AC4628-US_Intel_Assignments_All#page12.tif	

ASSIGNMENT

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we, the undersigned inventor(s):

**Amit K Srivastava; Majid Shushtarian; Anand K Enamandram; Jared W Havican;
Jeffrey A Pihlman; Michael J Karas; Ramamurthy Krithivas; Christine Watnik**

hereby sell, assign, and transfer to:

Intel Corporation

a corporation of Delaware, having a principal place of business at 2200 Mission College Boulevard, Santa Clara, California, 95054 USA ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all other countries, in and to any and all inventions and improvements that are disclosed in the application for the patent entitled:

METHOD TO DYNAMICALLY CONFIGURE MONITOR AND GOVERN THE MULTIPLE CHIPSETS IN THE SYSTEM TO ACT AS EXTENDED HIGH SPEED IO EXPANDER(S)

which was filed on February 13, 2020 as

US Application Number 16/790,648 and
COUNTRY or International Office

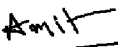
which has been executed by the undersigned prior hereto or concurrently herewith on the date(s) indicated below,

and in and to said application and all Additional Applications, and all other patent applications that have been or shall be filed in the United States and all other countries and international filing offices on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all other countries and international filing offices on said inventions and improvements; and in and to all rights of priority resulting from the filing of said applications; as used herein "Additional Applications" includes but is not limited to design, utility, utility model, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and national phase applications on said inventions and improvements;

and agree that said Assignee may apply for and receive a patent or patents for said inventions and improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all Additional Applications, and all other patent applications on any and all said inventions and improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and legal representatives all facts known to the undersigned relating to said inventions and improvements and

the history thereof; and generally assist said Assignee, its successors, assigns, or legal representatives in securing and maintaining proper patent protection for said inventions and improvements and for vesting title to said inventions and improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

 _____ Amit K Srivastava	_____ 3/3/2020 Date signed
_____ Majid Shushtarian	_____ Date signed
_____ Anand K Enamandram	_____ Date signed
_____ Jared W Havican	_____ Date signed
_____ Jeffrey A Pihlman	_____ Date signed
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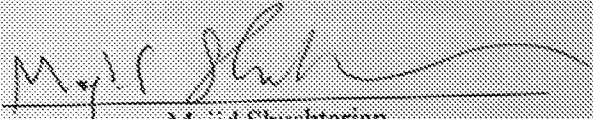
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Date signed



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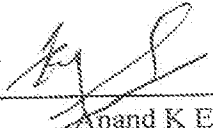
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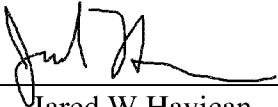
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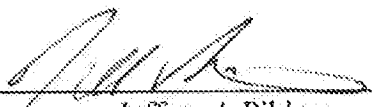
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 <hr/> Jared W Havican	<hr/> 6/1/2020 Date signed
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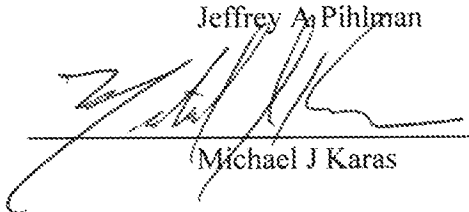
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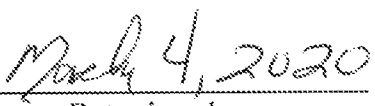
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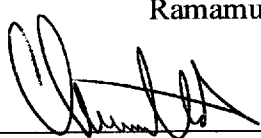
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