

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT6337465

SUBMISSION TYPE:	NEW ASSIGNMENT	
NATURE OF CONVEYANCE:	ASSIGNMENT	
CONVEYING PARTY DATA		
	Name	Execution Date
	MARVELL INTERNATIONAL LTD.	12/31/2019
RECEIVING PARTY DATA		
Name:	CAVIUM INTERNATIONAL	
Street Address:	C/O OCORIAN TRUST (CAYMAN) LIMITED	
Internal Address:	PO BOX 1350, CLIFTON HOUSE, 75 FORT STREET	
City:	GRAND CAYMAN	
State/Country:	CAYMAN ISLANDS	
Postal Code:	KY1-1108	
PROPERTY NUMBERS Total: 20		
	Property Type	Number
	Application Number:	16663413
	Application Number:	16520642
	Application Number:	15983263
	Application Number:	15988638
	Application Number:	16008176
	Application Number:	16189407
	Application Number:	16200689
	Application Number:	16525723
	Application Number:	16216369
	Application Number:	16256584
	Application Number:	16359076
	Application Number:	16375115
	Application Number:	16433567
	Application Number:	16512535
	Application Number:	16527100
	Application Number:	16568394
	Application Number:	16550953
	Application Number:	16599738
	Application Number:	16655283

PATENT

Property Type	Number
Application Number:	16662293
CORRESPONDENCE DATA	
Fax Number:	(248)641-0270
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	2486411600
Email:	hprovost@hdp.com
Correspondent Name:	HARNES, DICKEY & PIERCE, P.L.C.
Address Line 1:	P.O. BOX 828
Address Line 4:	BLOOMFIELD HILLS, MICHIGAN 48303
ATTORNEY DOCKET NUMBER:	5059-500027
NAME OF SUBMITTER:	HOLLY PROVOST
SIGNATURE:	/Holly Provost/
DATE SIGNED:	10/06/2020
Total Attachments: 4	
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ASSIGNMENT

WHEREAS, Marvell International Ltd., a corporation organized under the laws of Bermuda, and with offices at Canon's Court, 22 Victoria Street, Hamilton, Bermuda HM12 (hereinafter "**Assignor**") owns the patents and patent applications listed in Exhibit A attached hereto and incorporated herein by this reference (hereinafter "**Group A**");

WHEREAS, Cavium International, a corporation organized under the laws of Cayman Islands, and with offices at c/o Ocorian Trust (Cayman) Limited, PO Box 1350, Clifton House, 75 Fort Street, Grand Cayman KY1-1108, Cayman Islands (former address: Estera Trust (Cayman) Limited, PO Box 1350, Clifton House, 75 Fort Street, Grand Cayman KY1-1108, Cayman Islands), a limited liability corporation (hereinafter "**Assignee**"), desires to acquire the Assignor's ownership interest in, to and under the **Group A**; and

WHEREAS, Assignor has executed on an effective date of December 31, 2019 (hereinafter "**Effective Date**") that certain Transfer and Assignment Agreement assigning, among other things, all interest in and to the **Group A** to Assignee (the "**Purchase Agreement**").

NOW, THEREFORE, for consideration of one dollar (\$1.00) and other good and valuable consideration paid by Assignee to Assignor, the receipt and sufficiency of which hereby is acknowledged, Assignor does hereby sell, assign and transfer to Assignee its entire interest in and to the **Group A**, including all divisions, continuations, reexaminations, reissues, and foreign counterparts of the applications and patent registrations for the **Group A** (and the right to claim priority and the right to apply for any of the foregoing); including assignment of any and all provisional applications that are relied upon for priority; all rights to causes of action and remedies related thereto (including, without limitation, the right to sue for past, present or future infringement, misappropriation or violation of rights related to the foregoing); and any and all other rights and interests arising out of, in connection with or in relation to the **Group A**.


FURTHER, nothing contained herein shall be deemed to alter or amend the terms and provisions of the Purchase Agreement and in the event of any conflict between the terms and provisions of this Assignment and the Purchase Agreement, the terms and provisions of the Purchase Agreement shall be deemed to govern and be controlling in all circumstances. This Assignment is executed pursuant to the Purchase Agreement and is entitled to the benefits and subject to the provisions thereof and shall bind and inure to the benefit of the parties thereto and their respective successors and assigns.

FURTHER, Assignor hereby covenants and agrees to execute and deliver, at the request of Assignee, such further instruments of transfer and assignment and to take any other action as such Assignee may reasonably request to more effectively consummate the assignments contemplated by this Assignment. Specifically, Assignor agrees to, at Assignee's expense, execute, acknowledge and deliver such further documents, instruments, conveyances and assurances and take such further actions as may be reasonably required to register in the name of Assignee the assignment of any of the Patents in **Group A** in any appropriate governmental agency or registrar.

IN WITNESS WHEREOF, Assignor and Assignee have executed and delivered this Patent Assignment by their duly authorized representatives at 11:56 pm PST on the **Effective Date**.

ASSIGNOR:

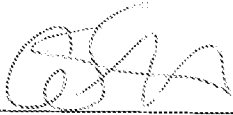
Marvell International Ltd.,
a corporation organized under the laws of Bermuda

By: 

Name: Sherman Taylor
Title: Director

ASSIGNEE:

Cavium International,
a corporation organized under the laws of Cayman Islands

By: 

Name: CAROL FEATHERS
Title: DIRECTOR

Exhibit A

Patents and Patent Applications

Country	AppNo	FileDt	PatNo	IssDt	Status	Title	HDP No.
US	16/663413	10/25/2019			Pending	Multi-Chip Module (MCM) With Chip-To-Chip Connection Redundancy And Method	5059-003733-US
US	16/520642	7/24/2019			Published	Zero Test Time Memory Using Background Built-In Self-Test	5059-004252-US-DVA
US	15/983263	5/18/2018			Published	Merged Write Driver Based On Local Source Line Mram Architecture	5059-004279-US
TW	108113231	4/16/2019			Pending	Merged Write Driver Based On Local Source Line Mram Architecture	5059-104279-TW
US	15/988638	5/24/2018			Published	Fan-Out Of Connections Of Processors On A Panel Assembly	5059-004280-US
US	16/008176	6/14/2018	10579774	3/3/2020	Grantd-NoMF	Integrated Circuit (IC) Design Systems And Methods Using Single-Pin Imaginar Devices	5059-004281-US
US	16/189407	11/13/2018			Allowed	Ultra-Low Voltage Level Shifter	5059-004283-US
TW	108136679	10/9/2019			Pending	Ultra-Low Voltage Level Shifter	5059-104283-TW
US	16/200689	11/27/2018			Allowed	Parallel-Prefix Adder And Method	5059-004284-US
DE	1020192163823	10/24/2019			Pending	Parallel-Prefix Adder & Method	5059-104284-DE
TW	108138626	10/25/2019			Pending	Parallel-Prefix Adder & Method	5059-104284-TW
US	16/525723	7/30/2019			Pending	Feed Forward Equalizer With Power-Optimized Distributed Arithmetic Architecture And Method	5059-004285-US-COA
US	16/216369	12/11/2018			Allowed	Low-Power Scan Flip-Flop	5059-004286-US
DE	1020192173667	11/11/2019			Pending	Low-Power Scan Flip-Flop	5059-104286-DE
TW	108140839	11/11/2019			Pending	Low-Power Scan Flip-Flop	5059-104286-TW
US	16/256584	1/24/2019			Pending	High-Density High-Bandwidth Static Random Access Memory (SRAM) With Phase Shifted Sequential Read	5059-004288-US
US	16/359076	3/20/2019			Pending	Dual-Port Memory With Integrated Wordline Coupling Mitigation Structure And Method	5059-004290-US

Patent-2A -- Avera Related

US	16/375115	4/4/2019			Pending	Memory Built-In Self Test Error Correcting Code (MBIST ECC) For Low Voltage Memories	5059-004291-US
US	16/433567	6/6/2019			Pending	Selection Circuit	5059-004293-US
US	16/512535	7/16/2019			Pending	Fully Digital, Static, True Single-Phase Clock (Tspc) Flip-Flop	5059-004294-US
US	16/527100	7/31/2019			Pending	Multi-Port Memory Architecture For A Systolic Array	5059-004295-US
US	16/568394	9/12/2019			Pending	Sensing Circuits For Charge Trap Transistors	5059-004296-US
US	16/550953	8/26/2019			Pending	Multi-Port High Performance Memory	5059-004297-US
US	16/599738	10/11/2019			Pending	Partitioned Substrates With Interconnect Bridge	5059-004298-US
US	16/655283	10/17/2019			Pending	Multiple Sense Amplifier And Data Path-Based Pseudo Dual Port Sram	5059-004299-US
US	16/662293	10/24/2019			Pending	IC Chip Package With Dummy Solder Structure Under Corner, And Related Method	5059-004300-US

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