## 506312193 10/20/2020

# **PATENT ASSIGNMENT COVER SHEET**

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT6358942

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

#### **CONVEYING PARTY DATA**

Name	Execution Date
VIBHOR JAIN	10/16/2020
JOHN J. ELLIS-MONAGHAN	10/15/2020
ANTHONY K. STAMPER	10/15/2020
STEVEN M. SHANK	10/15/2020
JOHN J. PEKARIK	10/16/2020

#### **RECEIVING PARTY DATA**

Name:	GLOBALFOUNDRIES U.S. INC.
Street Address:	2600 GREAT AMERICA WAY
City:	SANTA CLARA
State/Country:	CALIFORNIA
Postal Code:	95054

## **PROPERTY NUMBERS Total: 1**

Property Type	Number
Application Number:	17075056

### **CORRESPONDENCE DATA**

**Fax Number:** (703)848-2981

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Email: achernavtseva@rmsclaw.com
Correspondent Name: ANDREW M. CALDERON

Address Line 1: 7918 JONES BRANCH DRIVE, SUITE 500

Address Line 4: MCLEAN, VIRGINIA 22102

NAME OF SUBMITTER:	ANDREW M. CALDERON
SIGNATURE:	/Andrew M. Calderon/
DATE SIGNED:	10/20/2020
	This document serves as an Oath/Declaration (37 CFR 1.63).

**Total Attachments: 5** 

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PATENT 506312193 REEL: 054111 FRAME: 0676

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> PATENT REEL: 054111 FRAME: 0677

COMBINED DECLARATION and ASSIGNMENT (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)		
Title of	HETEROJUNCTION BIPOLAR TRANSISTOR WITH BURIED TRAP RICH	
Invention	ISOLATION REGION	
	DECLARATION	
As the below	named inventor, I hereby declare that:	
This declarated is directed to:	V V 11	
	United States application or PCT international application number filed on	
The above-id	entified application was made or authorized to be made by me.	
I believe that	I am the original inventor or an original joint inventor of a claimed invention in the application.	
	owledge that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by fine ent of not more than five (5) years, or both.	
	I have reviewed and understand the contents of the application and am aware of the duty to disclose to the US Patent and Trademark Office all information known to me to be material to patentability as defined in 37 CFR § 1.56.	
	ASSIGNMENT	
For good and valuable consideration, the receipt, sufficiency and adequacy of which are hereby acknowledged, I hereby sell, assign, transfer, to GLOBALFOUNDRIES U.S. Inc., having a business address of 2600 Great America Way, Santa Clara, CA 95054 U.S.A., its successors and assigns, absolutely and forever, my entire right, title and interest in and to the invention listed above, together with the Application, any and all Patents that may issue in the United States and/or any foreign countries, and any reissues, re-examinations, renewals, continuations, continuation-in-parts, divisionals or extensions thereof that may be issued or granted, and all right, title and interest to the inventions contained in said Patents and Applications, for the United States and all foreign countries, and all the rights and privileges relating thereto including but not limited to the priority rights arising from said Applications, the right to recover and take all such proceedings as may be necessary for the recovery of damages or otherwise in respect of past, present and future infringement, and the right to apply for, take and maintain patents on said inventions.		
	n of this Combined Declaration or Assignment is held unenforceable, then remaining portions hereof neless remain in full force and effect.	
LEGAL NAM	ME OF INVENTOR	
Inventor:	Vibhor JAIN Date: 10/16/2020	
Signature: WM Jir		

PATENT REEL: 054111 FRAME: 0678

COMBINED DECLARATION and ASSIGNMENT (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)	
Title of	HETEROJUNCTION BIPOLAR TRANSISTOR WITH BURIED TRAP RICH ISOLATION REGION
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This declaration is directed to:	on The attached application, or
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	of this Combined Declaration or Assignment is held unenforceable, then remaining portions hereof eless remain in full force and effect.
LEGAL NAM	E OF INVENTOR
Inventor:	John J. ELLIS-MONAGHAN Date: 10/15/2020
Signature:	John J Ellie Monaghan

COMBINED DECLARATION and ASSIGNMENT (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)	
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	n of this Combined Declaration or Assignment is held unenforceable, then remaining portions hereof eless remain in full force and effect.
LEGAL NAM	E OF INVENTOR
Inventor:	Anthony K. STAMPER Date: October 15, 2020
Signature:	Onty Stempe

COMBINED DECLARATION and ASSIGNMENT (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)	
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	n of this Combined Declaration or Assignment is held unenforceable, then remaining portions hereof teless remain in full force and effect.
LEGAL NAM	ME OF INVENTOR
Inventor: Signature:	Steven M. SHANK  Date: 10/15/20

COMBINED DECLARATION and ASSIGNMENT (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)	
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	United States application or PCT international application number filed on
The above-id	entified application was made or authorized to be made by me.
I believe that	I am the original inventor or an original joint inventor of a claimed invention in the application.
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If any portion of this Combined Declaration or Assignment is held unenforceable, then remaining portions hereof shall nevertheless remain in full force and effect.	
LEGAL NAM	ME OF INVENTOR
Inventor:	John J. PEKARIK Date: 10/16/2020
Signature:	ication data sheet (PTO/SB/14 or equivalent), including naming the entire inventive entity, must accompany this form.

Use an additional form for each additional inventor.

**RECORDED: 10/20/2020** 

PATENT REEL: 054111 FRAME: 0682