506313102 10/21/2020

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT6359851

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Kuo-Hsing Lee	10/15/2020
Sheng-Yuan Hsueh	10/15/2020
Chien-Liang Wu	10/15/2020
Te-Wei Yeh	10/15/2020
Yi-Chun Chen	10/15/2020

RECEIVING PARTY DATA

Name:	UNITED MICROELECTRONICS CORP.
Street Address:	No.3, Li-Hsin Road 2, Science-Based Industrial Park
City:	Hsin-Chu City
State/Country:	TAIWAN

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	17075707

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	NAUP3709USA
NAME OF SUBMITTER:	KATE YEH
SIGNATURE:	/KATE YEH/
DATE SIGNED:	10/21/2020

Total Attachments: 10 source=3991855#page1.tif source=3991855#page2.tif source=3991855#page3.tif

PATENT 506313102 REEL: 054117 FRAME: 0345

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ource=3991855#page9.tif	
ource=3991855#page10.tif	

PATENT REEL: 054117 FRAME: 0346

Title of Invention:

RESISTOR AND RESISTOR-TRANSISTOR-LOGIC CIRCUIT WITH GAN STRUCTURE AND METHOD OF MANUFACTURING THE SAME

As the below named inventor, I here This declaration is directed to:	by declare that:			
☑ The attached application, or				
☐ United States application nu	mber	filed o	n, c	or
☐ PCT international application	n number	filed	d on	
The above-identified application was	made or authorized to	be made by me.		
I believe that I am the original invent application.	or or an original joint in	ventor of a claimed	l invention in the	
I hereby acknowledge that any willfu under18 U.S.C. 1001 by fine or impr	I false statement made isonment of not more tl	in this declaration nan five (5) years,	is punishable or both.	
In consideration of the payment by	UNITED MICROEL CORP.	ECTRONICS	having a postal addre	ss of
No.3, Li-Hsin Road 2, Science	-Based Industrial I	Park, Hsin-Chu	City 300, Taiwan, I	₹.О.С
(referred to as "ASSIGNEE"below) to acknowledged, andfor other good ar	o I of the sum of One D nd valuable consideration	ollar (\$ 1.00), the r on.	eceipt of which is here	b y
I hereby sell, assign and transfer to a the entire right, title and interest in a invention as above-identified applica invention by the above application of substitutes, or extensions thereof, ar	nd to any and all impro tion and, in and to, all l r any continuations, cor	vements which are _etters Patent to be ntinuation-in-part, c	disclosed in the e obtained for said livisions, renewals,	Ξ
I hereby covenant that no assignment entered into which would conflict with	nt, sale, agreement or e h this assignment;	encumbrance has t	een or will be made or	•
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representatives any and all papers, i maintain, issue and enforce said app equivalents thereof which may be no IN WITNESS WHEREOF, I have her	olication, said invention ecessary or desirable to	and said Letters P carry out the prop	atent and said oses thereof.	
Note: An application data sheet (PTC inventive entity, must accompany thi	O/SB/14 or equivalent), s form. Use this form fo	including naming or each additional i	the entire nventor.	

Page 1 of 10

NPO#NAU-P3709-USA:0 CUST#UMCD-2020-0281

LEGAL NAME OF INVENTOR(ASSIGNOR)

Inventor: Kuo-Hsing Lee Date: OCT 1 5 2020

Signature: Kuo-Hsing Lee

Page 2 of 10

NPO#NAU-P3709-USA:0 CUST#UMCD-2020-0281

Title of Invention:

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(referred to as "ASSIGNEE"below) to acknowledged, andfor other good are	o I of the sum of One Dollar (\$ 1.0 nd valuable consideration.	0), the receipt of which is her	eby
I hereby sell, assign and transfer to a the entire right, title and interest in a invention as above-identified applica- invention by the above application of substitutes, or extensions thereof, an	nd to any and all improvements w ition and, in and to, all Letters Pat r any continuations, continuation-i	rhich are disclosed in the ent to be obtained for said in-part, divisions, renewals,	EE
I hereby covenant that no assignment entered into which would conflict with		ce has been or will be made	or
I further covenant that ASSIGNEE wand documents relating to said inverknown and accessible to I and will to related thereto and will promptly exe	ntion and said Letters Patent and estify as to the same in any interfe	legal equivalents as may be rence, litigation proceeding	ts
representatives any and all papers, i maintain, issue and enforce said app equivalents thereof which may be no IN WITNESS WHEREOF, I have he	olication, said invention and said lecessary or desirable to carry out	etters Patent and said the proposes thereof.	
Note: An application data sheet (PTo inventive entity, must accompany this			

Page 3 of 10

NPO#NAU-P3709-USA:0 CUST#UMCD-2020-0281

LEGAL NAME OF INVENTOR(ASSIGNOR)

Inventor:

Sheng-Yuan Hsueh

Date:

OCT 1 5 2020

Signature: Shang-Yuan Hsueh

Page

Title of Invention:

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I hereby sell, assign and transfer to a the entire right, title and interest in a invention as above-identified applica invention by the above application of substitutes, or extensions thereof, ar	nd to any and all im tion and, in and to, r any continuations,	orovements which all Letters Patent continuation-in-pa	n are disclosed in the to be obtained for said art, divisions, renewals	l s,
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I further covenant that ASSIGNEE wand documents relating to said inverknown and accessible to I and will to related thereto and will promptly exe	ntion and said Letter estify as to the same	s Patent and lega in any interferen	al equivalents as may b ce, litigation proceedin	ре
representatives any and all papers, i maintain, issue and enforce said app equivalents thereof which may be no IN WITNESS WHEREOF, I have he	olication, said invent ecessary or desirabl	ion and said Lette e to carry out the	ers Patent and said proposes thereof.	
Note: An application data sheet (PTo				

Page 5 of 10

NPO#NAU-P3709-USA:0 CUST#UMCD-2020-0281

LEGAL NAME OF INVENTOR(ASSIGNOR)

Inventor: Chien-Liang Wu

Date:

OCT 1 5 2020

Signature:

Chien-Liang Wu

Page 6 of 10

Title of Invention:

RESISTOR AND RESISTOR-TRANSISTOR-LOGIC CIRCUIT WITH GAN STRUCTURE AND METHOD OF MANUFACTURING THE SAME

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I hereby covenant that no assignmentered into which would conflict with		umbrance has	been or will be mad	le or
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representatives any and all papers, i maintain, issue and enforce said app equivalents thereof which may be no IN WITNESS WHEREOF, I have he	olication, said invention and ecessary or desirable to ca	d said Letters F rry out the prop	Patent and said poses thereof.	
Note: An application data sheet (PTo inventive entity, must accompany thi				

Page 7 of 10

NPO#NAU-P3709-USA:0 CUST#UMCD-2020-0281

LEGAL NAME OF INVENTOR(ASSIGNOR)						
Inventor:	Te-Wei Yeh	Date:	OCT 1 5 2020			
Signature:	Te-Wei Yeh					

Page 8 of 10

NPO#NAU-P3709-USA:0 CUST#UMCD-2020-0281

Title of Invention:

RESISTOR AND RESISTOR-TRANSISTOR-LOGIC CIRCUIT WITH GAN STRUCTURE AND METHOD OF MANUFACTURING THE SAME

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I hereby covenant that no assignment entered into which would conflict with	nt, sale, agreement or encumbrar n this assignment;	nce has been or will	be made or
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representatives any and all papers, i maintain, issue and enforce said app equivalents thereof which may be ne IN WITNESS WHEREOF, I have he	olication, said invention and said lecessary or desirable to carry out	Letters Patent and sa the proposes thereo	of.
Note: An application data sheet (PT)	0/SB/14 or equivalent) including	naming the entire	

Page 9 of 10

inventive entity, must accompany this form. Use this form for each additional inventor.

NPO#NAU-P3709-USA:0 CUST#UMCD-2020-0281

LEGAL NAME OF INVENTOR(ASSIGNOR)			
Inventor:	Yi-Chun Chen	Date:	OCT 1 5 2020
Signature:	Yi-Chun Chen.		

Page 10 of 10

F#NPO-P0002E-US1201 DSB0-109U028454

NPO#NAU-P3709-USA:0 CUST#UMCD-2020-0281

RECORDED: 10/21/2020

PATENT REEL: 054117 FRAME: 0356