506354122 11/13/2020

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE: NEW ASSIGNMENT NATURE OF CONVEYANCE: ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
KOJI ITO	11/11/2019
KEISUKE TADA	11/11/2019
MOTOTADA SAKASHITA	11/11/2019

RECEIVING PARTY DATA

Name:	MICRON TECHNOLOGY, INC.	
Street Address:	8000 SOUTH FEDERAL WAY	
City:	BOISE	
State/Country:	IDAHO	
Postal Code:	83716-9632	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	17097717

CORRESPONDENCE DATA

Fax Number: (206)903-8820

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 206-903-8800

Email: ip.docket.se@dorsey.com

DORSEY & WHITNEY LLP-IP DEPT.-MTI **Correspondent Name:**

Address Line 1: **COLUMBIA CENTER**

Address Line 2: 701 5TH AVENUE, SUITE 6100

Address Line 4: SEATTLE, WASHINGTON 98104-7043

P283467.US.02 ATTORNEY DOCKET NUMBER: **NAME OF SUBMITTER: JEAN WANG SIGNATURE:** /Jean Wang/ **DATE SIGNED:** 11/13/2020

Total Attachments: 3

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PATENT REEL: 054363 FRAME: 0595 506354122

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Koji Ito, Keisuke Tada and Mototada
Sakashita

Docket No. P283467.US.01

Docket No. P283467.US.01

Disclosure No. 2019-1034.00/US

Filed: Concurrently herewith

For: APPARATUSES AND METHODS
FOR CLOCK LEVELING IN
SEMICONDUCTOR MEMORIES

ASSIGNMENT:

X	Enclosed for recording
	Previously recorded
Date:	•
Reel:	

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned does hereby:

SELL, ASSIGN AND TRANSFER to **Micron Technology, Inc.** (the "Assignee"), a corporation of Delaware, having a place of business at 8000 South Federal Way, Boise, Idaho 83716-9632, the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned concurrently herewith and is entitled **"APPARATUSES AND METHODS FOR CLOCK LEVELING IN SEMICONDUCTOR MEMORIES"**; such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States of America;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such improvements to the Assignee;

WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or will be made to others by the undersigned, and that the full right to convey the same as herein expressed is possessed by the undersigned;

COVENANT that, when requested and at the expense of the Assignee, to carry out in good faith the intent and purpose of this assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all such improvements; execute all rightful oaths, declarations, assignments, powers of attorney and other papers; communicate to the Assignee all facts known to the undersigned relating to such improvements and the history thereof; and generally do everything possible which the Assignee shall consider desirable for securing, maintaining and enforcing proper patent protection for such improvements and for vesting title to such improvements in the Assignee;

TO BE BINDING on the heirs, assigns, representatives and successors of the undersigned and extend to the successors, assigns and nominees of the Assignee.

Signature:	<u>Kyr Isa</u>	Date:	11/11/2019
Assignor Nat	ne: Koji Ito		
Witness # 1 Signature: Witness Nar	Mosayoshi Yamazaki	Date:	11/11/2019
Witness # 2: Signature: ≝	2) sy	***************************************	11-11-2019

Signature: <u>Keisuke Todo</u> Assignor Name: Keisuke Tada	Date: 11/11/2019
Witness # 1: Signature: (1975) Jawa = 100 Ja	Date: 11 / 11 / 2019
Witness # 2: Signature Witness Name: Shoko Tomita	Date:
Signature: <u>Mota to da Salca Ai to</u> Assignor Name: Mototada Sakashita	Date: 11 /11 /2019
Witness # 1: Signature: Masayoshi Yamazaki Witness Name: Masayoshi Yamazaki	Date: 11/11/2019
Witness # 2: Signature: Signature: Shoko Tomita	Date: <u>11 - (1 - 20/9</u>