

PATENT ASSIGNMENT COVER SHEET

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EPAS ID: PAT6413488

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
OWEN R. FAY	11/17/2020
MADISON E. WALE	11/19/2020
JAMES L. VOELZ	11/18/2020
DYLAN W. SOUTHERN	11/20/2020
DUSTIN L. HOLLOWAY	11/18/2020
RECEIVING PARTY DATA	
Name:	MICRON TECHNOLOGY, INC.
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Internal Address:	P.O. BOX 6
City:	BOISE
State/Country:	IDAHO
Postal Code:	83707-0006
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	17100610
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ATTORNEY DOCKET NUMBER:	010829-9522.US01
NAME OF SUBMITTER:	SAM BEARD
SIGNATURE:	/Sam Beard/
DATE SIGNED:	11/20/2020
Total Attachments: 2	

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ASSIGNMENT BY INVENTORS

This Assignment is by the following individuals (the "Assignors"):

- **Owen R. Fay**, having a mailing address of c/o Micron Technology, Inc., 8000 S. Federal Way, P.O. Box 6, Boise, Idaho 83707-0006;
- **Madison E. Wale**, having a mailing address of c/o Micron Technology, Inc., 8000 S. Federal Way, P.O. Box 6, Boise, Idaho 83707-0006;
- **James L. Voelz**, having a mailing address of c/o Micron Technology, Inc., 8000 S. Federal Way, P.O. Box 6, Boise, Idaho 83707-0006;
- **Dylan W. Southern**, having a mailing address of c/o Micron Technology, Inc., 8000 S. Federal Way, P.O. Box 6, Boise, Idaho 83707-0006; and
- **Dustin L. Holloway**, having a mailing address of c/o Micron Technology, Inc., 8000 S. Federal Way, P.O. Box 6, Boise, Idaho 83707-0006.

The Assignors invented one or more certain inventions (the "Invention(s)") described in an application for Letters Patent of the United States titled SEMICONDUCTOR ASSEMBLIES WITH REDISTRIBUTION STRUCTURES FOR DIE STACK SIGNAL ROUTING, naming the Assignors as inventors, and filed on November 20, 2020 as U.S. Application No. 17/100,610 (the "Application"). The Assignors authorize the Assignee, identified below, or its representatives to insert the application number and filing date of the Application when known.

Micron Technology, Inc., a corporation of Delaware having its principal place of business at 8000 S. Federal Way, P.O. Box 6, Boise, Idaho 83707-0006 ("Assignee"), desires to acquire the entire right, title and interest in and to the Invention(s) and the Application, and in and to any patents (collectively, "Patents") that may be granted for the Invention(s) in the United States or in any foreign countries.

For valuable consideration, the receipt and sufficiency of which Assignors acknowledge, Assignors hereby sell, assign, and transfer to Assignee, its successors, legal representatives and assigns, the entire right, title and interest in and to: the Invention(s), the Application, and any Patents; any divisions, continuations, and continuations-in-part of the Application and any other application claiming priority rights from the Application; any reissues, reexaminations, or extensions of any and all Patents;

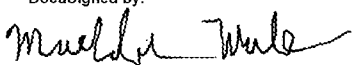
the right to file foreign applications directly in the name of Assignee; and the right to claim priority rights deriving from the Application (collectively, the "Rights"). Assignors warrant that Assignors own the Rights, and that the Rights are unencumbered. Assignors also agree to not sign any writing or do any act conflicting with this assignment, and, without further compensation, sign all documents and do such additional acts as Assignee deems necessary or desirable to: perfect Assignee's enjoyment of the Rights; conduct proceedings regarding the Rights, including any litigation or interference proceedings; or perfect or defend title to the Rights. Assignors request the Commissioner of Patents to issue any Patent of the United States that may be issued on the Invention(s) to Assignee. This Assignment may be executed in counterparts.

Date: November 17, 2020

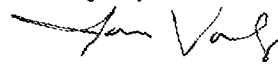
DocuSigned by:

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Owen R. Fay

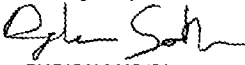
Date: November 19, 2020

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Madison E. Wale

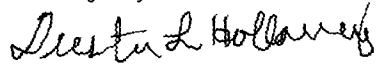
Date: November 18, 2020

DocuSigned by:

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James L. Voelz

Date: November 20, 2020

DocuSigned by:

588E4D99CC8D4B3...
Dylan W. Southern

Date: November 18, 2020

DocuSigned by:

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Dustin L. Holloway